Appendix for 5A_N41A

Product Name: CSX8

Model No: LGT-08QA-2301

Appendix A: Average Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power（dBm）	Power Class	Verdict
DC_5A_n41A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	20.83	PC3	PASS
DC_5A_n41A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	20.94	PC3	PASS
DC_5A_n41A	30	5+20	DFT-256QAM	M+L	Outer_Full	20.69	PC3	PASS
DC_5A_n41A	30	5+20	DFT-256QAM	M+L	Inner_Full	20.69	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.76	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.72	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	22.18	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Inner_Full	22.14	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	21.70	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	21.50	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	22.23	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Inner_Full	22.04	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	21.77	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	21.83	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	22.34	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Inner_Full	22.32	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	21.65	PC3	PASS

DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	22.11	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	22.37	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Inner_Full	22.29	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.29	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.66	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	22.98	PC3	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Inner_Full	23.04	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	22.39	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	22.58	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	22.98	PC3	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Inner_Full	23.08	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.86	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.84	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	22.27	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Inner_Full	22.29	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	21.84	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	21.78	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	22.32	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Inner_Full	22.30	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	22.33	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	22.15	PC3	PASS

DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	22.39	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Inner_Full	22.25	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	21.88	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	21.96	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	22.36	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Inner_Full	22.35	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.94	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	22.43	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	22.69	PC3	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Inner_Full	22.64	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	21.83	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	22.48	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	22.53	PC3	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Inner_Full	22.67	PC3	PASS
DC_5A_n41A	30	5+60	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.49	PC3	PASS
DC_5A_n41A	30	5+60	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.79	PC3	PASS
DC_5A_n41A	30	5+60	DFT-PI2BPSK	M+M	Outer_Full	22.28	PC3	PASS
DC_5A_n41A	30	5+60	DFT-PI2BPSK	M+M	Inner_Full	22.23	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	21.64	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	21.88	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	22.29	PC3	PASS

DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Inner_Full	22.28	PC3	PASS
DC_5A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	21.67	PC3	PASS
DC_5A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	21.97	PC3	PASS
DC_5A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	22.33	PC3	PASS
DC_5A_n41A	30	5+60	DFT-16QAM	M+M	Inner_Full	22.24	PC3	PASS
DC_5A_n41A	30	5+60	DFT-64QAM	M+M	Edge_1RB_Left	21.88	PC3	PASS
DC_5A_n41A	30	5+60	DFT-64QAM	M+M	Edge_1RB_Right	22.16	PC3	PASS
DC_5A_n41A	30	5+60	DFT-64QAM	M+M	Outer_Full	22.33	PC3	PASS
DC_5A_n41A	30	5+60	DFT-64QAM	M+M	Inner_Full	22.26	PC3	PASS
DC_5A_n41A	30	5+60	DFT-256QAM	M+M	Edge_1RB_Left	20.71	PC3	PASS
DC_5A_n41A	30	5+60	DFT-256QAM	M+M	Edge_1RB_Right	21.05	PC3	PASS
DC_5A_n41A	30	5+60	DFT-256QAM	M+M	Outer_Full	20.81	PC3	PASS
DC_5A_n41A	30	5+60	DFT-256QAM	M+M	Inner_Full	20.77	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	21.39	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	21.69	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	22.29	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Inner_Full	22.31	PC3	PASS
DC_5A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	21.53	PC3	PASS
DC_5A_n41A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	21.49	PC3	PASS
DC_5A_n41A	30	5+60	CP-16QAM	M+M	Outer_Full	22.28	PC3	PASS
DC_5A_n41A	30	5+60	CP-16QAM	M+M	Inner_Full	22.24	PC3	PASS

DC_5A_n41A	30	5+60	CP-64QAM	M+M	Edge_1RB_Left	21.75	PC3	PASS
DC_5A_n41A	30	5+60	CP-64QAM	M+M	Edge_1RB_Right	21.45	PC3	PASS
DC_5A_n41A	30	5+60	CP-64QAM	M+M	Outer_Full	21.82	PC3	PASS
DC_5A_n41A	30	5+60	CP-64QAM	M+M	Inner_Full	21.88	PC3	PASS
DC_5A_n41A	30	5+60	CP-256QAM	M+M	Edge_1RB_Left	18.73	PC3	PASS
DC_5A_n41A	30	5+60	CP-256QAM	M+M	Edge_1RB_Right	19.24	PC3	PASS
DC_5A_n41A	30	5+60	CP-256QAM	M+M	Outer_Full	19.18	PC3	PASS
DC_5A_n41A	30	5+60	CP-256QAM	M+M	Inner_Full	18.73	PC3	PASS
DC_5A_n41A	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.82	PC3	PASS
DC_5A_n41A	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.38	PC3	PASS
DC_5A_n41A	30	5+60	DFT-PI2BPSK	M+H	Outer_Full	22.66	PC3	PASS
DC_5A_n41A	30	5+60	DFT-PI2BPSK	M+H	Inner_Full	22.60	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	21.84	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.46	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	22.66	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Inner_Full	22.61	PC3	PASS
DC_5A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	21.68	PC3	PASS
DC_5A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.35	PC3	PASS
DC_5A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	22.67	PC3	PASS
DC_5A_n41A	30	5+60	DFT-16QAM	M+H	Inner_Full	22.63	PC3	PASS
DC_5A_n41A	30	5+60	DFT-64QAM	M+H	Edge_1RB_Left	22.24	PC3	PASS

DC_5A_n41A	30	5+60	DFT-64QAM	M+H	Edge_1RB_Right	22.77	PC3	PASS
DC_5A_n41A	30	5+60	DFT-64QAM	M+H	Outer_Full	22.69	PC3	PASS
DC_5A_n41A	30	5+60	DFT-64QAM	M+H	Inner_Full	22.63	PC3	PASS
DC_5A_n41A	30	5+60	DFT-256QAM	M+H	Edge_1RB_Left	20.81	PC3	PASS
DC_5A_n41A	30	5+60	DFT-256QAM	M+H	Edge_1RB_Right	21.72	PC3	PASS
DC_5A_n41A	30	5+60	DFT-256QAM	M+H	Outer_Full	21.12	PC3	PASS
DC_5A_n41A	30	5+60	DFT-256QAM	M+H	Inner_Full	21.06	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	22.52	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	23.09	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	22.68	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Inner_Full	22.57	PC3	PASS
DC_5A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	22.31	PC3	PASS
DC_5A_n41A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	22.94	PC3	PASS
DC_5A_n41A	30	5+60	CP-16QAM	M+H	Outer_Full	22.72	PC3	PASS
DC_5A_n41A	30	5+60	CP-16QAM	M+H	Inner_Full	22.64	PC3	PASS
DC_5A_n41A	30	5+60	CP-64QAM	M+H	Edge_1RB_Left	22.13	PC3	PASS
DC_5A_n41A	30	5+60	CP-64QAM	M+H	Edge_1RB_Right	22.61	PC3	PASS
DC_5A_n41A	30	5+60	CP-64QAM	M+H	Outer_Full	22.15	PC3	PASS
DC_5A_n41A	30	5+60	CP-64QAM	M+H	Inner_Full	22.15	PC3	PASS
DC_5A_n41A	30	5+60	CP-256QAM	M+H	Edge_1RB_Left	18.99	PC3	PASS
DC_5A_n41A	30	5+60	CP-256QAM	M+H	Edge_1RB_Right	19.50	PC3	PASS

DC_5A_n41A	30	5+60	CP-256QAM	M+H	Outer_Full	19.12	PC3	PASS
DC_5A_n41A	30	5+60	CP-256QAM	M+H	Inner_Full	19.14	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Left	21.32	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Right	21.31	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+L	Outer_Full	22.05	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+L	Inner_Full	22.03	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	21.37	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.36	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	24.04	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Inner_Full	21.88	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	21.88	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	21.90	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	22.12	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+L	Inner_Full	22.00	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+L	Edge_1RB_Left	21.67	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+L	Edge_1RB_Right	21.26	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+L	Outer_Full	22.10	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+L	Inner_Full	22.03	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+L	Edge_1RB_Left	20.51	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+L	Edge_1RB_Right	20.70	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+L	Outer_Full	20.76	PC3	PASS

DC_5A_n41A	30	5+100	DFT-256QAM	M+L	Inner_Full	20.55	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	21.47	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	21.63	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	23.96	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Inner_Full	21.98	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	21.60	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	21.39	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+L	Outer_Full	21.98	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+L	Inner_Full	22.00	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+L	Edge_1RB_Left	21.39	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+L	Edge_1RB_Right	21.49	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+L	Outer_Full	21.47	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+L	Inner_Full	21.49	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+L	Edge_1RB_Left	20.48	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+L	Edge_1RB_Right	20.75	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+L	Outer_Full	20.51	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+L	Inner_Full	20.54	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+M	Edge_1RB_Left	21.04	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+M	Edge_1RB_Right	21.70	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+M	Outer_Full	22.20	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+M	Inner_Full	22.18	PC3	PASS

DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	21.35	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.76	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	24.03	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Inner_Full	21.90	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	21.81	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	22.28	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	21.68	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+M	Inner_Full	22.13	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+M	Edge_1RB_Left	21.32	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+M	Edge_1RB_Right	21.94	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+M	Outer_Full	22.15	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+M	Inner_Full	22.15	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+M	Edge_1RB_Left	20.45	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+M	Edge_1RB_Right	20.74	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+M	Outer_Full	20.68	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+M	Inner_Full	20.68	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	21.36	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	21.79	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	23.16	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Inner_Full	22.09	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	21.15	PC3	PASS

DC_5A_n41A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	21.71	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+M	Outer_Full	22.06	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+M	Inner_Full	22.15	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+M	Edge_1RB_Left	21.63	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+M	Edge_1RB_Right	22.47	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+M	Outer_Full	21.63	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+M	Inner_Full	21.65	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+M	Edge_1RB_Left	20.20	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+M	Edge_1RB_Right	20.59	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+M	Outer_Full	20.66	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+M	Inner_Full	20.68	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Left	21.17	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.11	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	22.42	PC3	PASS
DC_5A_n41A	30	5+100	DFT-PI2BPSK	M+H	Inner_Full	22.28	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	21.35	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.17	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	22.31	PC3	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Inner_Full	25.14	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	21.86	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.74	PC3	PASS

DC_5A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	22.40	PC3	PASS
DC_5A_n41A	30	5+100	DFT-16QAM	M+H	Inner_Full	22.30	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+H	Edge_1RB_Left	21.36	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+H	Edge_1RB_Right	22.28	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+H	Outer_Full	22.29	PC3	PASS
DC_5A_n41A	30	5+100	DFT-64QAM	M+H	Inner_Full	22.33	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+H	Edge_1RB_Left	20.47	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+H	Edge_1RB_Right	21.37	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+H	Outer_Full	20.81	PC3	PASS
DC_5A_n41A	30	5+100	DFT-256QAM	M+H	Inner_Full	20.90	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	21.18	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	21.98	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	22.34	PC3	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Inner_Full	22.30	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+H	Edge_1RB_Left	21.32	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+H	Edge_1RB_Right	22.28	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+H	Outer_Full	22.35	PC3	PASS
DC_5A_n41A	30	5+100	CP-16QAM	M+H	Inner_Full	22.28	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+H	Edge_1RB_Left	21.31	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+H	Edge_1RB_Right	22.34	PC3	PASS
DC_5A_n41A	30	5+100	CP-64QAM	M+H	Outer_Full	21.86	PC3	PASS

DC_5A_n41A	30	5+100	CP-64QAM	M+H	Inner_Full	21.89	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+H	Edge_1RB_Left	20.31	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+H	Edge_1RB_Right	20.88	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+H	Outer_Full	20.91	PC3	PASS
DC_5A_n41A	30	5+100	CP-256QAM	M+H	Inner_Full	20.86	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.96	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	24.30	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	24.61	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+L	Inner_Full	24.52	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+L	Edge_1RB_Left	23.94	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+L	Edge_1RB_Right	24.28	PC3	PASS
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DC_5A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.30	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	24.57	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	24.91	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+M	Inner_Full	24.89	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+M	Edge_1RB_Left	24.21	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+M	Edge_1RB_Right	24.60	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+M	Outer_Full	24.86	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+M	Inner_Full	24.85	PC3	PASS

DC_5A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	24.31	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	24.70	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	24.90	PC3	PASS
DC_5A_n41A	30	5+30	DFT-QPSK	M+H	Inner_Full	24.80	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+H	Edge_1RB_Left	24.35	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+H	Edge_1RB_Right	24.13	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+H	Outer_Full	24.99	PC3	PASS
DC_5A_n41A	30	5+30	CP-QPSK	M+H	Inner_Full	24.94	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.61	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	24.02	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	24.14	PC3	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Inner_Full	24.49	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	23.61	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	24.25	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	24.52	PC3	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Inner_Full	24.55	PC3	PASS

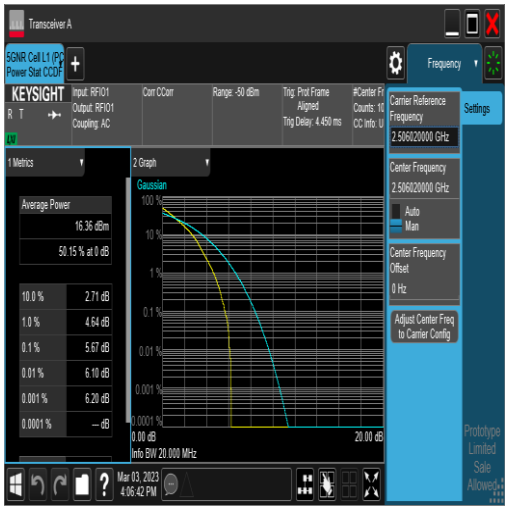
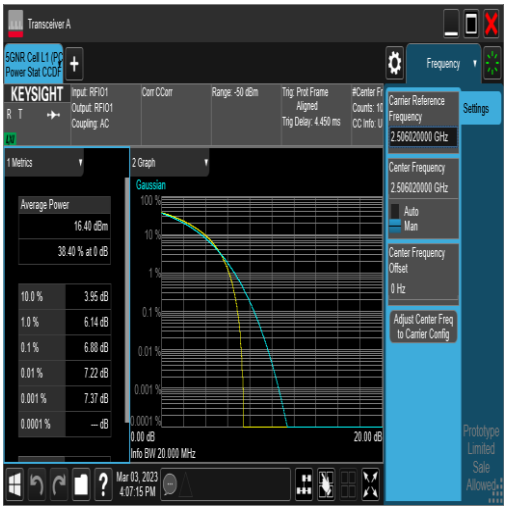
Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	5.67	≤13	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	6.88	≤13	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	5.82	≤13	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	5.82	≤13	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	5.19	≤13	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	5.02	≤13	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	5.70	≤13	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	6.71	≤13	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	5.89	≤13	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	5.82	≤13	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	5.60	≤13	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	5.08	≤13	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	5.73	≤13	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	6.85	≤13	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	5.98	≤13	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	5.72	≤13	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	5.66	≤13	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	4.09	≤13	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	5.54	≤13	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	7.19	≤13	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	5.87	≤13	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	5.27	≤13	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	5.93	≤13	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	5.16	≤13	PASS

Test Graphs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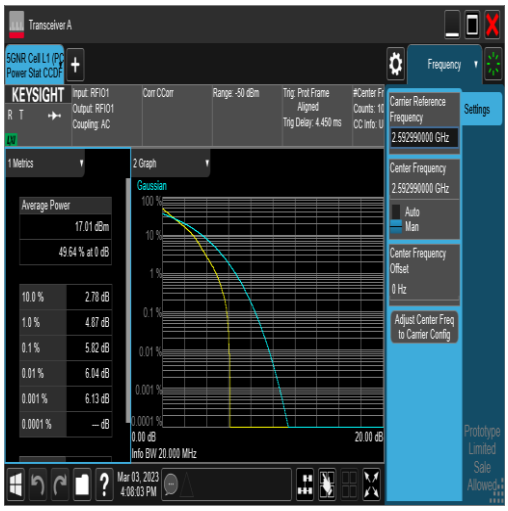	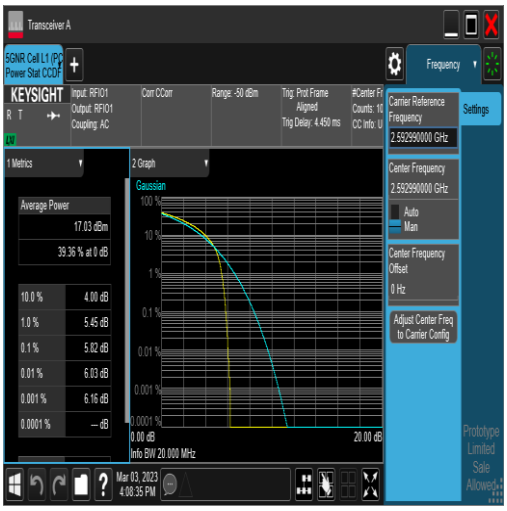
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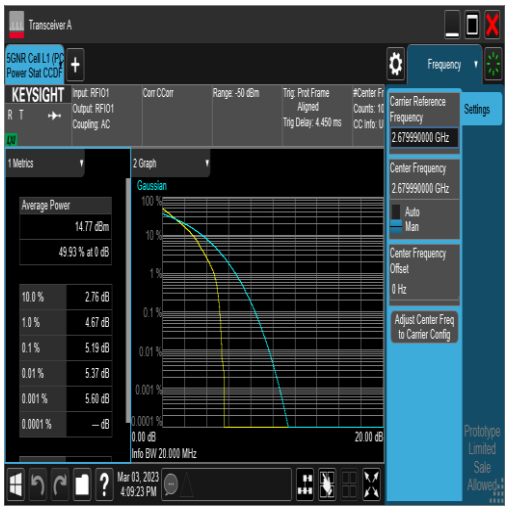	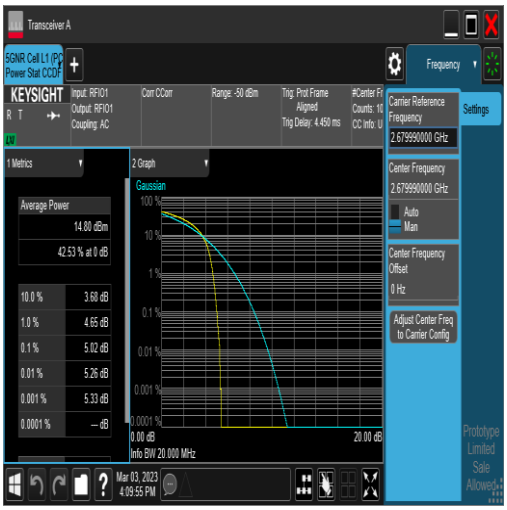
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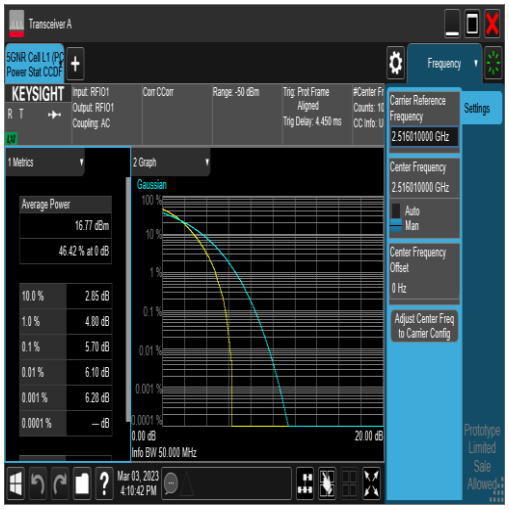	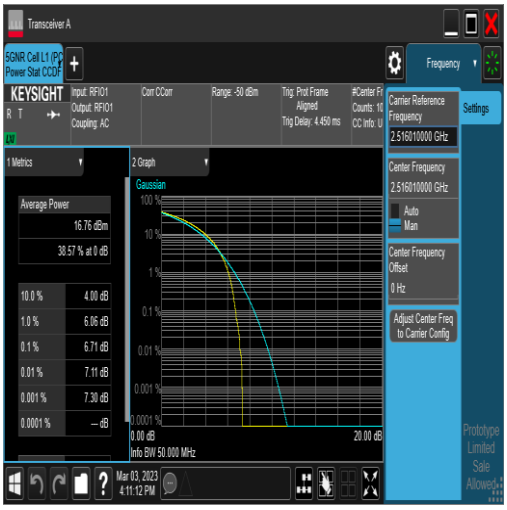
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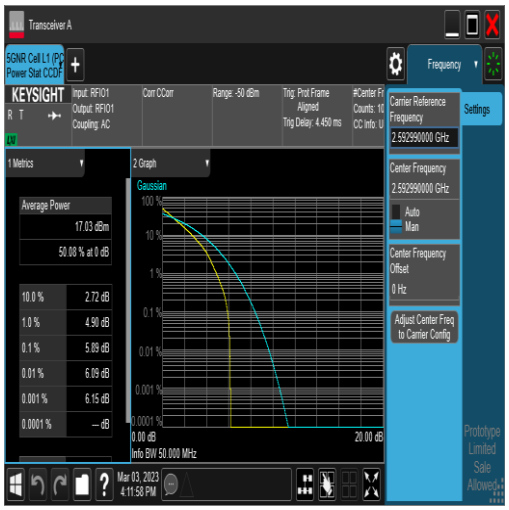	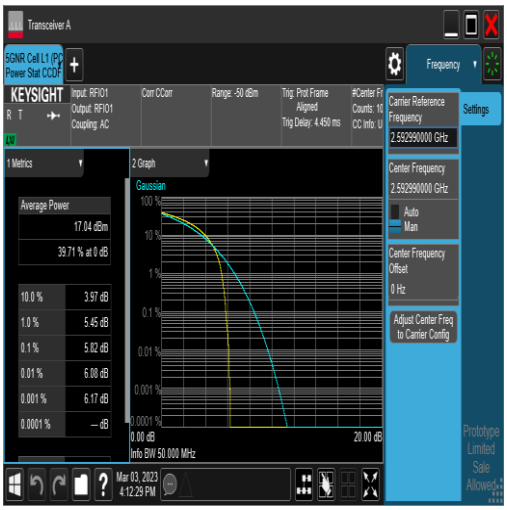

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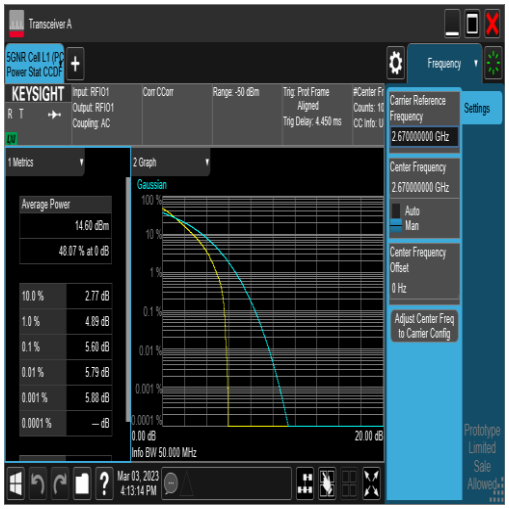	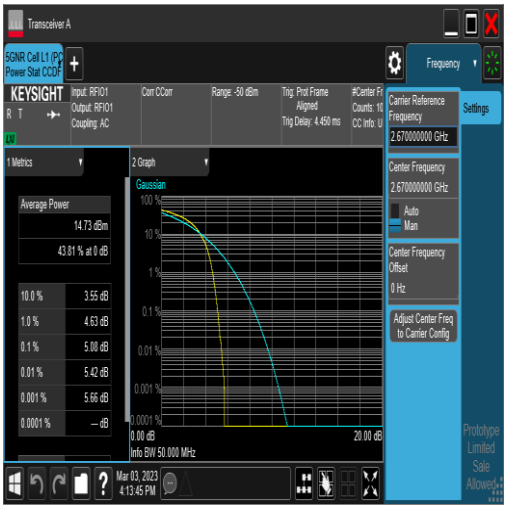
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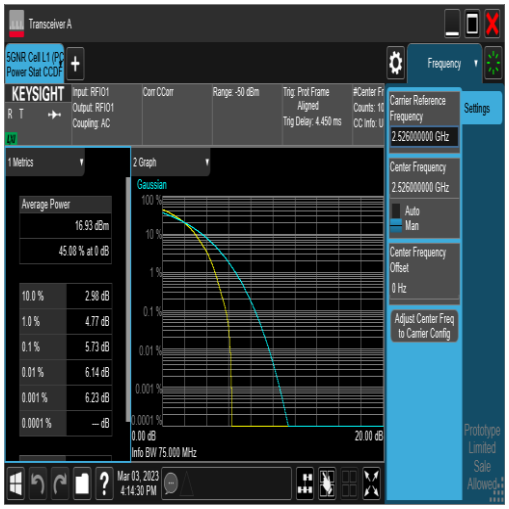	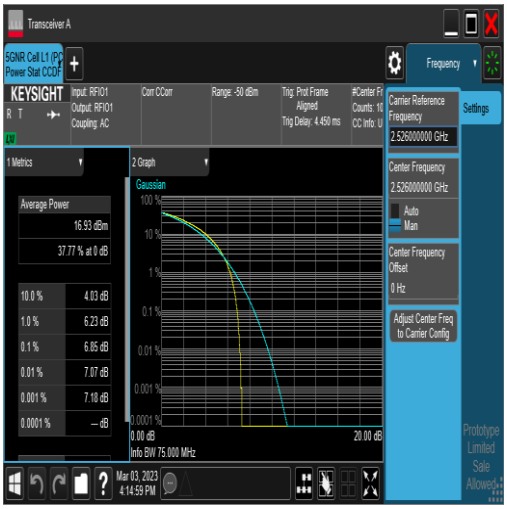
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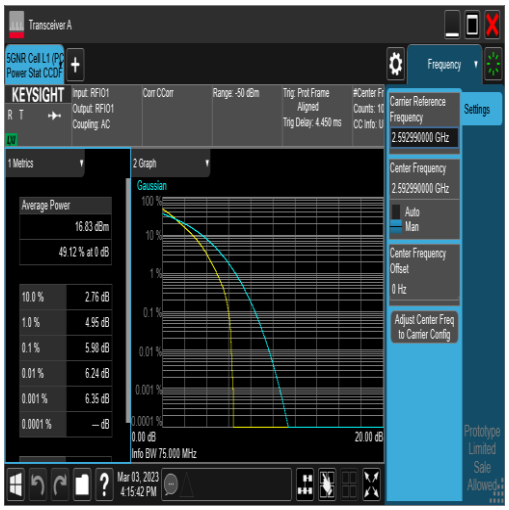

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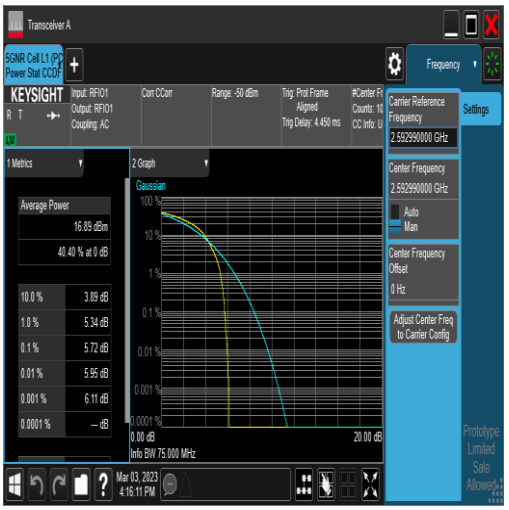

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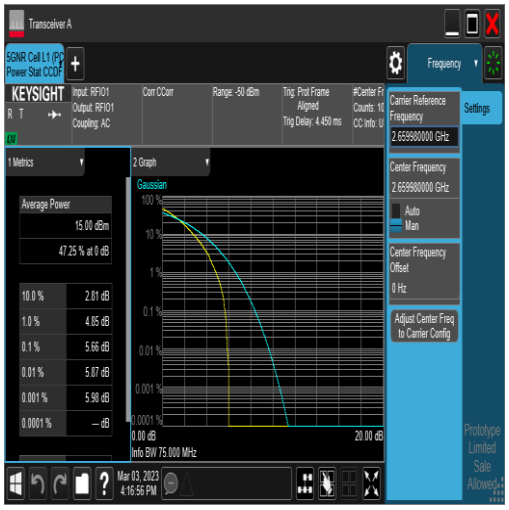

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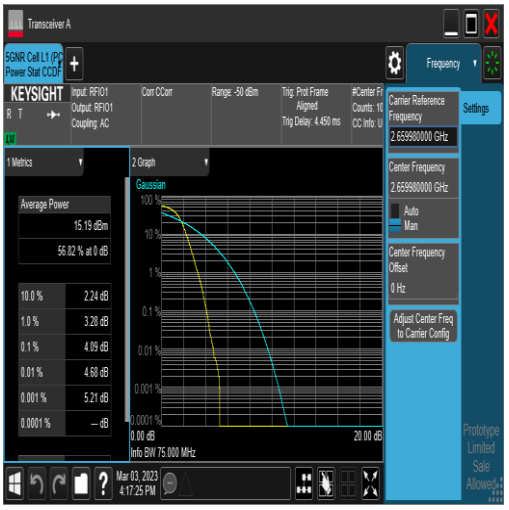

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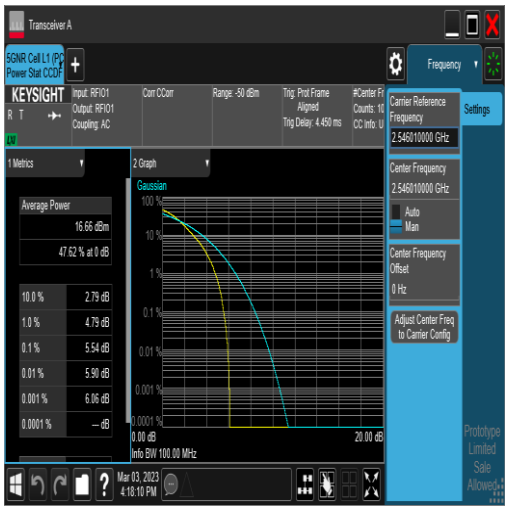

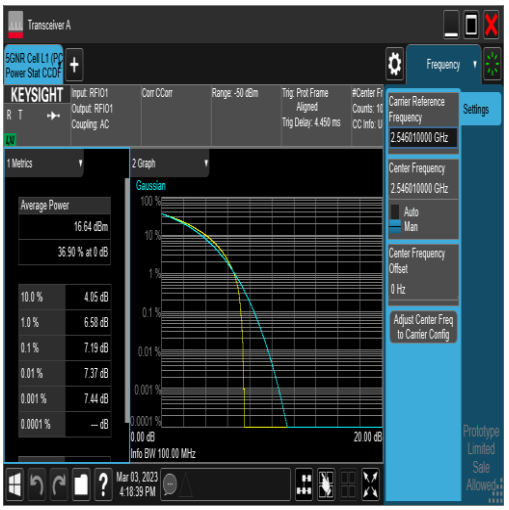

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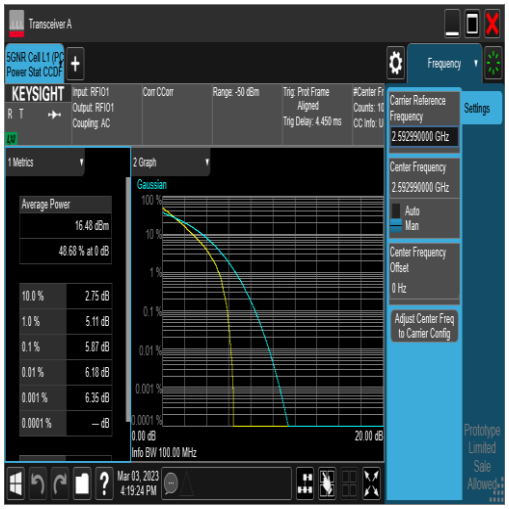	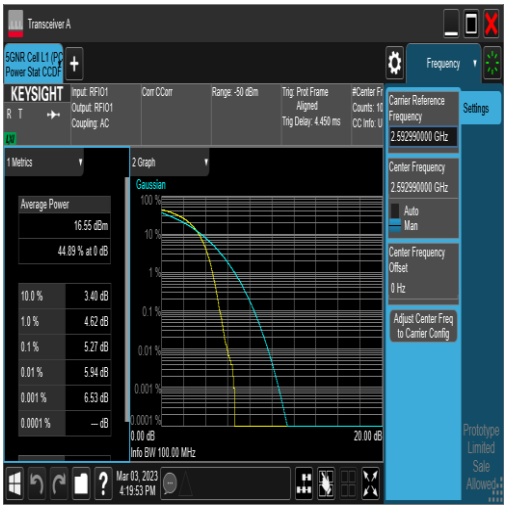
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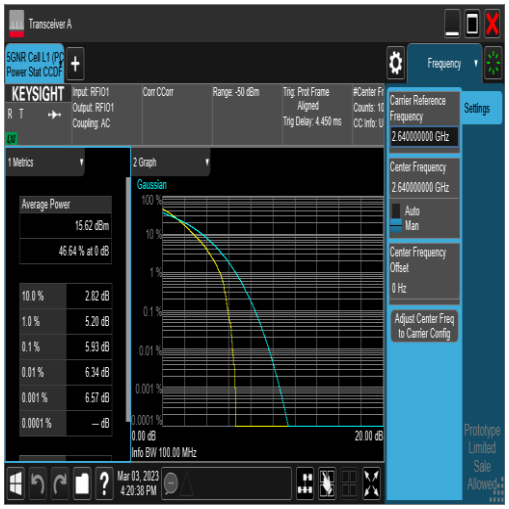	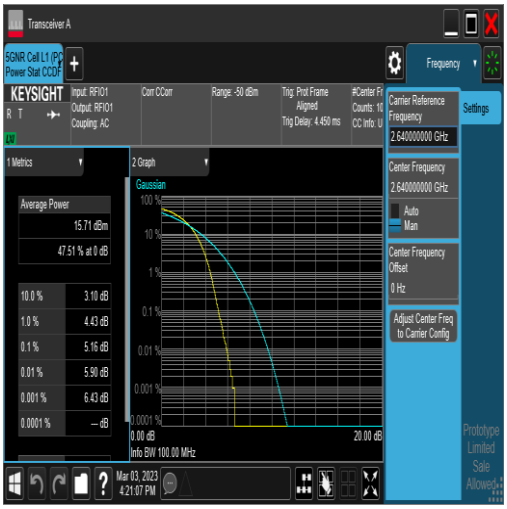
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Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	17.816	19.02	PASS
DC_5A_n41A	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	17.868	19.16	PASS
DC_5A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	17.869	18.96	PASS
DC_5A_n41A	30	5+20	DFT-64QAM	M+L	Outer_Full	17.821	18.97	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	18.236	19.87	PASS
DC_5A_n41A	30	5+20	CP-16QAM	M+L	Outer_Full	18.223	22.77	PASS
DC_5A_n41A	30	5+20	CP-64QAM	M+L	Outer_Full	18.258	20.24	PASS
DC_5A_n41A	30	5+20	CP-256QAM	M+L	Outer_Full	39.585	40.00	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	17.813	19.12	PASS
DC_5A_n41A	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	17.847	19.09	PASS
DC_5A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	17.850	18.89	PASS
DC_5A_n41A	30	5+20	DFT-64QAM	M+M	Outer_Full	17.853	18.92	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	18.236	19.48	PASS
DC_5A_n41A	30	5+20	CP-16QAM	M+M	Outer_Full	18.206	20.51	PASS
DC_5A_n41A	30	5+20	CP-64QAM	M+M	Outer_Full	18.223	19.15	PASS
DC_5A_n41A	30	5+20	CP-256QAM	M+M	Outer_Full	18.173	19.28	PASS
DC_5A_n41	30	5+20	DFT-QPSK	M+H	Outer_Ful	17.81	19.12	PASS

A					l	5		
DC_5A_n41A	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	17.831	18.88	PASS
DC_5A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	17.800	19.00	PASS
DC_5A_n41A	30	5+20	DFT-64QAM	M+H	Outer_Full	17.824	19.01	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	18.253	22.74	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	35.715	37.47	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	37.855	39.50	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	35.749	37.24	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+M	Outer_Full	37.873	39.41	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	35.685	37.35	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	37.767	39.63	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	57.730	60.14	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	57.800	59.99	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	57.780	59.99	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+M	Outer_Full	57.805	60.18	PASS
DC_5A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	57.773	60.14	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	57.791	59.95	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	95.976	99.50	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	97.199	100.7	PASS
DC_5A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	96.167	99.44	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+M	Outer_Full	97.474	100.9	PASS
DC_5A_n41	30	5+100	DFT-QPSK	M+H	Outer_Ful	96.04	99.46	PASS

A					I	6		
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	97.228	100.8	PASS

Test Graphs

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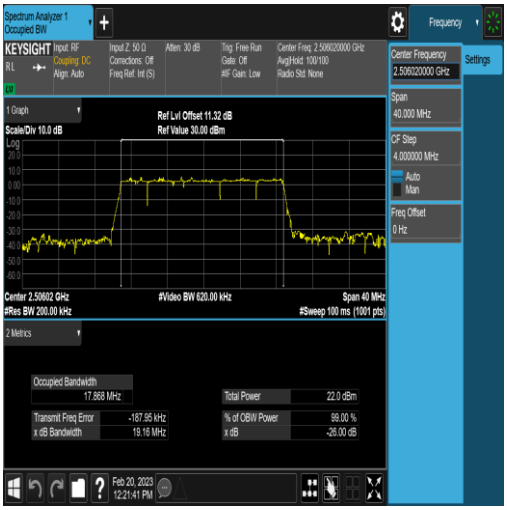
<tr><td>  </td><td>  </td></tr>
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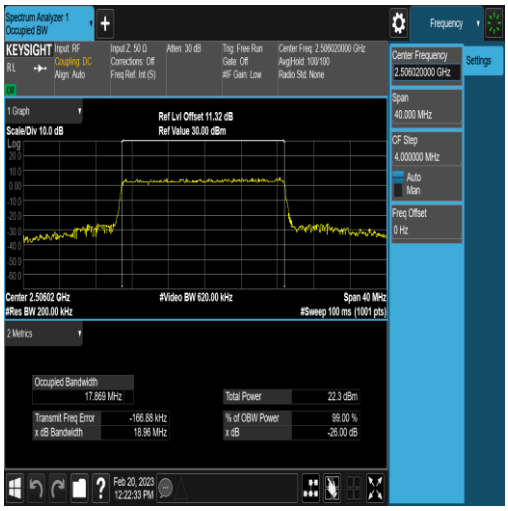
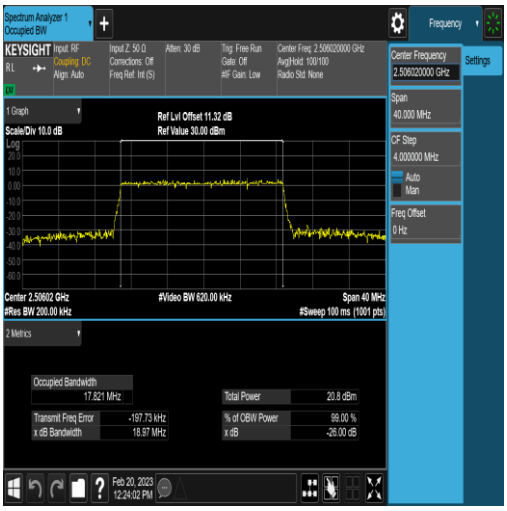
<tr><td>  </td><td>  </td></tr>
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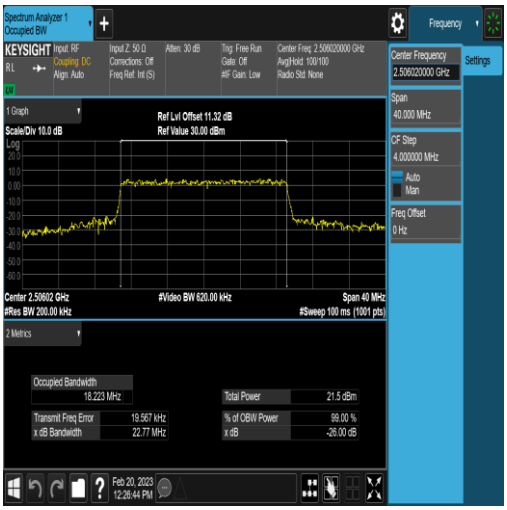

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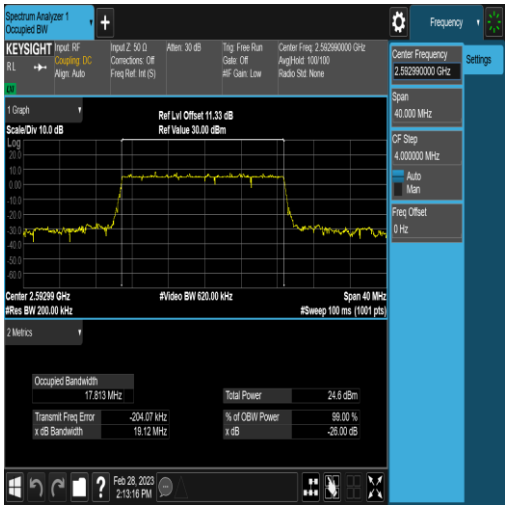

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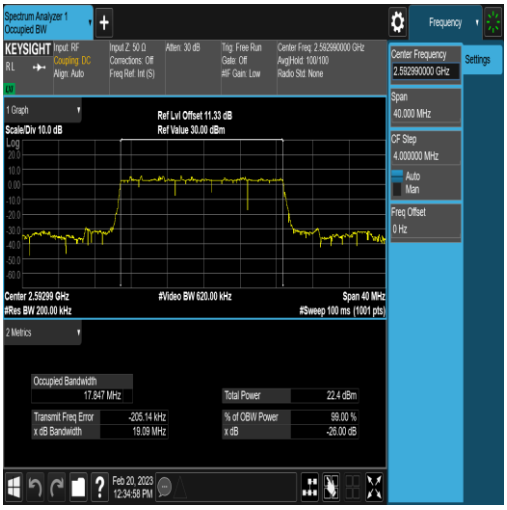

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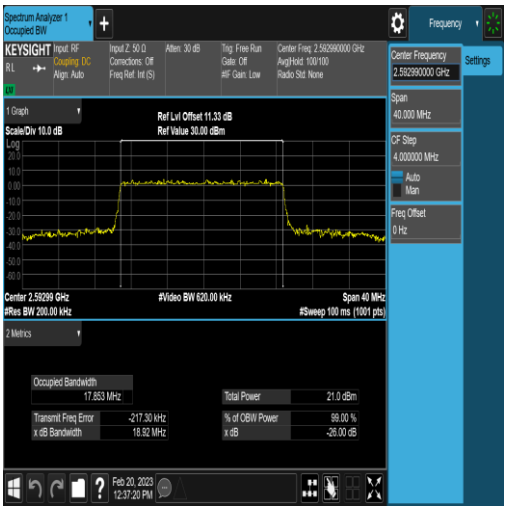

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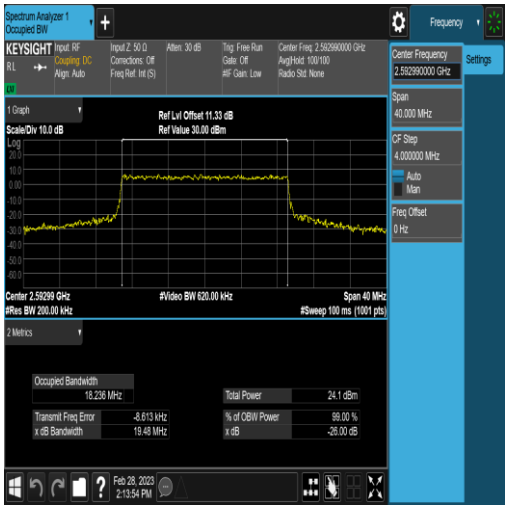	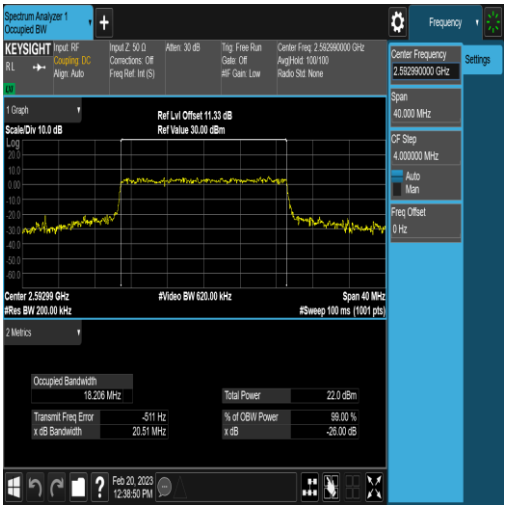
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	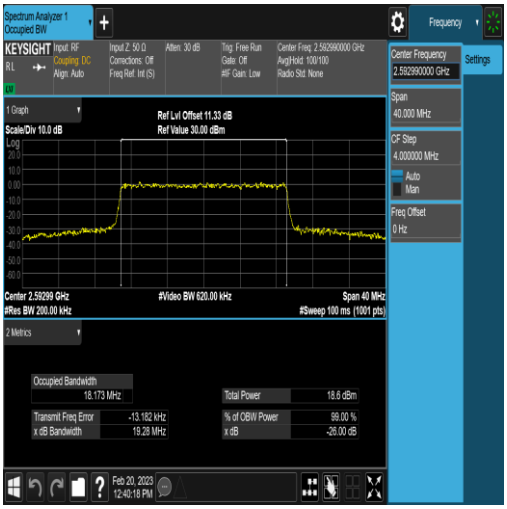
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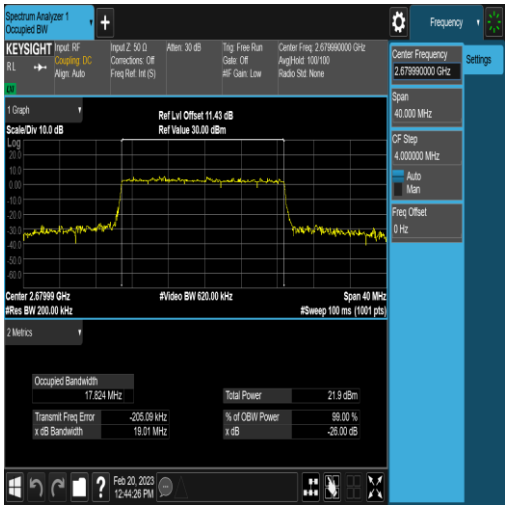

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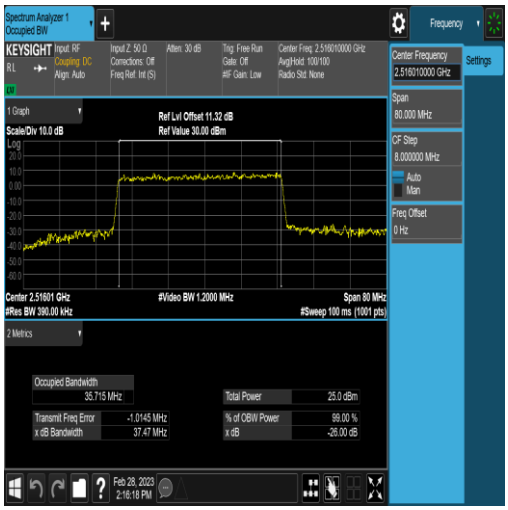

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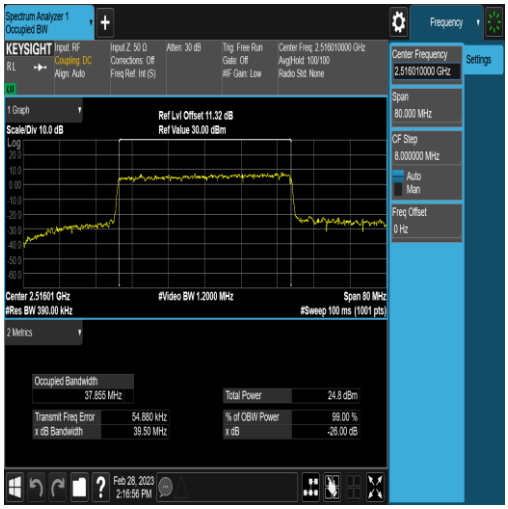	
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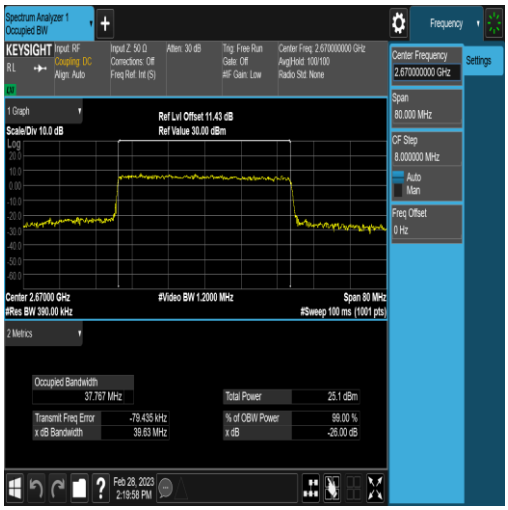

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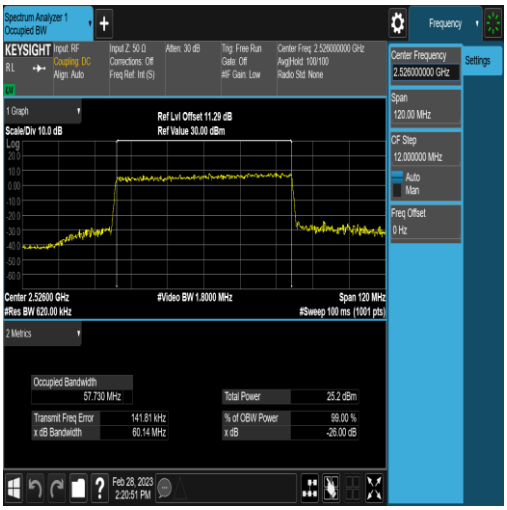

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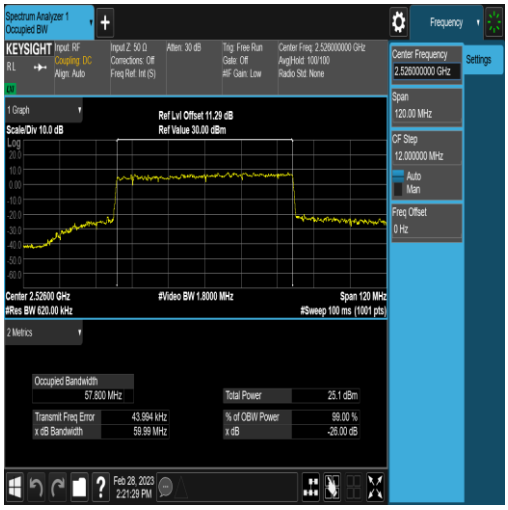

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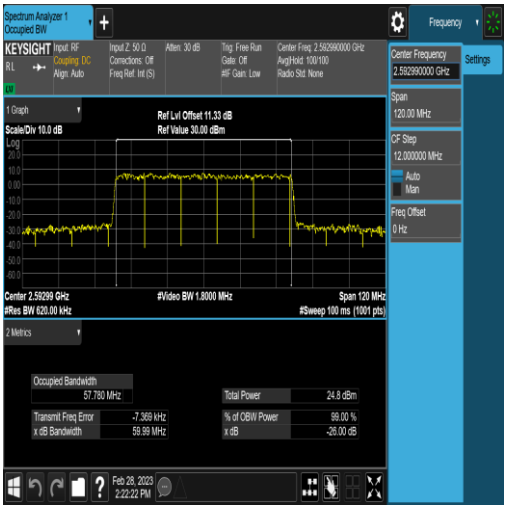

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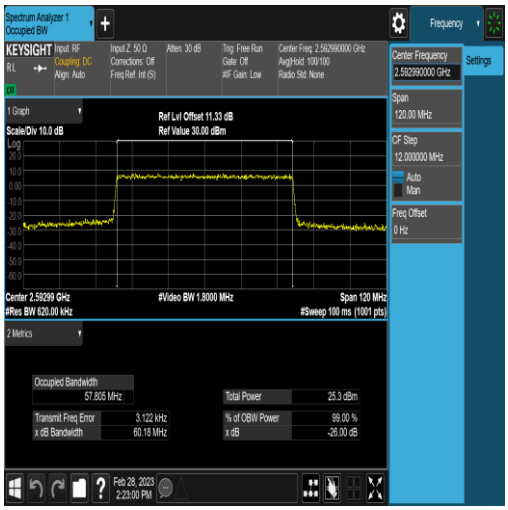

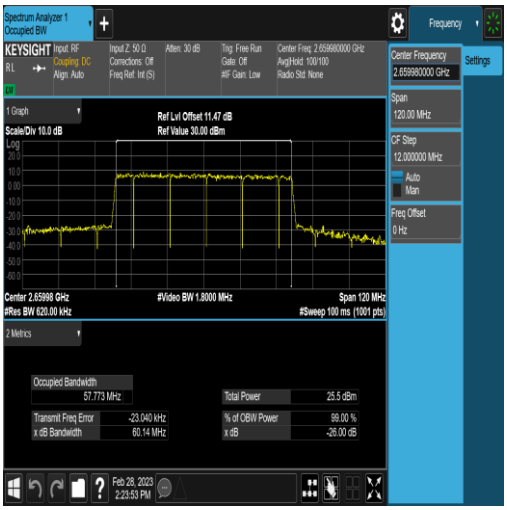

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	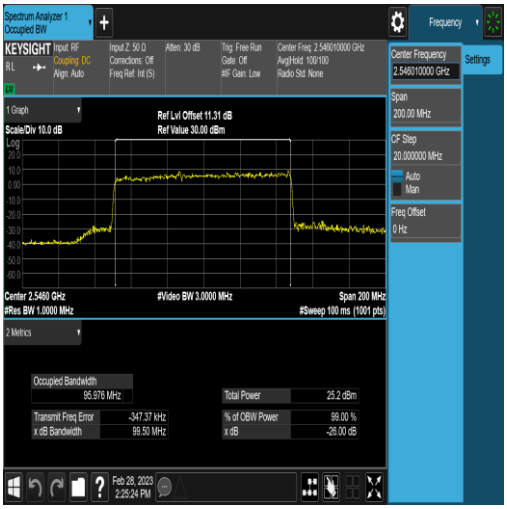
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	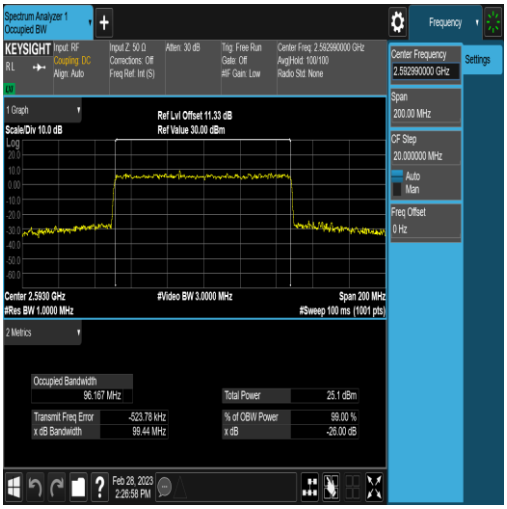
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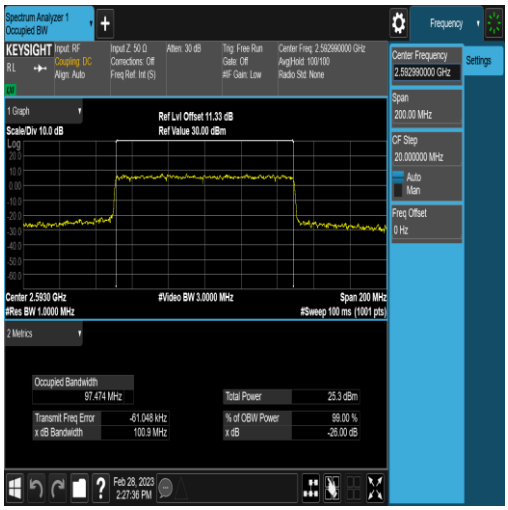	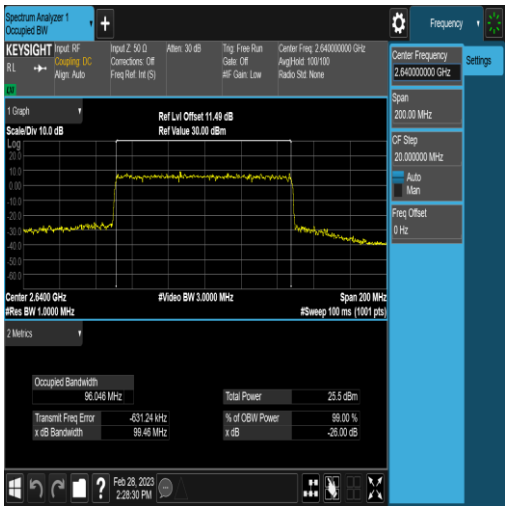
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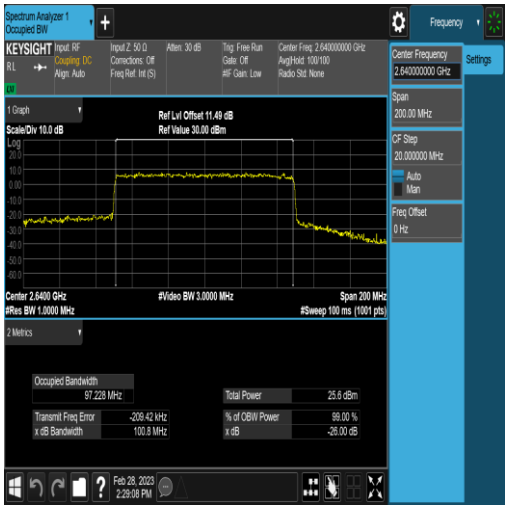	
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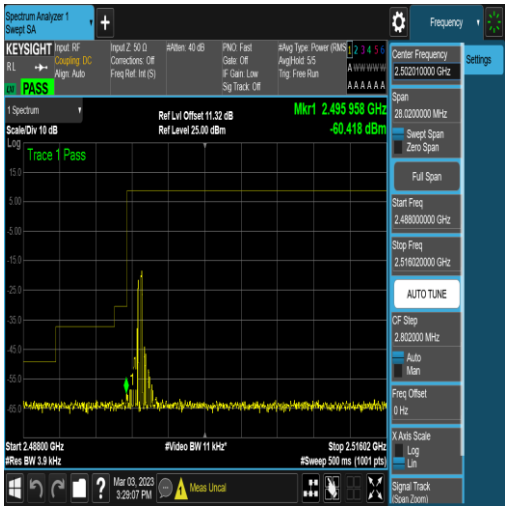
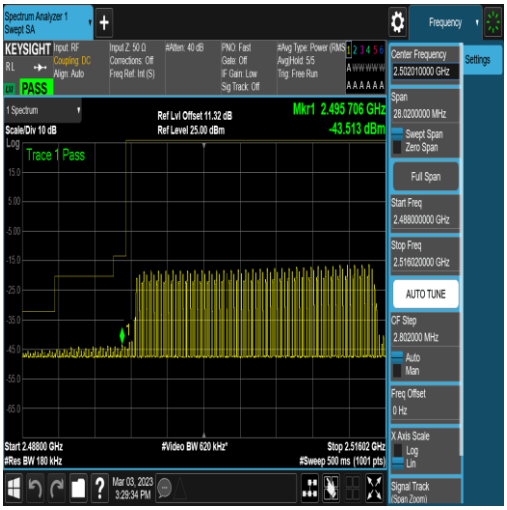
Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_5A_n41A	30	5+40	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_5A_n41A	30	5+60	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_5A_n41A	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS

Test Graphs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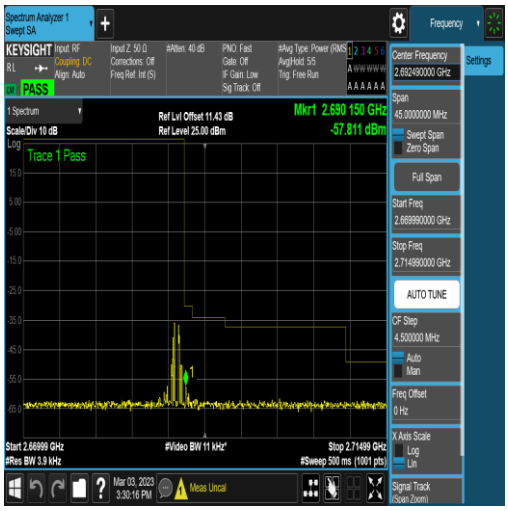	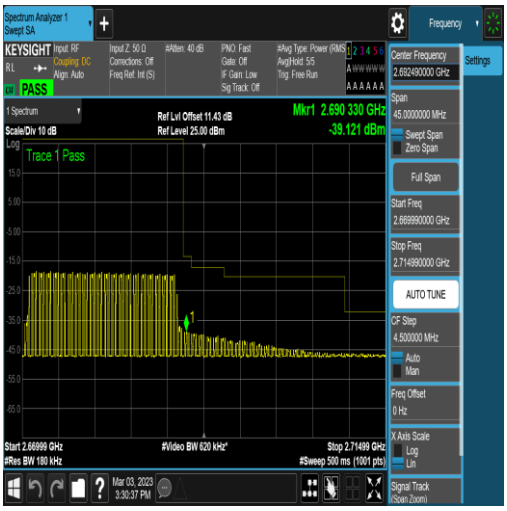
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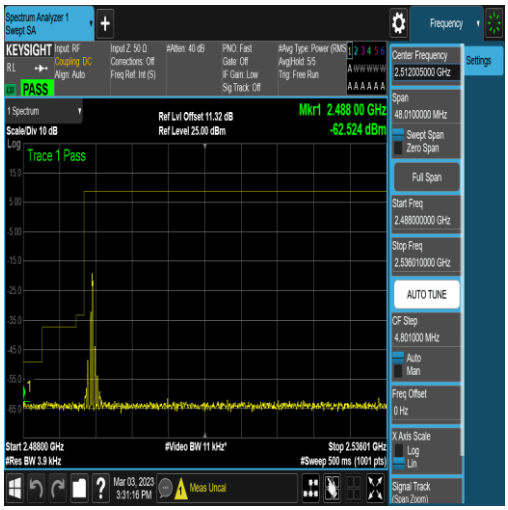

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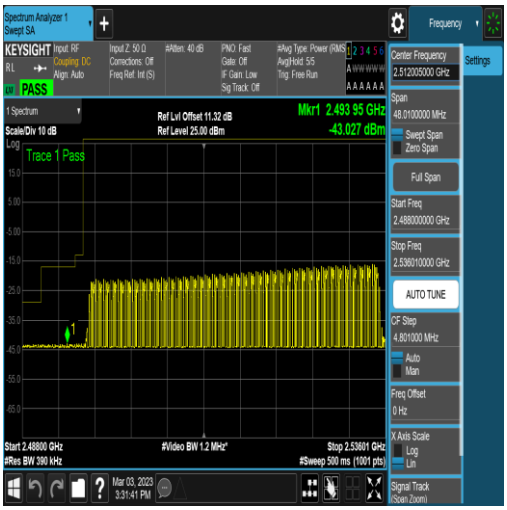

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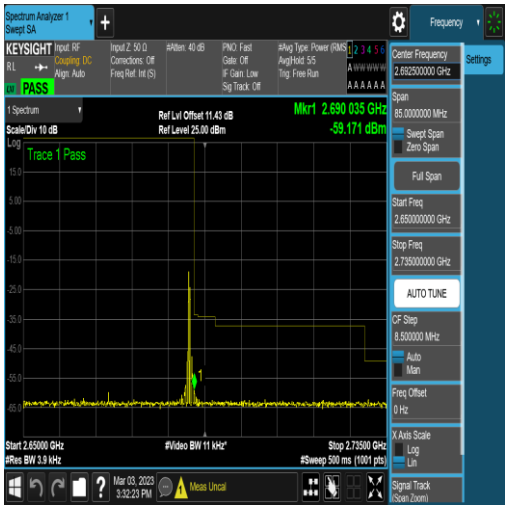

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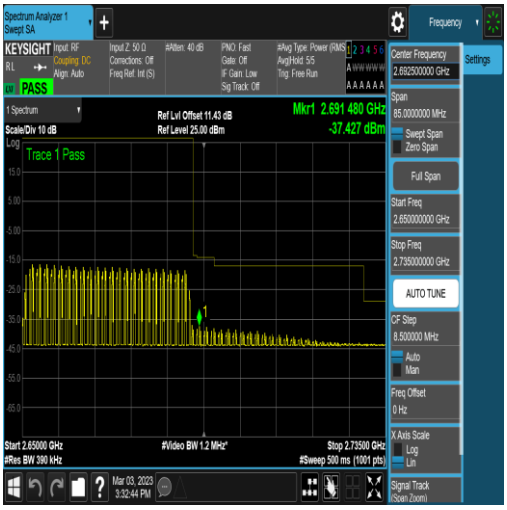

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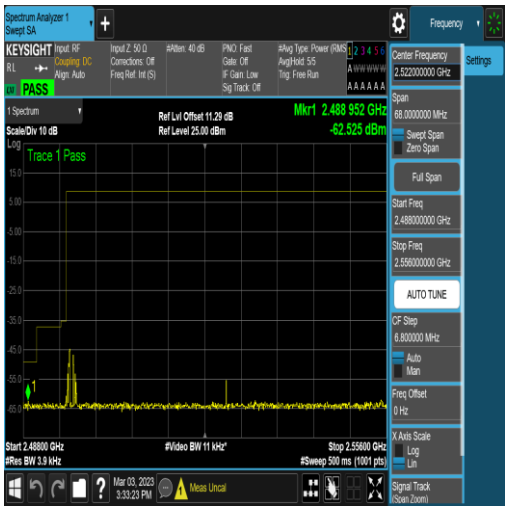

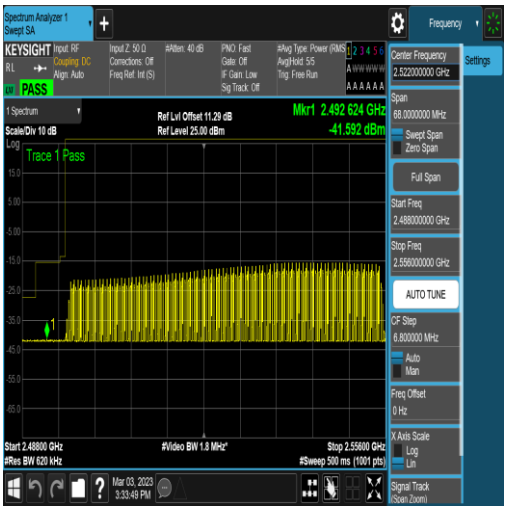

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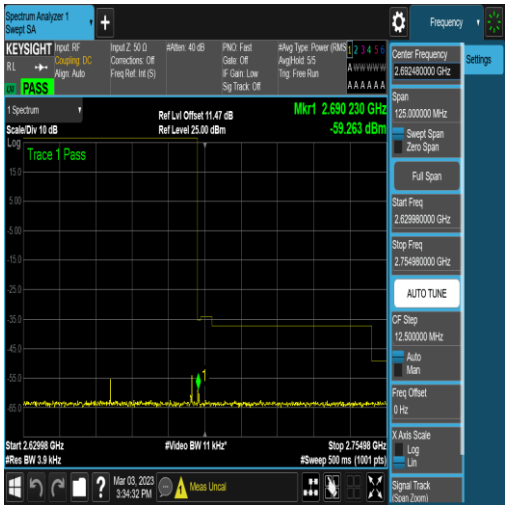	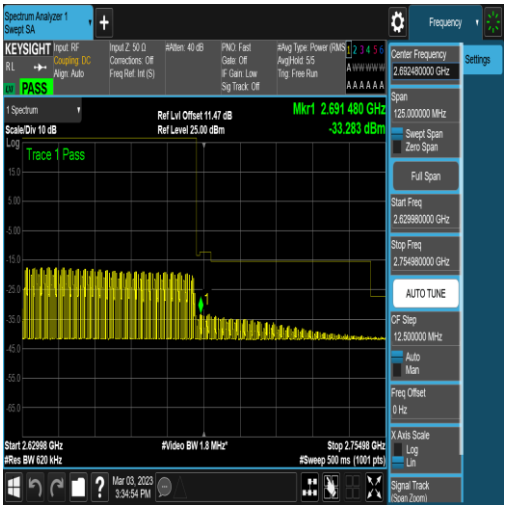
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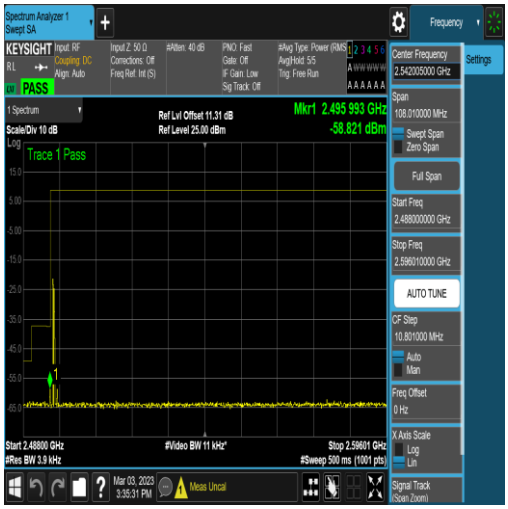	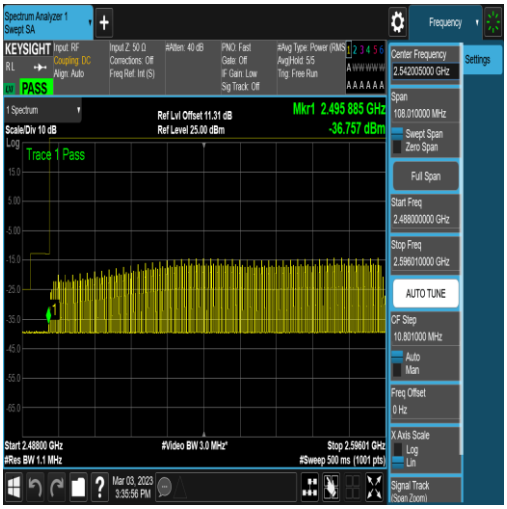
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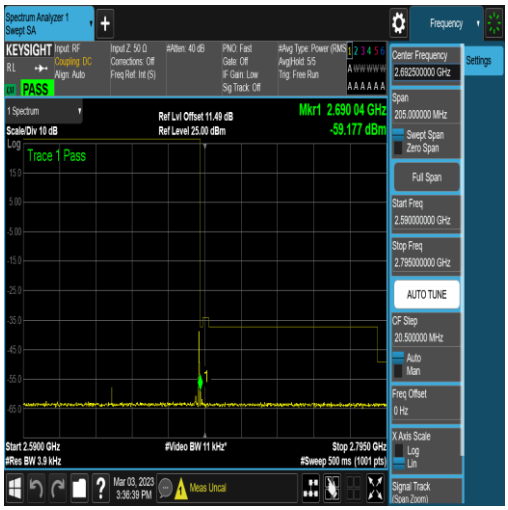	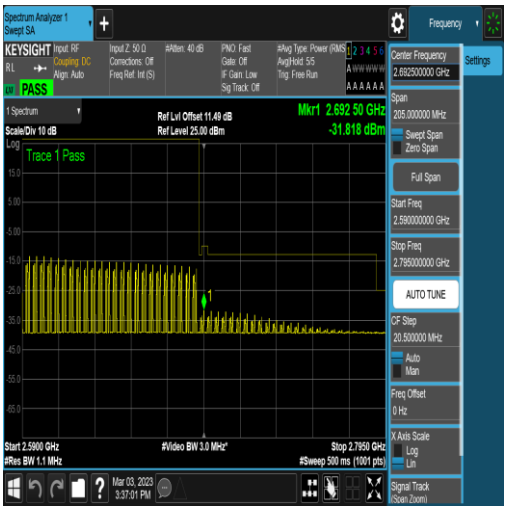
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Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-59.61	-55	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.74	-45	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.34	-35	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.95	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-42.90	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-44.42	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-60.45	-55	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.83	-45	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.38	-35	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-47.03	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-42.65	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-44.42	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.46	-55	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.88	-45	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-50.90	-35	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.14	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	12000	-42.61	-25	PASS
DC_5A_n	30	5+20	DFT-QP	M+L	Outer_Full	12000	20000	-44.	-25	PAS

41A			SK					44		S
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-60.19	-55	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.68	-45	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-55.44	-35	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-47.02	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-42.49	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-44.05	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-58.23	-55	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.63	-45	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-55.83	-35	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.96	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-42.80	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-44.47	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-58.31	-55	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-75.56	-45	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-51.60	-35	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-40.49	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-42.66	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-44.23	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-59.78	-55	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-63.90	-45	PASS
DC_5A_n	30	5+20	DFT-QP	M+M	Edge_1RB_	30	1000	-51.	-35	PAS

41A			SK		Left			53		S
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-46.96	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-42.63	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-44.17	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-59.87	-55	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-69.01	-45	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.06	-35	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-47.08	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-42.66	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-44.41	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-59.50	-55	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.77	-45	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-52.00	-35	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-44.60	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	12000	-42.93	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	12000	20000	-44.40	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.84	-55	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-60.52	-45	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-53.26	-35	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-46.94	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-42.39	-25	PASS
DC_5A_n	30	5+20	CP-QP	M+M	Edge_1RB_	12000	20000	-44.	-25	PAS

41A			SK		Left			36		S
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-59.20	-55	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.70	-45	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-52.01	-35	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-47.12	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-42.91	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-44.13	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-59.42	-55	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-73.26	-45	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-45.58	-35	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-40.63	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-42.65	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-44.14	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-59.04	-55	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.41	-45	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-52.92	-35	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.97	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-42.72	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-44.20	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-59.08	-55	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.10	-45	PASS
DC_5A_n	30	5+20	DFT-QP	M+H	Edge_1RB_	30	1000	-51.	-35	PAS

41A			SK		Right			78		S
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-47.01	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-42.62	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-43.74	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-59.30	-55	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.24	-45	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-52.52	-35	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.47	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	12000	-42.51	-25	PASS
DC_5A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	12000	20000	-44.43	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.70	-55	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.83	-45	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-55.48	-35	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.76	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-42.37	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-44.39	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-60.27	-55	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-70.16	-45	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-54.83	-35	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-46.91	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-42.55	-25	PASS
DC_5A_n	30	5+20	CP-QP	M+H	Edge_1RB_	12000	20000	-44.	-25	PAS

41A			SK		Right			31		S
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-60.21	-55	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-76.50	-45	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-51.53	-35	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-39.04	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-42.72	-25	PASS
DC_5A_n41A	30	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-44.35	-25	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-60.51	-55	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.65	-45	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.53	-35	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.91	-25	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-42.46	-25	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-44.19	-25	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-59.26	-55	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.59	-45	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.02	-35	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.90	-25	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-42.53	-25	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-44.35	-25	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.27	-55	PASS
DC_5A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.68	-45	PASS
DC_5A_n	30	5+40	DFT-QP	M+L	Outer_Full	30	1000	-54.	-35	PAS