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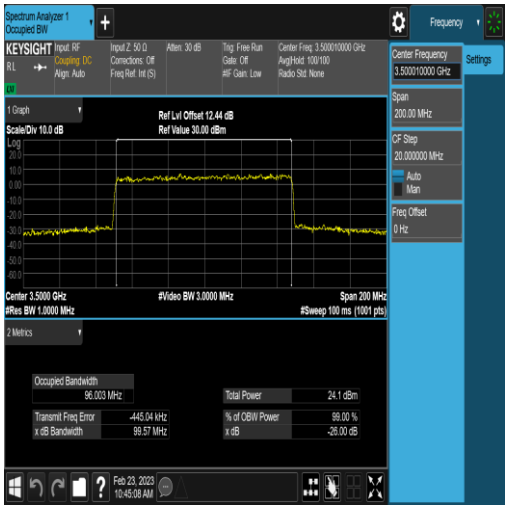	
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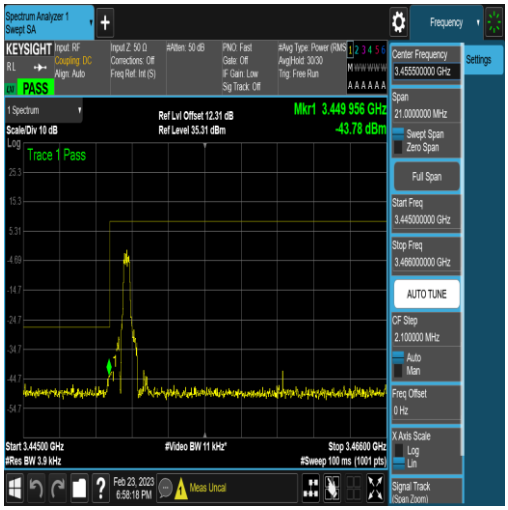
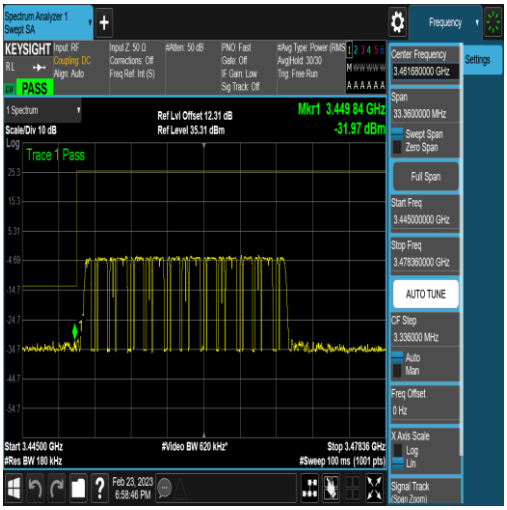
Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	see graph	PASS

DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS

Test Graphs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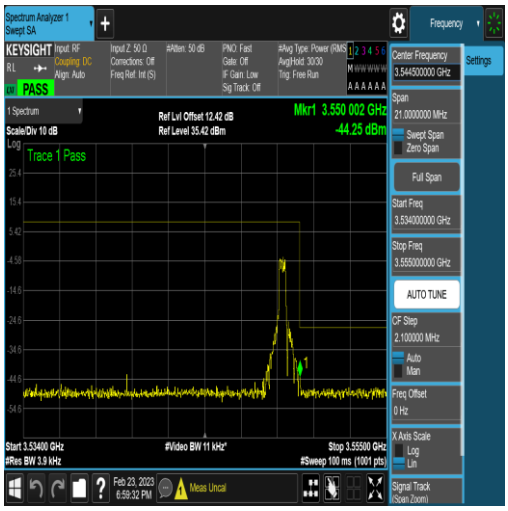	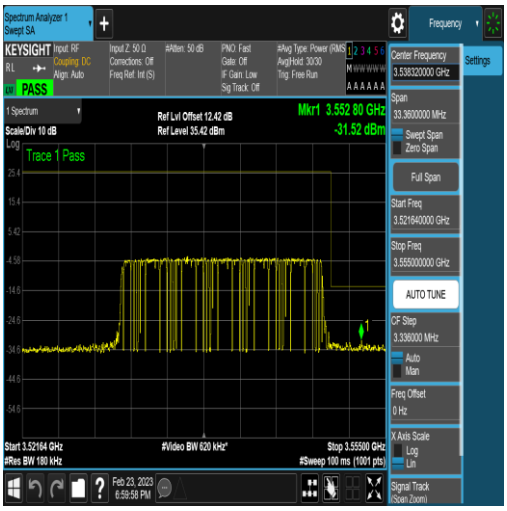
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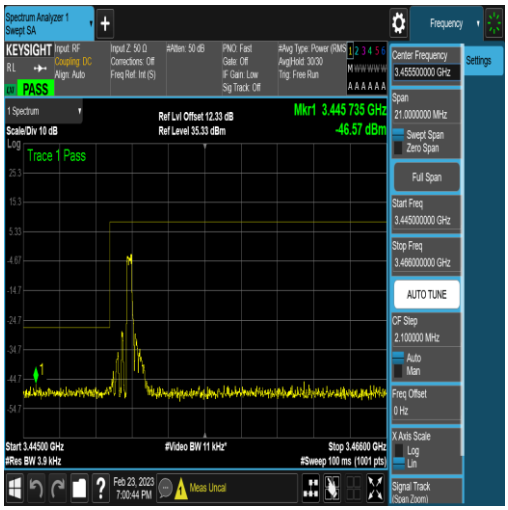	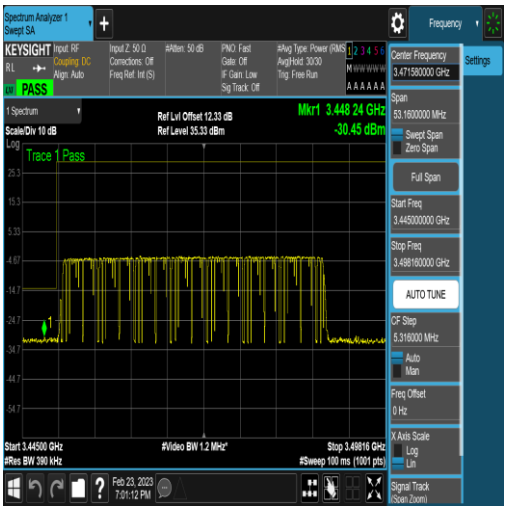
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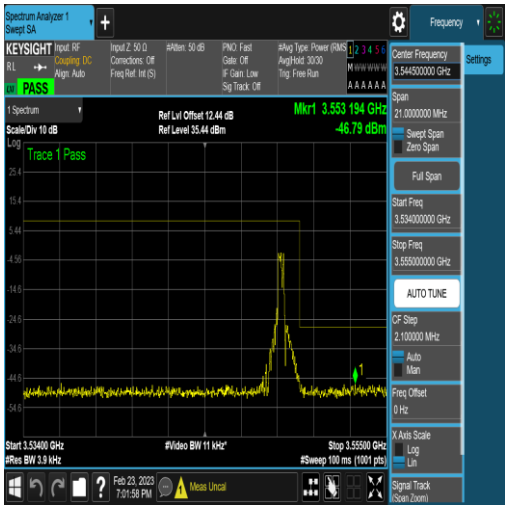	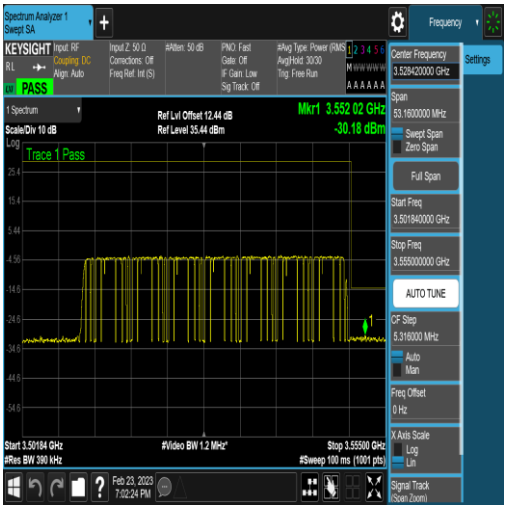
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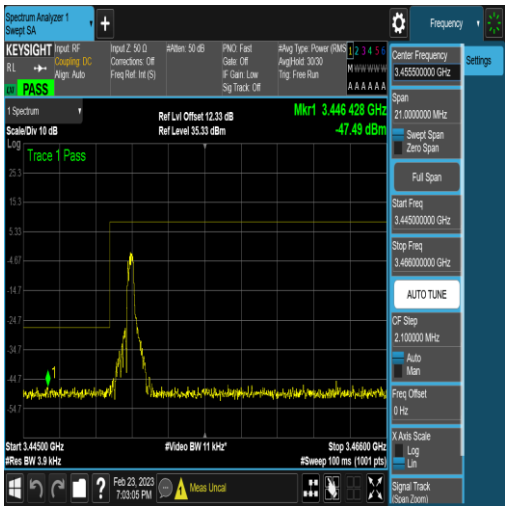	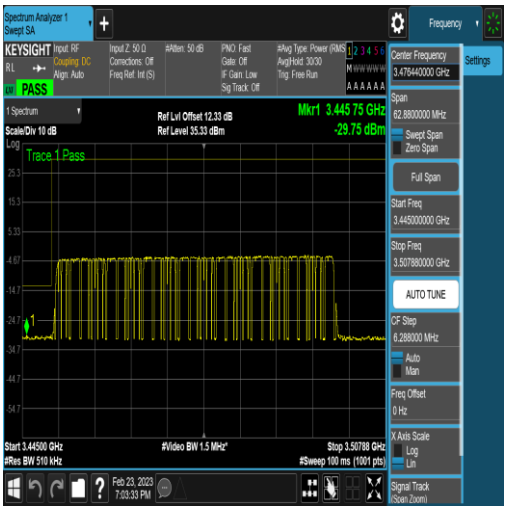

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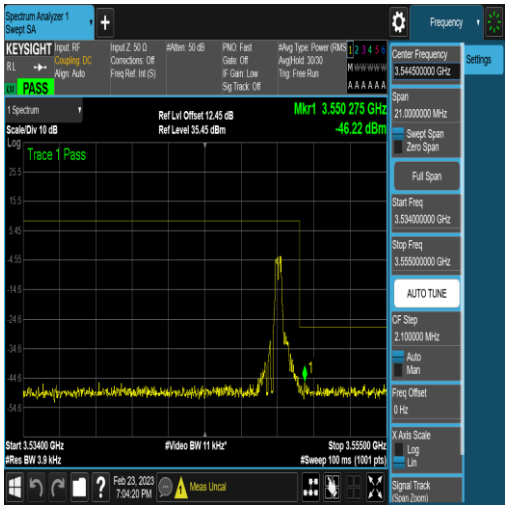	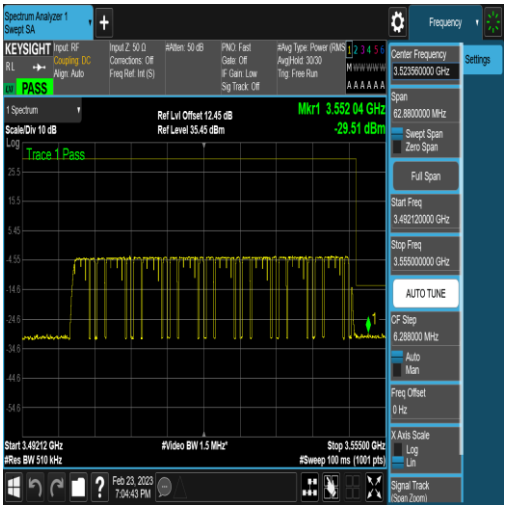
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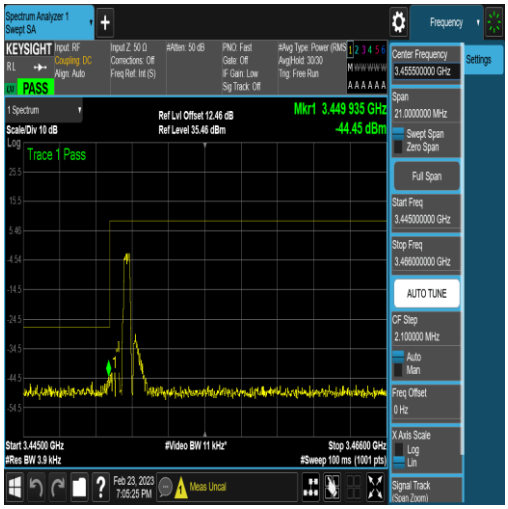	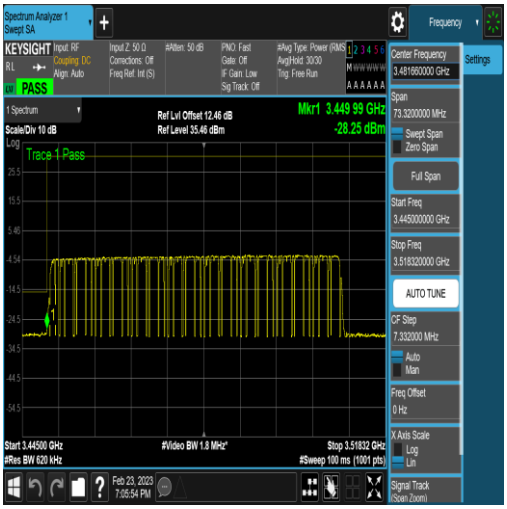
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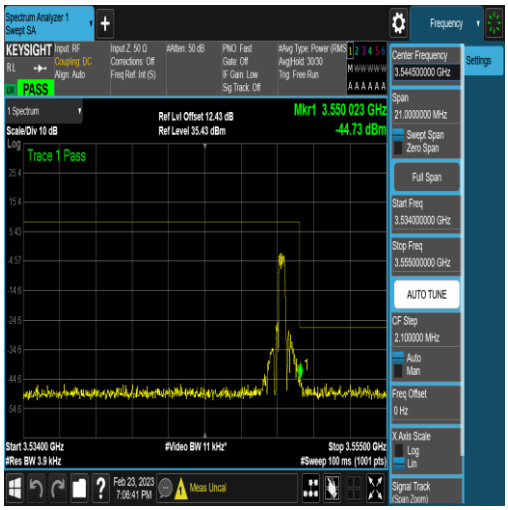

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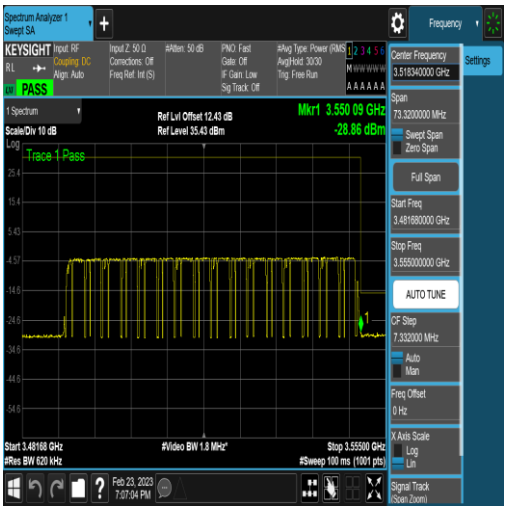

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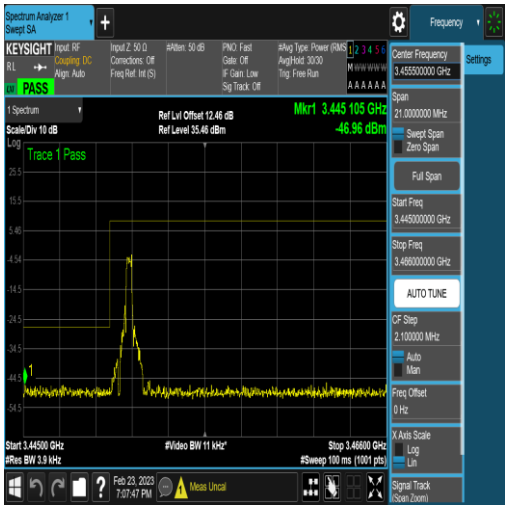

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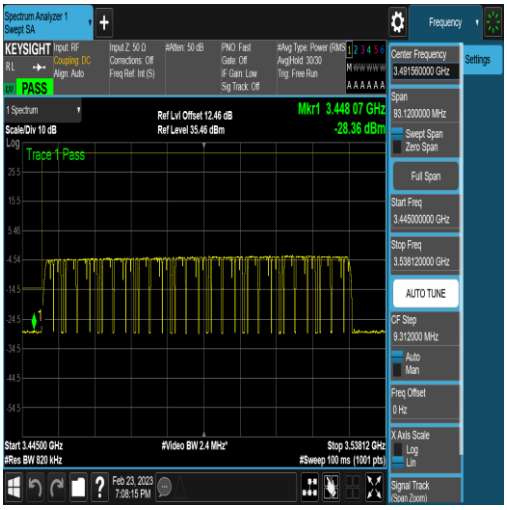

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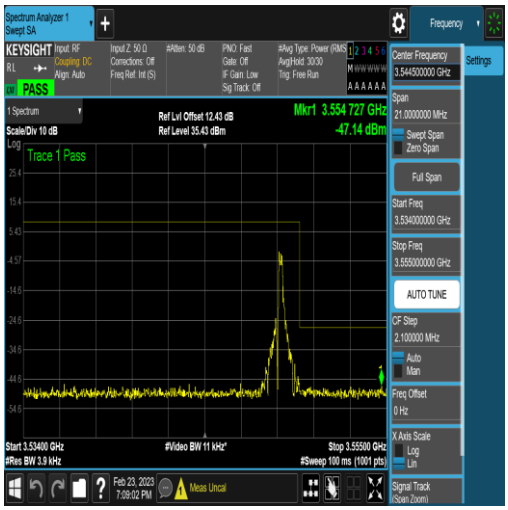

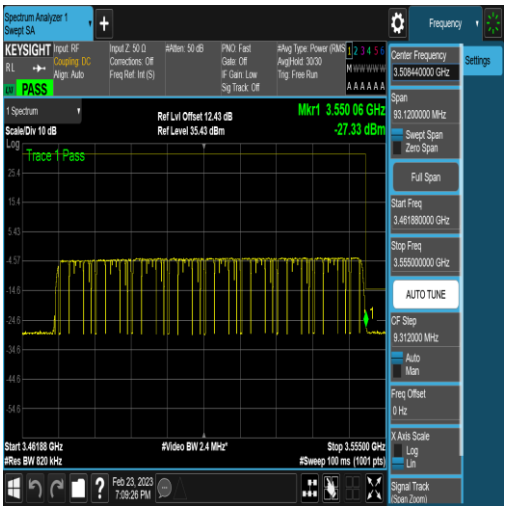

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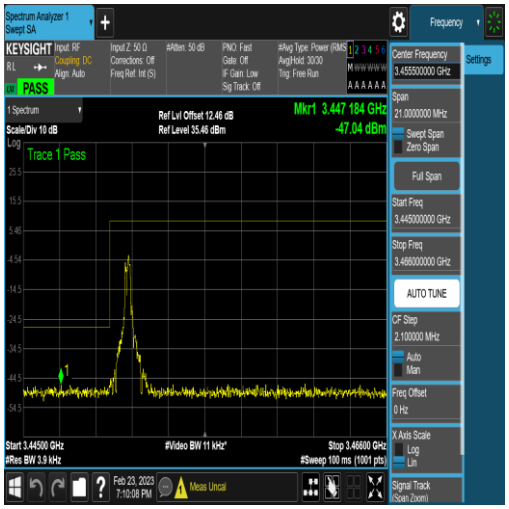	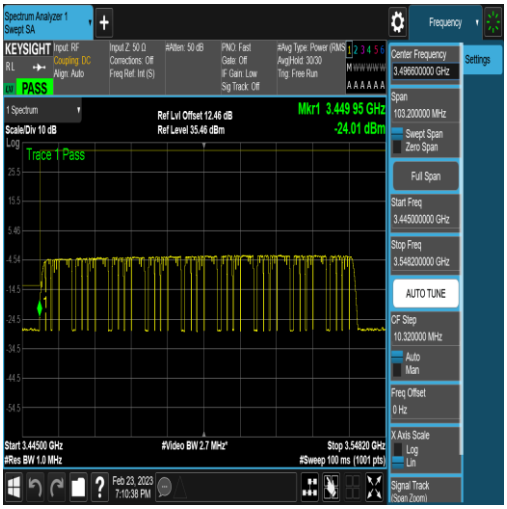
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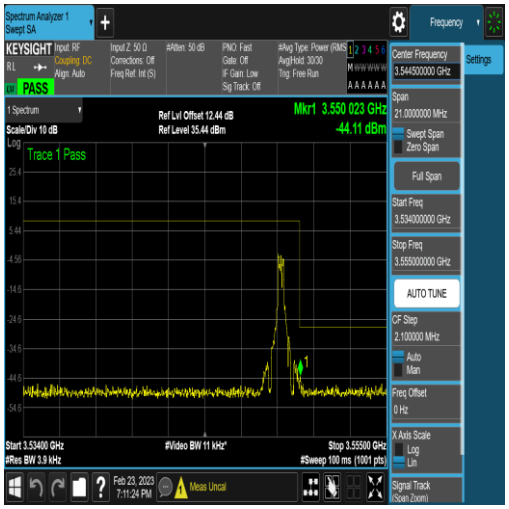	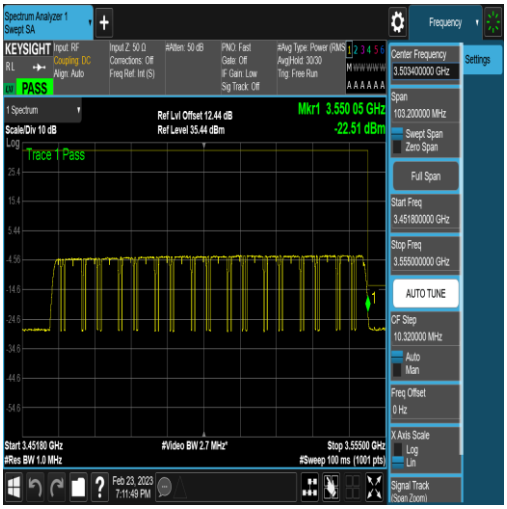
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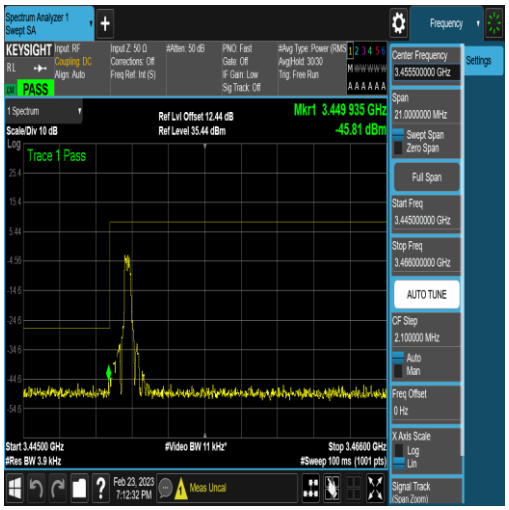	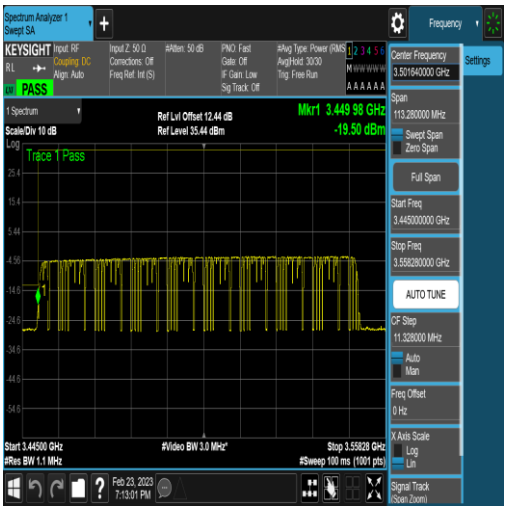
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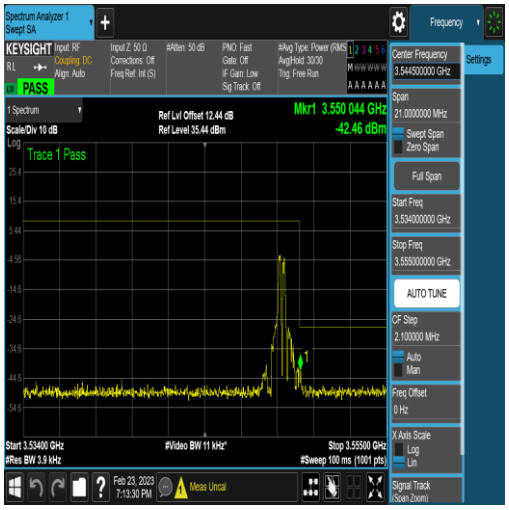	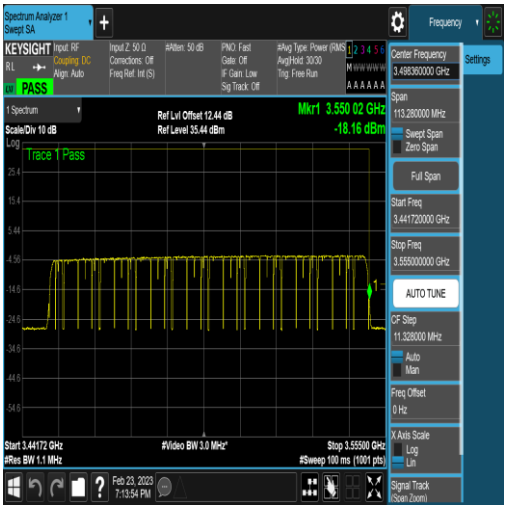
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Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-60.06	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.44	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.76	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-42.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.83	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.75	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.28	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.14	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.94	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-69.71	-23	PASS

DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-42.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-49.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.87	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.49	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-69.98	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-50.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.66	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.22	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.03	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-47.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-32.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-60.66	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-75.37	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-70.18	-23	PASS

DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-49.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	3000	6000	-46.82	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	26000	40000	-32.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.23	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.03	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.17	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-46.94	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-32.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.61	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.59	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.72	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-51.08	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.34	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-62.65	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.53	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-70.74	-23	PASS

DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-48.02	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.48	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.43	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.32	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-49.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.42	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.11	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.52	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-15.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.05	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.45	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-74.57	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-70.59	-23	PASS

DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-46.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	3000	6000	-48.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	6000	26000	-29.82	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	26000	40000	-33.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.68	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.60	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.62	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-60.72	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.19	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.47	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-50.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.22	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.76	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-70.42	-23	PASS

DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-48.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-49.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.22	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.27	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.01	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-48.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.61	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.29	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.09	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-46.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-63.64	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-76.03	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-70.28	-23	PASS

DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-48.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	3000	6000	-49.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	26000	40000	-33.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.81	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.18	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-69.83	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-48.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.07	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.45	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-63.00	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-47.14	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-54.92	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.12	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-69.60	-23	PASS

DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-43.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-53.99	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.36	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-58.20	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-42.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.27	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.54	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-59.30	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-42.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.02	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-32.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	0.009	0.15	-57.18	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	0.15	30	-75.27	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	30	1000	-70.72	-23	PASS

DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	1000	3000	-42.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	3000	6000	-47.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.38	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.19	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.43	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.19	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.37	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.32	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.58	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-54.36	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.63	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-70.69	-23	PASS

DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-41.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-49.20	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-54.81	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.63	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.75	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.01	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.41	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.09	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	0.009	0.15	-54.13	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	0.15	30	-72.93	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	30	1000	-70.34	-23	PASS

DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	1000	3000	-42.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	3000	6000	-45.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	26000	40000	-32.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-56.03	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.37	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.67	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-42.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-54.36	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.69	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.61	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.54	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.48	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-70.64	-23	PASS

DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-41.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-49.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.69	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.97	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.52	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.40	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.83	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.53	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.34	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	0.009	0.15	-53.99	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	0.15	30	-75.93	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	30	1000	-70.65	-23	PASS

DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	1000	3000	-39.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	3000	6000	-46.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-54.18	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.46	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.65	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.48	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.79	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.33	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-53.91	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.80	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-70.59	-23	PASS

DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-49.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-32.99	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-52.72	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.30	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.86	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.14	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.50	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.53	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-30.20	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	0.009	0.15	-54.12	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	0.15	30	-75.29	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	30	1000	-70.88	-23	PASS

DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	1000	3000	-38.23	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	3000	6000	-46.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.27	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.77	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.50	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-32.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-54.24	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.98	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.45	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.00	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-55.47	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.29	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-70.69	-23	PASS

DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.14	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-49.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-30.02	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.19	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.15	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.83	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.82	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.13	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-52.06	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.98	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.60	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.97	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	0.009	0.15	-53.64	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	0.15	30	-74.66	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	30	1000	-70.69	-23	PASS

DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	1000	3000	-40.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	3000	6000	-47.04	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.11	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.44	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.58	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-54.03	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.46	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.83	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-32.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-54.24	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.53	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-70.24	-23	PASS

DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-48.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.61	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.75	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-69.96	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-54.68	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.71	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.42	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	0.009	0.15	-54.97	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	0.15	30	-74.28	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	30	1000	-70.53	-23	PASS

DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	1000	3000	-41.02	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	3000	6000	-47.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	6000	26000	-29.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.26	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.75	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.21	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.58	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.59	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.25	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.45	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.13	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-70.83	-23	PASS

DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-37.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-49.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.16	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.92	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.34	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.34	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.72	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.43	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	0.009	0.15	-54.63	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	0.15	30	-74.74	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	30	1000	-70.47	-23	PASS

DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	1000	3000	-41.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	3000	6000	-46.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+L	Outer_Full	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-54.65	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.41	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.38	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.99	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-56.07	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.17	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.79	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-54.07	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.77	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-70.44	-23	PASS

DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-41.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-49.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-53.34	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.66	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.62	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.02	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-54.84	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.18	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.71	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-32.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	0.009	0.15	-53.45	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	0.15	30	-74.33	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	30	1000	-70.99	-23	PASS

DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	1000	3000	-38.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	3000	6000	-47.02	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	26000	40000	-33.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-53.98	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.53	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.70	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-42.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-54.75	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.54	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.53	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-41.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.22	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.71	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-70.34	-23	PASS

DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-38.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-48.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.43	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.78	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.72	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-54.74	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.17	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.13	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	0.009	0.15	-55.48	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	0.15	30	-73.26	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	30	1000	-67.58	-23	PASS

DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	1000	3000	-38.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	3000	6000	-48.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	6000	26000	-29.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	CP-QPSK	M+H	Outer_Full	26000	40000	-33.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-54.63	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.19	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.34	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.20	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-71.00	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-50.07	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.52	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-67.73	-23	PASS

DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-41.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.19	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-54.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.26	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.91	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.94	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.13	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.39	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	0.009	0.15	-52.51	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	0.15	30	-73.58	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	30	1000	-70.57	-23	PASS

DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	1000	3000	-39.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	3000	6000	-47.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	6000	26000	-29.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+L	Outer_Full	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.43	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.09	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.37	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-36.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-54.55	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.79	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.73	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.20	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-32.97	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-55.13	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.50	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-70.67	-23	PASS

DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-41.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-53.84	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.99	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.54	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-41.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-54.19	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.58	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.44	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	0.009	0.15	-54.59	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	0.15	30	-76.46	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	30	1000	-70.15	-23	PASS

DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	1000	3000	-41.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	3000	6000	-46.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	26000	40000	-32.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.18	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.62	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.73	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.16	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.93	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.54	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.08	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.06	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.20	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-70.36	-23	PASS

DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-43.01	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.49	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.80	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.20	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-53.74	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.84	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.46	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-46.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	0.009	0.15	-55.32	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	0.15	30	-74.11	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	30	1000	-70.71	-23	PASS

DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	1000	3000	-42.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	3000	6000	-45.96	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Outer_Full	26000	40000	-33.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-54.29	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.83	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.53	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.08	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.36	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.66	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-53.68	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.74	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-70.28	-23	PASS

DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-41.13	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.78	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.08	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-54.27	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.65	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.61	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.02	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.58	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.47	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.82	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	0.009	0.15	-54.31	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	0.15	30	-75.39	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	30	1000	-70.73	-23	PASS

DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	1000	3000	-38.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	3000	6000	-46.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+L	Outer_Full	26000	40000	-33.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-53.82	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.88	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.93	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-53.33	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.77	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.70	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-52.13	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.83	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-70.71	-23	PASS

DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-48.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-30.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-52.41	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.33	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.56	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.66	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.51	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.42	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.29	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	0.009	0.15	-52.72	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	0.15	30	-75.56	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	30	1000	-70.55	-23	PASS

DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	1000	3000	-41.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	3000	6000	-47.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	6000	26000	-30.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.40	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.62	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.63	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-54.42	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.63	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.48	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-54.61	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.62	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-70.86	-23	PASS

DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-42.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-48.63	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-30.00	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.55	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.17	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.21	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.06	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.78	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.59	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-41.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	0.009	0.15	-55.51	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	0.15	30	-74.55	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	30	1000	-70.09	-23	PASS

DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	1000	3000	-49.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	3000	6000	-47.00	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	6000	26000	-29.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-53.22	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.65	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.70	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-54.03	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.10	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.78	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-32.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-53.58	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.16	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	30	1000	-70.75	-23	PASS

DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	1000	3000	-42.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-53.10	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.30	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.47	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.05	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.34	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.47	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.92	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.62	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-47.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Outer_Full	0.009	0.15	-53.52	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Outer_Full	0.15	30	-74.08	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Outer_Full	30	1000	-70.28	-23	PASS

DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Outer_Full	1000	3000	-41.97	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Outer_Full	3000	6000	-46.18	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Outer_Full	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+L	Outer_Full	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-53.85	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.99	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.46	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-54.42	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.29	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.42	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-47.97	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-50.38	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.95	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	30	1000	-70.75	-23	PASS

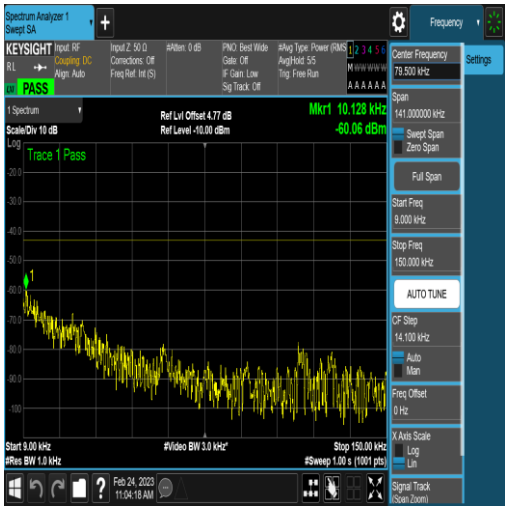
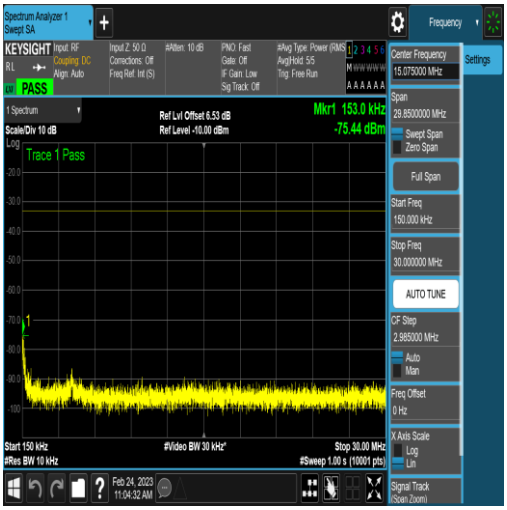
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	3000	6000	-48.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-53.78	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.39	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.49	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-50.25	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.82	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.21	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.78	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Outer_Full	0.009	0.15	-54.06	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Outer_Full	0.15	30	-76.83	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Outer_Full	30	1000	-70.82	-23	PASS

DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Outer_Full	1000	3000	-39.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Outer_Full	3000	6000	-46.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Outer_Full	6000	26000	-30.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Outer_Full	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.01	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.54	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.31	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-53.75	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.51	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.73	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.00	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-54.12	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.24	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	30	1000	-70.71	-23	PASS

DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.22	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	3000	6000	-48.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-53.05	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.25	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.83	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.24	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-52.33	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.08	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.43	-23	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.04	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Outer_Full	0.009	0.15	-54.43	-43	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Outer_Full	0.15	30	-74.65	-33	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Outer_Full	30	1000	-70.25	-23	PASS

DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Outer_Full	1000	3000	-38.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Outer_Full	3000	6000	-46.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Outer_Full	6000	26000	-29.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+100	CP-QPSK	M+H	Outer_Full	26000	40000	-33.51	-13	PASS

Test Graphs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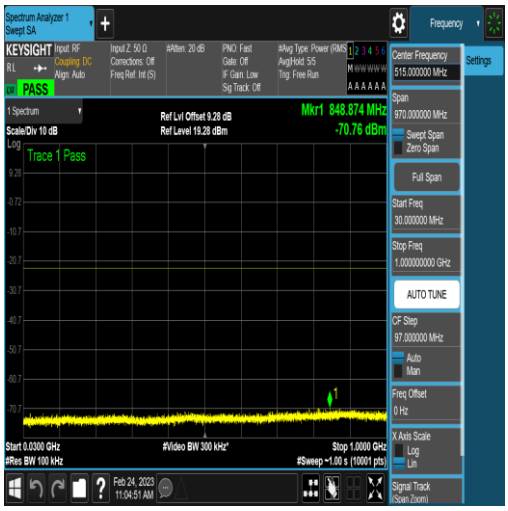	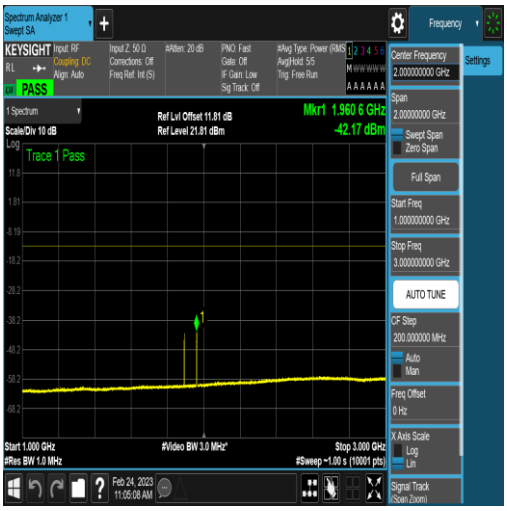
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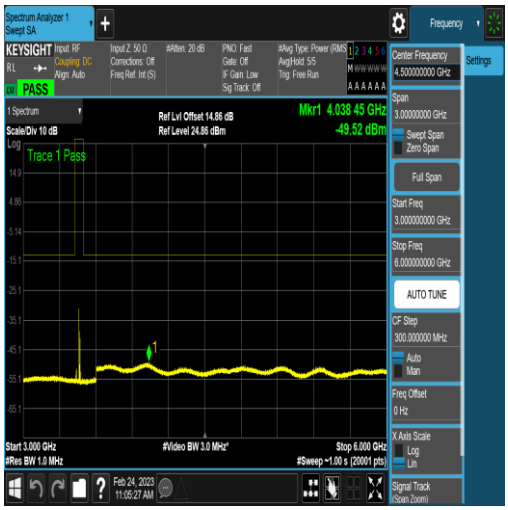

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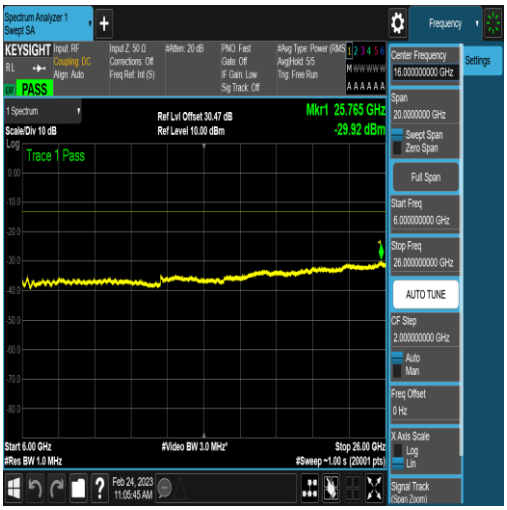

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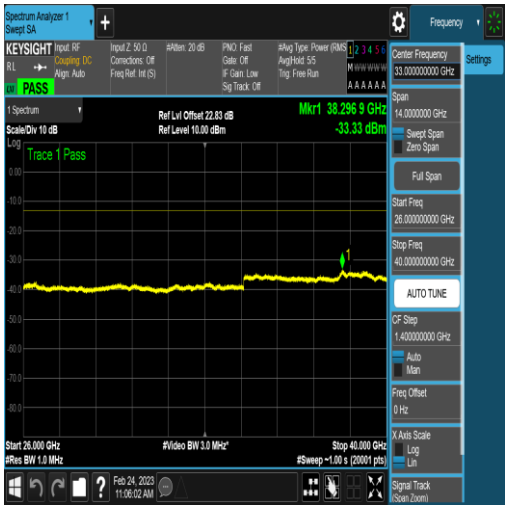

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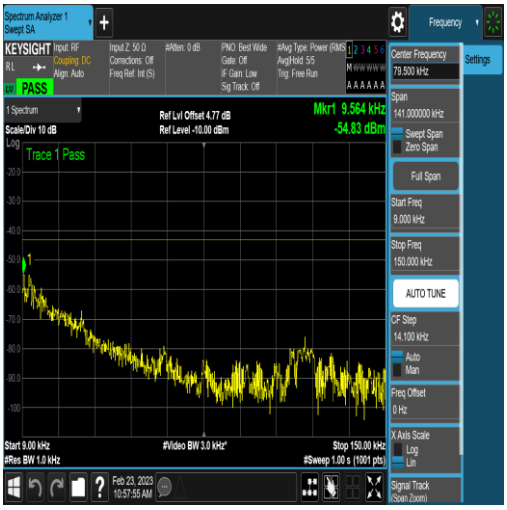

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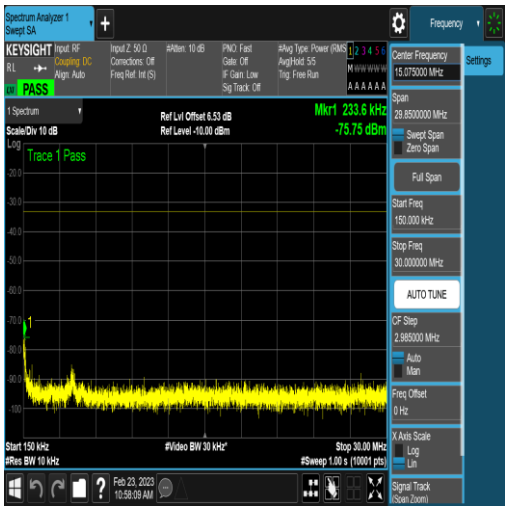

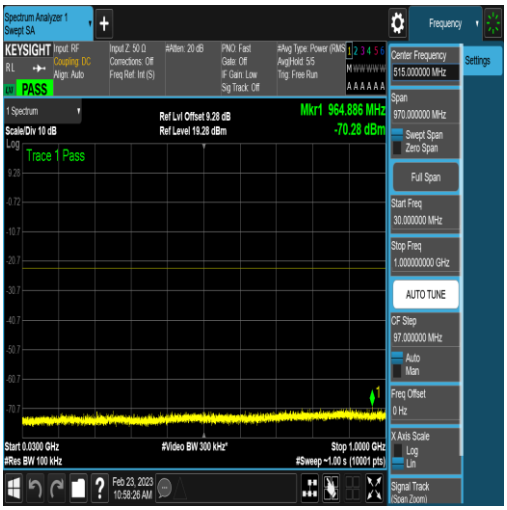

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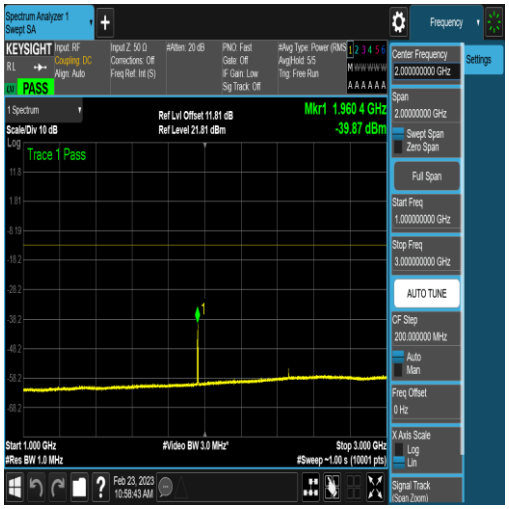	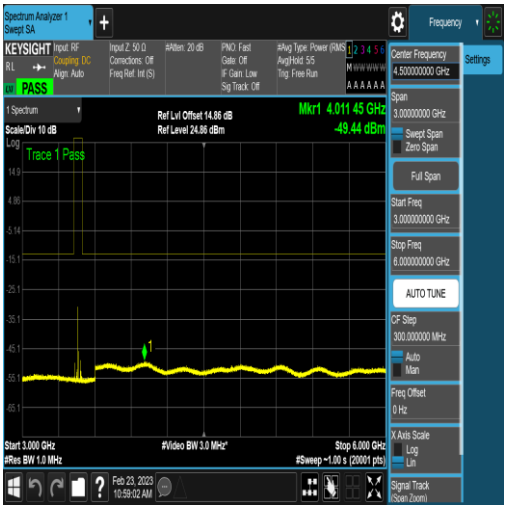
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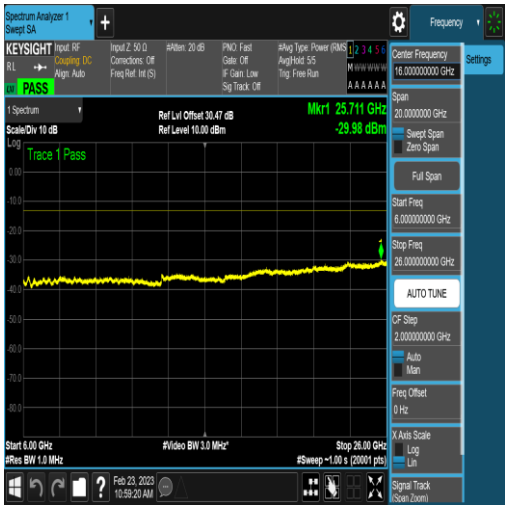	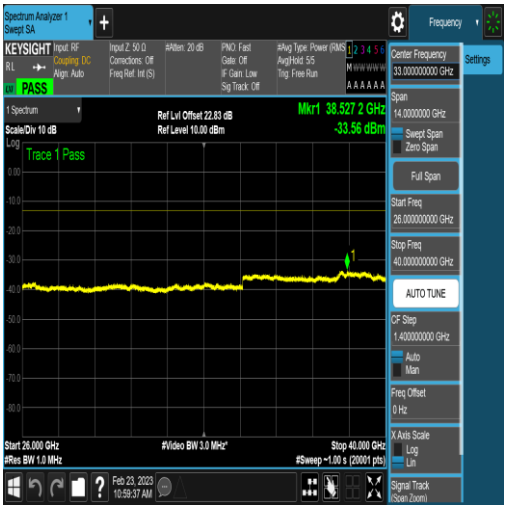
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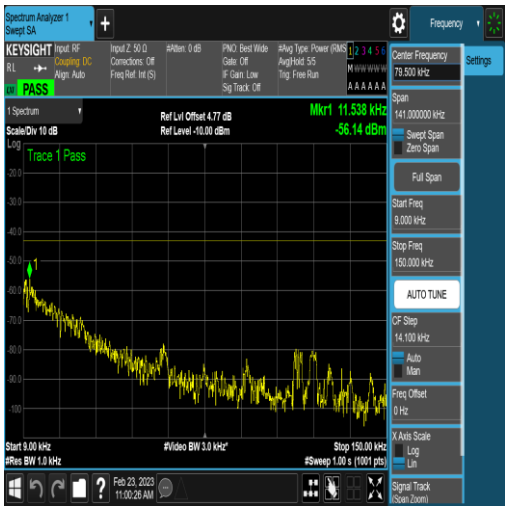

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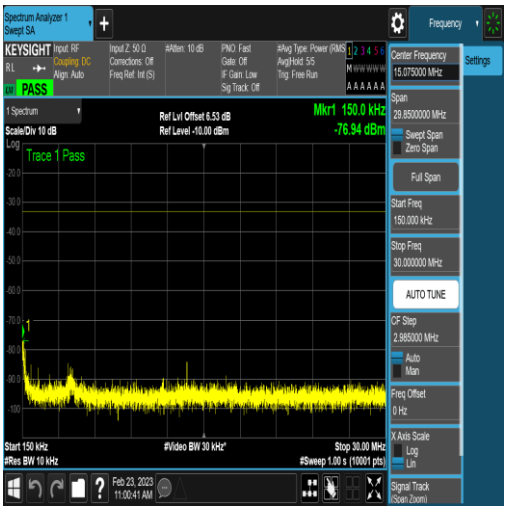

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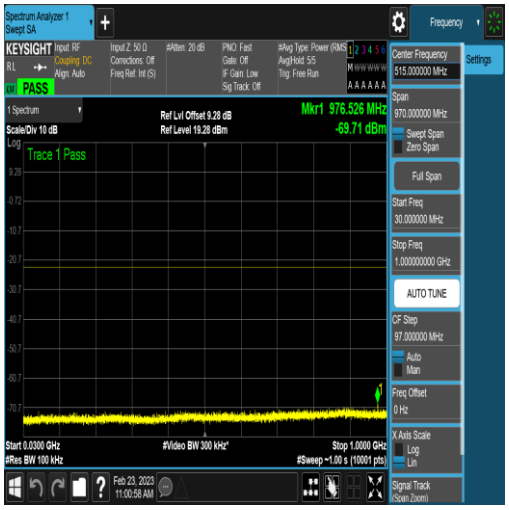

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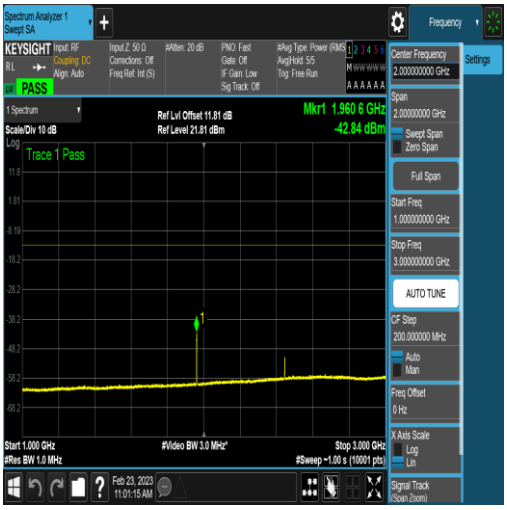

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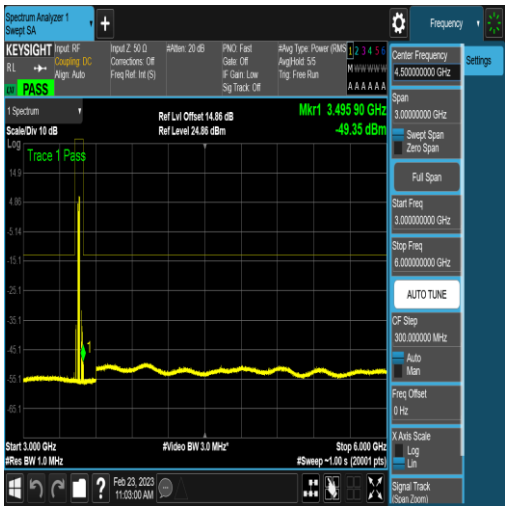

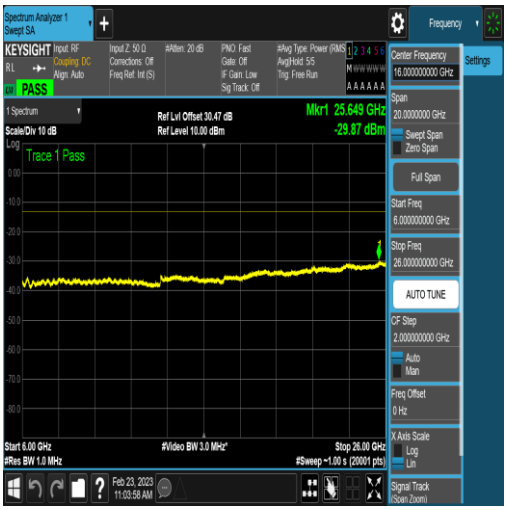

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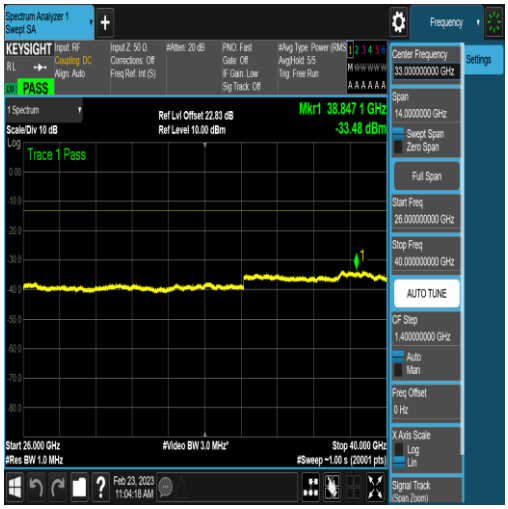	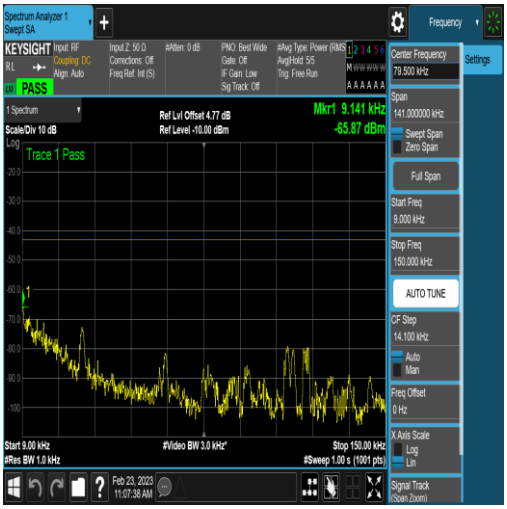
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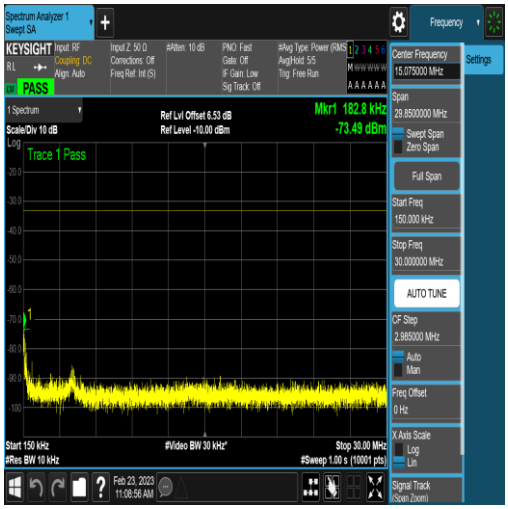	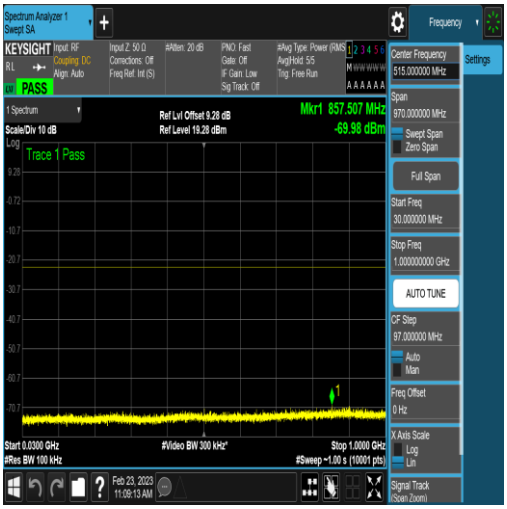
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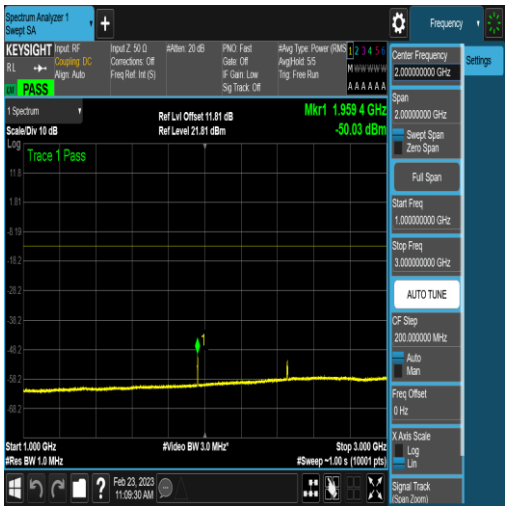

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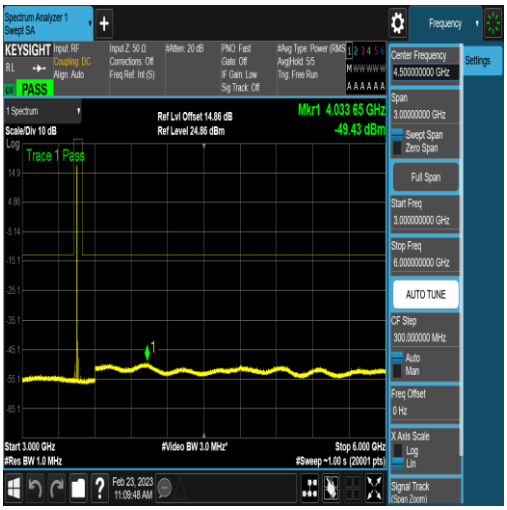

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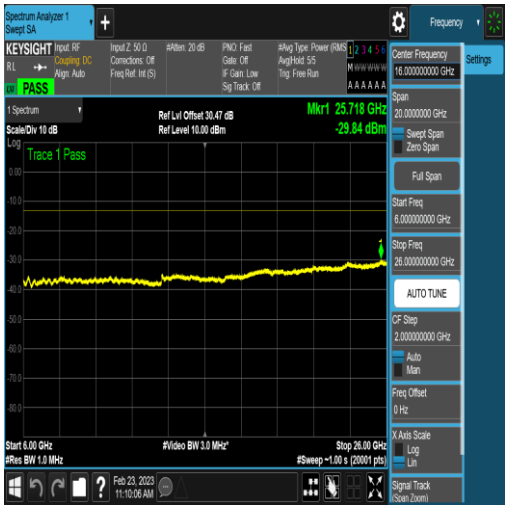

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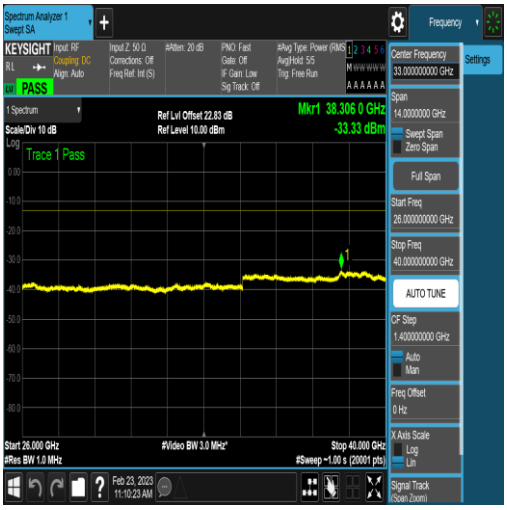
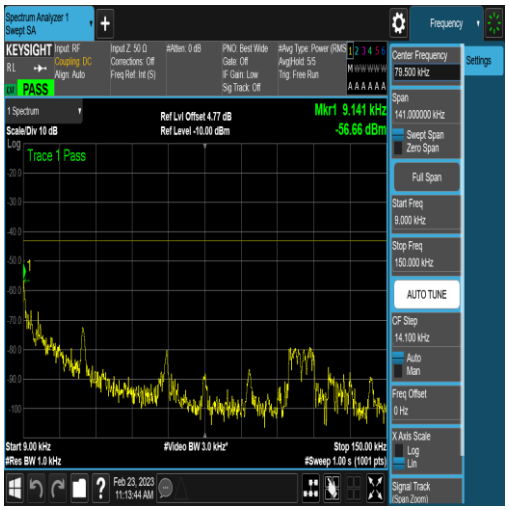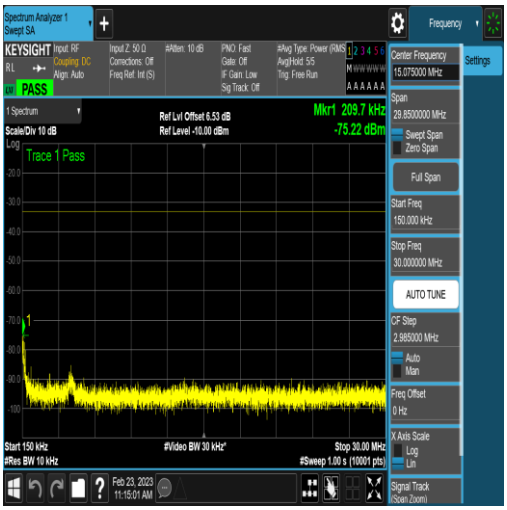
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