

## **Appendix for 2A\_N78A\_3450-3550MHz**

Product Name: CSX8

Model No: LGT-08QA-2301

## Appendix A: Average Power Output Data for NSA

### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.16	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.97	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	24.11	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	24.17	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	22.99	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	23.83	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	24.10	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Inner_Full	24.16	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.04	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.54	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	24.38	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	24.44	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	23.93	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	23.42	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	24.37	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Inner_Full	24.40	PC3	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.50	PC3	PASS

3550			SK		eft			S
DC_2A_n78A-3450-3550	30	5+20	DFT-QP SK	M+H	Edge_1RB_Ri ght	22.68	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+20	DFT-QP SK	M+H	Outer_Full	23.60	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+20	DFT-QP SK	M+H	Inner_Full	23.52	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+20	CP-QPS K	M+H	Edge_1RB_L eft	23.42	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+20	CP-QPS K	M+H	Edge_1RB_Ri ght	22.61	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+20	CP-QPS K	M+H	Outer_Full	23.51	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+20	CP-QPS K	M+H	Inner_Full	23.47	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+L	Edge_1RB_L eft	23.26	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+L	Edge_1RB_Ri ght	23.93	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+L	Outer_Full	24.33	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+L	Inner_Full	24.38	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+L	Edge_1RB_L eft	23.12	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+L	Edge_1RB_Ri ght	23.78	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+L	Outer_Full	24.08	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+L	Inner_Full	24.37	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+M	Edge_1RB_L eft	18.77	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+M	Edge_1RB_Ri ght	23.55	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+M	Outer_Full	24.41	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+M	Inner_Full	24.52	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+M	Edge_1RB_L eft	23.70	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS	M+M	Edge_1RB_Ri	23.29	PC3	PAS

3550			K		ght			S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+M	Outer_Full	24.25	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+M	Inner_Full	24.50	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+H	Edge_1RB_L eft	23.44	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+H	Edge_1RB_Ri ght	22.83	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+H	Outer_Full	23.82	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	DFT-QP SK	M+H	Inner_Full	23.90	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+H	Edge_1RB_L eft	23.30	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+H	Edge_1RB_Ri ght	22.67	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+H	Outer_Full	23.76	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+40	CP-QPS K	M+H	Inner_Full	23.89	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+L	Edge_1RB_L eft	23.07	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+L	Edge_1RB_Ri ght	23.78	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+L	Outer_Full	24.14	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+L	Inner_Full	24.34	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+L	Edge_1RB_L eft	23.26	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+L	Edge_1RB_Ri ght	23.57	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+L	Outer_Full	24.20	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+L	Inner_Full	24.28	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+M	Edge_1RB_L eft	23.73	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+M	Edge_1RB_Ri ght	23.27	PC3	PAS S
DC_2A_n78A-3450-	30	5+50	DFT-QP	M+M	Outer_Full	24.09	PC3	PAS

3550			SK					S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+M	Inner_Full	24.28	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+M	Edge_1RB_L eft	23.91	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+M	Edge_1RB_Ri ght	23.16	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+M	Outer_Full	24.05	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+M	Inner_Full	24.22	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+H	Edge_1RB_L eft	23.65	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+H	Edge_1RB_Ri ght	22.53	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+H	Outer_Full	23.72	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	DFT-QP SK	M+H	Inner_Full	23.72	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+H	Edge_1RB_L eft	23.62	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+H	Edge_1RB_Ri ght	22.31	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+H	Outer_Full	23.68	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+50	CP-QPS K	M+H	Inner_Full	23.77	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+L	Edge_1RB_L eft	23.06	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+L	Edge_1RB_Ri ght	23.28	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+L	Outer_Full	24.17	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+L	Inner_Full	24.31	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+L	Edge_1RB_L eft	22.84	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+L	Edge_1RB_Ri ght	23.04	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+L	Outer_Full	24.11	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS	M+L	Inner_Full	24.25	PC3	PAS

3550			K					S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+M	Edge_1RB_L eft	23.80	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+M	Edge_1RB_Ri ght	23.30	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+M	Outer_Full	24.09	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+M	Inner_Full	24.16	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+M	Edge_1RB_L eft	23.66	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+M	Edge_1RB_Ri ght	23.08	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+M	Outer_Full	23.93	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+M	Inner_Full	24.13	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+H	Edge_1RB_L eft	23.73	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+H	Edge_1RB_Ri ght	22.61	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+H	Outer_Full	23.83	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	DFT-QP SK	M+H	Inner_Full	23.85	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+H	Edge_1RB_L eft	23.71	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+H	Edge_1RB_Ri ght	22.40	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+H	Outer_Full	23.79	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+60	CP-QPS K	M+H	Inner_Full	23.81	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+L	Edge_1RB_L eft	22.89	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+L	Edge_1RB_Ri ght	23.07	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+L	Outer_Full	23.95	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+L	Inner_Full	24.10	PC3	PAS S
DC_2A_n78A-3450-	30	5+80	CP-QPS	M+L	Edge_1RB_L	23.09	PC3	PAS

3550			K		eft			S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+L	Edge_1RB_Ri ght	23.24	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+L	Outer_Full	23.89	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+L	Inner_Full	24.08	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+M	Edge_1RB_L eft	23.55	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+M	Edge_1RB_Ri ght	22.78	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+M	Outer_Full	23.82	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+M	Inner_Full	23.98	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+M	Edge_1RB_L eft	23.33	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+M	Edge_1RB_Ri ght	22.64	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+M	Outer_Full	23.87	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+M	Inner_Full	23.93	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+H	Edge_1RB_L eft	23.72	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+H	Edge_1RB_Ri ght	22.40	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+H	Outer_Full	23.72	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	DFT-QP SK	M+H	Inner_Full	23.84	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+H	Edge_1RB_L eft	23.51	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+H	Edge_1RB_Ri ght	22.48	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+H	Outer_Full	23.68	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+80	CP-QPS K	M+H	Inner_Full	23.81	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+L	Edge_1RB_L eft	22.82	PC3	PAS S
DC_2A_n78A-3450-	30	5+90	DFT-QP	M+L	Edge_1RB_Ri	22.75	PC3	PAS

3550			SK		ght			S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+L	Outer_Full	23.81	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+L	Inner_Full	24.07	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+L	Edge_1RB_L eft	22.93	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+L	Edge_1RB_Ri ght	22.63	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+L	Outer_Full	23.81	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+L	Inner_Full	23.92	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+M	Edge_1RB_L eft	23.17	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+M	Edge_1RB_Ri ght	22.50	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+M	Outer_Full	23.82	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+M	Inner_Full	23.94	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+M	Edge_1RB_L eft	23.17	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+M	Edge_1RB_Ri ght	22.69	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+M	Outer_Full	23.81	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+M	Inner_Full	23.93	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+H	Edge_1RB_L eft	23.48	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+H	Edge_1RB_Ri ght	22.49	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+H	Outer_Full	23.77	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	DFT-QP SK	M+H	Inner_Full	23.96	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+H	Edge_1RB_L eft	23.38	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+H	Edge_1RB_Ri ght	22.18	PC3	PAS S
DC_2A_n78A-3450-	30	5+90	CP-QPS	M+H	Outer_Full	23.77	PC3	PAS



3550			K					S
DC_2A_n78A-3450-3550	30	5+90	CP-QPS K	M+H	Inner_Full	23.93	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+L	Edge_1RB_L eft	22.67	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+L	Edge_1RB_Ri ght	22.27	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+L	Outer_Full	23.74	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+L	Inner_Full	24.52	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+L	Edge_1RB_L eft	23.54	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+L	Edge_1RB_Ri ght	23.08	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+L	Outer_Full	23.74	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+L	Inner_Full	23.86	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+M	Edge_1RB_L eft	22.63	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+M	Edge_1RB_Ri ght	24.35	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+M	Outer_Full	23.82	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+M	Inner_Full	23.87	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+M	Edge_1RB_L eft	23.52	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+M	Edge_1RB_Ri ght	23.07	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+M	Outer_Full	23.73	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+M	Inner_Full	23.86	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+H	Edge_1RB_L eft	23.71	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+H	Edge_1RB_Ri ght	22.35	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP SK	M+H	Outer_Full	23.72	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	DFT-QP	M+H	Inner_Full	23.90	PC3	PAS

3550			SK					S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+H	Edge_1RB_L eft	23.51	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+H	Edge_1RB_Ri ght	23.49	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+H	Outer_Full	23.72	PC3	PAS S
DC_2A_n78A-3450-3550	30	5+100	CP-QPS K	M+H	Inner_Full	23.86	PC3	PAS S

## Appendix B: Peak-to-Average Ratio for NSA

### Peak-to-Average Ratio(CCDF)

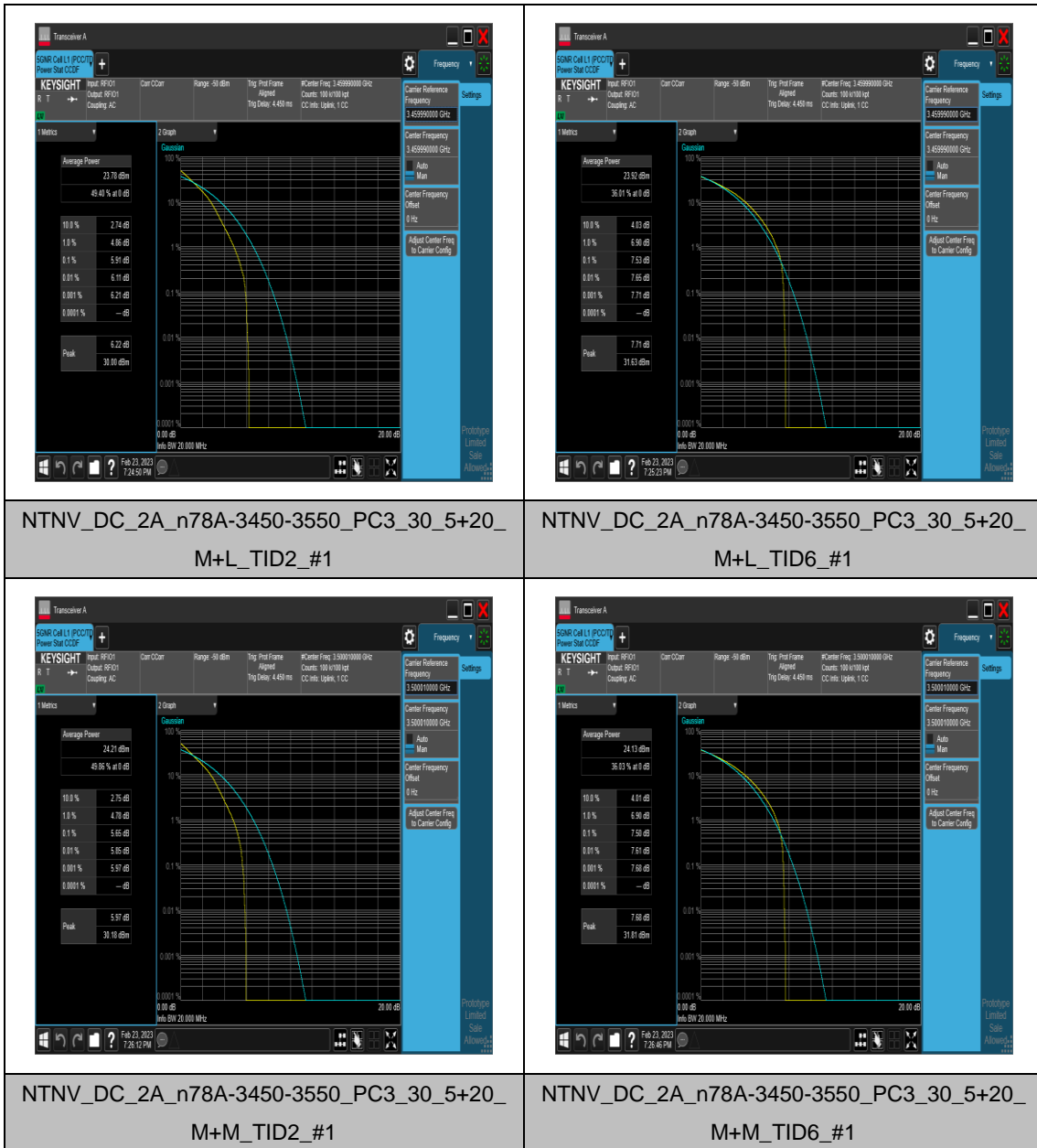
#### Test Result

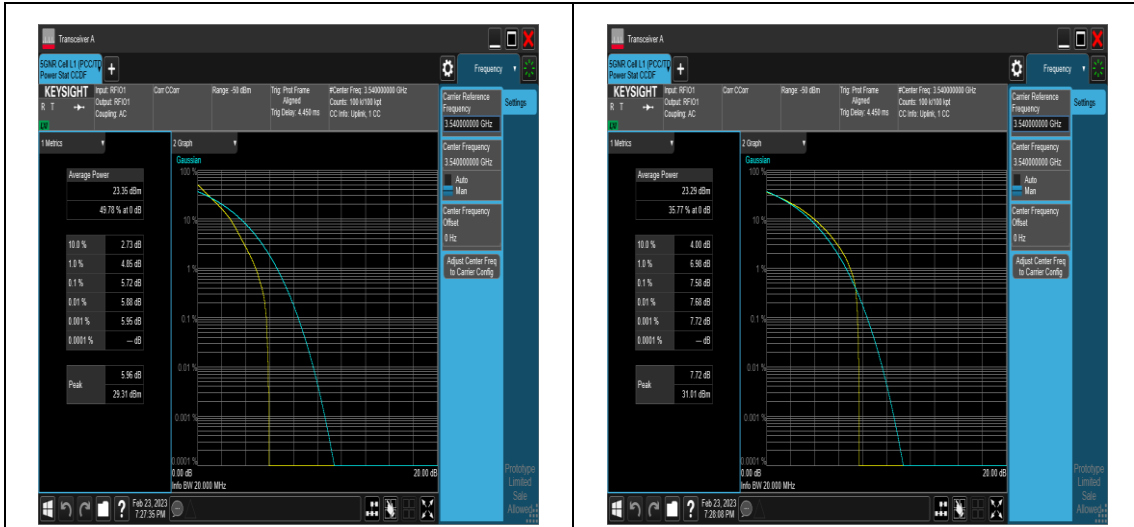
Band	SC S	Bandwidth h	Modulation n	Channel el	RB Config	Result	Limit	Verdict
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+L	Outer_Fu ll	5.91	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+L	Outer_Fu ll	7.53	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+M	Outer_Fu ll	5.65	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+M	Outer_Fu ll	7.50	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+H	Outer_Fu ll	5.72	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+H	Outer_Fu ll	7.58	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+L	Outer_Fu ll	5.31	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+L	Outer_Fu ll	7.83	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+M	Outer_Fu ll	5.25	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+M	Outer_Fu ll	7.17	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+H	Outer_Fu ll	5.46	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+H	Outer_Fu ll	7.39	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+L	Outer_Fu ll	5.45	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+L	Outer_Fu ll	7.25	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+M	Outer_Fu ll	5.51	≤13	PASS
DC_2A_n78A-3450-35	30	5+50	CP-QPSK	M+M	Outer_Fu	7.31	≤13	PASS

50					II			
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPS K	M+H	Outer_Fu II	5.16	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+H	Outer_Fu II	7.45	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPS K	M+L	Outer_Fu II	5.41	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+L	Outer_Fu II	7.28	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPS K	M+M	Outer_Fu II	5.54	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+M	Outer_Fu II	7.43	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPS K	M+H	Outer_Fu II	5.56	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+H	Outer_Fu II	7.50	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPS K	M+L	Outer_Fu II	5.62	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+L	Outer_Fu II	7.50	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPS K	M+M	Outer_Fu II	5.66	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+M	Outer_Fu II	7.54	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPS K	M+H	Outer_Fu II	5.29	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+H	Outer_Fu II	7.78	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPS K	M+L	Outer_Fu II	5.61	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+L	Outer_Fu II	7.52	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPS K	M+M	Outer_Fu II	5.61	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+M	Outer_Fu II	7.59	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPS K	M+H	Outer_Fu II	5.73	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+H	Outer_Fu II	7.65	≤13	PASS
DC_2A_n78A-3450-35	30	5+100	DFT-QPS	M+L	Outer_Fu	5.61	≤13	PASS

50			K		II			
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+L	Outer_Fu II	7.70	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPS K	M+M	Outer_Fu II	5.64	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+M	Outer_Fu II	7.71	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPS K	M+H	Outer_Fu II	5.58	≤13	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+H	Outer_Fu II	7.71	≤13	PASS

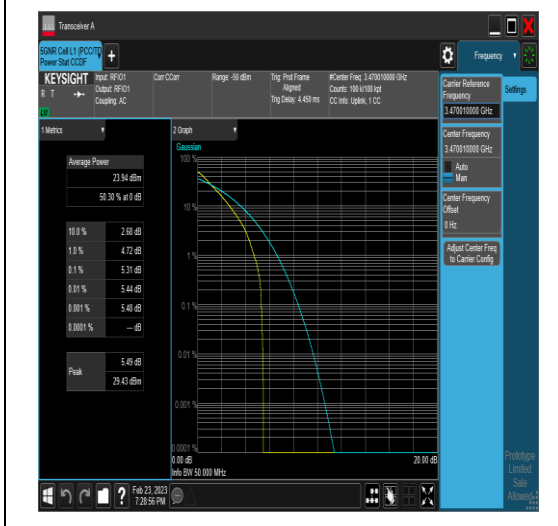
### Test Graphs



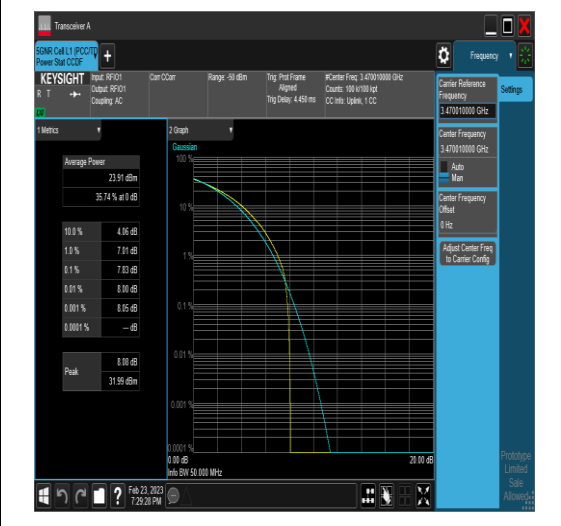


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M+H\_TID2\_#1

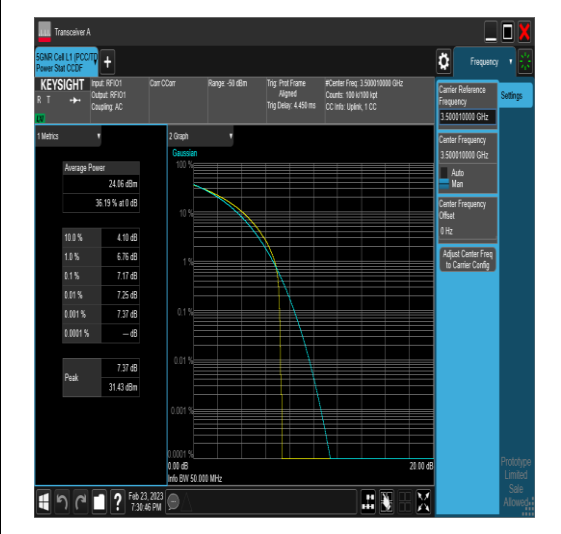
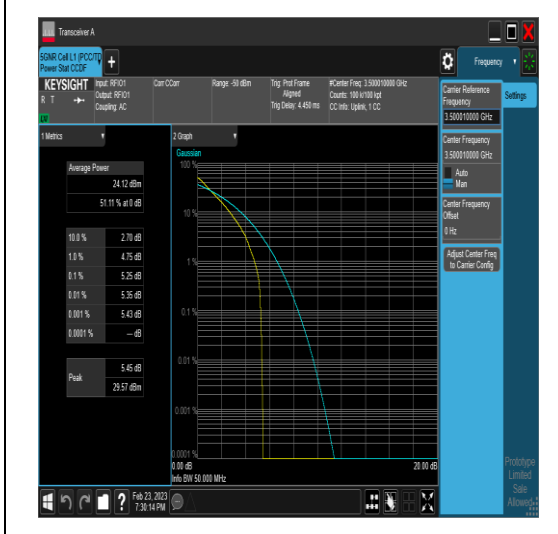
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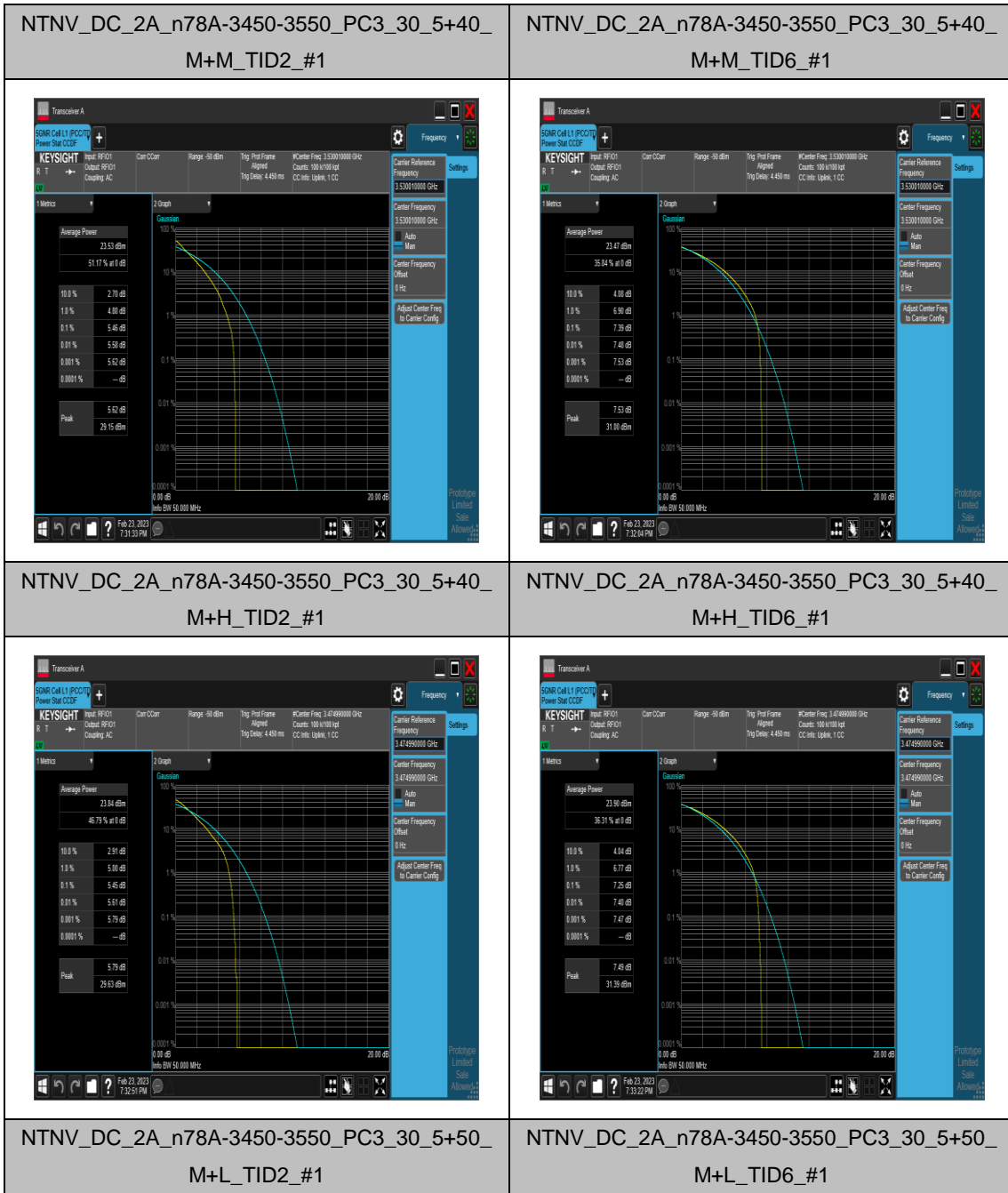


NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+40\_#1  
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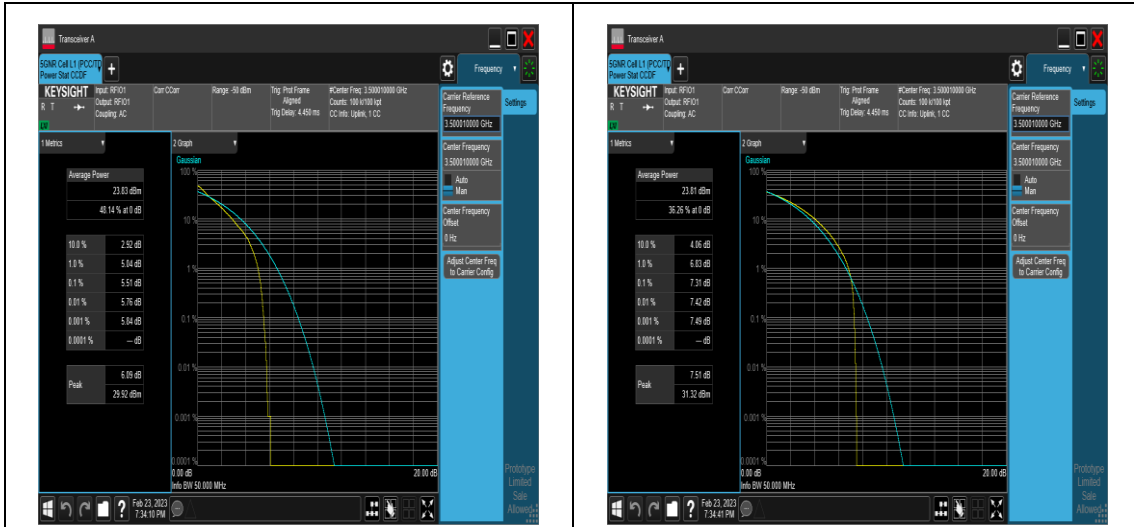


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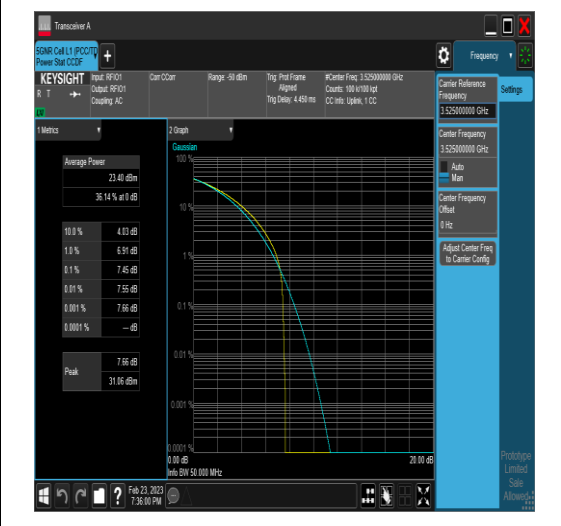
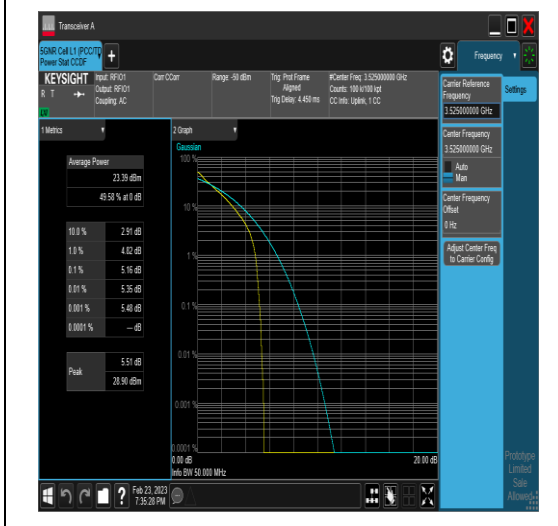






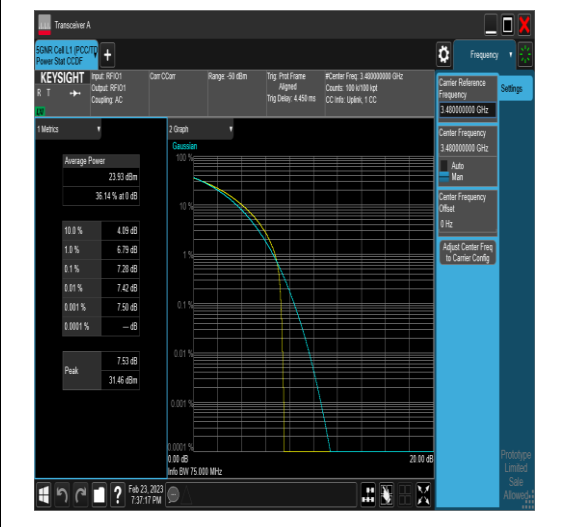
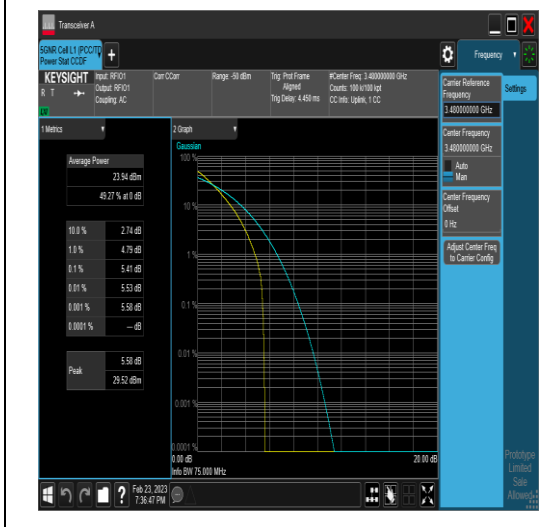
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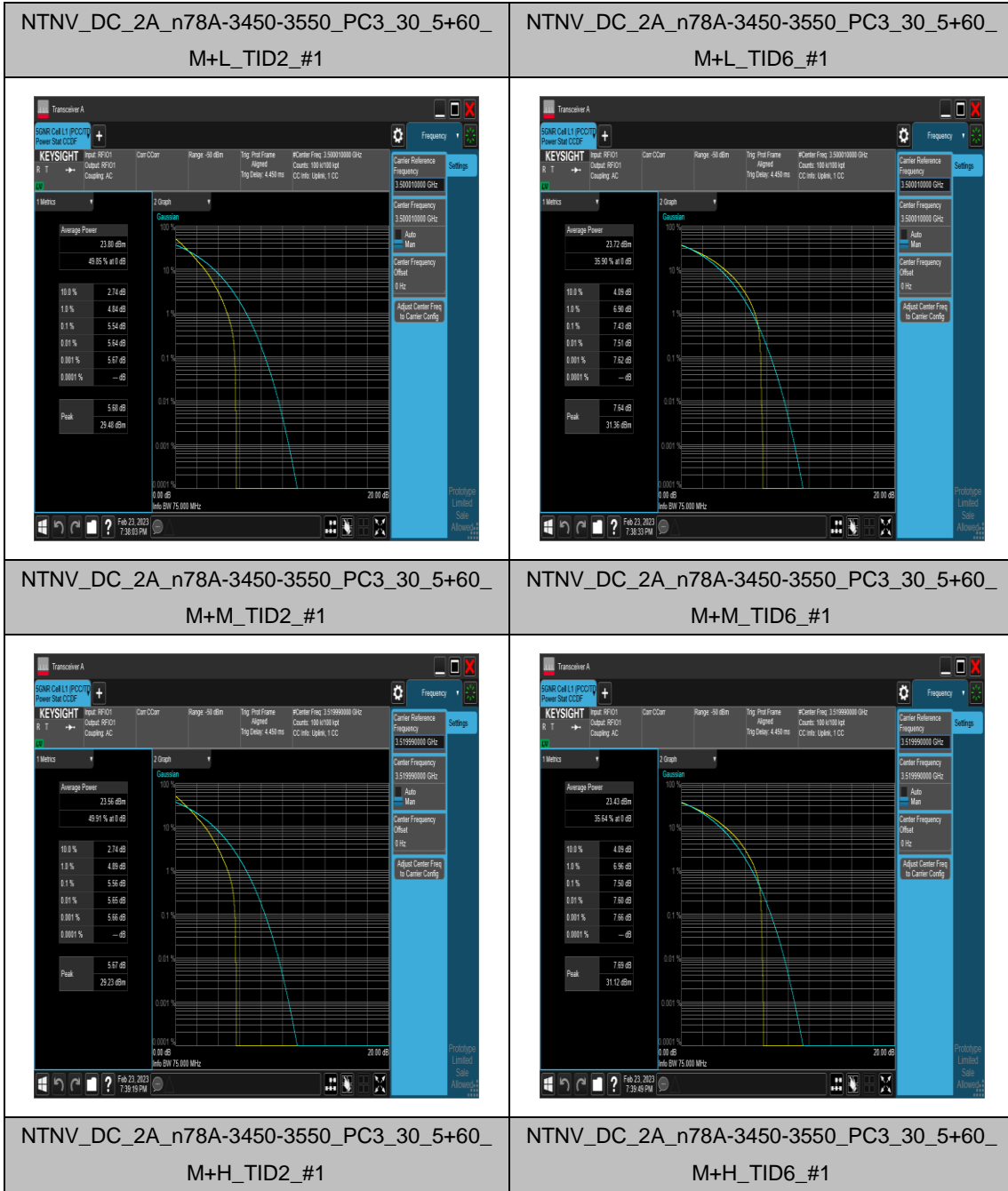
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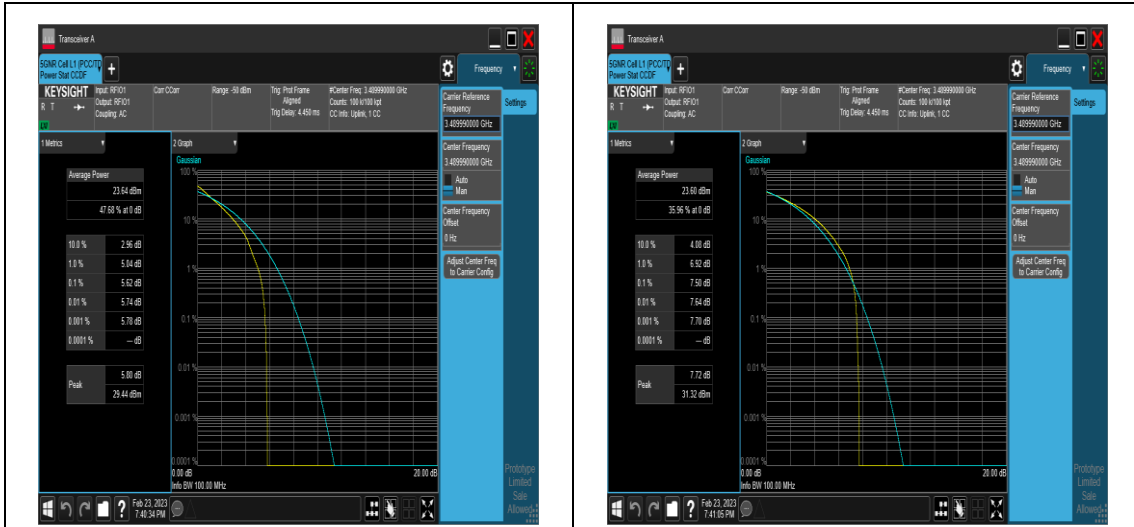


NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+50\_ M+H\_TID2\_#1

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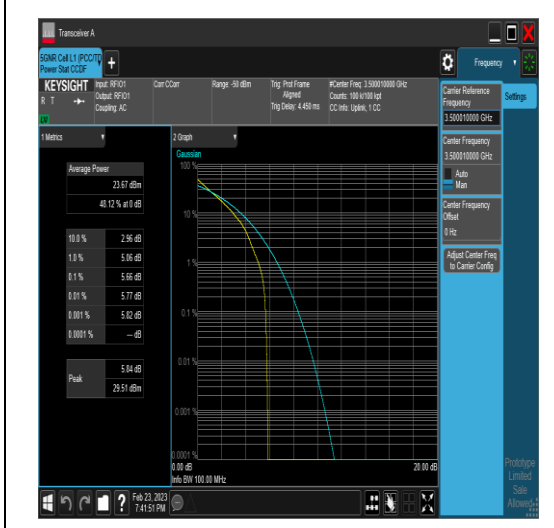




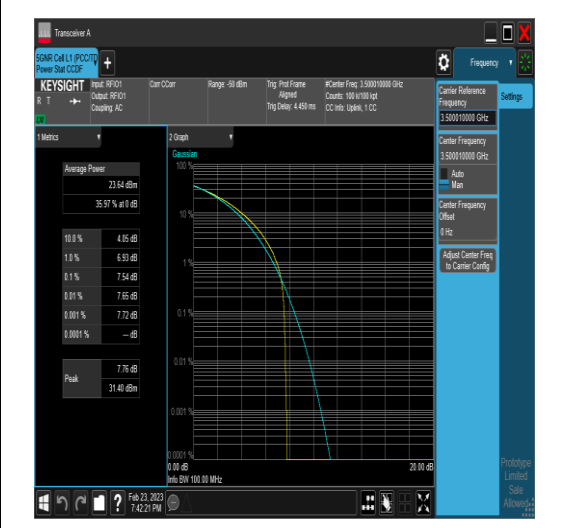


NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+80\_ M+L\_TID2\_#1

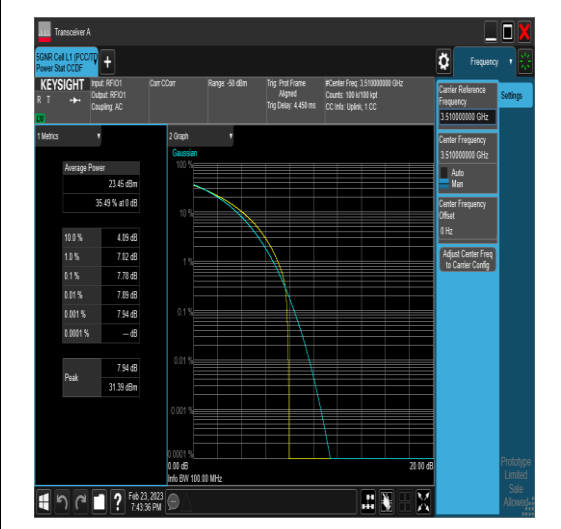
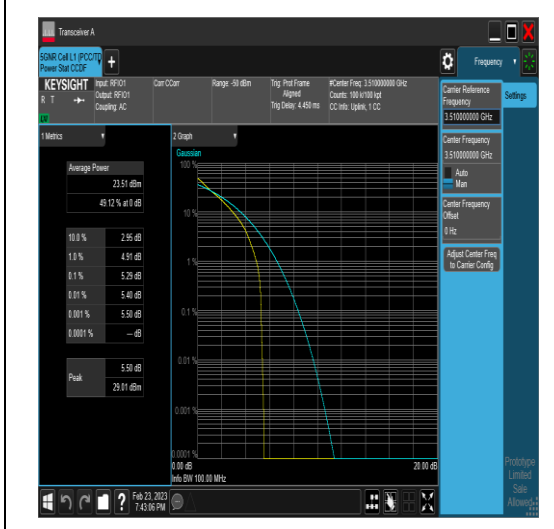
NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+80\_ M+L\_TID6\_#1

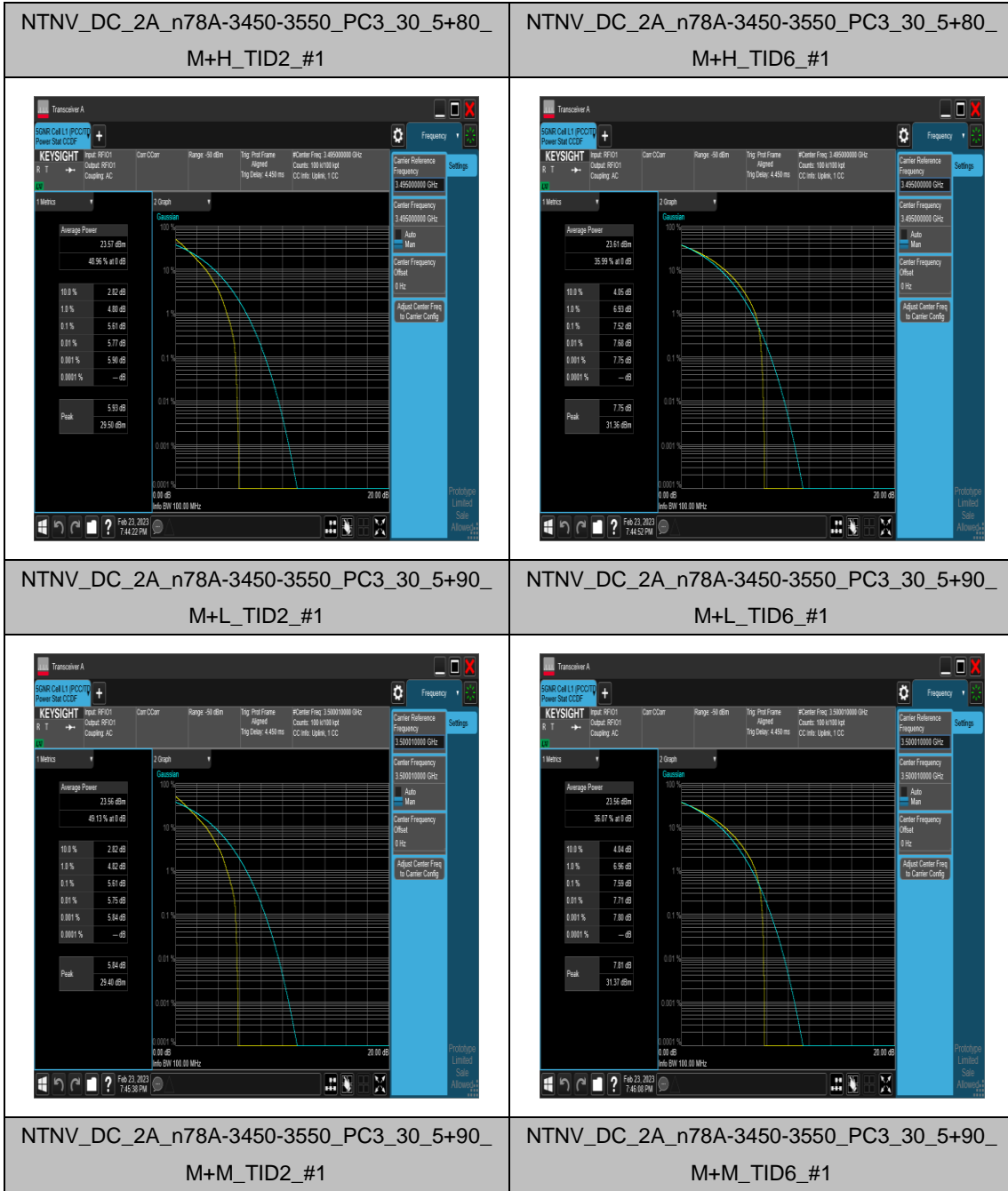


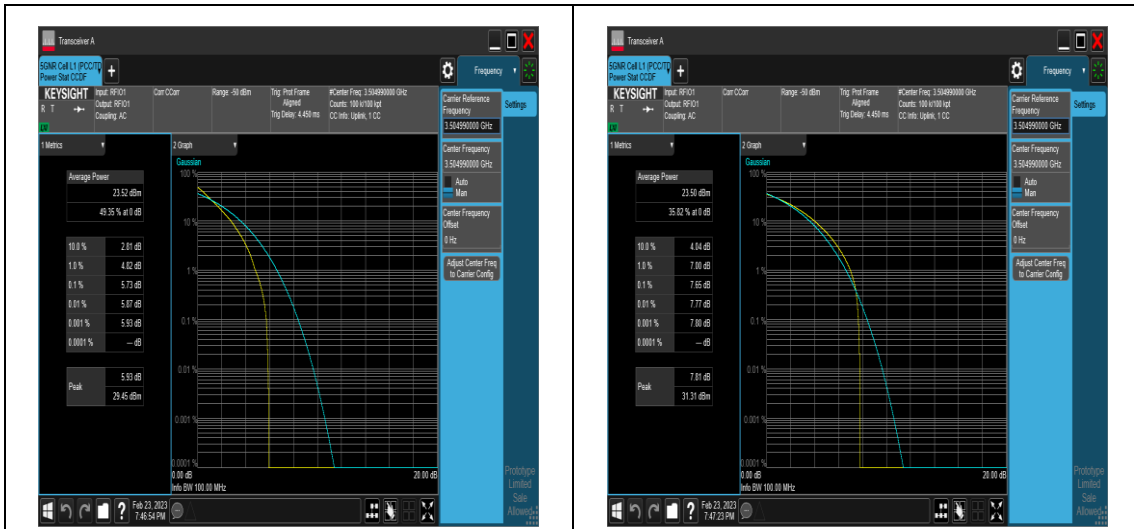
NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+80\_ M+M\_TID2\_#1



NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+80\_ M+M\_TID6\_#1

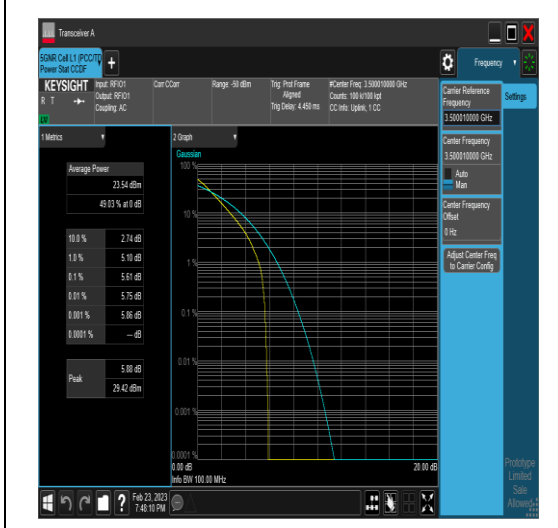




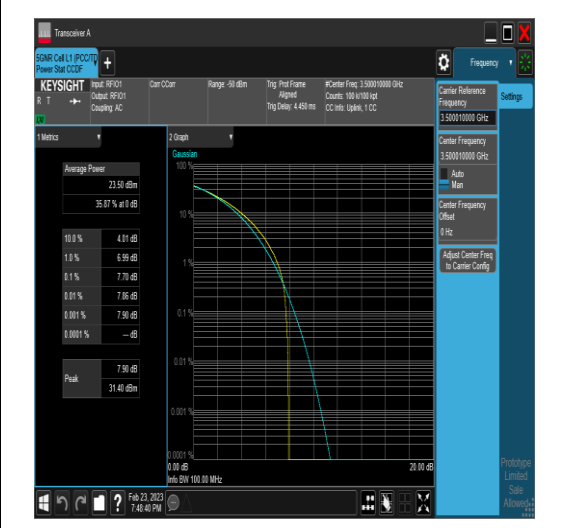


NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+90\_#1  
M+H\_TID2\_#1

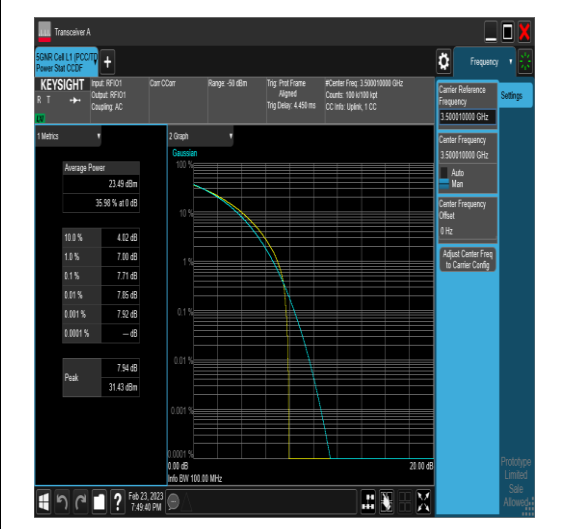
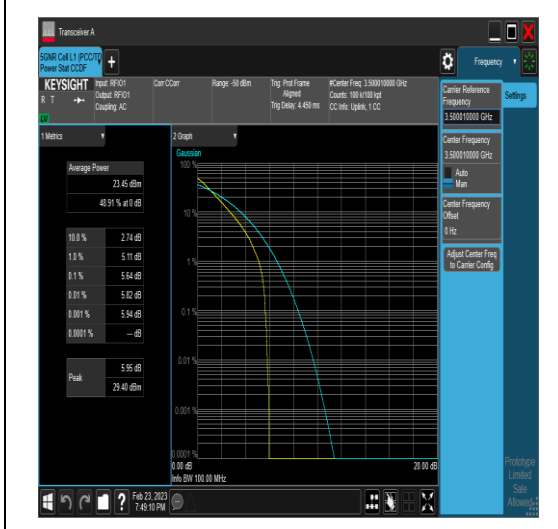
NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+90\_#1  
M+H\_TID6\_#1

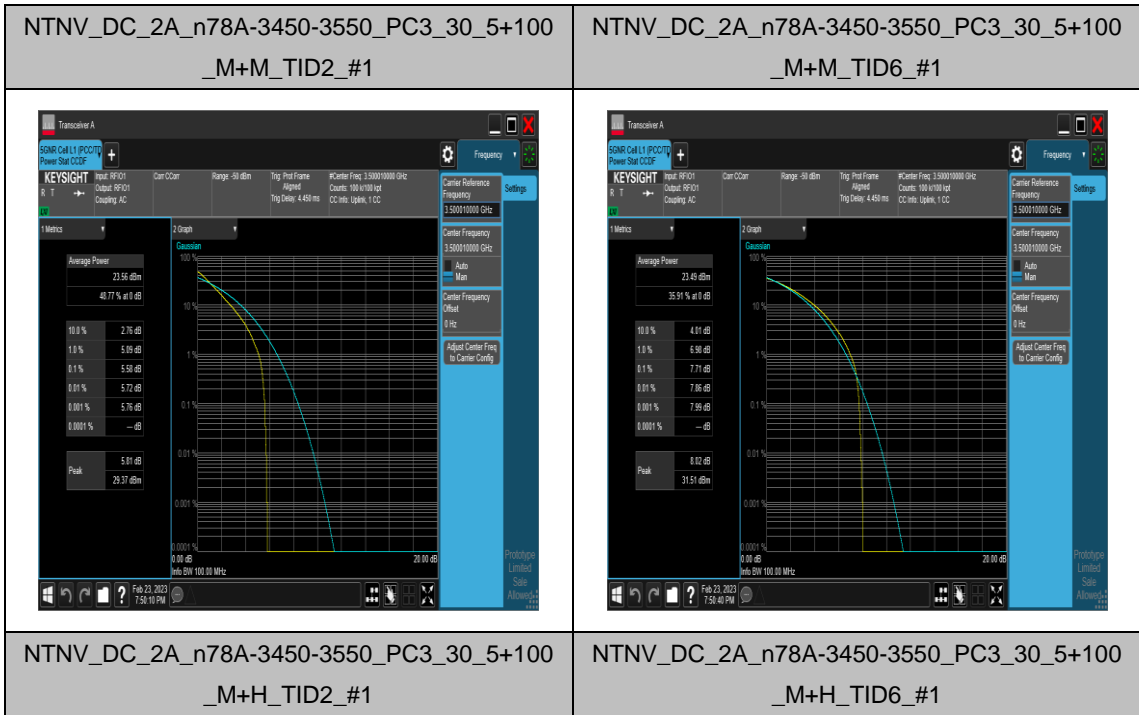


NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+100\_#1  
\_M+L\_TID2\_#1



NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+100\_#1  
\_M+L\_TID6\_#1





## Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

### Test Result

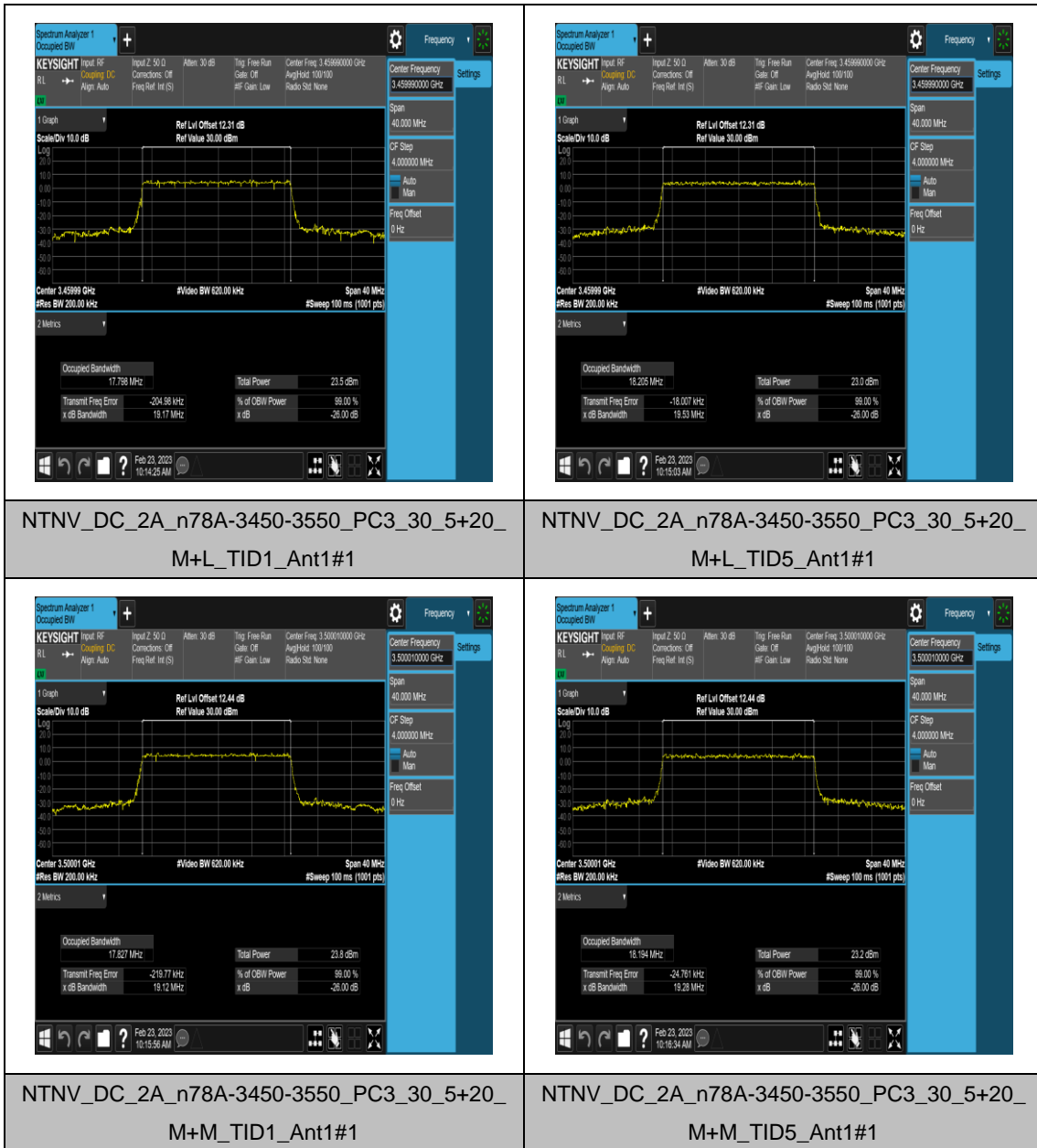
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+L	Outer_Full	17.798	19.17	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+L	Outer_Full	18.205	19.53	PASS
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+M	Outer_Full	17.827	19.12	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+M	Outer_Full	18.194	19.28	PASS
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+H	Outer_Full	17.847	19.09	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+H	Outer_Full	18.268	19.66	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+L	Outer_Full	35.774	37.52	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+L	Outer_Full	37.763	39.34	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+M	Outer_Full	35.725	37.58	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+M	Outer_Full	37.829	39.41	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+H	Outer_Full	35.679	37.55	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+H	Outer_Full	37.770	39.66	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+L	Outer_Full	45.695	47.42	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+L	Outer_Full	47.491	49.22	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+M	Outer_Full	45.629	47.49	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+M	Outer_Full	47.435	49.47	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+H	Outer_Full	45.469	47.46	PASS

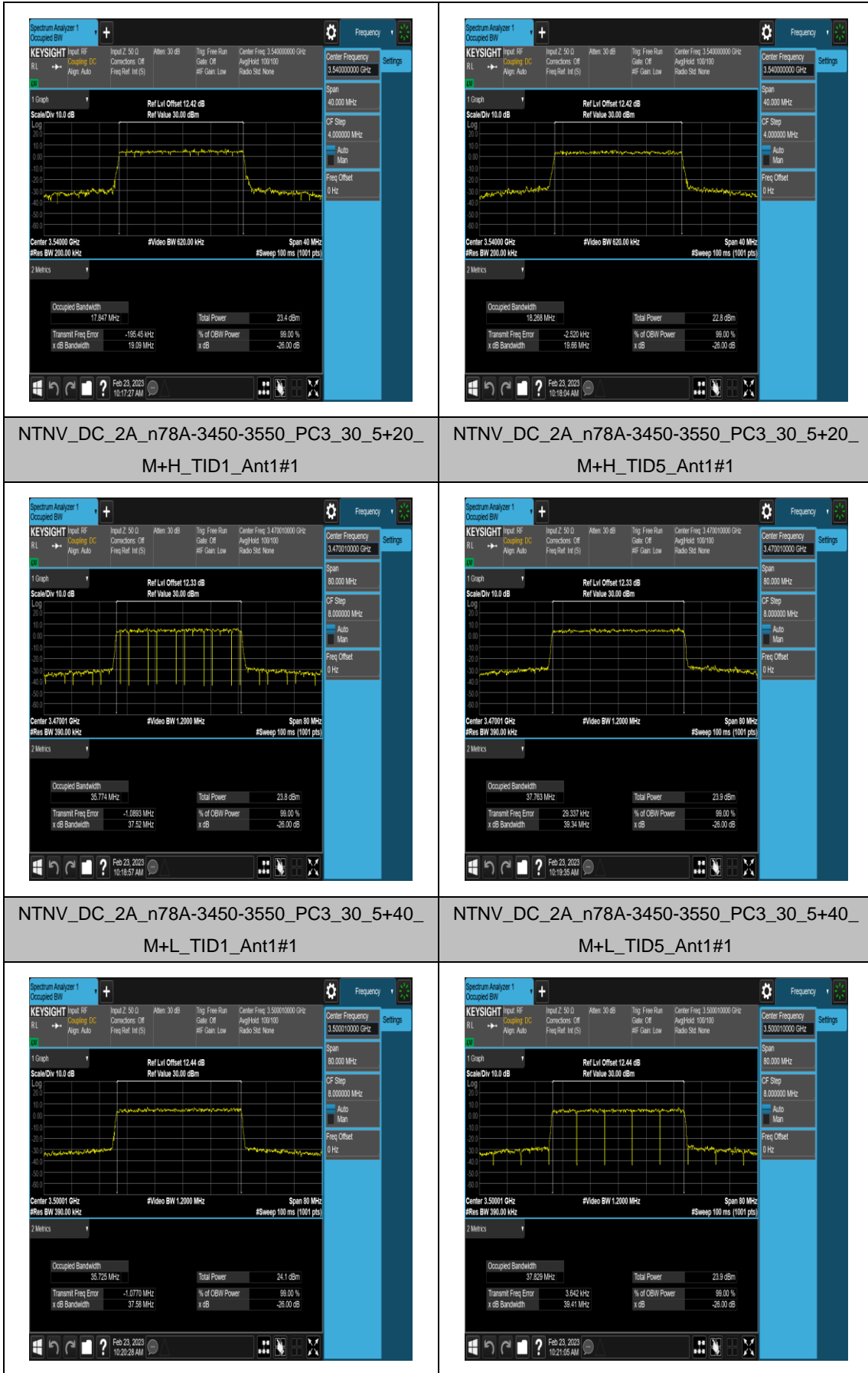
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+H	Outer_Full	47.390	49.42	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPSK	M+L	Outer_Full	57.707	59.95	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+L	Outer_Full	57.848	59.90	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPSK	M+M	Outer_Full	57.691	60.05	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+M	Outer_Full	57.780	59.98	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPSK	M+H	Outer_Full	57.762	59.97	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+H	Outer_Full	57.821	60.25	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPSK	M+L	Outer_Full	77.120	79.67	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+L	Outer_Full	77.309	80.08	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPSK	M+M	Outer_Full	77.083	80.00	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+M	Outer_Full	77.358	80.24	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPSK	M+H	Outer_Full	77.080	79.73	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+H	Outer_Full	77.406	79.98	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPSK	M+L	Outer_Full	86.417	89.66	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+L	Outer_Full	87.236	90.38	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPSK	M+M	Outer_Full	86.376	89.60	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+M	Outer_Full	87.243	90.29	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPSK	M+H	Outer_Full	86.486	89.43	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+H	Outer_Full	87.432	90.45	PASS
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPSK	M+L	Outer_Full	95.960	99.50	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+L	Outer_Full	97.229	100.7	PASS

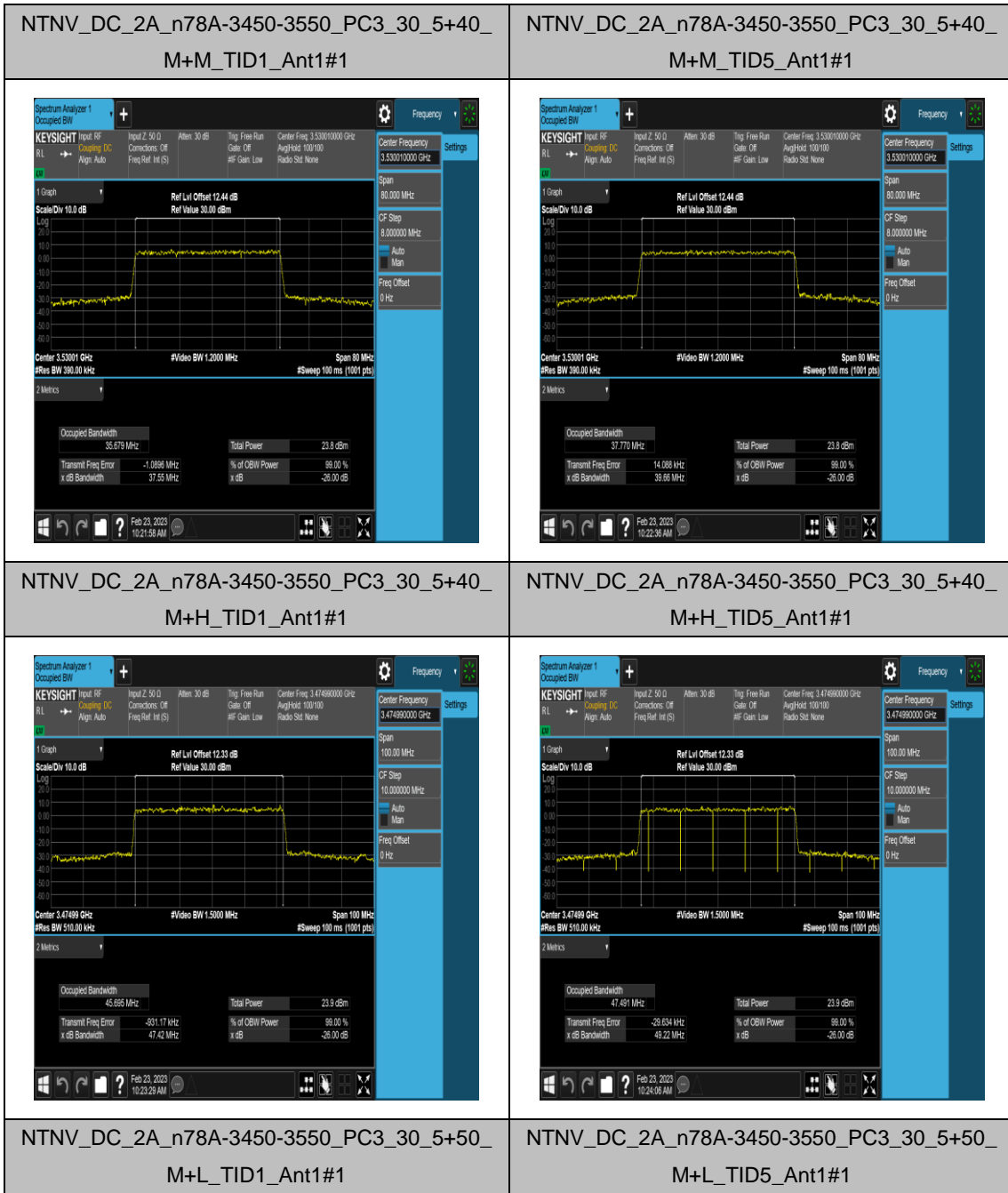


DC_2A_n78A-3450-35 50	30	5+100	DFT-QPSK	M+M	Outer_Full	95.972	99.50	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+M	Outer_Full	97.316	100.6	PASS
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPSK	M+H	Outer_Full	96.003	99.57	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+H	Outer_Full	97.318	100.8	PASS

### Test Graphs

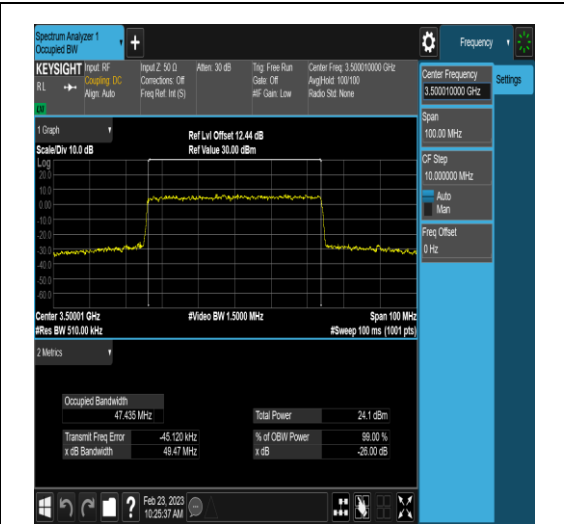








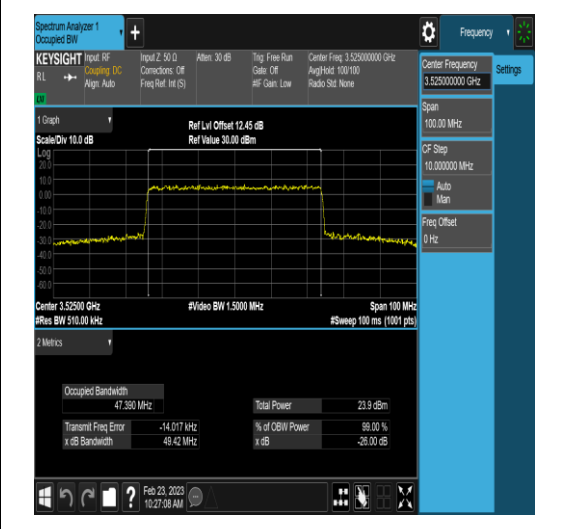
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NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+50\_M+M\_TID5\_Ant1#1



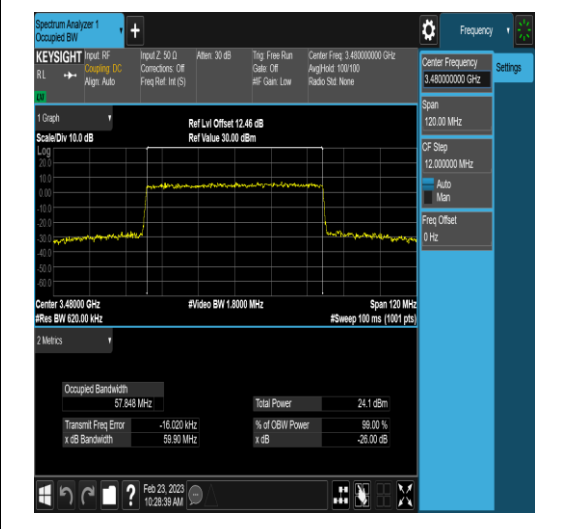
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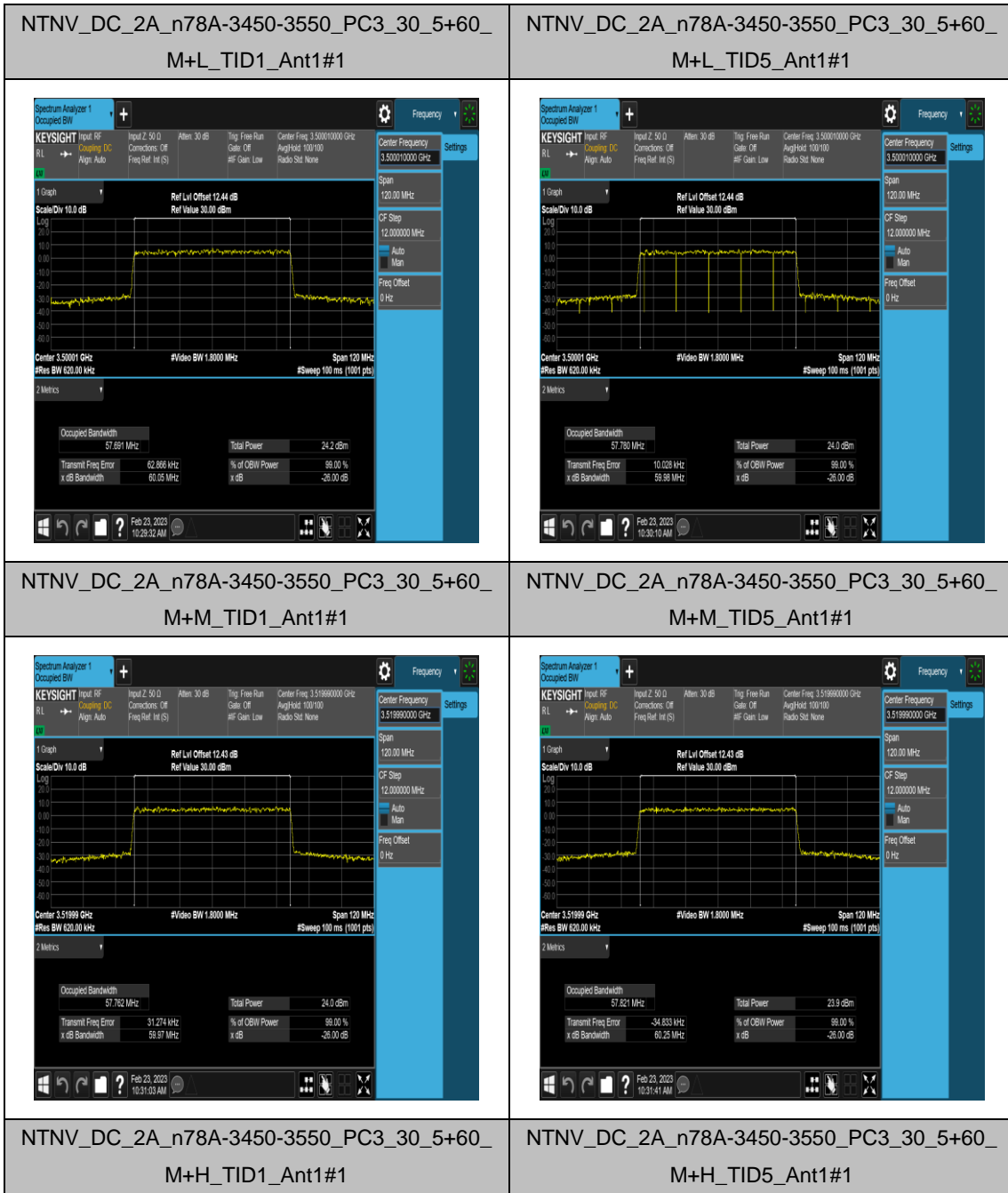
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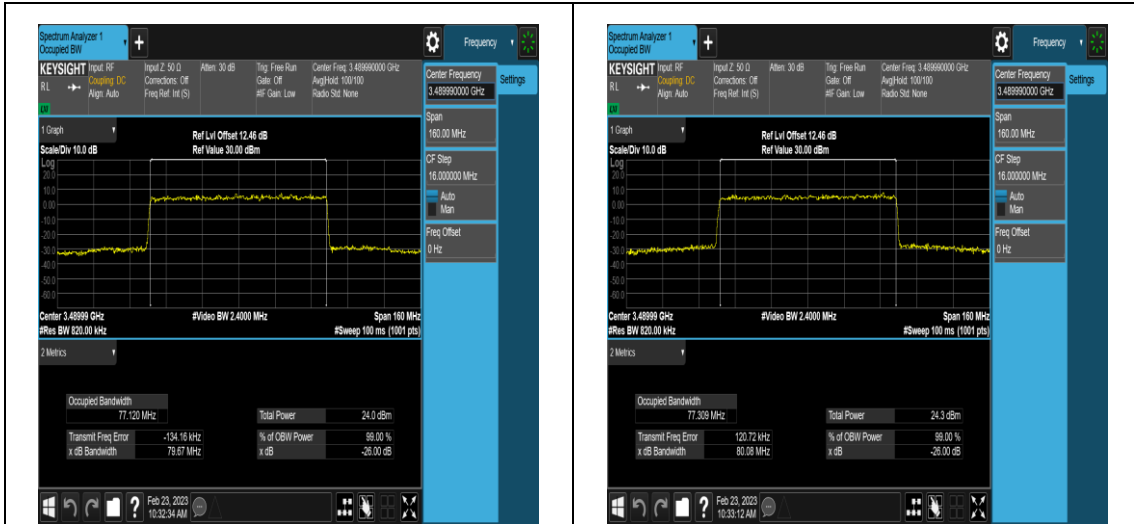


NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+50\_M+M\_TID1\_Ant1#1



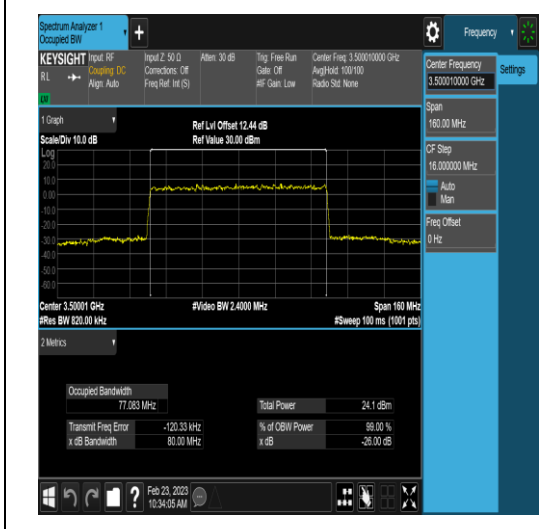
NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+50\_M+M\_TID5\_Ant1#1





NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+80\_ M+L\_TID1\_Ant1#1

NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+80\_ M+L\_TID5\_Ant1#1



NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+80\_ M+M\_TID1\_Ant1#1

NTNV\_DC\_2A\_n78A-3450-3550\_PC3\_30\_5+80\_ M+M\_TID5\_Ant1#1

