Appendix for N77_3700-3980MHz

Product Name: CSX8

Model No: LGT-08QA-2301

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power（dBm）	Power Class	Verdict
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	22.70	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	23.69	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	26.46	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Inner_Full	27.05	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	22.76	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Right	23.74	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	26.08	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Inner_Full	27.19	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	L	Edge_1RB_Left	22.97	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	L	Edge_1RB_Right	24.03	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	L	Outer_Full	25.02	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	L	Inner_Full	26.12	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	L	Edge_1RB_Left	22.80	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	L	Edge_1RB_Right	23.78	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	L	Outer_Full	24.51	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	L	Inner_Full	24.66	PC2	PASS

N77-3700-3980	30	100	DFT-256QAM	L	Edge_1RB_Left	21.54	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	L	Edge_1RB_Right	22.56	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	L	Outer_Full	22.45	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	L	Inner_Full	22.65	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	22.85	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Right	23.68	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	24.11	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	L	Inner_Full	25.65	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	L	Edge_1RB_Left	22.95	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	L	Edge_1RB_Right	23.87	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	L	Outer_Full	24.00	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	L	Inner_Full	25.07	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	L	Edge_1RB_Left	22.55	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	L	Edge_1RB_Right	23.70	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	L	Outer_Full	23.51	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	L	Inner_Full	23.66	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	L	Edge_1RB_Left	20.05	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	L	Edge_1RB_Right	21.02	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	L	Outer_Full	20.56	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	L	Inner_Full	20.60	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	23.48	PC2	PASS

N77-3700-3980	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	23.06	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	26.40	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Inner_Full	26.88	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	23.52	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Right	23.12	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	25.87	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Inner_Full	26.89	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	M	Edge_1RB_Left	23.79	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	M	Edge_1RB_Right	23.36	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	M	Outer_Full	24.90	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	M	Inner_Full	25.78	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	M	Edge_1RB_Left	23.44	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	M	Edge_1RB_Right	23.42	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	M	Outer_Full	24.48	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	M	Inner_Full	24.37	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	M	Edge_1RB_Left	22.28	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	M	Edge_1RB_Right	21.84	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	M	Outer_Full	22.43	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	M	Inner_Full	22.44	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Left	23.55	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Right	23.02	PC2	PASS

N77-3700-3980	30	100	CP-QPSK	M	Outer_Full	23.93	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	M	Inner_Full	25.40	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	M	Edge_1RB_Left	23.69	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	M	Edge_1RB_Right	23.40	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	M	Outer_Full	23.95	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	M	Inner_Full	24.88	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	M	Edge_1RB_Left	23.81	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	M	Edge_1RB_Right	23.48	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	M	Outer_Full	23.42	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	M	Inner_Full	23.38	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	M	Edge_1RB_Left	20.86	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	M	Edge_1RB_Right	20.43	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	M	Outer_Full	20.44	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	M	Inner_Full	20.31	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	22.98	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	22.58	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	25.84	PC2	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Inner_Full	26.30	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	22.89	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	22.35	PC2	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	25.29	PC2	PASS

N77-3700-3980	30	100	DFT-QPSK	H	Inner_Full	26.22	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	H	Edge_1RB_Left	23.15	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	H	Edge_1RB_Right	22.51	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	H	Outer_Full	24.33	PC2	PASS
N77-3700-3980	30	100	DFT-16QAM	H	Inner_Full	23.14	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	H	Edge_1RB_Left	25.26	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	H	Edge_1RB_Right	25.22	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	H	Outer_Full	25.21	PC2	PASS
N77-3700-3980	30	100	DFT-64QAM	H	Inner_Full	25.22	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	H	Edge_1RB_Left	23.46	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	H	Edge_1RB_Right	21.21	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	H	Outer_Full	21.18	PC2	PASS
N77-3700-3980	30	100	DFT-256QAM	H	Inner_Full	21.15	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Left	23.19	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	22.17	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	23.28	PC2	PASS
N77-3700-3980	30	100	CP-QPSK	H	Inner_Full	24.59	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	H	Edge_1RB_Left	23.32	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	H	Edge_1RB_Right	23.30	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	H	Outer_Full	23.41	PC2	PASS
N77-3700-3980	30	100	CP-16QAM	H	Inner_Full	23.39	PC2	PASS

N77-3700-3980	30	100	CP-64QAM	H	Edge_1RB_Left	23.31	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	H	Edge_1RB_Right	23.38	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	H	Outer_Full	23.30	PC2	PASS
N77-3700-3980	30	100	CP-64QAM	H	Inner_Full	23.39	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	H	Edge_1RB_Left	23.65	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	H	Edge_1RB_Right	19.61	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	H	Outer_Full	20.28	PC2	PASS
N77-3700-3980	30	100	CP-256QAM	H	Inner_Full	20.28	PC2	PASS

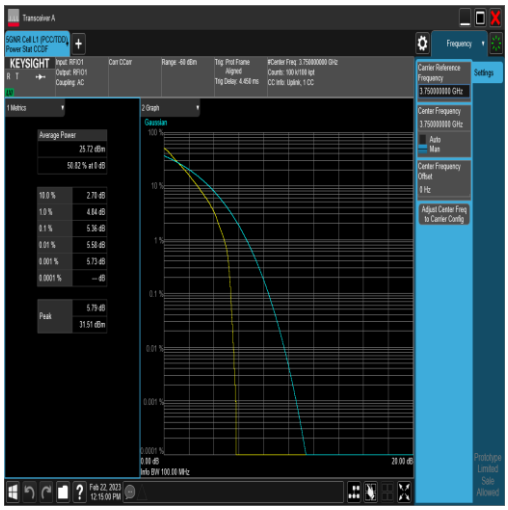
Appendix B: Peak-to-Average Ratio for SA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	5.36	≤13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	7.31	≤13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	5.22	≤13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Outer_Full	7.14	≤13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	5.49	≤13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	7.50	≤13	PASS

Test Graphs



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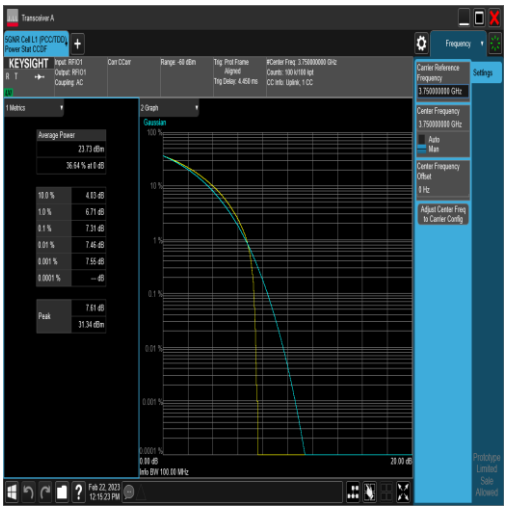

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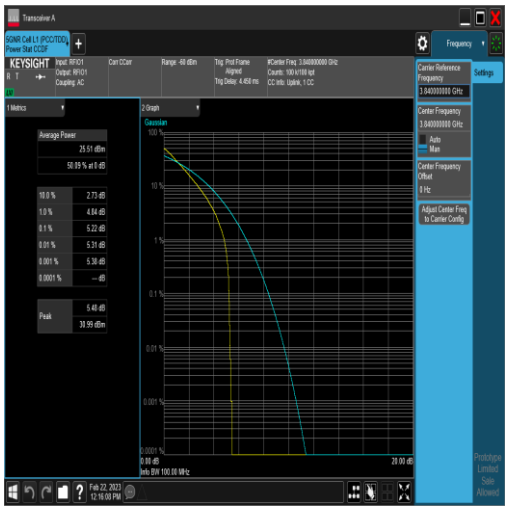	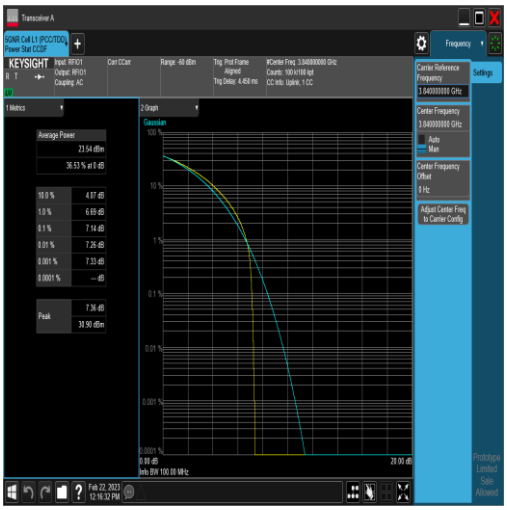
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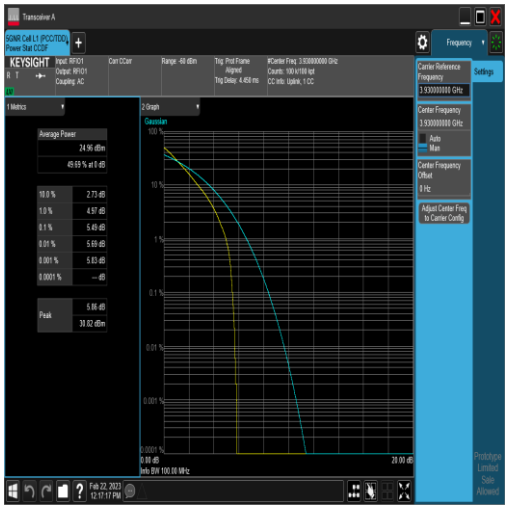	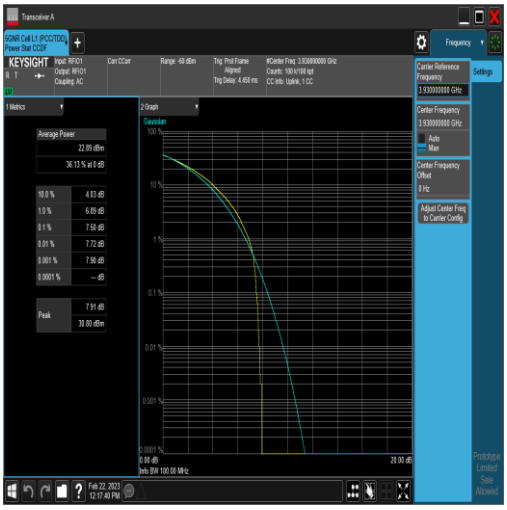
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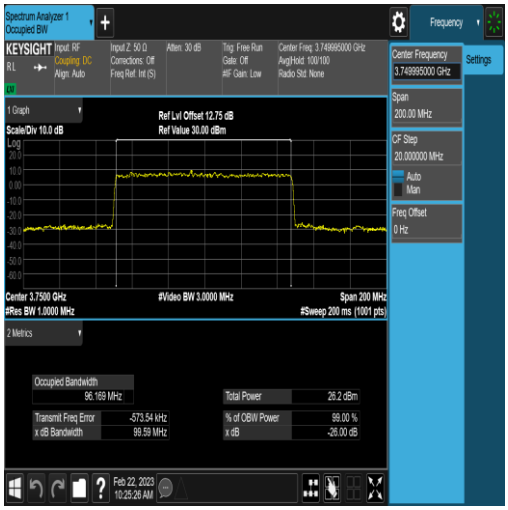
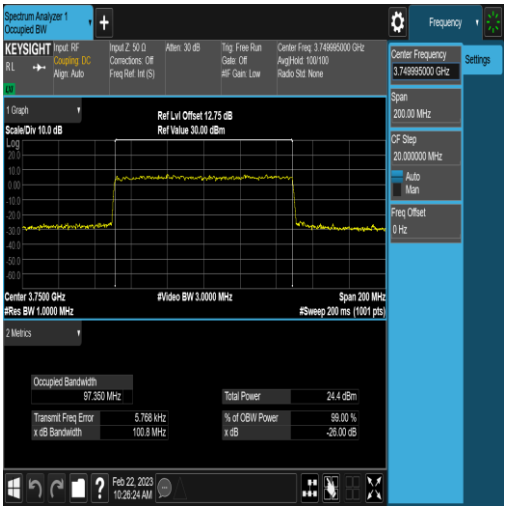
Appendix C: 26dB Bandwidth and Occupied Bandwidth for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	96.169	99.59	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	97.350	100.8	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	96.179	99.54	PASS
N77-3700-3980	30	100	CP-QPSK	M	Outer_Full	97.365	100.6	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	96.147	99.56	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	97.382	100.8	PASS

Test Graphs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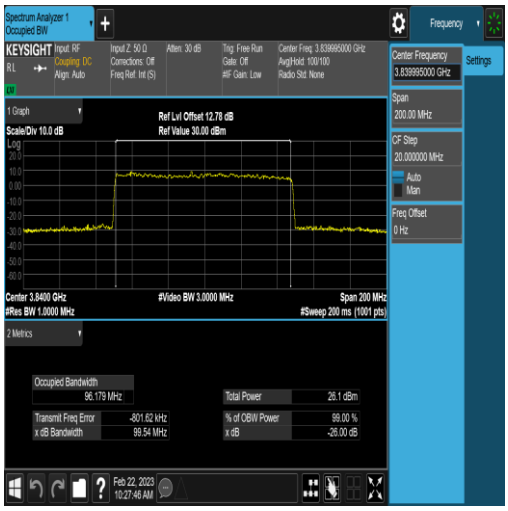	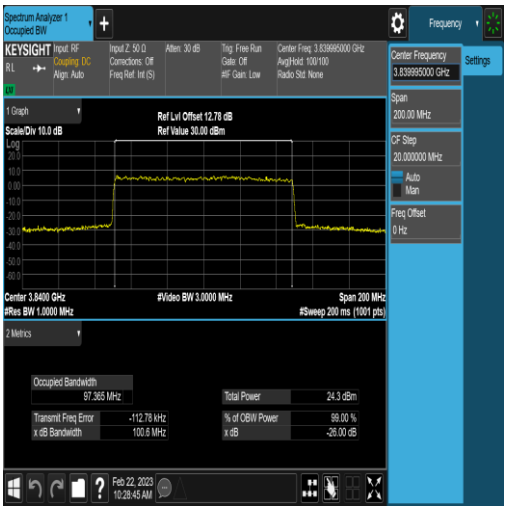
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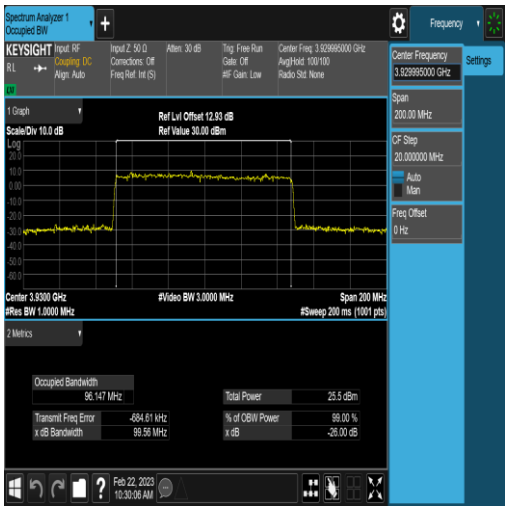	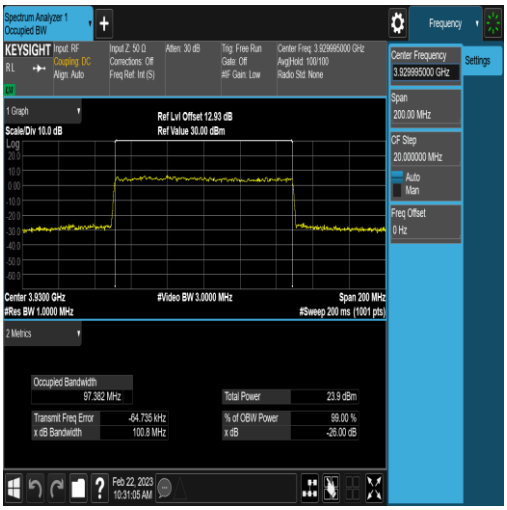
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Appendix D: Band Edge for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	see graph	PASS

Test Graphs

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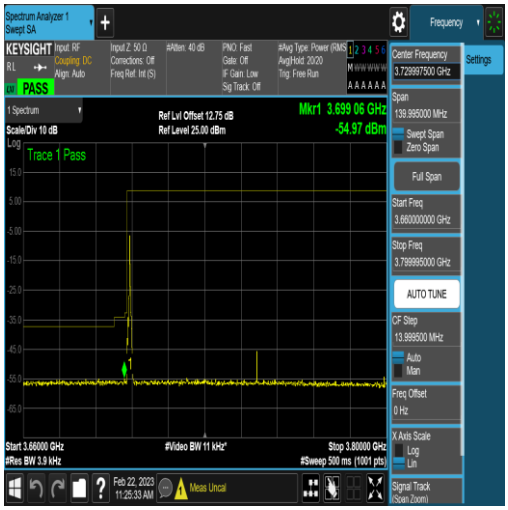
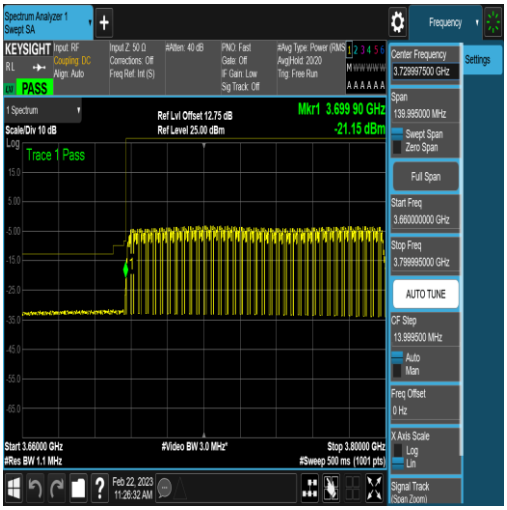
<tr><td>  </td><td>  </td></tr>
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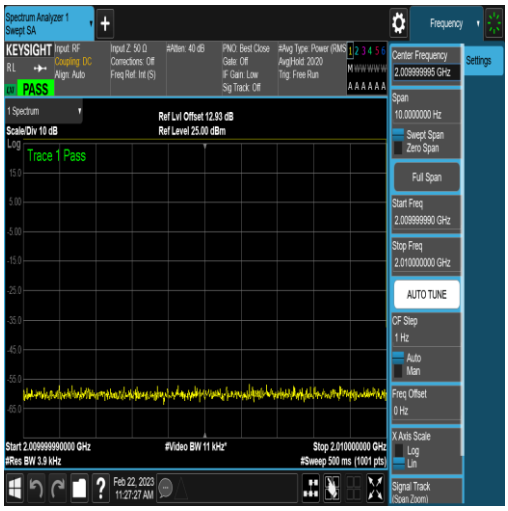
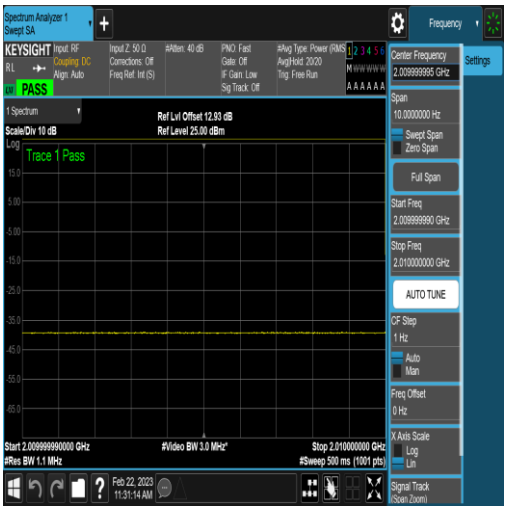
<tr><td>  </td><td>  </td></tr>
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Appendix E: Conducted Spurious Emission for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	0.009	0.15	-48.18	-43	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	0.15	30	-73.67	-33	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	30	1000	-70.81	-23	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-57.24	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-49.60	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-30.20	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-33.60	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Right	0.009	0.15	-49.23	-43	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Right	0.15	30	-72.44	-33	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Right	30	1000	-70.66	-23	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Right	1000	3000	-57.08	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Right	3000	6000	-49.96	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Right	6000	26000	-29.94	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Right	26000	40000	-33.54	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	0.009	0.15	-50.40	-43	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	0.15	30	-71.59	-33	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	30	1000	-70.54	-23	PASS
N77-3700-	30	100	DFT-QP	L	Outer_Full	1000	3000	-57.	-13	PAS

3980			SK					22		S
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	3000	6000	-42.68	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	6000	26000	-29.77	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	26000	40000	-33.57	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	0.009	0.15	-51.81	-43	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	0.15	30	-72.88	-33	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	30	1000	-70.99	-23	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	1000	3000	-57.27	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	3000	6000	-49.78	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	6000	26000	-30.14	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Left	26000	40000	-33.43	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Right	0.009	0.15	-50.48	-43	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Right	0.15	30	-72.48	-33	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Right	30	1000	-70.89	-23	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Right	1000	3000	-57.13	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Right	3000	6000	-49.93	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Right	6000	26000	-30.15	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Edge_1RB_Right	26000	40000	-33.23	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	0.009	0.15	-49.90	-43	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	0.15	30	-72.14	-33	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	30	1000	-70.28	-23	PASS
N77-3700-	30	100	CP-QP	L	Outer_Full	1000	3000	-56.	-13	PAS

3980			SK					53		S
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	3000	6000	-43.23	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	6000	26000	-29.71	-13	PASS
N77-3700-3980	30	100	CP-QPSK	L	Outer_Full	26000	40000	-33.42	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	0.009	0.15	-51.64	-43	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	0.15	30	-73.71	-33	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	30	1000	-70.66	-23	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-56.26	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	3000	6000	-49.86	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	6000	26000	-30.16	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-33.45	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Right	0.009	0.15	-51.67	-43	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Right	0.15	30	-72.27	-33	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Right	30	1000	-70.70	-23	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Right	1000	3000	-56.97	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Right	3000	6000	-49.85	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Right	6000	26000	-30.16	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Right	26000	40000	-33.55	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	0.009	0.15	-49.64	-43	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	0.15	30	-70.11	-33	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	30	1000	-70.96	-23	PASS
N77-3700-	30	100	DFT-QP	M	Outer_Full	1000	3000	-56.	-13	PAS

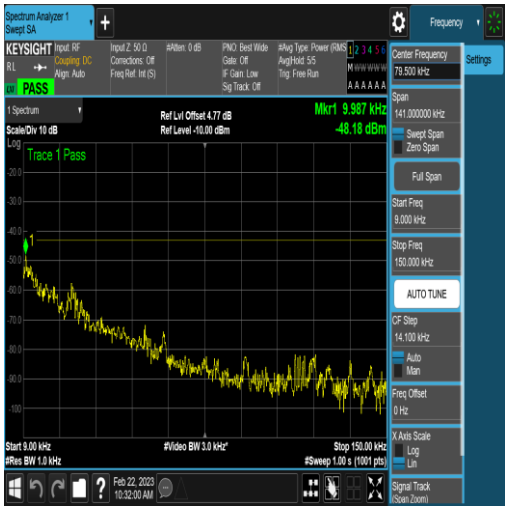
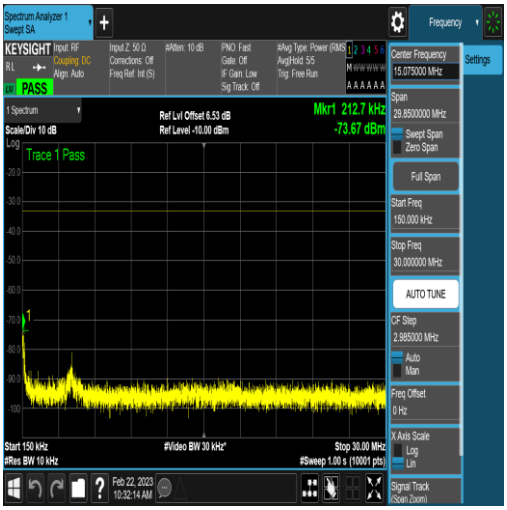
3980			SK					93		S
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	3000	6000	-42.89	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	6000	26000	-30.02	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	26000	40000	-33.48	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Left	0.009	0.15	-51.92	-43	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Left	0.15	30	-72.80	-33	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Left	30	1000	-70.38	-23	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Left	1000	3000	-56.98	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Left	3000	6000	-49.35	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Left	6000	26000	-30.07	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Left	26000	40000	-33.69	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Right	0.009	0.15	-48.43	-43	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Right	0.15	30	-72.71	-33	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Right	30	1000	-70.86	-23	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Right	1000	3000	-57.20	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Right	3000	6000	-49.71	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Right	6000	26000	-30.07	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Edge_1RB_Right	26000	40000	-33.58	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Outer_Full	0.009	0.15	-50.90	-43	PASS
N77-3700-3980	30	100	CP-QPSK	M	Outer_Full	0.15	30	-71.53	-33	PASS
N77-3700-3980	30	100	CP-QPSK	M	Outer_Full	30	1000	-70.71	-23	PASS
N77-3700-	30	100	CP-QP	M	Outer_Full	1000	3000	-57.	-13	PAS

3980			SK					38		S
N77-3700-3980	30	100	CP-QPSK	M	Outer_Full	3000	6000	-42.55	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Outer_Full	6000	26000	-29.96	-13	PASS
N77-3700-3980	30	100	CP-QPSK	M	Outer_Full	26000	40000	-33.41	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	0.009	0.15	-49.10	-43	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	0.15	30	-71.18	-33	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	30	1000	-70.40	-23	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-57.38	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-49.84	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-29.78	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-33.81	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	0.009	0.15	-49.51	-43	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	0.15	30	-72.99	-33	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	30	1000	-70.52	-23	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	1000	3000	-50.10	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	3000	6000	-50.32	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	6000	26000	-30.10	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	26000	40000	-33.73	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	0.009	0.15	-50.99	-43	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	0.15	30	-73.86	-33	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	30	1000	-70.57	-23	PASS
N77-3700-	30	100	DFT-QP	H	Outer_Full	1000	3000	-57.	-13	PAS

3980			SK					14		S
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	3000	6000	-43.80	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	6000	26000	-30.07	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	26000	40000	-33.37	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Left	0.009	0.15	-51.30	-43	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Left	0.15	30	-71.82	-33	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Left	30	1000	-70.40	-23	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Left	1000	3000	-57.28	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Left	3000	6000	-50.09	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Left	6000	26000	-30.05	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Left	26000	40000	-33.53	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	0.009	0.15	-47.94	-43	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	0.15	30	-70.54	-33	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	30	1000	-70.66	-23	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	1000	3000	-57.19	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	3000	6000	-50.20	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	6000	26000	-29.56	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Edge_1RB_Right	26000	40000	-33.65	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	0.009	0.15	-49.68	-43	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	0.15	30	-70.19	-33	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	30	1000	-70.47	-23	PASS
N77-3700-	30	100	CP-QP	H	Outer_Full	1000	3000	-57.	-13	PAS

3980			SK					14		S
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	3000	6000	-45.10	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	6000	26000	-30.21	-13	PASS
N77-3700-3980	30	100	CP-QPSK	H	Outer_Full	26000	40000	-33.39	-13	PASS

Test Graphs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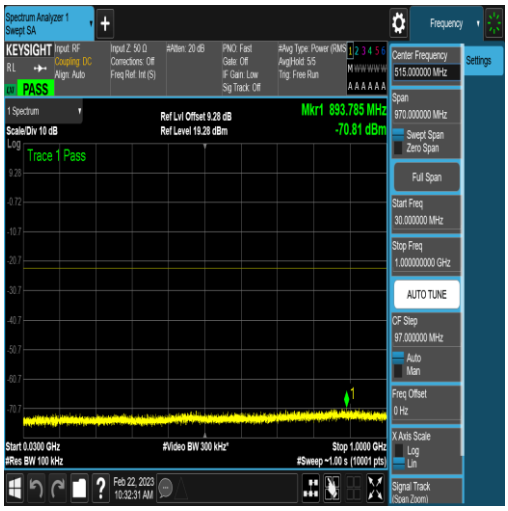	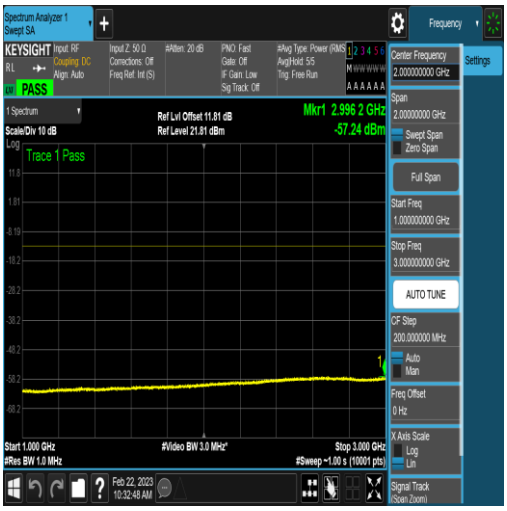
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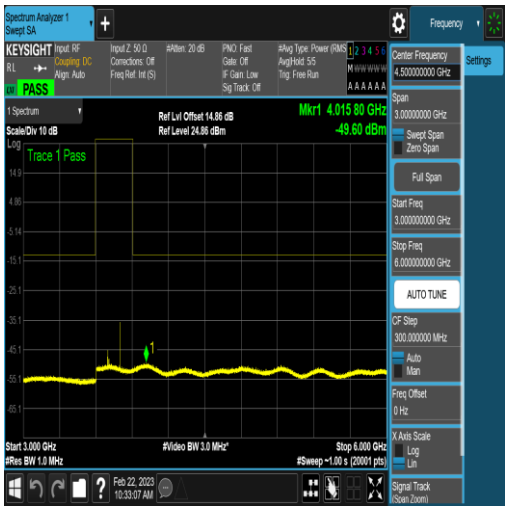

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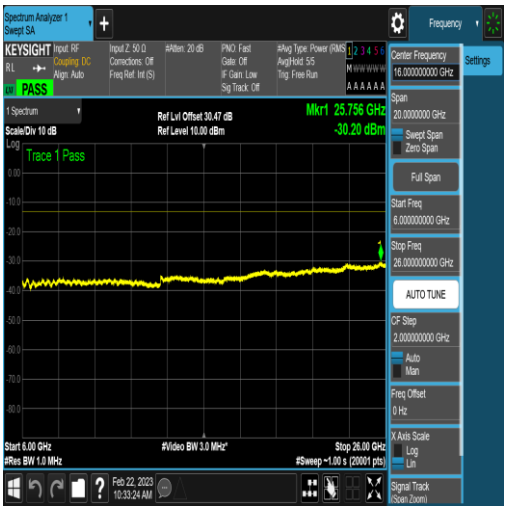

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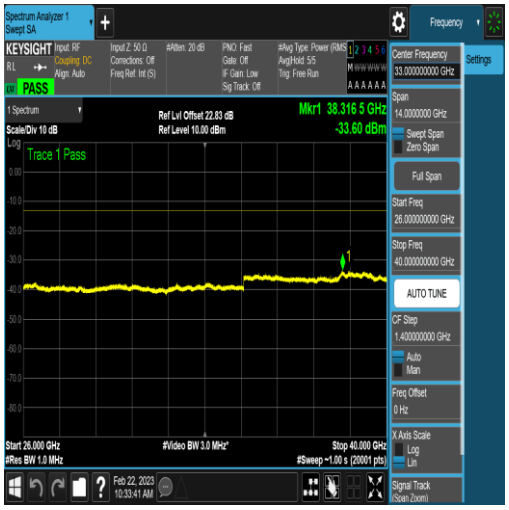

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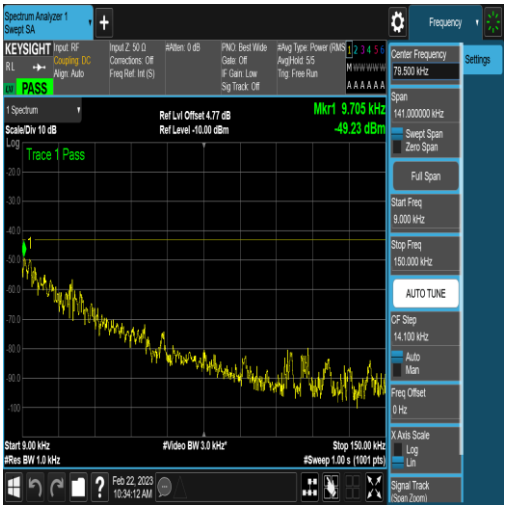

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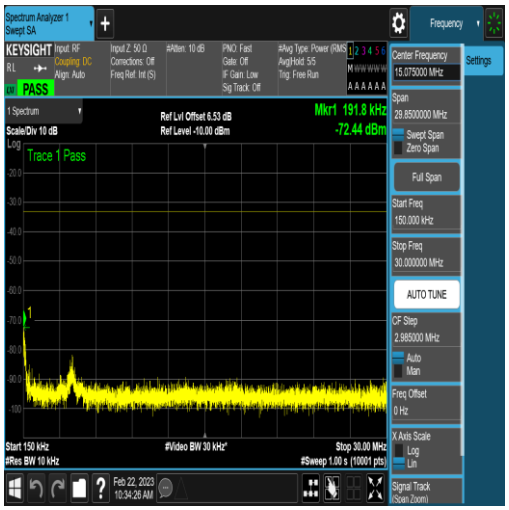

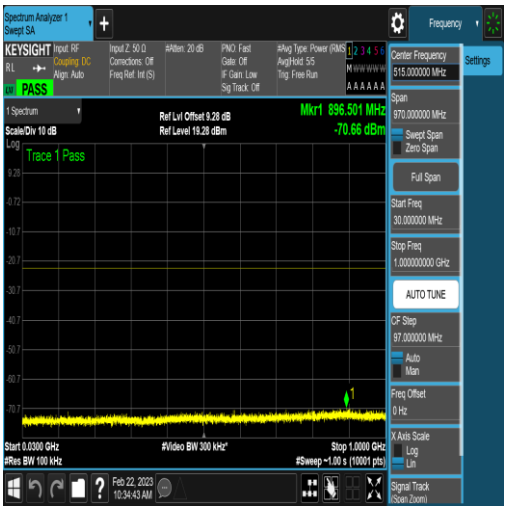

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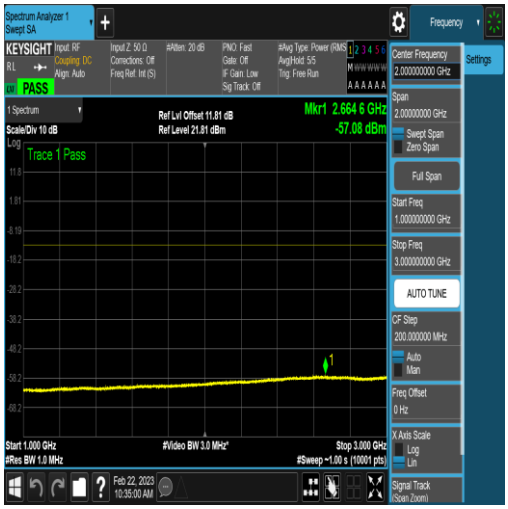	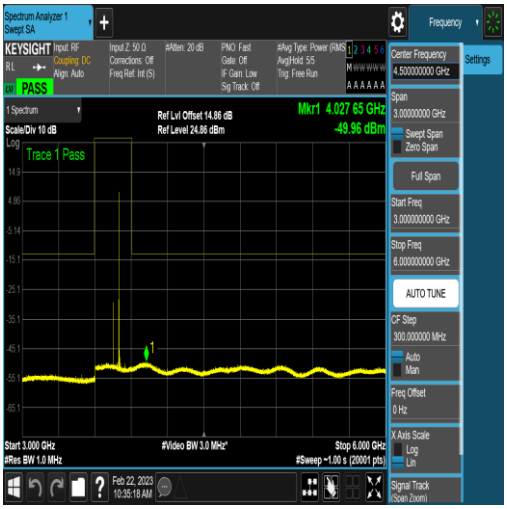
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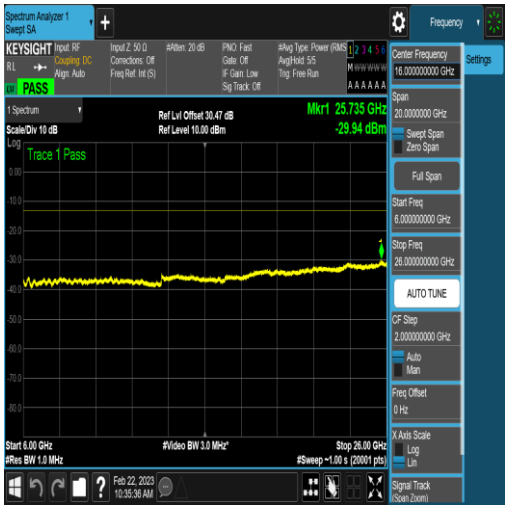	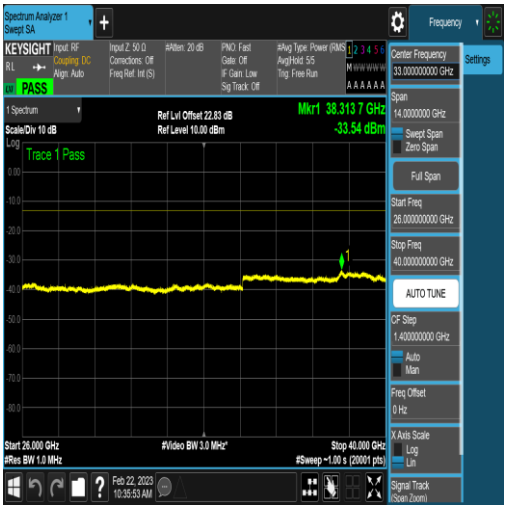
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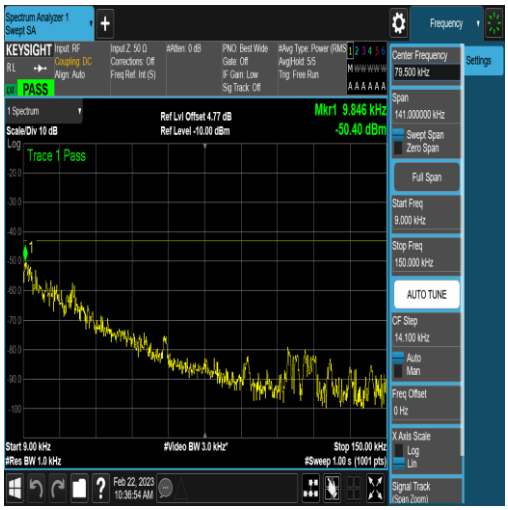

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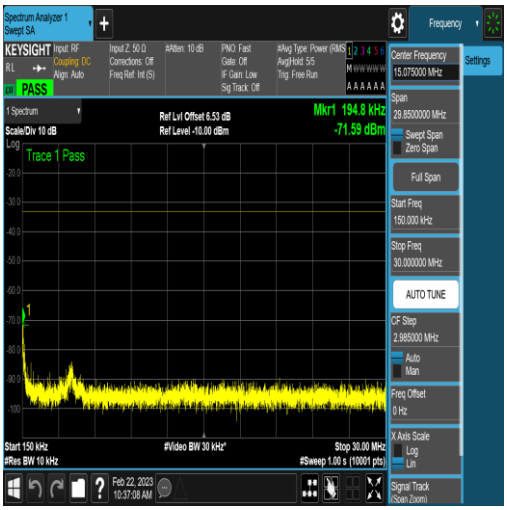

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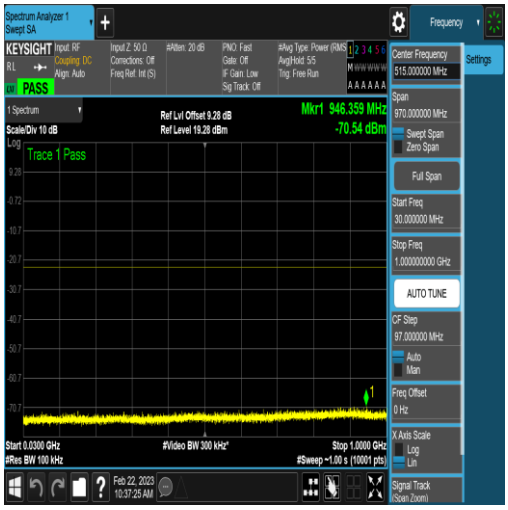

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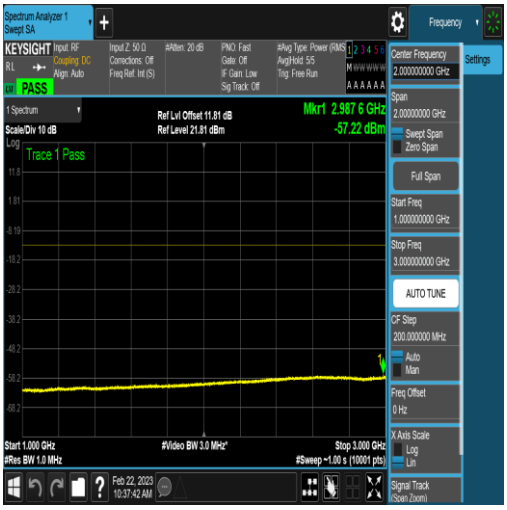

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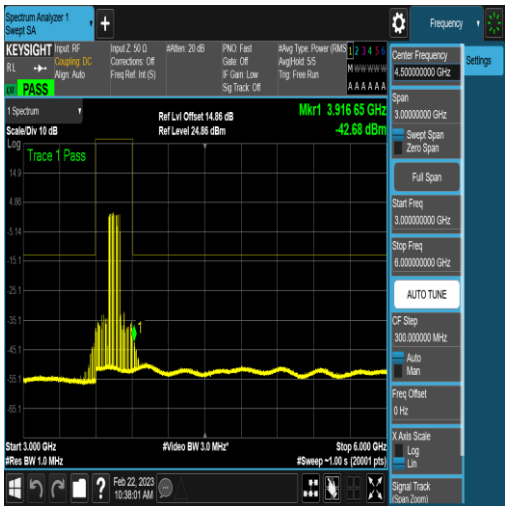

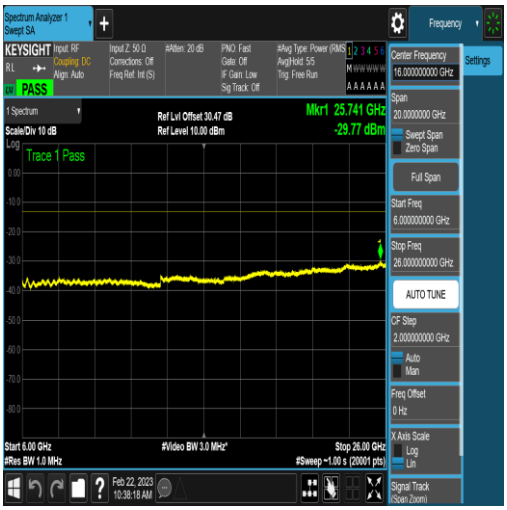

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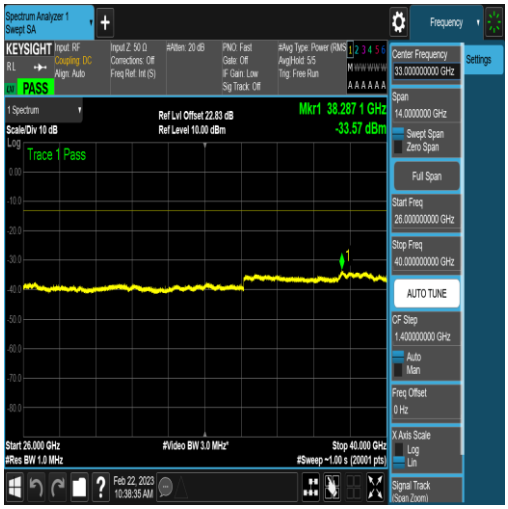	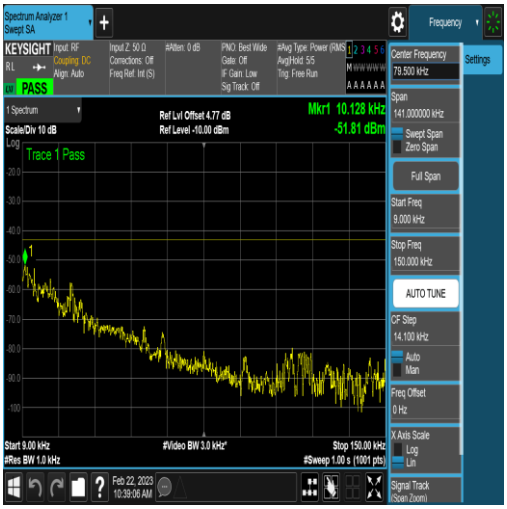
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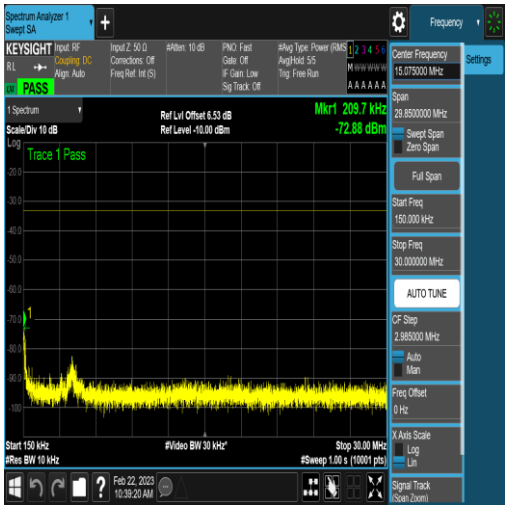	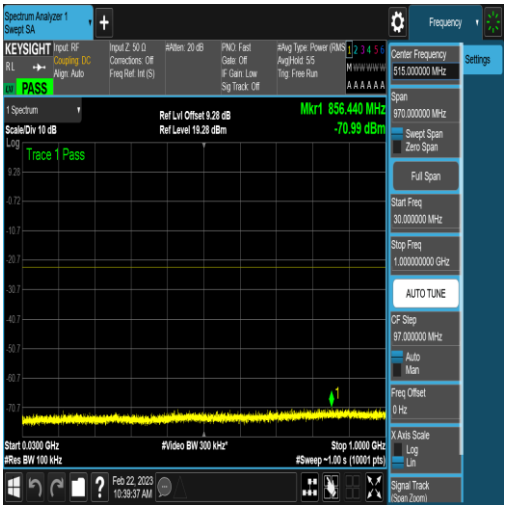
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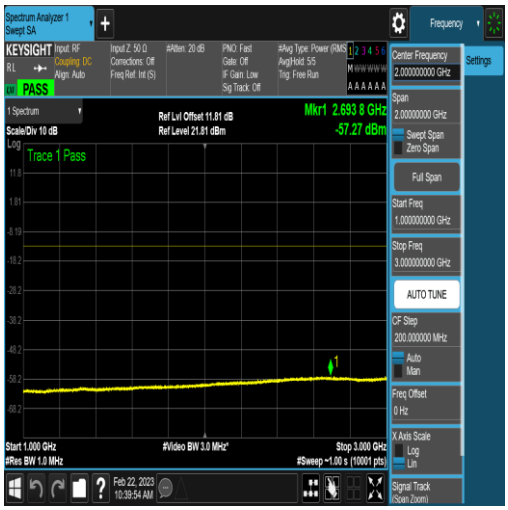

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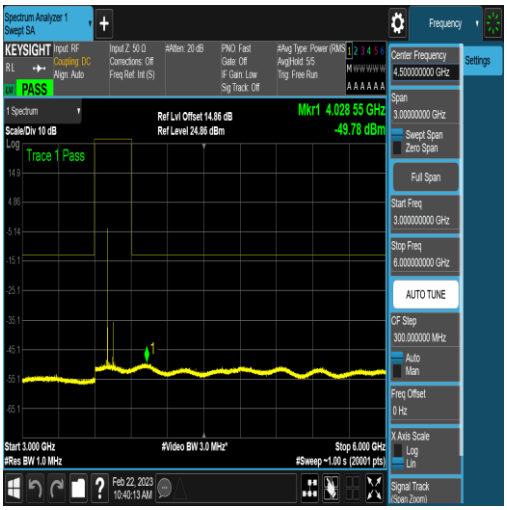

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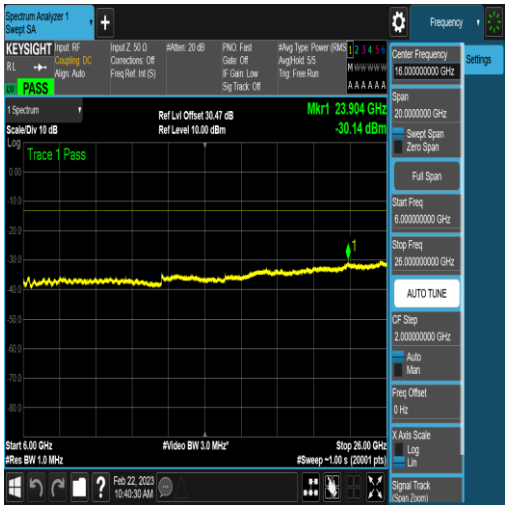

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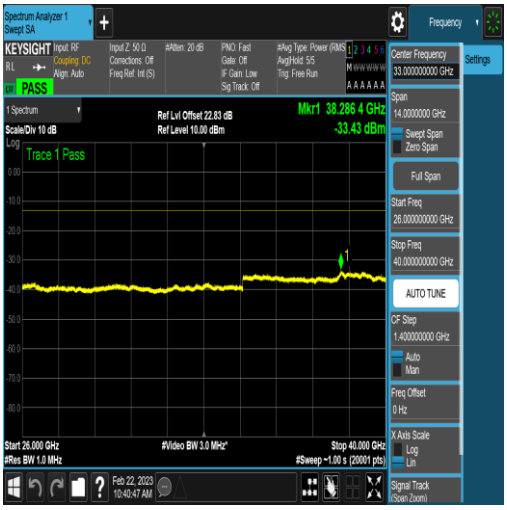

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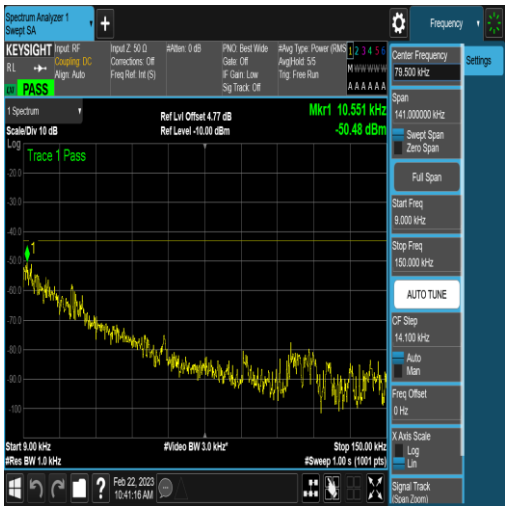

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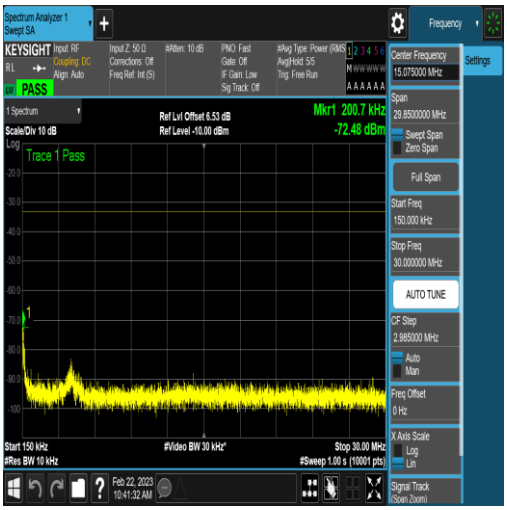

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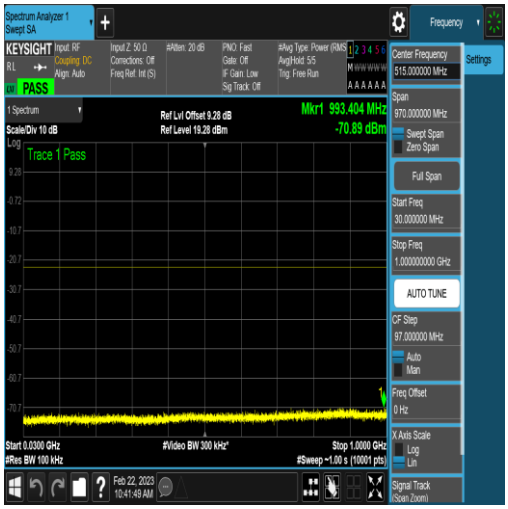	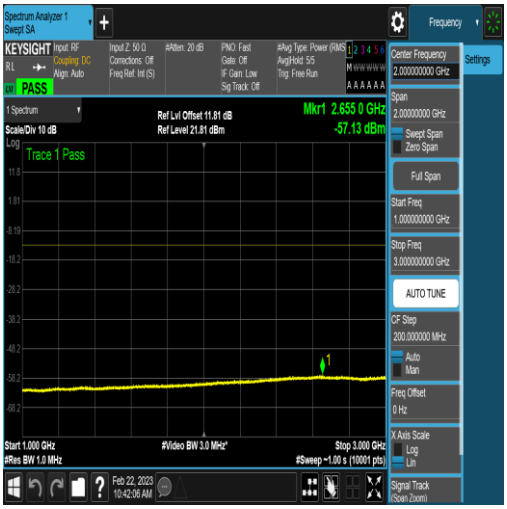
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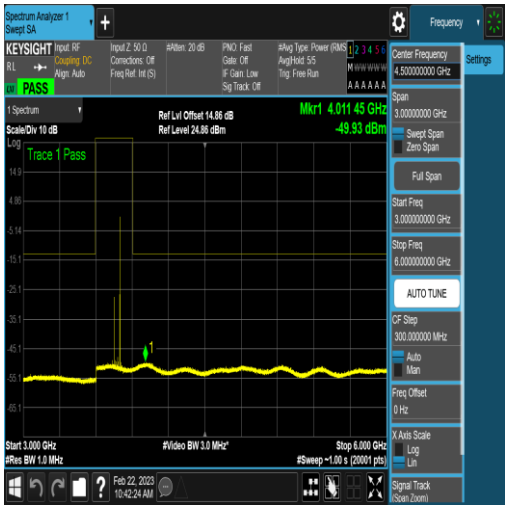	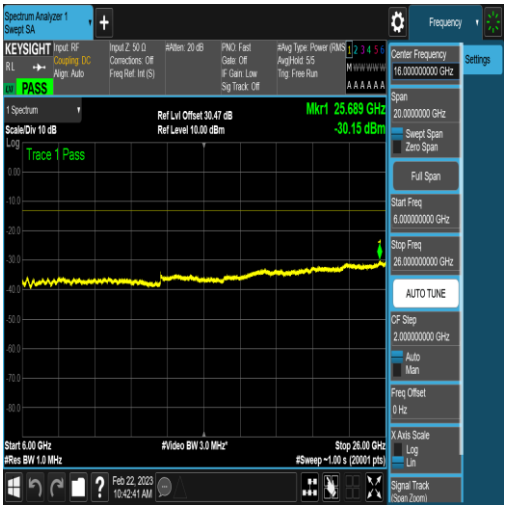
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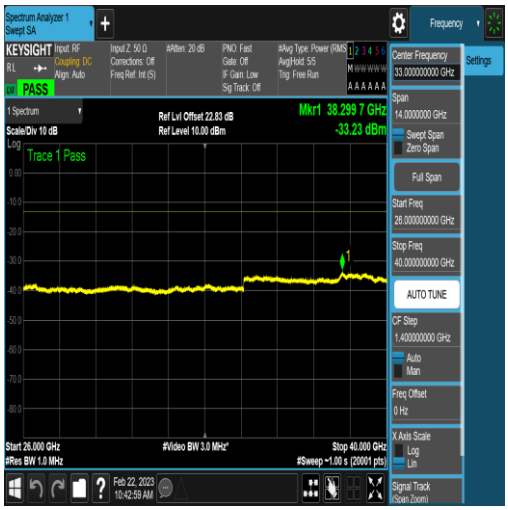

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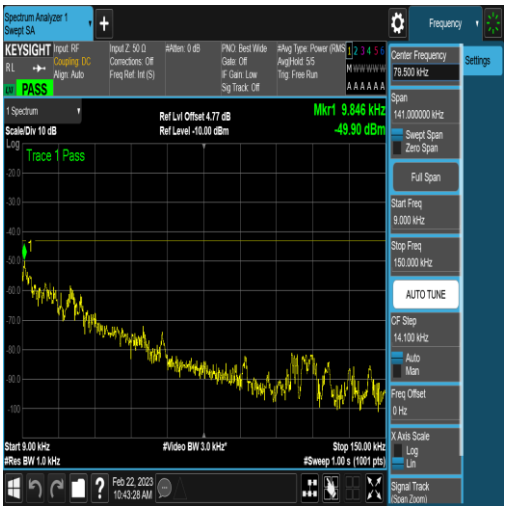

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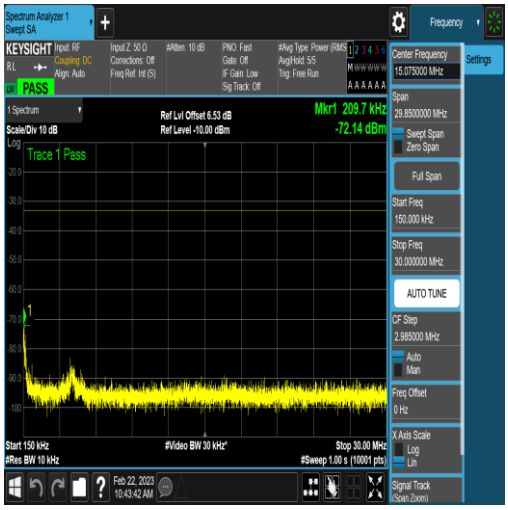

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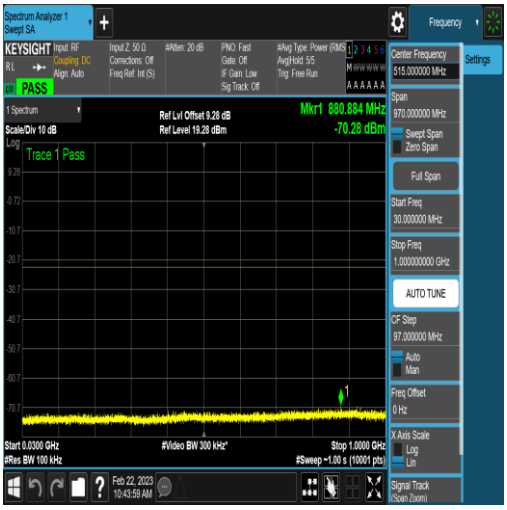

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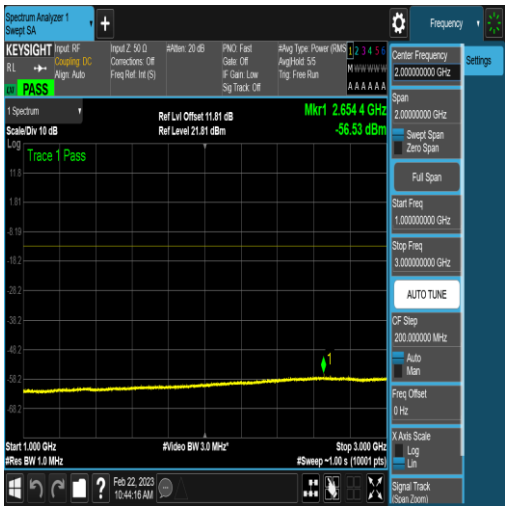

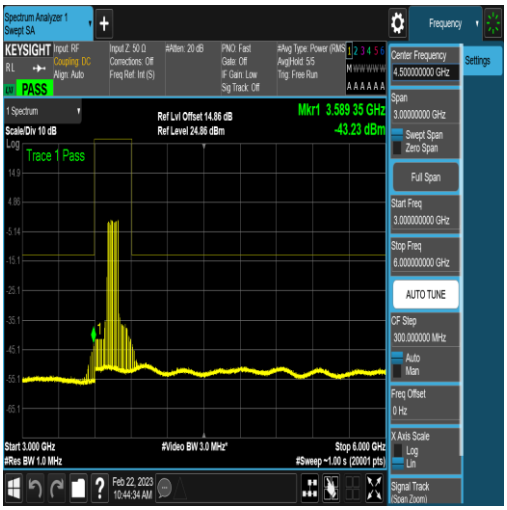

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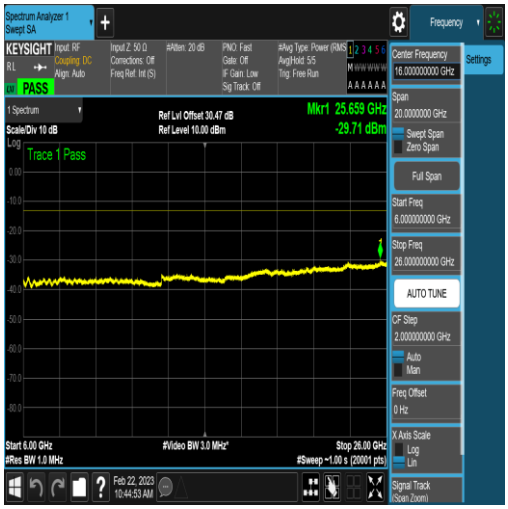	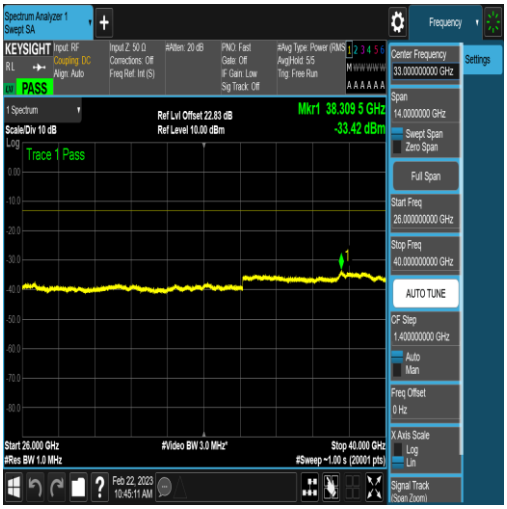
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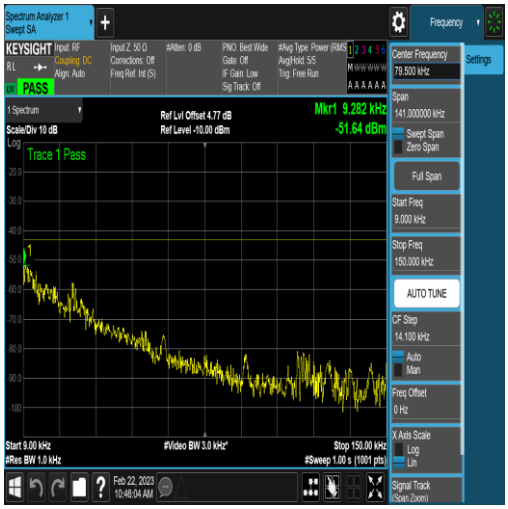	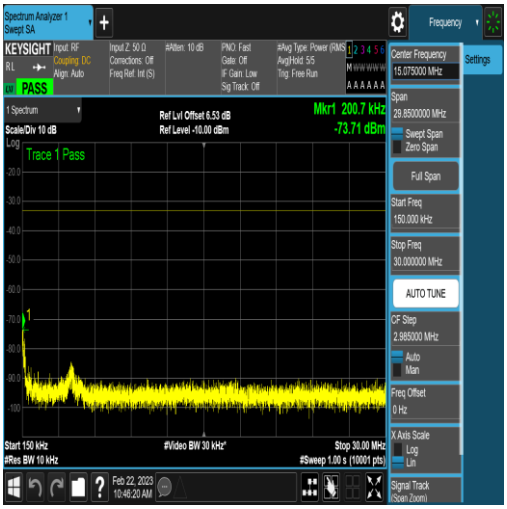
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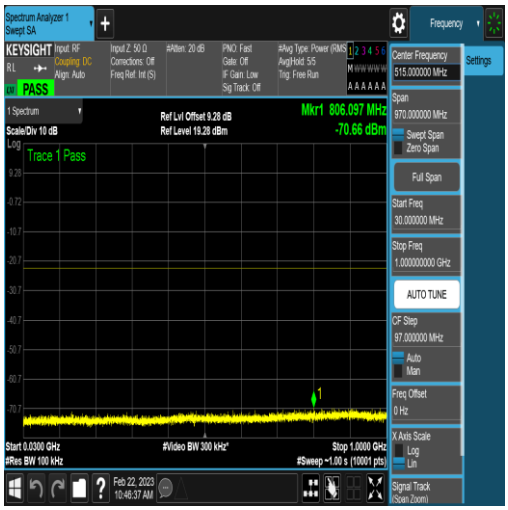

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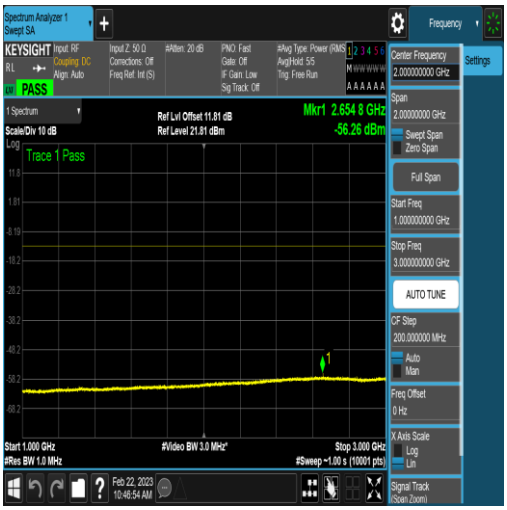

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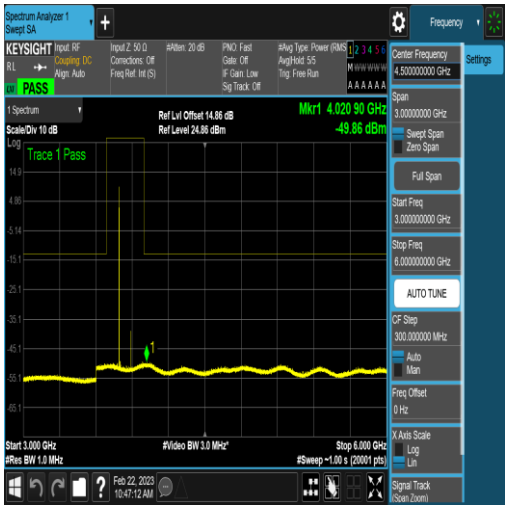

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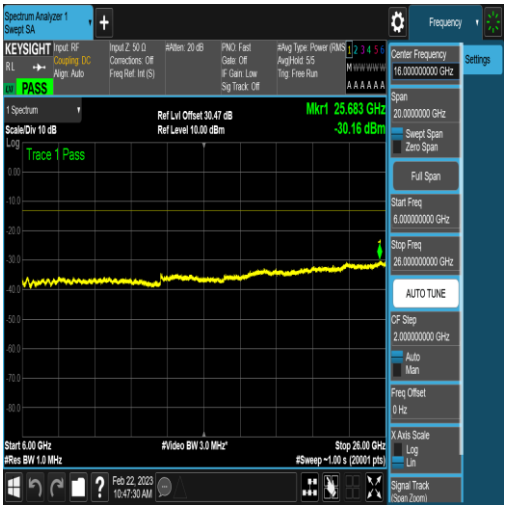

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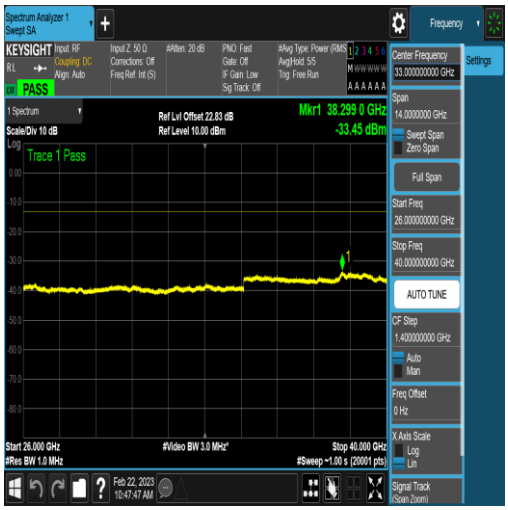

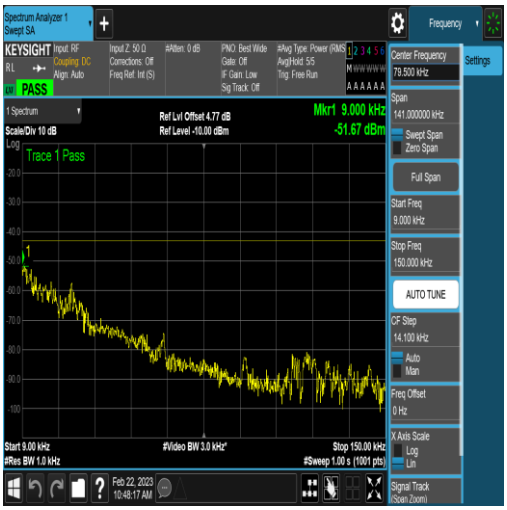

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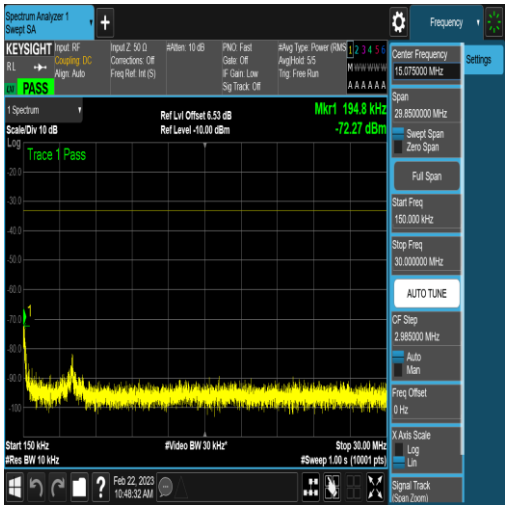	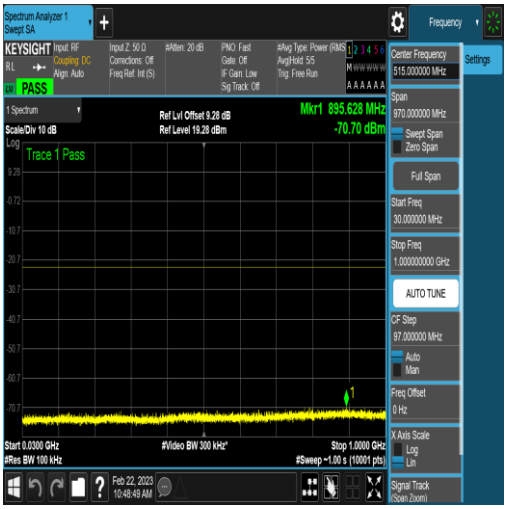
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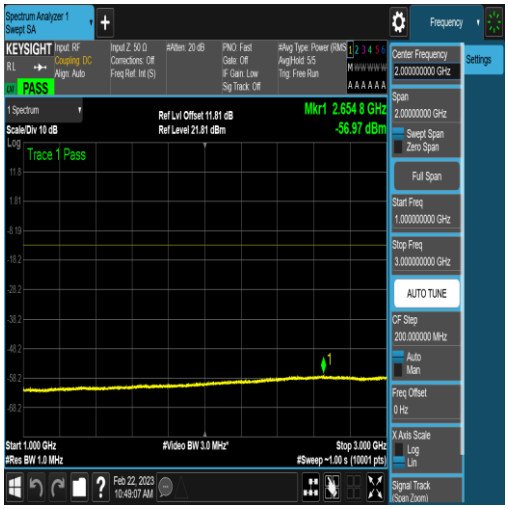	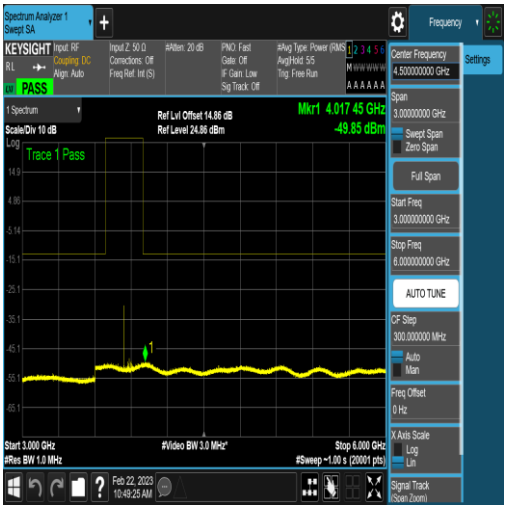
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