

Appendix for N41

Product Name: CSX8

Model No: LGT-08QA-2301

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	23.71	PC2	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	23.89	PC2	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	26.72	PC2	PASS
N41	30	20	DFT-PI2BPSK	L	Inner_Full	27.20	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	23.69	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	23.71	PC2	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	26.23	PC2	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	27.21	PC2	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Left	24.27	PC2	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Right	24.42	PC2	PASS
N41	30	20	DFT-16QAM	L	Outer_Full	25.23	PC2	PASS
N41	30	20	DFT-16QAM	L	Inner_Full	26.14	PC2	PASS
N41	30	20	DFT-64QAM	L	Edge_1RB_Left	23.70	PC2	PASS
N41	30	20	DFT-64QAM	L	Edge_1RB_Right	23.80	PC2	PASS
N41	30	20	DFT-64QAM	L	Outer_Full	24.74	PC2	PASS
N41	30	20	DFT-64QAM	L	Inner_Full	24.77	PC2	PASS
N41	30	20	DFT-256QAM	L	Edge_1RB_Left	22.99	PC2	PASS
N41	30	20	DFT-256QAM	L	Edge_1RB_Right	22.93	PC2	PASS
N41	30	20	DFT-256QAM	L	Outer_Full	22.80	PC2	PASS
N41	30	20	DFT-256QAM	L	Inner_Full	22.68	PC2	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	23.55	PC2	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	23.69	PC2	PASS
N41	30	20	CP-QPSK	L	Outer_Full	24.25	PC2	PASS
N41	30	20	CP-QPSK	L	Inner_Full	24.16	PC2	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Left	23.27	PC2	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Right	23.60	PC2	PASS
N41	30	20	CP-16QAM	L	Outer_Full	24.20	PC2	PASS
N41	30	20	CP-16QAM	L	Inner_Full	24.18	PC2	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Left	24.05	PC2	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Right	24.23	PC2	PASS
N41	30	20	CP-64QAM	L	Outer_Full	23.78	PC2	PASS
N41	30	20	CP-64QAM	L	Inner_Full	23.77	PC2	PASS
N41	30	20	CP-256QAM	L	Edge_1RB_Left	21.18	PC2	PASS

N41	30	20	CP-256QAM	L	Edge_1RB_Right	21.17	PC2	PASS
N41	30	20	CP-256QAM	L	Outer_Full	20.71	PC2	PASS
N41	30	20	CP-256QAM	L	Inner_Full	20.72	PC2	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	23.87	PC2	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	23.96	PC2	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	26.96	PC2	PASS
N41	30	20	DFT-PI2BPSK	M	Inner_Full	27.51	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	23.84	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	23.93	PC2	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	26.49	PC2	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	27.43	PC2	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Left	24.49	PC2	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Right	24.54	PC2	PASS
N41	30	20	DFT-16QAM	M	Outer_Full	25.53	PC2	PASS
N41	30	20	DFT-16QAM	M	Inner_Full	26.53	PC2	PASS
N41	30	20	DFT-64QAM	M	Edge_1RB_Left	23.88	PC2	PASS
N41	30	20	DFT-64QAM	M	Edge_1RB_Right	24.06	PC2	PASS
N41	30	20	DFT-64QAM	M	Outer_Full	25.06	PC2	PASS
N41	30	20	DFT-64QAM	M	Inner_Full	25.00	PC2	PASS
N41	30	20	DFT-256QAM	M	Edge_1RB_Left	22.92	PC2	PASS
N41	30	20	DFT-256QAM	M	Edge_1RB_Right	22.86	PC2	PASS
N41	30	20	DFT-256QAM	M	Outer_Full	22.98	PC2	PASS
N41	30	20	DFT-256QAM	M	Inner_Full	22.96	PC2	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	24.00	PC2	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	23.90	PC2	PASS
N41	30	20	CP-QPSK	M	Outer_Full	24.59	PC2	PASS
N41	30	20	CP-QPSK	M	Inner_Full	24.54	PC2	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Left	24.12	PC2	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Right	24.17	PC2	PASS
N41	30	20	CP-16QAM	M	Outer_Full	24.46	PC2	PASS
N41	30	20	CP-16QAM	M	Inner_Full	24.63	PC2	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Left	24.37	PC2	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Right	24.29	PC2	PASS
N41	30	20	CP-64QAM	M	Outer_Full	24.08	PC2	PASS
N41	30	20	CP-64QAM	M	Inner_Full	24.11	PC2	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Left	20.96	PC2	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Right	21.26	PC2	PASS
N41	30	20	CP-256QAM	M	Outer_Full	20.97	PC2	PASS
N41	30	20	CP-256QAM	M	Inner_Full	21.10	PC2	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	24.37	PC2	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	24.55	PC2	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	27.51	PC2	PASS

N41	30	20	DFT-PI2BPSK	H	Inner_Full	28.05	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	24.40	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	24.60	PC2	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	27.07	PC2	PASS
N41	30	20	DFT-QPSK	H	Inner_Full	27.16	PC2	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Left	24.30	PC2	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Right	24.61	PC2	PASS
N41	30	20	DFT-16QAM	H	Outer_Full	26.15	PC2	PASS
N41	30	20	DFT-16QAM	H	Inner_Full	27.12	PC2	PASS
N41	30	20	DFT-64QAM	H	Edge_1RB_Left	24.57	PC2	PASS
N41	30	20	DFT-64QAM	H	Edge_1RB_Right	24.78	PC2	PASS
N41	30	20	DFT-64QAM	H	Outer_Full	25.66	PC2	PASS
N41	30	20	DFT-64QAM	H	Inner_Full	25.62	PC2	PASS
N41	30	20	DFT-256QAM	H	Edge_1RB_Left	23.48	PC2	PASS
N41	30	20	DFT-256QAM	H	Edge_1RB_Right	23.66	PC2	PASS
N41	30	20	DFT-256QAM	H	Outer_Full	23.59	PC2	PASS
N41	30	20	DFT-256QAM	H	Inner_Full	23.62	PC2	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	24.43	PC2	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	24.64	PC2	PASS
N41	30	20	CP-QPSK	H	Outer_Full	25.15	PC2	PASS
N41	30	20	CP-QPSK	H	Inner_Full	25.10	PC2	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Left	24.26	PC2	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Right	24.17	PC2	PASS
N41	30	20	CP-16QAM	H	Outer_Full	25.07	PC2	PASS
N41	30	20	CP-16QAM	H	Inner_Full	25.28	PC2	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Left	24.97	PC2	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Right	25.16	PC2	PASS
N41	30	20	CP-64QAM	H	Outer_Full	24.64	PC2	PASS
N41	30	20	CP-64QAM	H	Inner_Full	24.65	PC2	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Left	21.89	PC2	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Right	22.02	PC2	PASS
N41	30	20	CP-256QAM	H	Outer_Full	21.73	PC2	PASS
N41	30	20	CP-256QAM	H	Inner_Full	21.67	PC2	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	23.66	PC2	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	23.81	PC2	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	26.81	PC2	PASS
N41	30	40	DFT-PI2BPSK	L	Inner_Full	27.32	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	23.78	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Right	23.81	PC2	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	26.28	PC2	PASS
N41	30	40	DFT-QPSK	L	Inner_Full	27.31	PC2	PASS
N41	30	40	DFT-16QAM	L	Edge_1RB_Left	23.94	PC2	PASS

N41	30	40	DFT-16QAM	L	Edge_1RB_Right	23.98	PC2	PASS
N41	30	40	DFT-16QAM	L	Outer_Full	25.34	PC2	PASS
N41	30	40	DFT-16QAM	L	Inner_Full	26.24	PC2	PASS
N41	30	40	DFT-64QAM	L	Edge_1RB_Left	23.77	PC2	PASS
N41	30	40	DFT-64QAM	L	Edge_1RB_Right	23.79	PC2	PASS
N41	30	40	DFT-64QAM	L	Outer_Full	24.86	PC2	PASS
N41	30	40	DFT-64QAM	L	Inner_Full	24.88	PC2	PASS
N41	30	40	DFT-256QAM	L	Edge_1RB_Left	22.76	PC2	PASS
N41	30	40	DFT-256QAM	L	Edge_1RB_Right	22.92	PC2	PASS
N41	30	40	DFT-256QAM	L	Outer_Full	22.90	PC2	PASS
N41	30	40	DFT-256QAM	L	Inner_Full	22.83	PC2	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	23.77	PC2	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	23.77	PC2	PASS
N41	30	40	CP-QPSK	L	Outer_Full	24.32	PC2	PASS
N41	30	40	CP-QPSK	L	Inner_Full	25.80	PC2	PASS
N41	30	40	CP-16QAM	L	Edge_1RB_Left	23.58	PC2	PASS
N41	30	40	CP-16QAM	L	Edge_1RB_Right	23.64	PC2	PASS
N41	30	40	CP-16QAM	L	Outer_Full	24.34	PC2	PASS
N41	30	40	CP-16QAM	L	Inner_Full	25.34	PC2	PASS
N41	30	40	CP-64QAM	L	Edge_1RB_Left	23.88	PC2	PASS
N41	30	40	CP-64QAM	L	Edge_1RB_Right	24.20	PC2	PASS
N41	30	40	CP-64QAM	L	Outer_Full	23.84	PC2	PASS
N41	30	40	CP-64QAM	L	Inner_Full	23.86	PC2	PASS
N41	30	40	CP-256QAM	L	Edge_1RB_Left	21.19	PC2	PASS
N41	30	40	CP-256QAM	L	Edge_1RB_Right	21.28	PC2	PASS
N41	30	40	CP-256QAM	L	Outer_Full	20.83	PC2	PASS
N41	30	40	CP-256QAM	L	Inner_Full	20.80	PC2	PASS
N41	30	40	DFT-PI2BPSK	M	Edge_1RB_Left	23.92	PC2	PASS
N41	30	40	DFT-PI2BPSK	M	Edge_1RB_Right	24.21	PC2	PASS
N41	30	40	DFT-PI2BPSK	M	Outer_Full	27.00	PC2	PASS
N41	30	40	DFT-PI2BPSK	M	Inner_Full	27.48	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	23.90	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	24.26	PC2	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	26.52	PC2	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	27.48	PC2	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Left	23.64	PC2	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Right	24.08	PC2	PASS
N41	30	40	DFT-16QAM	M	Outer_Full	25.56	PC2	PASS
N41	30	40	DFT-16QAM	M	Inner_Full	26.45	PC2	PASS
N41	30	40	DFT-64QAM	M	Edge_1RB_Left	23.96	PC2	PASS
N41	30	40	DFT-64QAM	M	Edge_1RB_Right	24.40	PC2	PASS
N41	30	40	DFT-64QAM	M	Outer_Full	25.06	PC2	PASS

N41	30	40	DFT-64QAM	M	Inner_Full	25.14	PC2	PASS
N41	30	40	DFT-256QAM	M	Edge_1RB_Left	22.87	PC2	PASS
N41	30	40	DFT-256QAM	M	Edge_1RB_Right	23.27	PC2	PASS
N41	30	40	DFT-256QAM	M	Outer_Full	23.08	PC2	PASS
N41	30	40	DFT-256QAM	M	Inner_Full	23.05	PC2	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	23.80	PC2	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	24.23	PC2	PASS
N41	30	40	CP-QPSK	M	Outer_Full	24.57	PC2	PASS
N41	30	40	CP-QPSK	M	Inner_Full	26.02	PC2	PASS
N41	30	40	CP-16QAM	M	Edge_1RB_Left	24.10	PC2	PASS
N41	30	40	CP-16QAM	M	Edge_1RB_Right	24.44	PC2	PASS
N41	30	40	CP-16QAM	M	Outer_Full	24.59	PC2	PASS
N41	30	40	CP-16QAM	M	Inner_Full	25.43	PC2	PASS
N41	30	40	CP-64QAM	M	Edge_1RB_Left	23.93	PC2	PASS
N41	30	40	CP-64QAM	M	Edge_1RB_Right	24.34	PC2	PASS
N41	30	40	CP-64QAM	M	Outer_Full	24.09	PC2	PASS
N41	30	40	CP-64QAM	M	Inner_Full	24.09	PC2	PASS
N41	30	40	CP-256QAM	M	Edge_1RB_Left	21.29	PC2	PASS
N41	30	40	CP-256QAM	M	Edge_1RB_Right	21.80	PC2	PASS
N41	30	40	CP-256QAM	M	Outer_Full	21.03	PC2	PASS
N41	30	40	CP-256QAM	M	Inner_Full	21.08	PC2	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	24.35	PC2	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	24.69	PC2	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	27.50	PC2	PASS
N41	30	40	DFT-PI2BPSK	H	Inner_Full	27.92	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	24.41	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Right	24.72	PC2	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	26.98	PC2	PASS
N41	30	40	DFT-QPSK	H	Inner_Full	27.94	PC2	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Left	24.65	PC2	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Right	24.89	PC2	PASS
N41	30	40	DFT-16QAM	H	Outer_Full	26.07	PC2	PASS
N41	30	40	DFT-16QAM	H	Inner_Full	26.92	PC2	PASS
N41	30	40	DFT-64QAM	H	Edge_1RB_Left	24.37	PC2	PASS
N41	30	40	DFT-64QAM	H	Edge_1RB_Right	24.64	PC2	PASS
N41	30	40	DFT-64QAM	H	Outer_Full	25.60	PC2	PASS
N41	30	40	DFT-64QAM	H	Inner_Full	25.50	PC2	PASS
N41	30	40	DFT-256QAM	H	Edge_1RB_Left	23.46	PC2	PASS
N41	30	40	DFT-256QAM	H	Edge_1RB_Right	23.75	PC2	PASS
N41	30	40	DFT-256QAM	H	Outer_Full	23.57	PC2	PASS
N41	30	40	DFT-256QAM	H	Inner_Full	23.51	PC2	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	24.48	PC2	PASS

N41	30	40	CP-QPSK	H	Edge_1RB_Right	24.56	PC2	PASS
N41	30	40	CP-QPSK	H	Outer_Full	25.06	PC2	PASS
N41	30	40	CP-QPSK	H	Inner_Full	26.51	PC2	PASS
N41	30	40	CP-16QAM	H	Edge_1RB_Left	24.31	PC2	PASS
N41	30	40	CP-16QAM	H	Edge_1RB_Right	24.59	PC2	PASS
N41	30	40	CP-16QAM	H	Outer_Full	25.06	PC2	PASS
N41	30	40	CP-16QAM	H	Inner_Full	25.98	PC2	PASS
N41	30	40	CP-64QAM	H	Edge_1RB_Left	25.11	PC2	PASS
N41	30	40	CP-64QAM	H	Edge_1RB_Right	25.15	PC2	PASS
N41	30	40	CP-64QAM	H	Outer_Full	24.50	PC2	PASS
N41	30	40	CP-64QAM	H	Inner_Full	24.54	PC2	PASS
N41	30	40	CP-256QAM	H	Edge_1RB_Left	21.90	PC2	PASS
N41	30	40	CP-256QAM	H	Edge_1RB_Right	22.19	PC2	PASS
N41	30	40	CP-256QAM	H	Outer_Full	21.54	PC2	PASS
N41	30	40	CP-256QAM	H	Inner_Full	21.56	PC2	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	23.47	PC2	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	23.61	PC2	PASS
N41	30	60	DFT-PI2BPSK	L	Outer_Full	26.79	PC2	PASS
N41	30	60	DFT-PI2BPSK	L	Inner_Full	27.28	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	23.41	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Right	23.72	PC2	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	26.35	PC2	PASS
N41	30	60	DFT-QPSK	L	Inner_Full	27.26	PC2	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Left	23.66	PC2	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Right	23.67	PC2	PASS
N41	30	60	DFT-16QAM	L	Outer_Full	25.17	PC2	PASS
N41	30	60	DFT-16QAM	L	Inner_Full	26.26	PC2	PASS
N41	30	60	DFT-64QAM	L	Edge_1RB_Left	23.35	PC2	PASS
N41	30	60	DFT-64QAM	L	Edge_1RB_Right	23.84	PC2	PASS
N41	30	60	DFT-64QAM	L	Outer_Full	24.85	PC2	PASS
N41	30	60	DFT-64QAM	L	Inner_Full	24.87	PC2	PASS
N41	30	60	DFT-256QAM	L	Edge_1RB_Left	22.37	PC2	PASS
N41	30	60	DFT-256QAM	L	Edge_1RB_Right	22.59	PC2	PASS
N41	30	60	DFT-256QAM	L	Outer_Full	22.84	PC2	PASS
N41	30	60	DFT-256QAM	L	Inner_Full	22.83	PC2	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	23.47	PC2	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Right	23.63	PC2	PASS
N41	30	60	CP-QPSK	L	Outer_Full	24.29	PC2	PASS
N41	30	60	CP-QPSK	L	Inner_Full	25.73	PC2	PASS
N41	30	60	CP-16QAM	L	Edge_1RB_Left	23.31	PC2	PASS
N41	30	60	CP-16QAM	L	Edge_1RB_Right	23.49	PC2	PASS
N41	30	60	CP-16QAM	L	Outer_Full	24.31	PC2	PASS

N41	30	60	CP-16QAM	L	Inner_Full	25.32	PC2	PASS
N41	30	60	CP-64QAM	L	Edge_1RB_Left	23.58	PC2	PASS
N41	30	60	CP-64QAM	L	Edge_1RB_Right	23.71	PC2	PASS
N41	30	60	CP-64QAM	L	Outer_Full	23.85	PC2	PASS
N41	30	60	CP-64QAM	L	Inner_Full	23.80	PC2	PASS
N41	30	60	CP-256QAM	L	Edge_1RB_Left	20.85	PC2	PASS
N41	30	60	CP-256QAM	L	Edge_1RB_Right	20.93	PC2	PASS
N41	30	60	CP-256QAM	L	Outer_Full	20.80	PC2	PASS
N41	30	60	CP-256QAM	L	Inner_Full	20.81	PC2	PASS
N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Left	23.53	PC2	PASS
N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Right	24.03	PC2	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	27.01	PC2	PASS
N41	30	60	DFT-PI2BPSK	M	Inner_Full	27.48	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	23.62	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Right	24.06	PC2	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	26.46	PC2	PASS
N41	30	60	DFT-QPSK	M	Inner_Full	27.47	PC2	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Left	23.74	PC2	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Right	24.21	PC2	PASS
N41	30	60	DFT-16QAM	M	Outer_Full	25.48	PC2	PASS
N41	30	60	DFT-16QAM	M	Inner_Full	26.44	PC2	PASS
N41	30	60	DFT-64QAM	M	Edge_1RB_Left	23.57	PC2	PASS
N41	30	60	DFT-64QAM	M	Edge_1RB_Right	24.01	PC2	PASS
N41	30	60	DFT-64QAM	M	Outer_Full	24.95	PC2	PASS
N41	30	60	DFT-64QAM	M	Inner_Full	24.97	PC2	PASS
N41	30	60	DFT-256QAM	M	Edge_1RB_Left	22.49	PC2	PASS
N41	30	60	DFT-256QAM	M	Edge_1RB_Right	23.19	PC2	PASS
N41	30	60	DFT-256QAM	M	Outer_Full	22.97	PC2	PASS
N41	30	60	DFT-256QAM	M	Inner_Full	22.97	PC2	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	23.69	PC2	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	24.05	PC2	PASS
N41	30	60	CP-QPSK	M	Outer_Full	24.50	PC2	PASS
N41	30	60	CP-QPSK	M	Inner_Full	25.98	PC2	PASS
N41	30	60	CP-16QAM	M	Edge_1RB_Left	23.49	PC2	PASS
N41	30	60	CP-16QAM	M	Edge_1RB_Right	23.91	PC2	PASS
N41	30	60	CP-16QAM	M	Outer_Full	24.51	PC2	PASS
N41	30	60	CP-16QAM	M	Inner_Full	25.47	PC2	PASS
N41	30	60	CP-64QAM	M	Edge_1RB_Left	24.14	PC2	PASS
N41	30	60	CP-64QAM	M	Edge_1RB_Right	24.46	PC2	PASS
N41	30	60	CP-64QAM	M	Outer_Full	24.04	PC2	PASS
N41	30	60	CP-64QAM	M	Inner_Full	24.02	PC2	PASS
N41	30	60	CP-256QAM	M	Edge_1RB_Left	20.92	PC2	PASS

N41	30	60	CP-256QAM	M	Edge_1RB_Right	21.46	PC2	PASS
N41	30	60	CP-256QAM	M	Outer_Full	21.06	PC2	PASS
N41	30	60	CP-256QAM	M	Inner_Full	20.97	PC2	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	24.04	PC2	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	24.45	PC2	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	27.33	PC2	PASS
N41	30	60	DFT-PI2BPSK	H	Inner_Full	27.83	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	24.10	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Right	24.47	PC2	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	26.88	PC2	PASS
N41	30	60	DFT-QPSK	H	Inner_Full	27.81	PC2	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Left	24.26	PC2	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Right	24.77	PC2	PASS
N41	30	60	DFT-16QAM	H	Outer_Full	25.83	PC2	PASS
N41	30	60	DFT-16QAM	H	Inner_Full	26.80	PC2	PASS
N41	30	60	DFT-64QAM	H	Edge_1RB_Left	24.08	PC2	PASS
N41	30	60	DFT-64QAM	H	Edge_1RB_Right	24.46	PC2	PASS
N41	30	60	DFT-64QAM	H	Outer_Full	25.32	PC2	PASS
N41	30	60	DFT-64QAM	H	Inner_Full	25.39	PC2	PASS
N41	30	60	DFT-256QAM	H	Edge_1RB_Left	23.05	PC2	PASS
N41	30	60	DFT-256QAM	H	Edge_1RB_Right	23.49	PC2	PASS
N41	30	60	DFT-256QAM	H	Outer_Full	23.42	PC2	PASS
N41	30	60	DFT-256QAM	H	Inner_Full	23.36	PC2	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	24.11	PC2	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	24.48	PC2	PASS
N41	30	60	CP-QPSK	H	Outer_Full	24.88	PC2	PASS
N41	30	60	CP-QPSK	H	Inner_Full	26.38	PC2	PASS
N41	30	60	CP-16QAM	H	Edge_1RB_Left	23.73	PC2	PASS
N41	30	60	CP-16QAM	H	Edge_1RB_Right	24.48	PC2	PASS
N41	30	60	CP-16QAM	H	Outer_Full	24.90	PC2	PASS
N41	30	60	CP-16QAM	H	Inner_Full	25.89	PC2	PASS
N41	30	60	CP-64QAM	H	Edge_1RB_Left	24.56	PC2	PASS
N41	30	60	CP-64QAM	H	Edge_1RB_Right	25.10	PC2	PASS
N41	30	60	CP-64QAM	H	Outer_Full	24.44	PC2	PASS
N41	30	60	CP-64QAM	H	Inner_Full	24.35	PC2	PASS
N41	30	60	CP-256QAM	H	Edge_1RB_Left	21.59	PC2	PASS
N41	30	60	CP-256QAM	H	Edge_1RB_Right	22.78	PC2	PASS
N41	30	60	CP-256QAM	H	Outer_Full	21.42	PC2	PASS
N41	30	60	CP-256QAM	H	Inner_Full	21.41	PC2	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	23.20	PC2	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	23.64	PC2	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	26.58	PC2	PASS

N41	30	100	DFT-PI2BPSK	L	Inner_Full	27.12	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	23.35	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Right	23.70	PC2	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	26.12	PC2	PASS
N41	30	100	DFT-QPSK	L	Inner_Full	27.12	PC2	PASS
N41	30	100	DFT-16QAM	L	Edge_1RB_Left	23.28	PC2	PASS
N41	30	100	DFT-16QAM	L	Edge_1RB_Right	23.59	PC2	PASS
N41	30	100	DFT-16QAM	L	Outer_Full	25.17	PC2	PASS
N41	30	100	DFT-16QAM	L	Inner_Full	26.11	PC2	PASS
N41	30	100	DFT-64QAM	L	Edge_1RB_Left	22.88	PC2	PASS
N41	30	100	DFT-64QAM	L	Edge_1RB_Right	23.38	PC2	PASS
N41	30	100	DFT-64QAM	L	Outer_Full	24.65	PC2	PASS
N41	30	100	DFT-64QAM	L	Inner_Full	24.61	PC2	PASS
N41	30	100	DFT-256QAM	L	Edge_1RB_Left	22.24	PC2	PASS
N41	30	100	DFT-256QAM	L	Edge_1RB_Right	22.55	PC2	PASS
N41	30	100	DFT-256QAM	L	Outer_Full	22.62	PC2	PASS
N41	30	100	DFT-256QAM	L	Inner_Full	22.63	PC2	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	23.20	PC2	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	23.54	PC2	PASS
N41	30	100	CP-QPSK	L	Outer_Full	24.11	PC2	PASS
N41	30	100	CP-QPSK	L	Inner_Full	27.61	PC2	PASS
N41	30	100	CP-16QAM	L	Edge_1RB_Left	23.35	PC2	PASS
N41	30	100	CP-16QAM	L	Edge_1RB_Right	23.65	PC2	PASS
N41	30	100	CP-16QAM	L	Outer_Full	24.18	PC2	PASS
N41	30	100	CP-16QAM	L	Inner_Full	25.14	PC2	PASS
N41	30	100	CP-64QAM	L	Edge_1RB_Left	23.59	PC2	PASS
N41	30	100	CP-64QAM	L	Edge_1RB_Right	23.88	PC2	PASS
N41	30	100	CP-64QAM	L	Outer_Full	23.70	PC2	PASS
N41	30	100	CP-64QAM	L	Inner_Full	23.56	PC2	PASS
N41	30	100	CP-256QAM	L	Edge_1RB_Left	20.73	PC2	PASS
N41	30	100	CP-256QAM	L	Edge_1RB_Right	21.30	PC2	PASS
N41	30	100	CP-256QAM	L	Outer_Full	20.65	PC2	PASS
N41	30	100	CP-256QAM	L	Inner_Full	20.72	PC2	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	23.34	PC2	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	23.99	PC2	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	26.90	PC2	PASS
N41	30	100	DFT-PI2BPSK	M	Inner_Full	27.23	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	23.32	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	23.91	PC2	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	26.33	PC2	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	27.32	PC2	PASS
N41	30	100	DFT-16QAM	M	Edge_1RB_Left	23.27	PC2	PASS

N41	30	100	DFT-16QAM	M	Edge_1RB_Right	23.85	PC2	PASS
N41	30	100	DFT-16QAM	M	Outer_Full	25.36	PC2	PASS
N41	30	100	DFT-16QAM	M	Inner_Full	26.32	PC2	PASS
N41	30	100	DFT-64QAM	M	Edge_1RB_Left	23.56	PC2	PASS
N41	30	100	DFT-64QAM	M	Edge_1RB_Right	24.10	PC2	PASS
N41	30	100	DFT-64QAM	M	Outer_Full	24.85	PC2	PASS
N41	30	100	DFT-64QAM	M	Inner_Full	24.81	PC2	PASS
N41	30	100	DFT-256QAM	M	Edge_1RB_Left	22.31	PC2	PASS
N41	30	100	DFT-256QAM	M	Edge_1RB_Right	22.97	PC2	PASS
N41	30	100	DFT-256QAM	M	Outer_Full	22.87	PC2	PASS
N41	30	100	DFT-256QAM	M	Inner_Full	22.86	PC2	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	23.37	PC2	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	24.07	PC2	PASS
N41	30	100	CP-QPSK	M	Outer_Full	24.44	PC2	PASS
N41	30	100	CP-QPSK	M	Inner_Full	25.80	PC2	PASS
N41	30	100	CP-16QAM	M	Edge_1RB_Left	23.28	PC2	PASS
N41	30	100	CP-16QAM	M	Edge_1RB_Right	24.04	PC2	PASS
N41	30	100	CP-16QAM	M	Outer_Full	24.41	PC2	PASS
N41	30	100	CP-16QAM	M	Inner_Full	25.35	PC2	PASS
N41	30	100	CP-64QAM	M	Edge_1RB_Left	23.55	PC2	PASS
N41	30	100	CP-64QAM	M	Edge_1RB_Right	24.14	PC2	PASS
N41	30	100	CP-64QAM	M	Outer_Full	23.94	PC2	PASS
N41	30	100	CP-64QAM	M	Inner_Full	23.88	PC2	PASS
N41	30	100	CP-256QAM	M	Edge_1RB_Left	20.77	PC2	PASS
N41	30	100	CP-256QAM	M	Edge_1RB_Right	21.34	PC2	PASS
N41	30	100	CP-256QAM	M	Outer_Full	20.88	PC2	PASS
N41	30	100	CP-256QAM	M	Inner_Full	20.79	PC2	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	23.59	PC2	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	24.27	PC2	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	27.00	PC2	PASS
N41	30	100	DFT-PI2BPSK	H	Inner_Full	27.57	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	23.56	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	24.28	PC2	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	26.54	PC2	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	28.20	PC2	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Left	23.42	PC2	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Right	24.35	PC2	PASS
N41	30	100	DFT-16QAM	H	Outer_Full	25.58	PC2	PASS
N41	30	100	DFT-16QAM	H	Inner_Full	26.60	PC2	PASS
N41	30	100	DFT-64QAM	H	Edge_1RB_Left	23.80	PC2	PASS
N41	30	100	DFT-64QAM	H	Edge_1RB_Right	24.48	PC2	PASS
N41	30	100	DFT-64QAM	H	Outer_Full	25.08	PC2	PASS

N41	30	100	DFT-64QAM	H	Inner_Full	25.16	PC2	PASS
N41	30	100	DFT-256QAM	H	Edge_1RB_Left	22.57	PC2	PASS
N41	30	100	DFT-256QAM	H	Edge_1RB_Right	23.29	PC2	PASS
N41	30	100	DFT-256QAM	H	Outer_Full	23.12	PC2	PASS
N41	30	100	DFT-256QAM	H	Inner_Full	23.10	PC2	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	23.71	PC2	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	24.17	PC2	PASS
N41	30	100	CP-QPSK	H	Outer_Full	24.57	PC2	PASS
N41	30	100	CP-QPSK	H	Inner_Full	26.12	PC2	PASS
N41	30	100	CP-16QAM	H	Edge_1RB_Left	23.70	PC2	PASS
N41	30	100	CP-16QAM	H	Edge_1RB_Right	24.27	PC2	PASS
N41	30	100	CP-16QAM	H	Outer_Full	24.64	PC2	PASS
N41	30	100	CP-16QAM	H	Inner_Full	25.54	PC2	PASS
N41	30	100	CP-64QAM	H	Edge_1RB_Left	23.72	PC2	PASS
N41	30	100	CP-64QAM	H	Edge_1RB_Right	24.43	PC2	PASS
N41	30	100	CP-64QAM	H	Outer_Full	24.14	PC2	PASS
N41	30	100	CP-64QAM	H	Inner_Full	24.10	PC2	PASS
N41	30	100	CP-256QAM	H	Edge_1RB_Left	21.02	PC2	PASS
N41	30	100	CP-256QAM	H	Edge_1RB_Right	21.77	PC2	PASS
N41	30	100	CP-256QAM	H	Outer_Full	21.25	PC2	PASS
N41	30	100	CP-256QAM	H	Inner_Full	21.09	PC2	PASS

Appendix B: Peak-to-Average Ratio for SA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N41	30	20	DFT-PI2BPSK	L	Outer_Full	4.37	≤13	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	5.27	≤13	PASS
N41	30	20	DFT-16QAM	L	Outer_Full	6.29	≤13	PASS
N41	30	20	DFT-64QAM	L	Outer_Full	6.30	≤13	PASS
N41	30	20	DFT-256QAM	L	Outer_Full	7.09	≤13	PASS
N41	30	20	CP-QPSK	L	Outer_Full	7.10	≤13	PASS
N41	30	20	CP-16QAM	L	Outer_Full	7.09	≤13	PASS
N41	30	20	CP-64QAM	L	Outer_Full	7.56	≤13	PASS
N41	30	20	CP-256QAM	L	Outer_Full	9.20	≤13	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	4.09	≤13	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	5.18	≤13	PASS
N41	30	20	DFT-16QAM	M	Outer_Full	6.08	≤13	PASS
N41	30	20	DFT-64QAM	M	Outer_Full	6.34	≤13	PASS
N41	30	20	DFT-256QAM	M	Outer_Full	7.13	≤13	PASS
N41	30	20	CP-QPSK	M	Outer_Full	7.25	≤13	PASS
N41	30	20	CP-16QAM	M	Outer_Full	7.25	≤13	PASS
N41	30	20	CP-64QAM	M	Outer_Full	7.66	≤13	PASS
N41	30	20	CP-256QAM	M	Outer_Full	9.06	≤13	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	3.95	≤13	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	4.45	≤13	PASS
N41	30	20	DFT-16QAM	H	Outer_Full	5.39	≤13	PASS
N41	30	20	DFT-64QAM	H	Outer_Full	5.90	≤13	PASS
N41	30	20	DFT-256QAM	H	Outer_Full	7.08	≤13	PASS
N41	30	20	CP-QPSK	H	Outer_Full	6.48	≤13	PASS
N41	30	20	CP-16QAM	H	Outer_Full	6.52	≤13	PASS
N41	30	20	CP-64QAM	H	Outer_Full	7.02	≤13	PASS
N41	30	20	CP-256QAM	H	Outer_Full	9.05	≤13	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	4.12	≤13	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	5.10	≤13	PASS
N41	30	40	DFT-16QAM	L	Outer_Full	5.93	≤13	PASS
N41	30	40	DFT-64QAM	L	Outer_Full	6.26	≤13	PASS

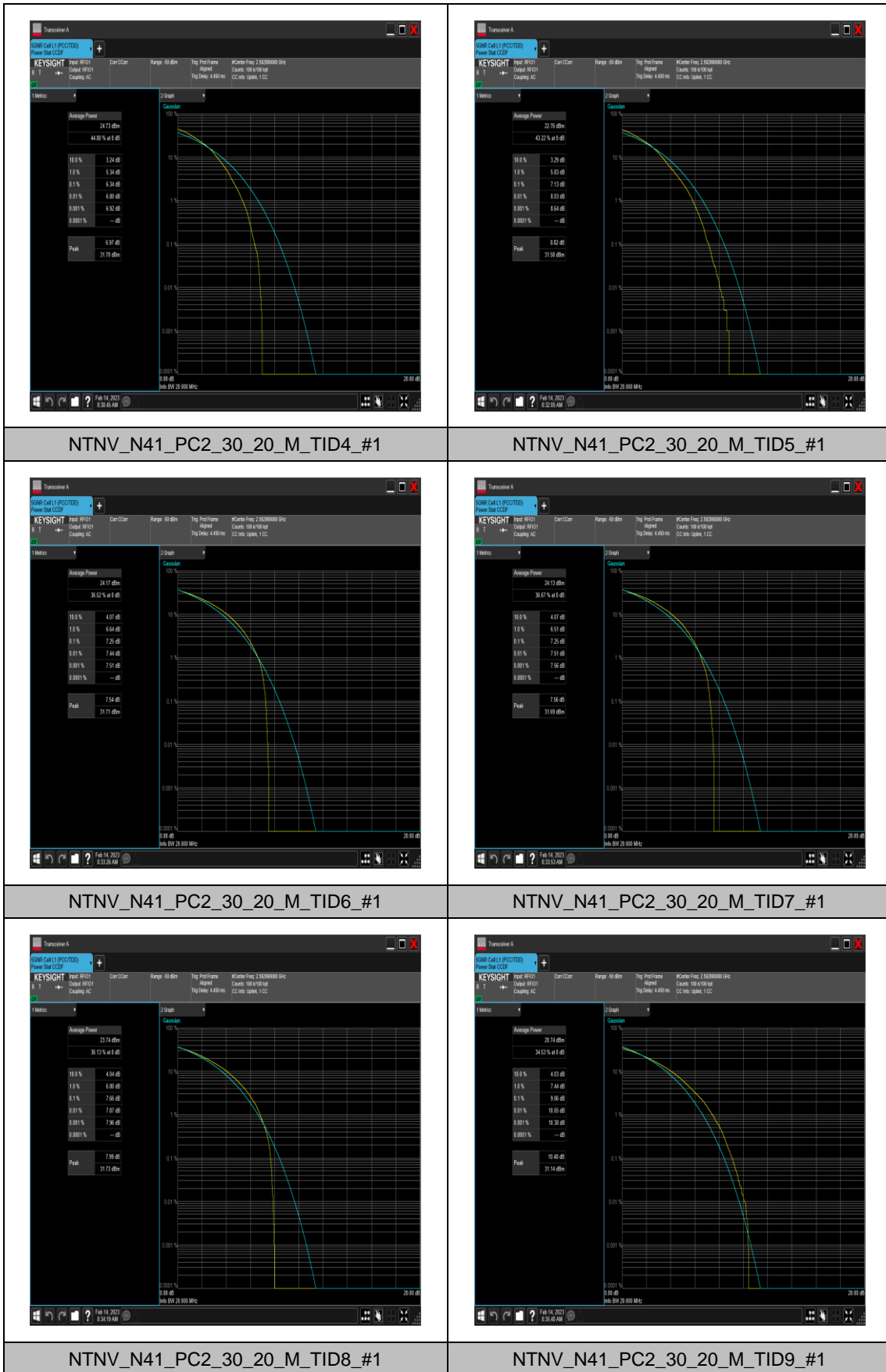
N41	30	40	DFT-256QAM	L	Outer_Full	7.01	≤13	PASS
N41	30	40	CP-QPSK	L	Outer_Full	7.18	≤13	PASS
N41	30	40	CP-16QAM	L	Outer_Full	7.22	≤13	PASS
N41	30	40	CP-64QAM	L	Outer_Full	7.20	≤13	PASS
N41	30	40	CP-256QAM	L	Outer_Full	9.00	≤13	PASS
N41	30	40	DFT-PI2BPSK	M	Outer_Full	4.09	≤13	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	5.14	≤13	PASS
N41	30	40	DFT-16QAM	M	Outer_Full	6.05	≤13	PASS
N41	30	40	DFT-64QAM	M	Outer_Full	6.28	≤13	PASS
N41	30	40	DFT-256QAM	M	Outer_Full	7.18	≤13	PASS
N41	30	40	CP-QPSK	M	Outer_Full	7.17	≤13	PASS
N41	30	40	CP-16QAM	M	Outer_Full	7.17	≤13	PASS
N41	30	40	CP-64QAM	M	Outer_Full	7.59	≤13	PASS
N41	30	40	CP-256QAM	M	Outer_Full	9.15	≤13	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	4.12	≤13	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	5.06	≤13	PASS
N41	30	40	DFT-16QAM	H	Outer_Full	5.91	≤13	PASS
N41	30	40	DFT-64QAM	H	Outer_Full	6.25	≤13	PASS
N41	30	40	DFT-256QAM	H	Outer_Full	7.11	≤13	PASS
N41	30	40	CP-QPSK	H	Outer_Full	7.03	≤13	PASS
N41	30	40	CP-16QAM	H	Outer_Full	6.95	≤13	PASS
N41	30	40	CP-64QAM	H	Outer_Full	7.29	≤13	PASS
N41	30	40	CP-256QAM	H	Outer_Full	9.06	≤13	PASS
N41	30	60	DFT-PI2BPSK	L	Outer_Full	4.28	≤13	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	5.21	≤13	PASS
N41	30	60	DFT-16QAM	L	Outer_Full	5.92	≤13	PASS
N41	30	60	DFT-64QAM	L	Outer_Full	6.33	≤13	PASS
N41	30	60	DFT-256QAM	L	Outer_Full	6.96	≤13	PASS
N41	30	60	CP-QPSK	L	Outer_Full	7.00	≤13	PASS
N41	30	60	CP-16QAM	L	Outer_Full	6.98	≤13	PASS
N41	30	60	CP-64QAM	L	Outer_Full	7.47	≤13	PASS
N41	30	60	CP-256QAM	L	Outer_Full	9.14	≤13	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	4.02	≤13	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	5.01	≤13	PASS
N41	30	60	DFT-16QAM	M	Outer_Full	5.93	≤13	PASS
N41	30	60	DFT-64QAM	M	Outer_Full	6.22	≤13	PASS
N41	30	60	DFT-256QAM	M	Outer_Full	7.04	≤13	PASS
N41	30	60	CP-QPSK	M	Outer_Full	7.10	≤13	PASS
N41	30	60	CP-16QAM	M	Outer_Full	7.09	≤13	PASS
N41	30	60	CP-64QAM	M	Outer_Full	7.59	≤13	PASS
N41	30	60	CP-256QAM	M	Outer_Full	9.27	≤13	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	4.12	≤13	PASS

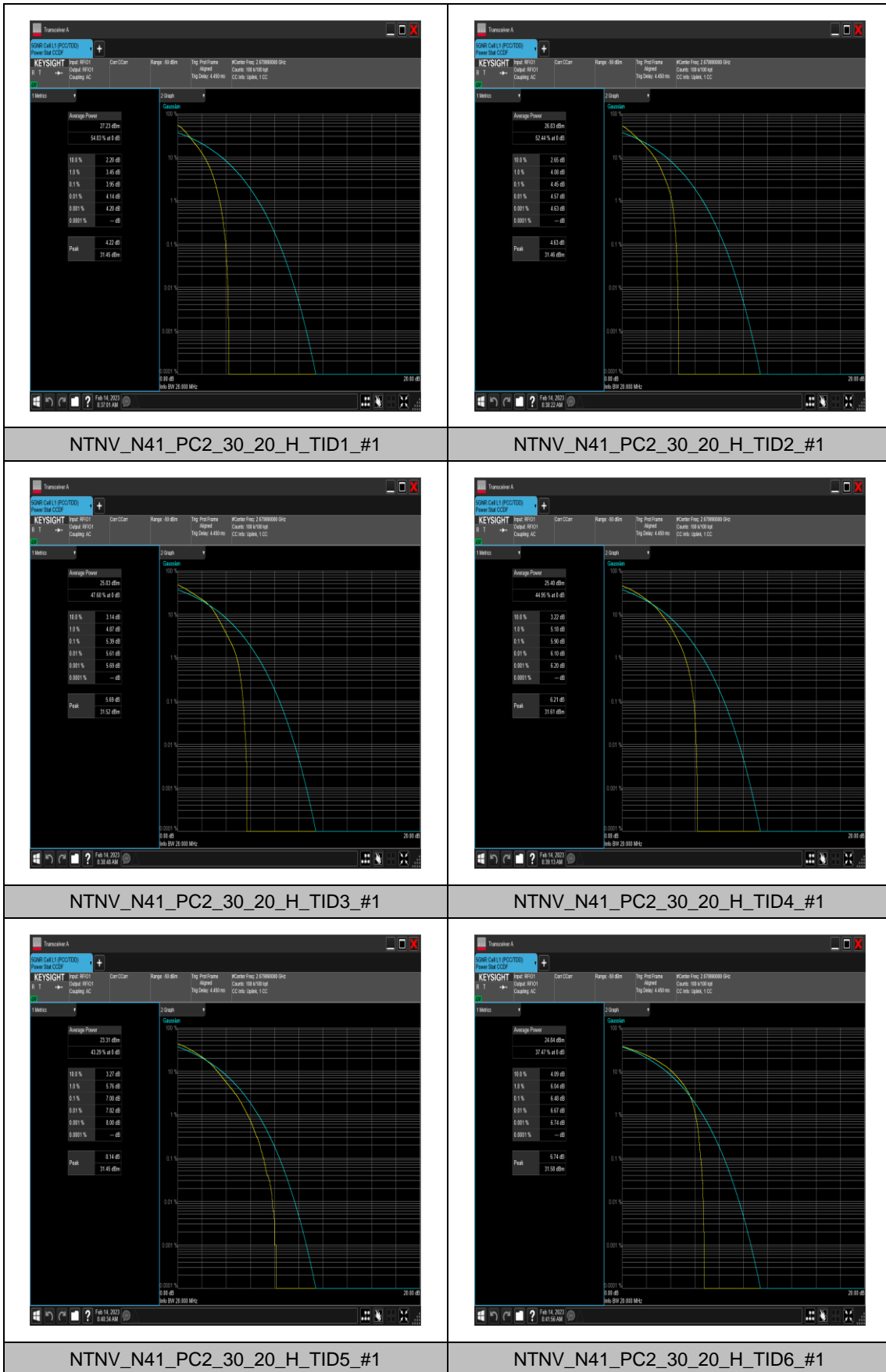
N41	30	60	DFT-QPSK	H	Outer_Full	5.05	≤13	PASS
N41	30	60	DFT-16QAM	H	Outer_Full	5.89	≤13	PASS
N41	30	60	DFT-64QAM	H	Outer_Full	6.36	≤13	PASS
N41	30	60	DFT-256QAM	H	Outer_Full	6.97	≤13	PASS
N41	30	60	CP-QPSK	H	Outer_Full	6.90	≤13	PASS
N41	30	60	CP-16QAM	H	Outer_Full	6.89	≤13	PASS
N41	30	60	CP-64QAM	H	Outer_Full	7.36	≤13	PASS
N41	30	60	CP-256QAM	H	Outer_Full	9.18	≤13	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	4.36	≤13	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	5.33	≤13	PASS
N41	30	100	DFT-16QAM	L	Outer_Full	6.18	≤13	PASS
N41	30	100	DFT-64QAM	L	Outer_Full	6.43	≤13	PASS
N41	30	100	DFT-256QAM	L	Outer_Full	7.07	≤13	PASS
N41	30	100	CP-QPSK	L	Outer_Full	7.50	≤13	PASS
N41	30	100	CP-16QAM	L	Outer_Full	7.45	≤13	PASS
N41	30	100	CP-64QAM	L	Outer_Full	7.84	≤13	PASS
N41	30	100	CP-256QAM	L	Outer_Full	9.19	≤13	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	4.56	≤13	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	5.41	≤13	PASS
N41	30	100	DFT-16QAM	M	Outer_Full	6.27	≤13	PASS
N41	30	100	DFT-64QAM	M	Outer_Full	6.49	≤13	PASS
N41	30	100	DFT-256QAM	M	Outer_Full	7.02	≤13	PASS
N41	30	100	CP-QPSK	M	Outer_Full	7.17	≤13	PASS
N41	30	100	CP-16QAM	M	Outer_Full	7.67	≤13	PASS
N41	30	100	CP-64QAM	M	Outer_Full	7.80	≤13	PASS
N41	30	100	CP-256QAM	M	Outer_Full	9.11	≤13	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	4.87	≤13	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	5.53	≤13	PASS
N41	30	100	DFT-16QAM	H	Outer_Full	6.27	≤13	PASS
N41	30	100	DFT-64QAM	H	Outer_Full	6.48	≤13	PASS
N41	30	100	DFT-256QAM	H	Outer_Full	7.02	≤13	PASS
N41	30	100	CP-QPSK	H	Outer_Full	7.62	≤13	PASS
N41	30	100	CP-16QAM	H	Outer_Full	7.60	≤13	PASS
N41	30	100	CP-64QAM	H	Outer_Full	7.62	≤13	PASS
N41	30	100	CP-256QAM	H	Outer_Full	9.09	≤13	PASS

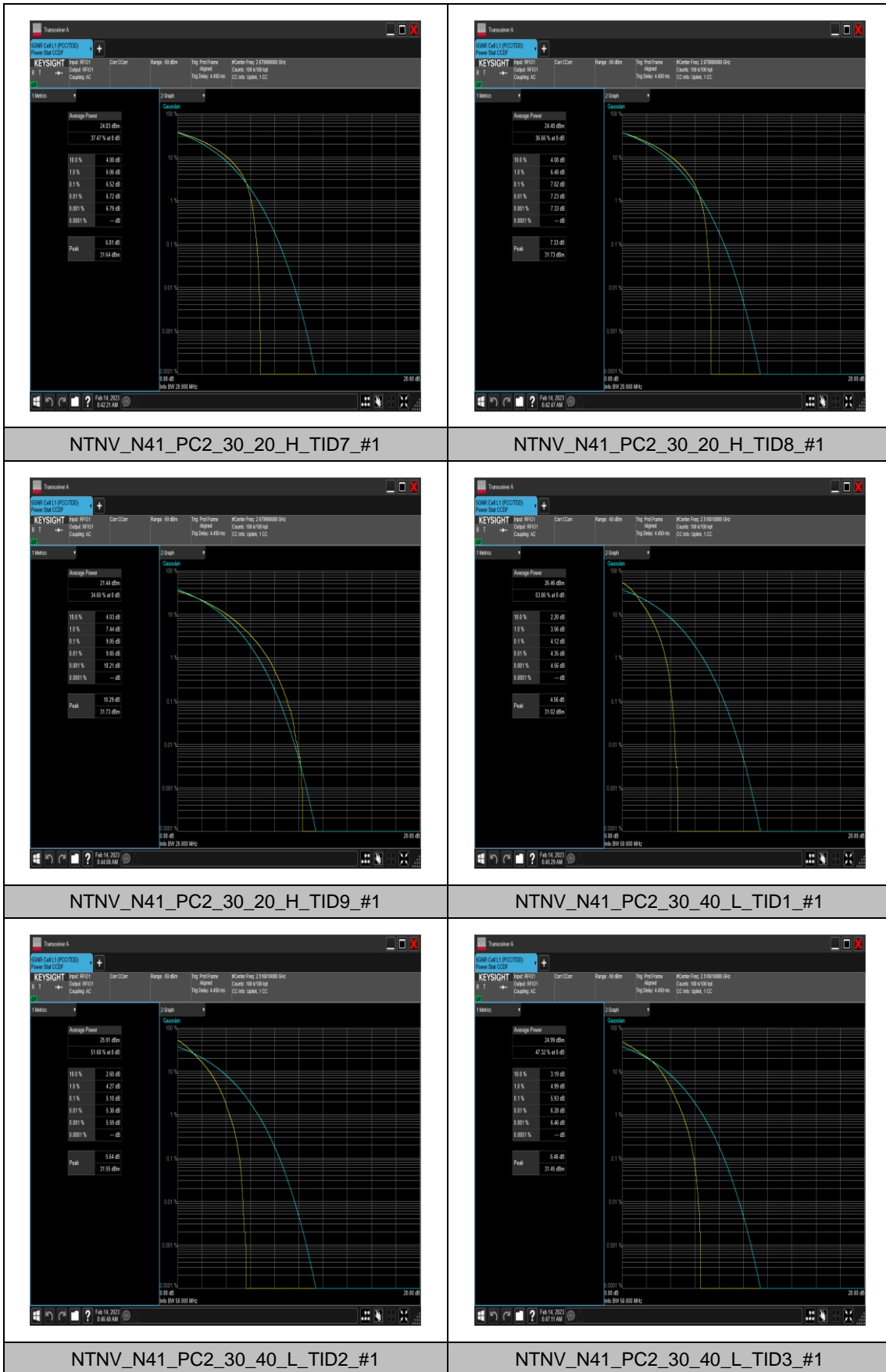
Test Graphs

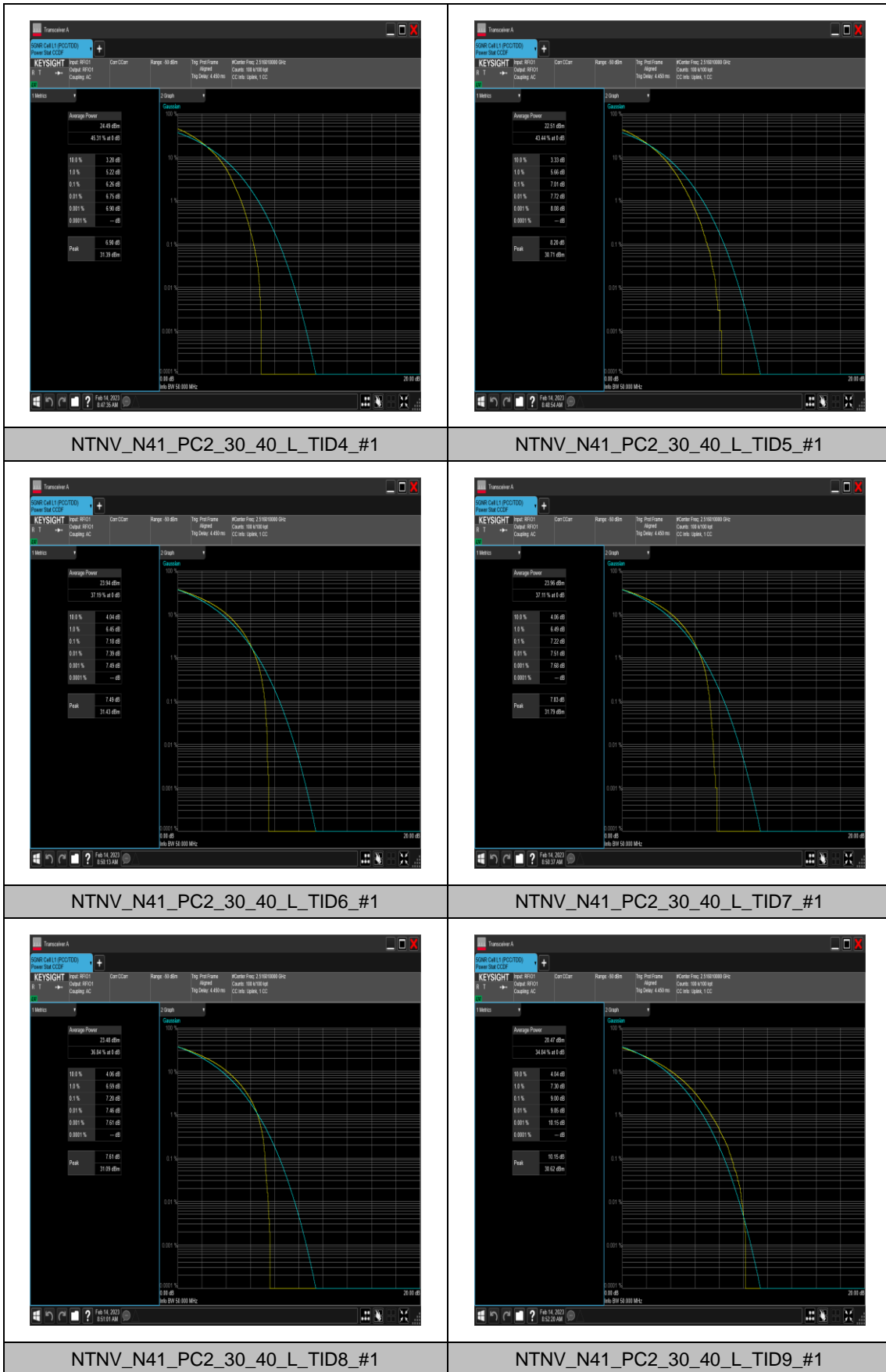


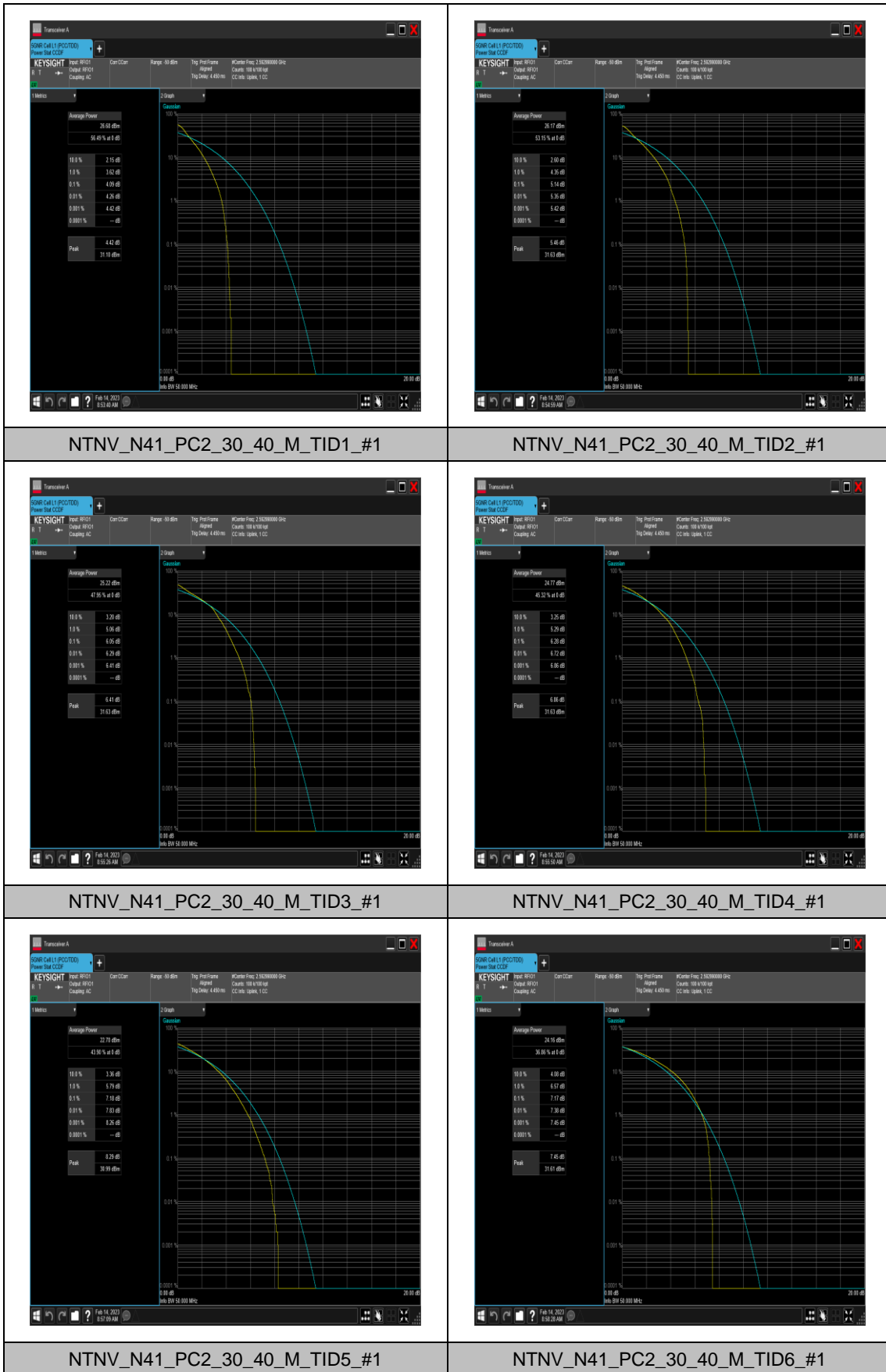


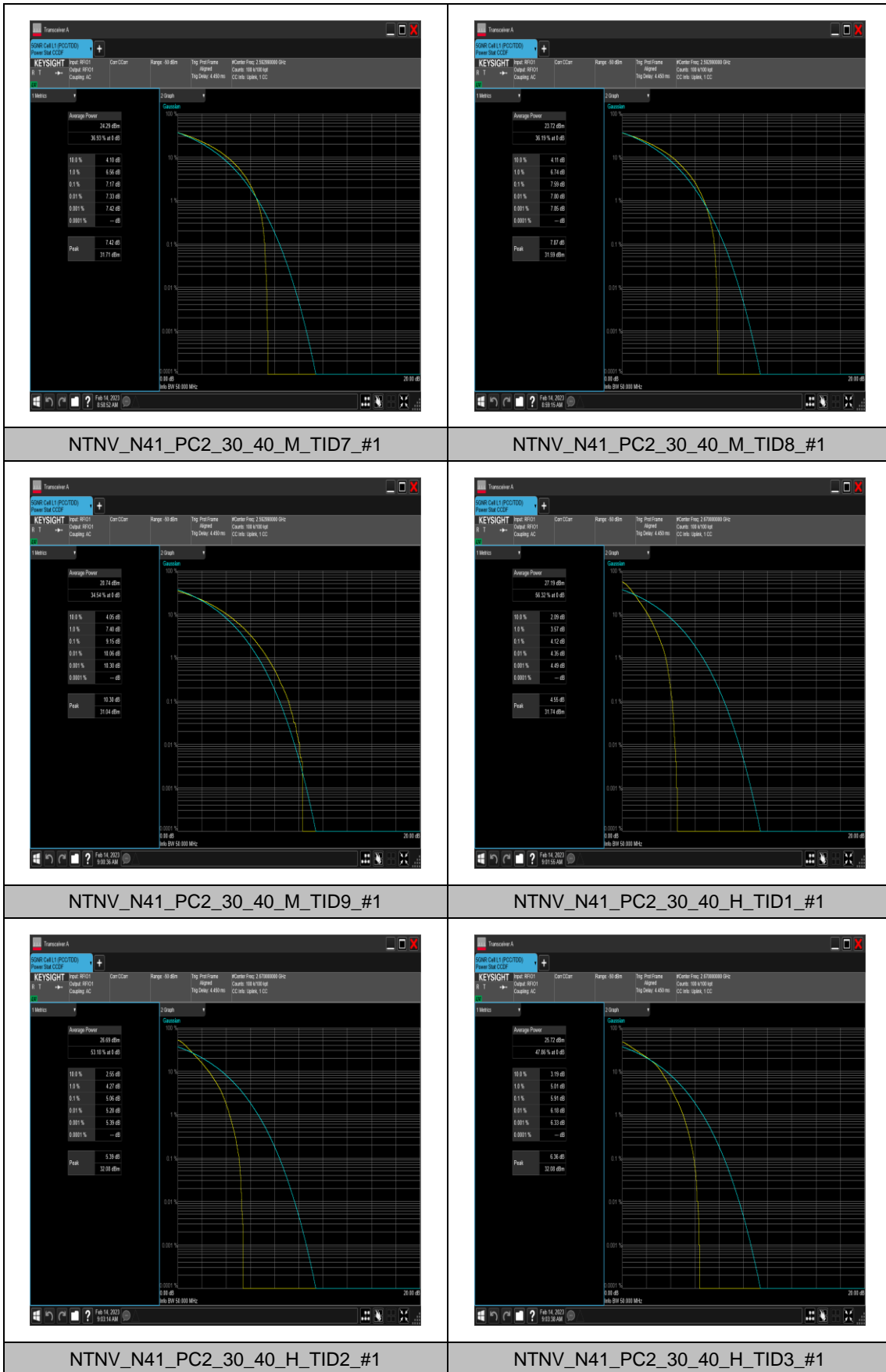


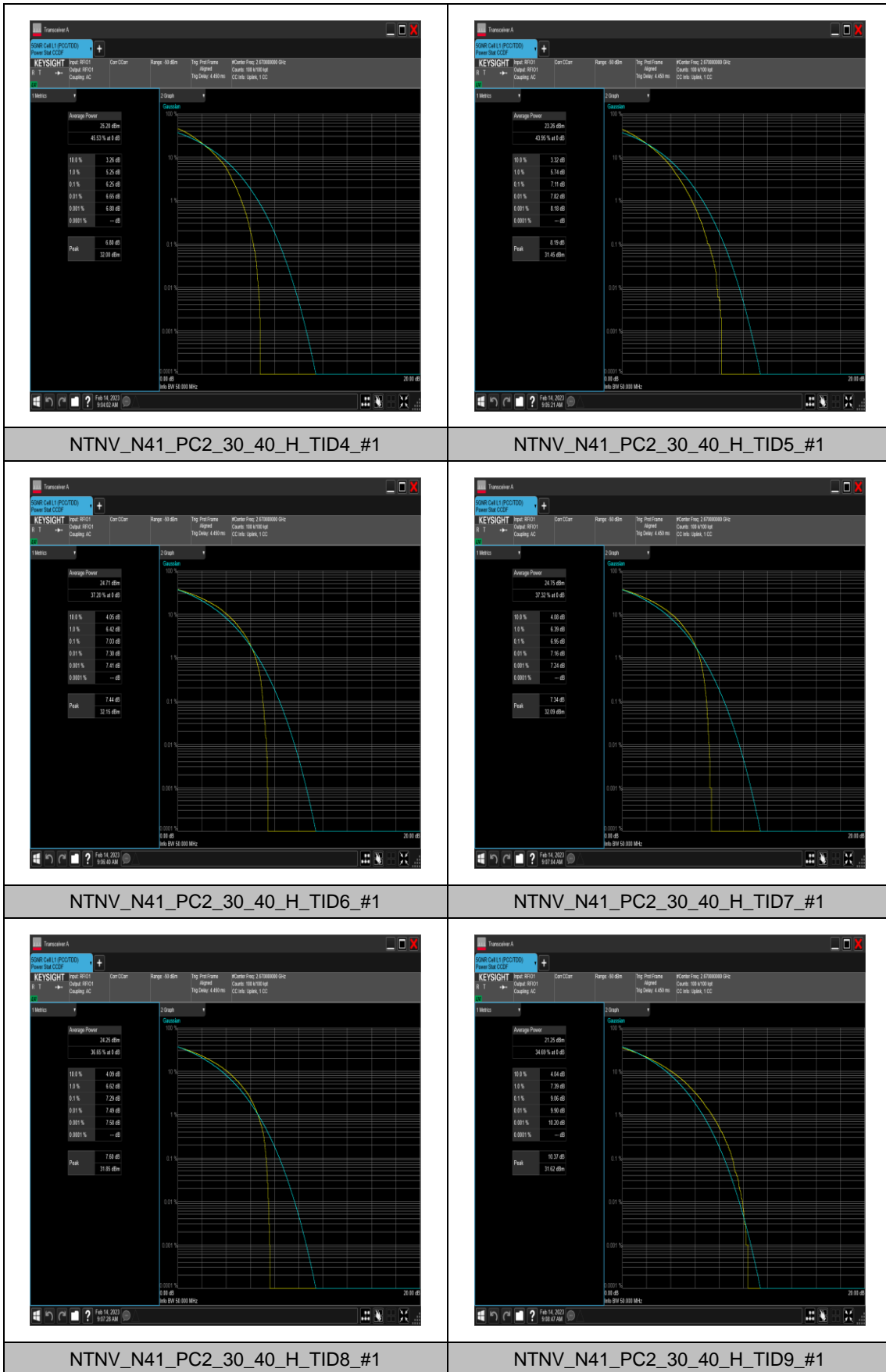


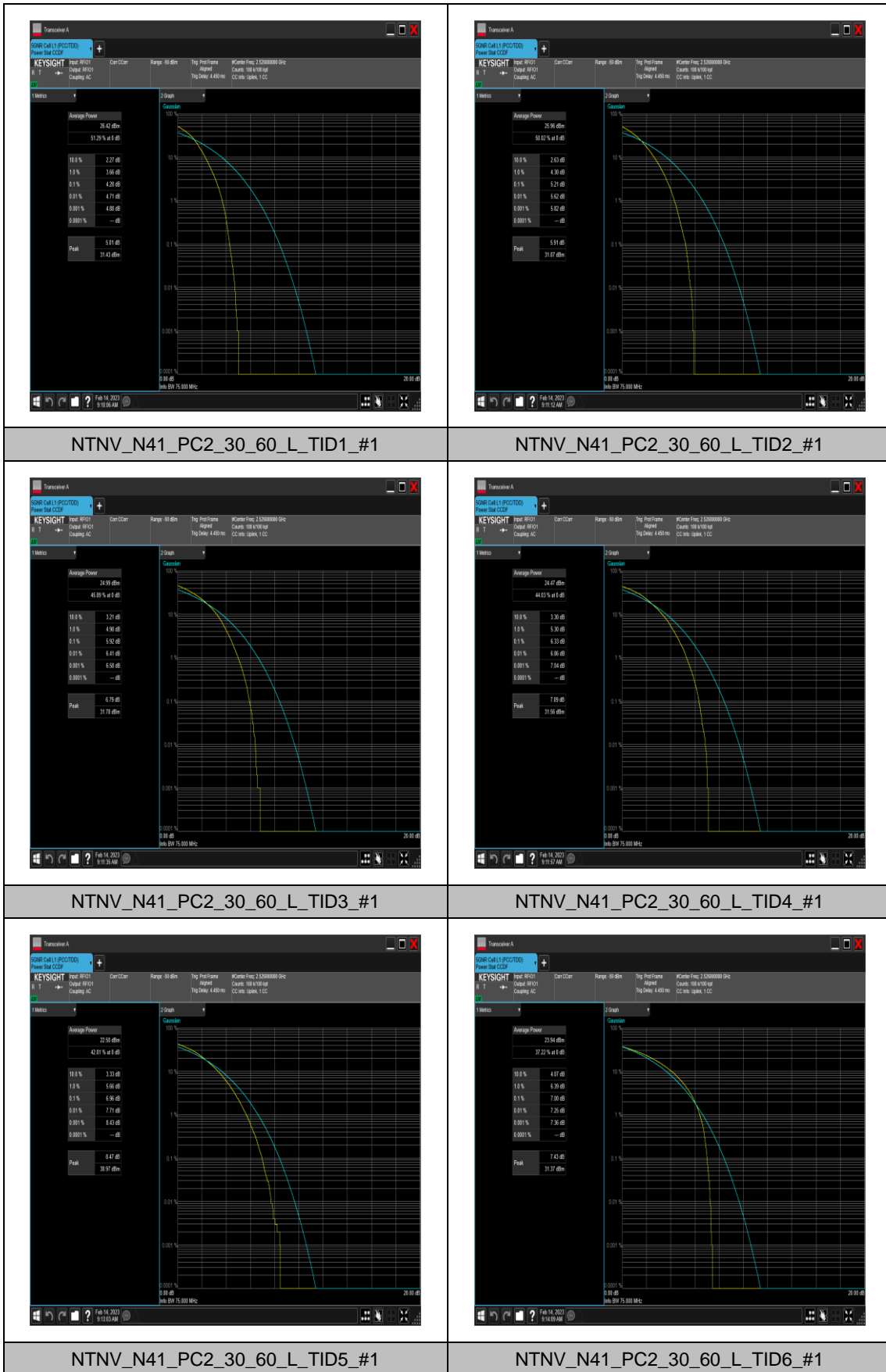


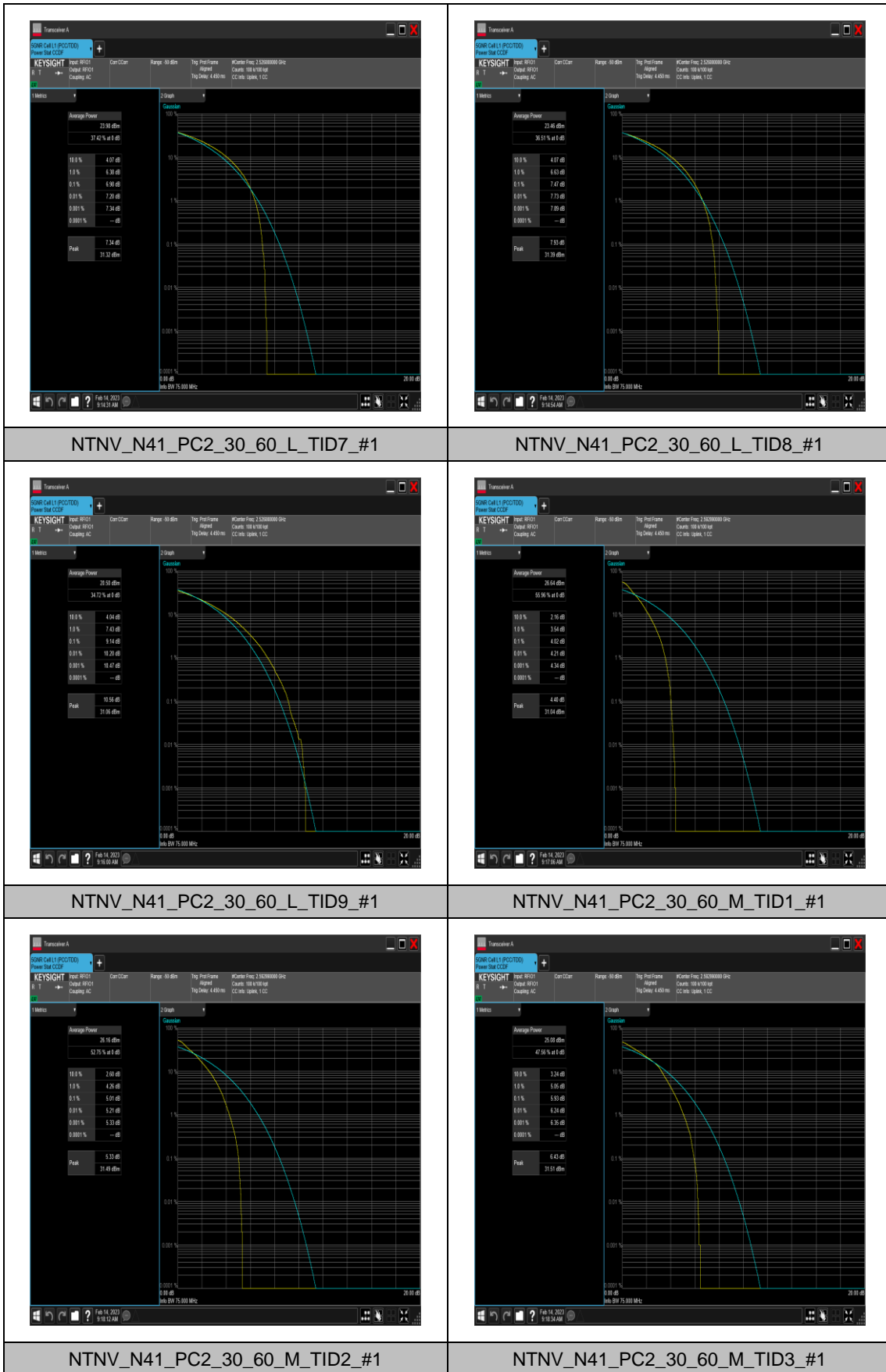


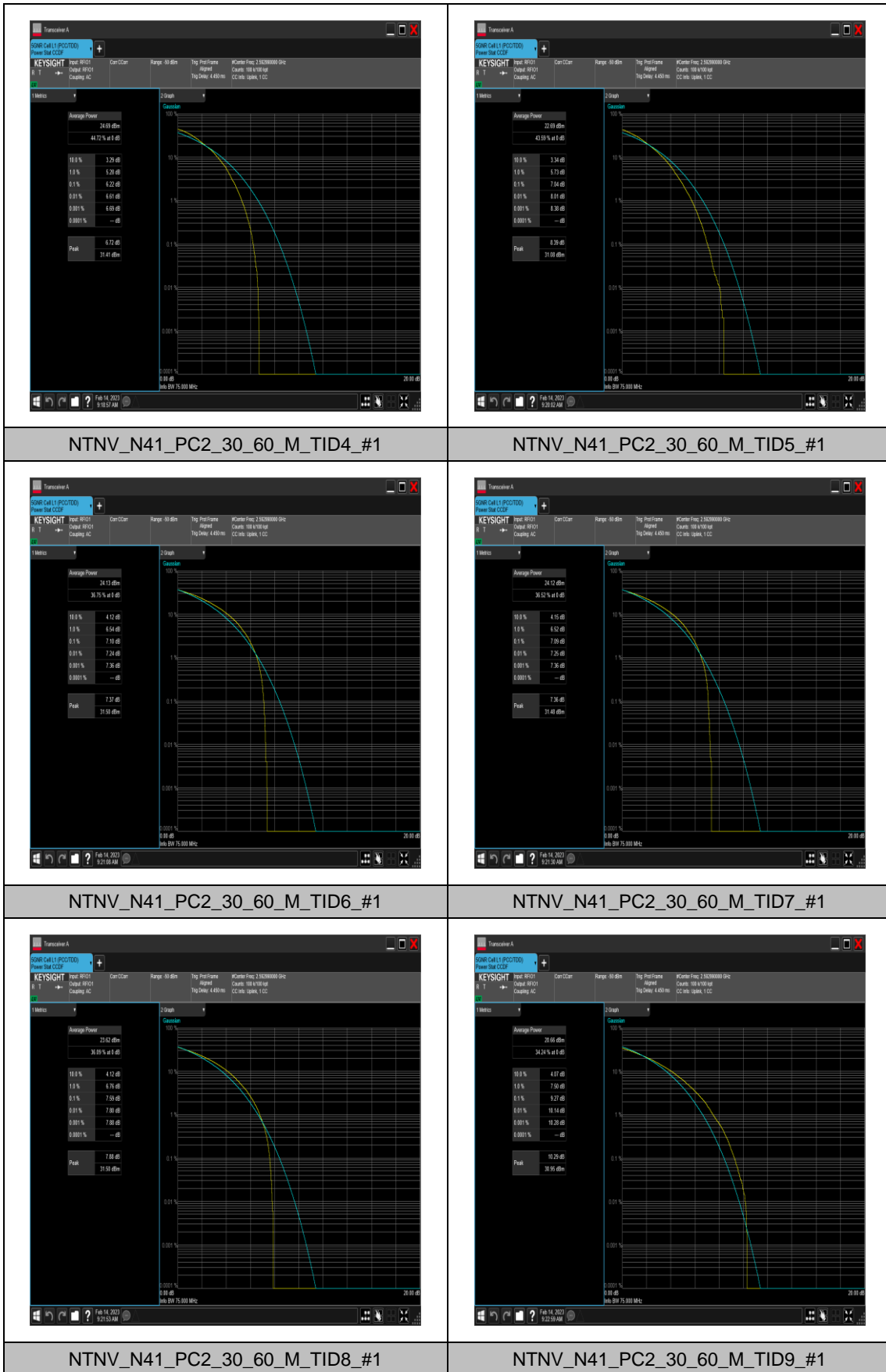


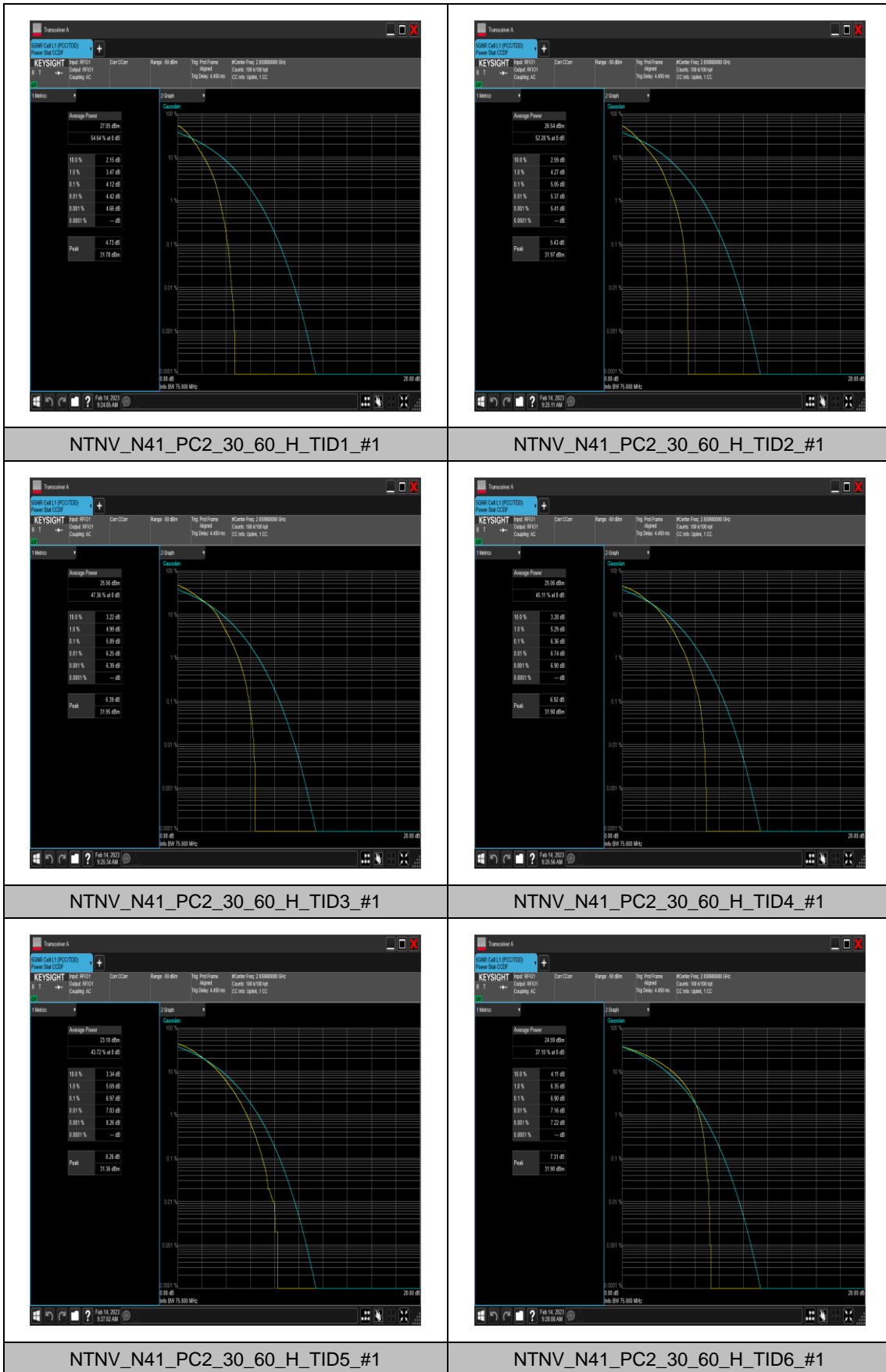


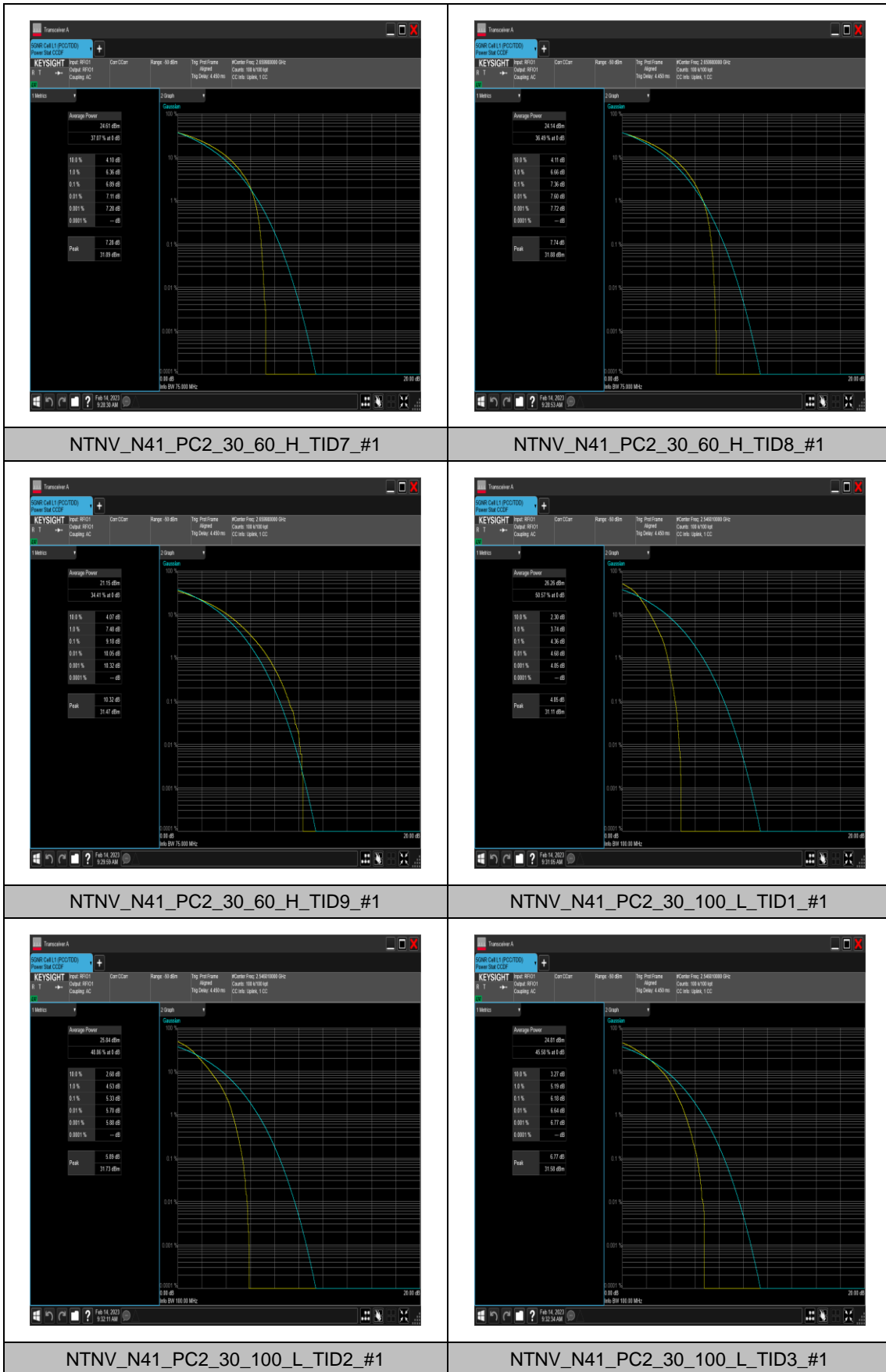


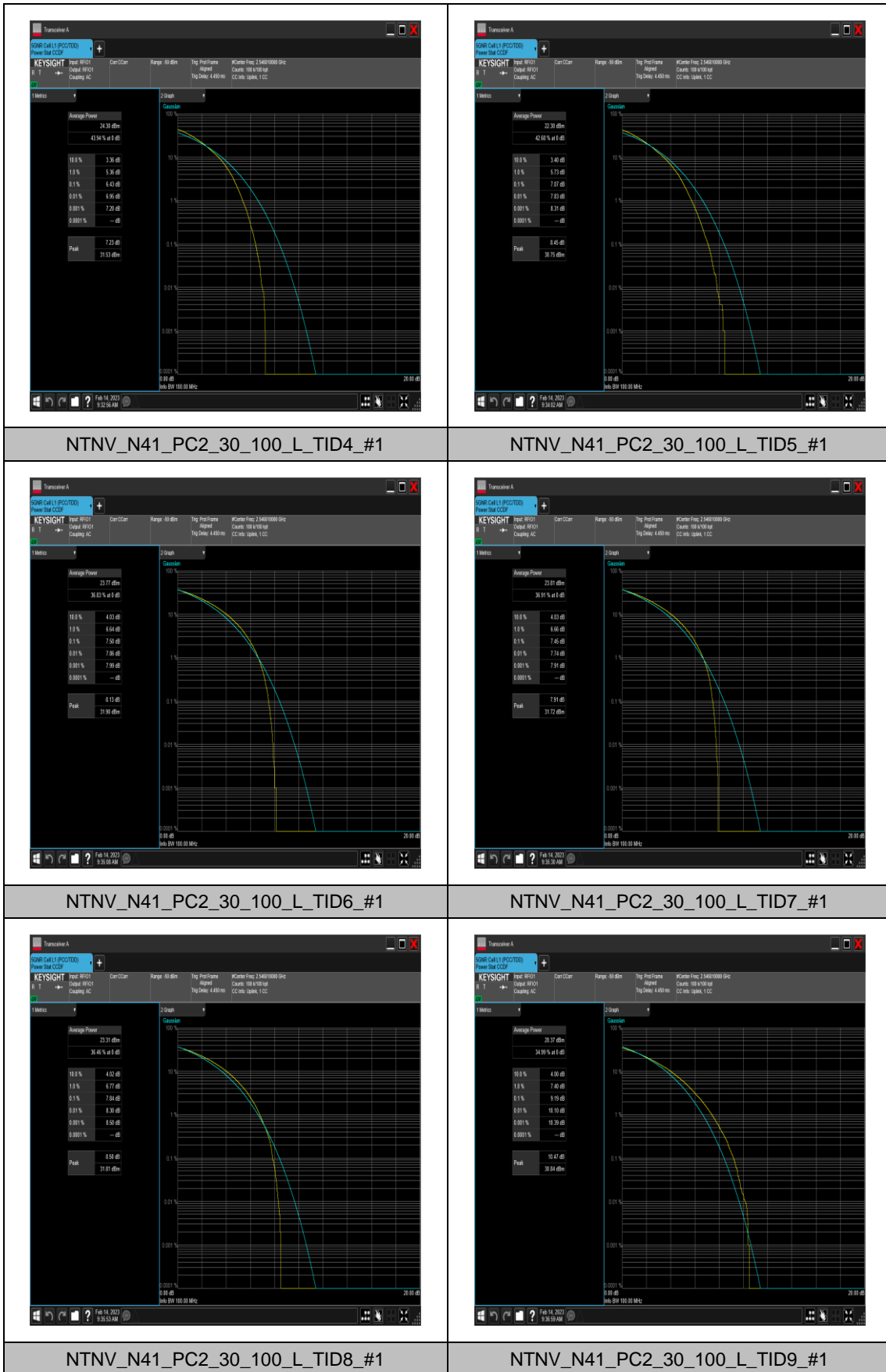


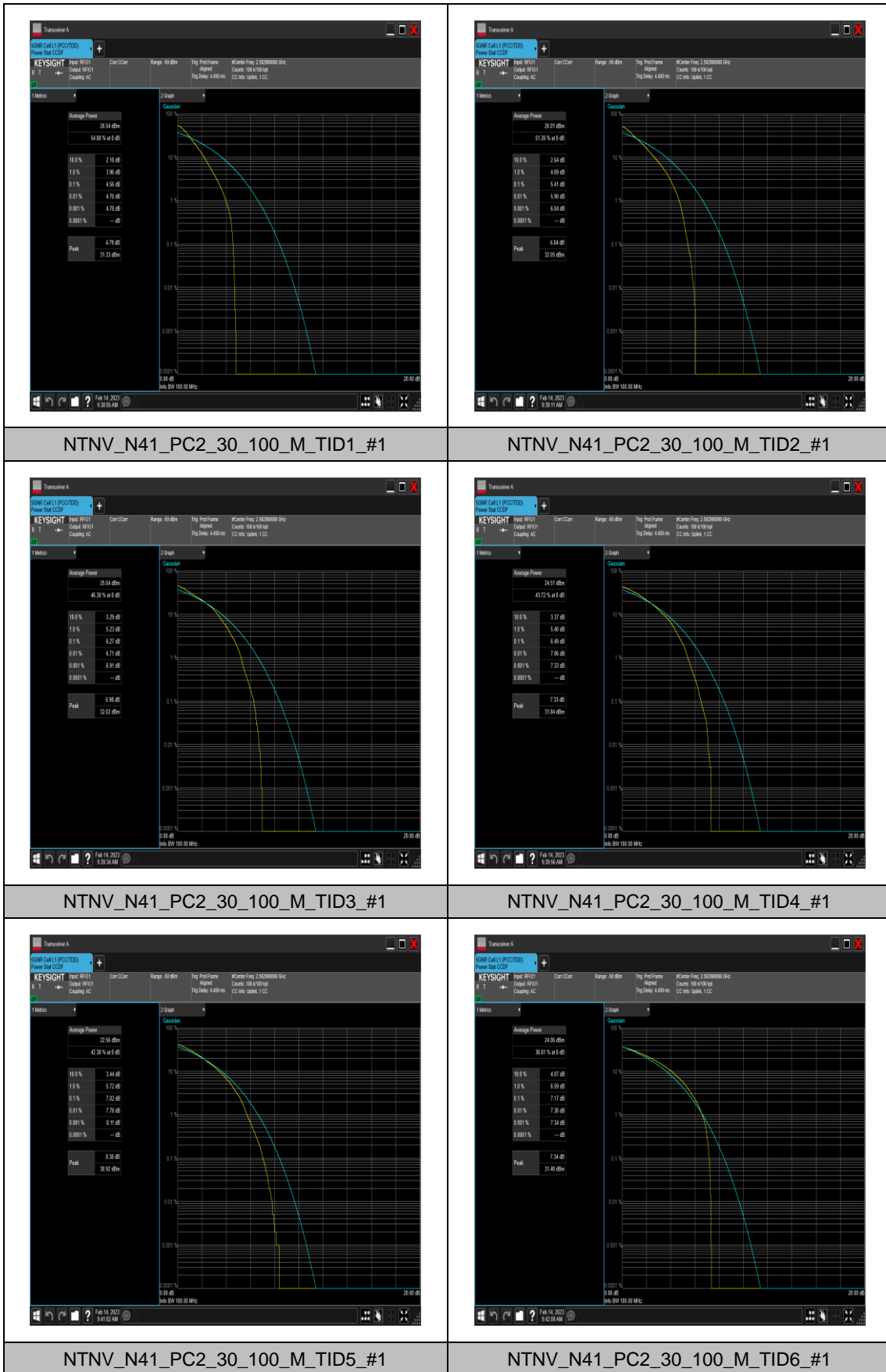


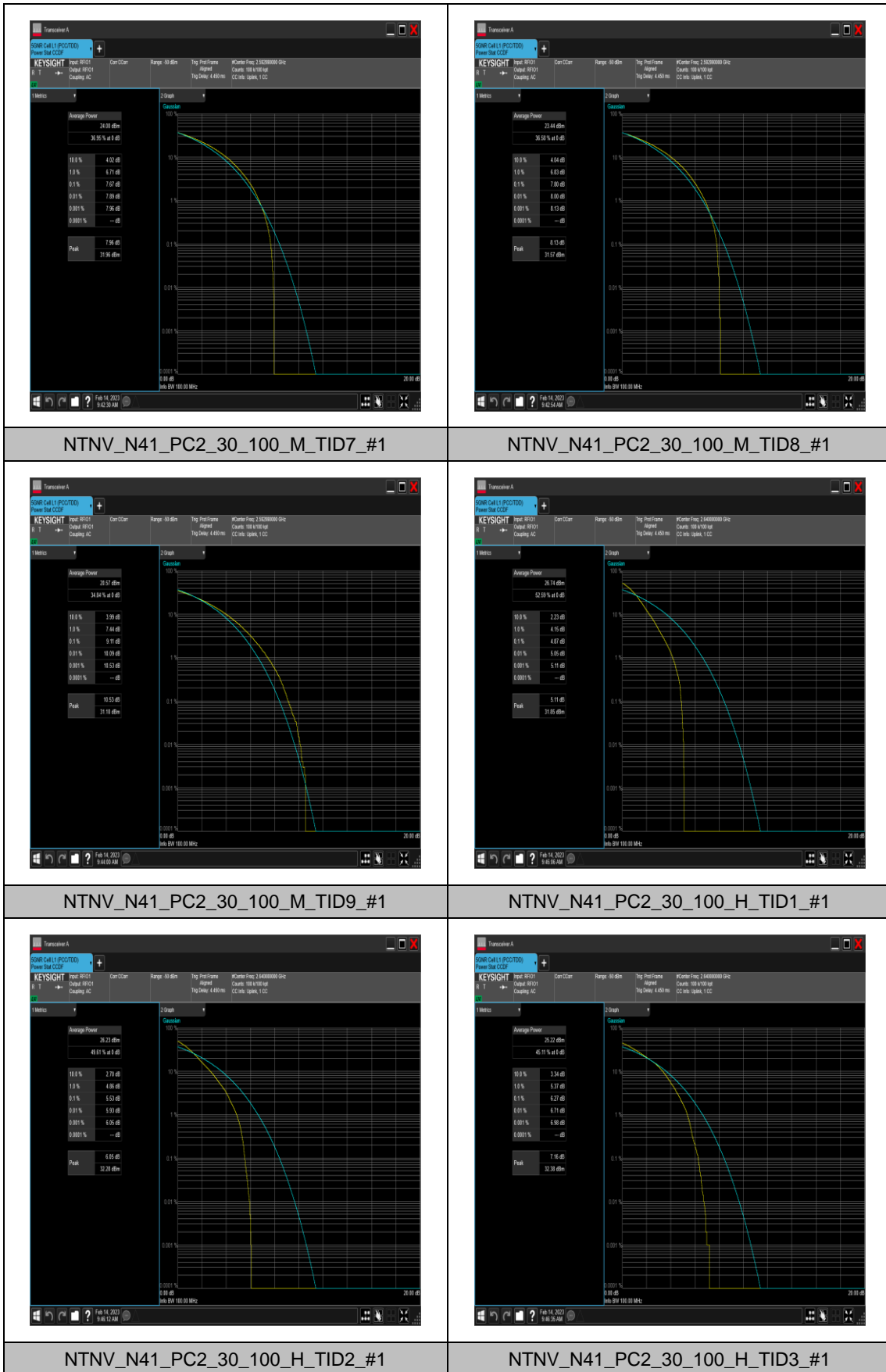


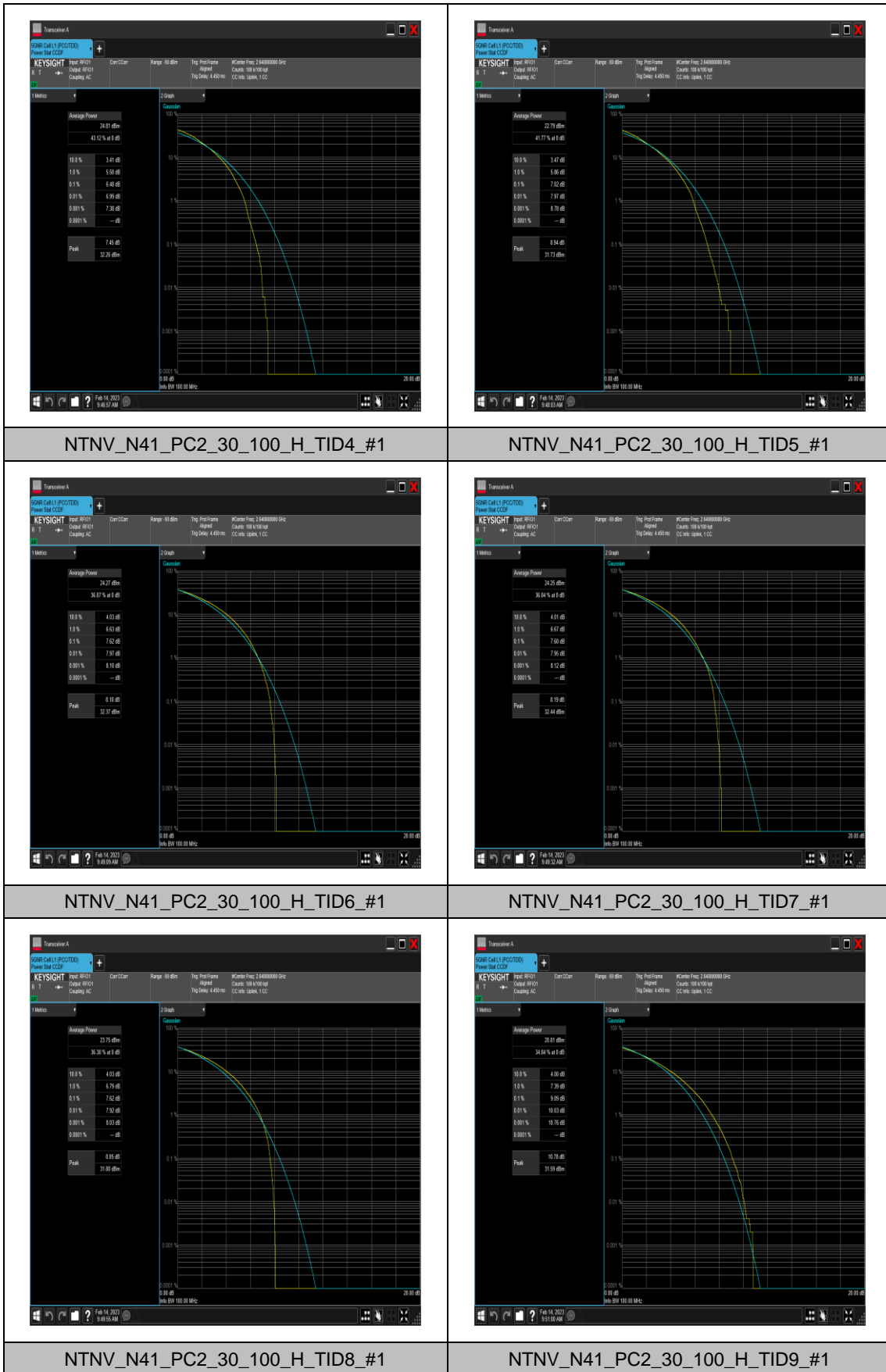












Appendix C: 26dB Bandwidth and Occupied Bandwidth for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
N41	30	20	DFT-QPSK	L	Outer_Full	17.827	19.14	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	17.826	18.93	PASS
N41	30	20	DFT-16QAM	L	Outer_Full	17.895	19.16	PASS
N41	30	20	DFT-64QAM	L	Outer_Full	17.858	19.03	PASS
N41	30	20	CP-QPSK	L	Outer_Full	18.228	19.58	PASS
N41	30	20	CP-16QAM	L	Outer_Full	18.264	19.67	PASS
N41	30	20	CP-64QAM	L	Outer_Full	18.295	19.73	PASS
N41	30	20	CP-256QAM	L	Outer_Full	18.232	19.53	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	17.871	19.06	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	17.821	19.04	PASS
N41	30	20	DFT-16QAM	M	Outer_Full	17.916	19.21	PASS
N41	30	20	DFT-64QAM	M	Outer_Full	17.870	19.13	PASS
N41	30	20	CP-QPSK	M	Outer_Full	18.234	19.50	PASS
N41	30	20	CP-16QAM	M	Outer_Full	18.236	19.47	PASS
N41	30	20	CP-64QAM	M	Outer_Full	18.254	19.64	PASS
N41	30	20	CP-256QAM	M	Outer_Full	18.231	19.55	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	17.794	19.01	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	17.802	18.89	PASS
N41	30	20	DFT-16QAM	H	Outer_Full	17.881	19.24	PASS
N41	30	20	DFT-64QAM	H	Outer_Full	17.849	19.09	PASS
N41	30	20	CP-QPSK	H	Outer_Full	18.254	22.25	PASS
N41	30	20	CP-16QAM	H	Outer_Full	18.220	23.77	PASS
N41	30	20	CP-64QAM	H	Outer_Full	18.311	20.53	PASS
N41	30	20	CP-256QAM	H	Outer_Full	18.225	19.41	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	35.733	37.57	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	35.704	37.40	PASS
N41	30	40	DFT-16QAM	L	Outer_Full	35.740	37.48	PASS
N41	30	40	DFT-64QAM	L	Outer_Full	35.744	37.40	PASS
N41	30	40	CP-QPSK	L	Outer_Full	37.881	39.49	PASS
N41	30	40	CP-16QAM	L	Outer_Full	37.861	39.60	PASS
N41	30	40	CP-64QAM	L	Outer_Full	37.874	39.57	PASS
N41	30	40	CP-256QAM	L	Outer_Full	37.857	39.47	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	35.706	37.34	PASS
N41	30	40	DFT-PI2BPSK	M	Outer_Full	35.740	37.44	PASS
N41	30	40	DFT-16QAM	M	Outer_Full	35.743	37.32	PASS

N41	30	40	DFT-64QAM	M	Outer_Full	35.741	37.48	PASS
N41	30	40	CP-QPSK	M	Outer_Full	37.901	39.63	PASS
N41	30	40	CP-16QAM	M	Outer_Full	37.820	39.57	PASS
N41	30	40	CP-64QAM	M	Outer_Full	37.853	39.52	PASS
N41	30	40	CP-256QAM	M	Outer_Full	37.828	39.39	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	35.635	37.47	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	35.612	37.44	PASS
N41	30	40	DFT-16QAM	H	Outer_Full	35.642	37.31	PASS
N41	30	40	DFT-64QAM	H	Outer_Full	35.616	37.21	PASS
N41	30	40	CP-QPSK	H	Outer_Full	37.767	39.34	PASS
N41	30	40	CP-16QAM	H	Outer_Full	37.759	39.61	PASS
N41	30	40	CP-64QAM	H	Outer_Full	37.755	39.53	PASS
N41	30	40	CP-256QAM	H	Outer_Full	37.747	39.40	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	57.742	60.14	PASS
N41	30	60	DFT-PI2BPSK	L	Outer_Full	57.779	60.08	PASS
N41	30	60	DFT-16QAM	L	Outer_Full	57.841	60.11	PASS
N41	30	60	DFT-64QAM	L	Outer_Full	57.743	59.97	PASS
N41	30	60	CP-QPSK	L	Outer_Full	57.775	60.01	PASS
N41	30	60	CP-16QAM	L	Outer_Full	57.884	60.08	PASS
N41	30	60	CP-64QAM	L	Outer_Full	57.752	60.09	PASS
N41	30	60	CP-256QAM	L	Outer_Full	57.800	60.09	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	57.904	60.01	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	57.788	60.14	PASS
N41	30	60	DFT-16QAM	M	Outer_Full	57.904	60.15	PASS
N41	30	60	DFT-64QAM	M	Outer_Full	57.831	60.01	PASS
N41	30	60	CP-QPSK	M	Outer_Full	57.747	60.13	PASS
N41	30	60	CP-16QAM	M	Outer_Full	57.934	60.17	PASS
N41	30	60	CP-64QAM	M	Outer_Full	57.844	60.03	PASS
N41	30	60	CP-256QAM	M	Outer_Full	57.878	60.15	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	57.734	60.01	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	57.660	60.01	PASS
N41	30	60	DFT-16QAM	H	Outer_Full	57.798	60.06	PASS
N41	30	60	DFT-64QAM	H	Outer_Full	57.672	60.00	PASS
N41	30	60	CP-QPSK	H	Outer_Full	57.674	60.01	PASS
N41	30	60	CP-16QAM	H	Outer_Full	57.833	60.05	PASS
N41	30	60	CP-64QAM	H	Outer_Full	57.638	59.97	PASS
N41	30	60	CP-256QAM	H	Outer_Full	57.748	60.12	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	96.002	99.39	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	95.872	99.56	PASS
N41	30	100	DFT-16QAM	L	Outer_Full	96.219	99.40	PASS
N41	30	100	DFT-64QAM	L	Outer_Full	95.963	99.60	PASS
N41	30	100	CP-QPSK	L	Outer_Full	96.988	100.7	PASS

N41	30	100	CP-16QAM	L	Outer_Full	97.072	100.7	PASS
N41	30	100	CP-64QAM	L	Outer_Full	97.120	100.8	PASS
N41	30	100	CP-256QAM	L	Outer_Full	97.204	100.4	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	96.197	99.69	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	96.170	99.67	PASS
N41	30	100	DFT-16QAM	M	Outer_Full	96.346	99.53	PASS
N41	30	100	DFT-64QAM	M	Outer_Full	96.355	99.55	PASS
N41	30	100	CP-QPSK	M	Outer_Full	97.393	100.8	PASS
N41	30	100	CP-16QAM	M	Outer_Full	97.195	100.7	PASS
N41	30	100	CP-64QAM	M	Outer_Full	97.347	100.6	PASS
N41	30	100	CP-256QAM	M	Outer_Full	97.474	100.6	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	96.003	99.61	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	95.930	99.60	PASS
N41	30	100	DFT-16QAM	H	Outer_Full	96.120	99.54	PASS
N41	30	100	DFT-64QAM	H	Outer_Full	96.058	99.58	PASS
N41	30	100	CP-QPSK	H	Outer_Full	97.236	100.6	PASS
N41	30	100	CP-16QAM	H	Outer_Full	96.953	100.6	PASS
N41	30	100	CP-64QAM	H	Outer_Full	97.012	100.6	PASS
N41	30	100	CP-256QAM	H	Outer_Full	97.229	100.7	PASS

Test Graphs

