

3100 Operational Principle

This project uses the Brecis MSP2007 CPU the processor and it offers a memory configuration of 4M bytes Flash and 256M bytes SDRAM.

This device supports 10/100 Base-TX LAN network interfaces using Davicom DM9161 as the PHY controller. The interface to the LAN network is through an RJ45 STP port.

A parallel port is available with I/O transmission by AT7601F.

USB print port uses NEC uPD720101 host controller, which fully compliant USB specification 2.0.

WE support one USB ports.

Using WLAN module using mini PCI wireless module.

There are 4 bi-color LEDs. All control by firmware.

This device derives its power from a 12V DC @ 1A power adapter which needs to be converted to 1.8V, 3.3V and 5V DC.