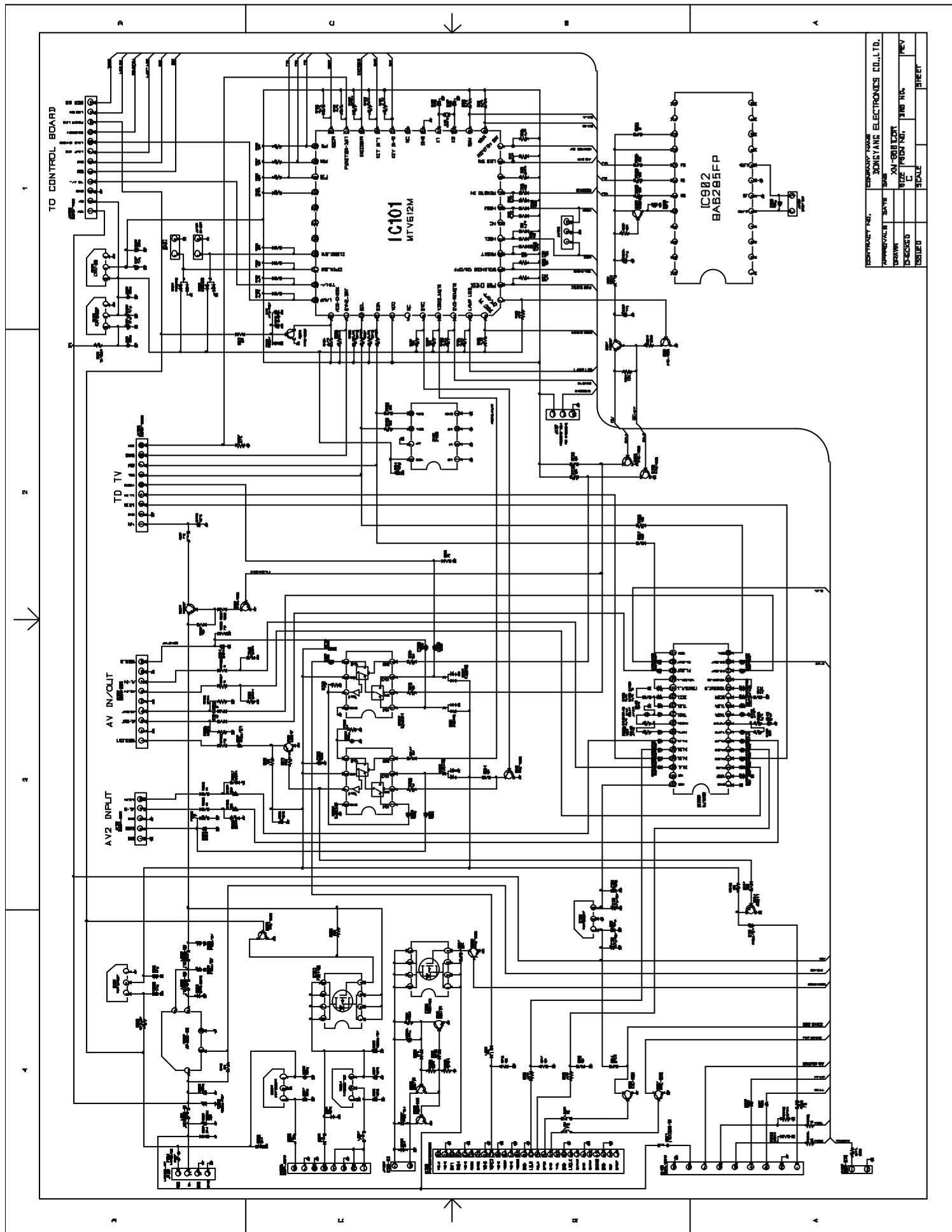


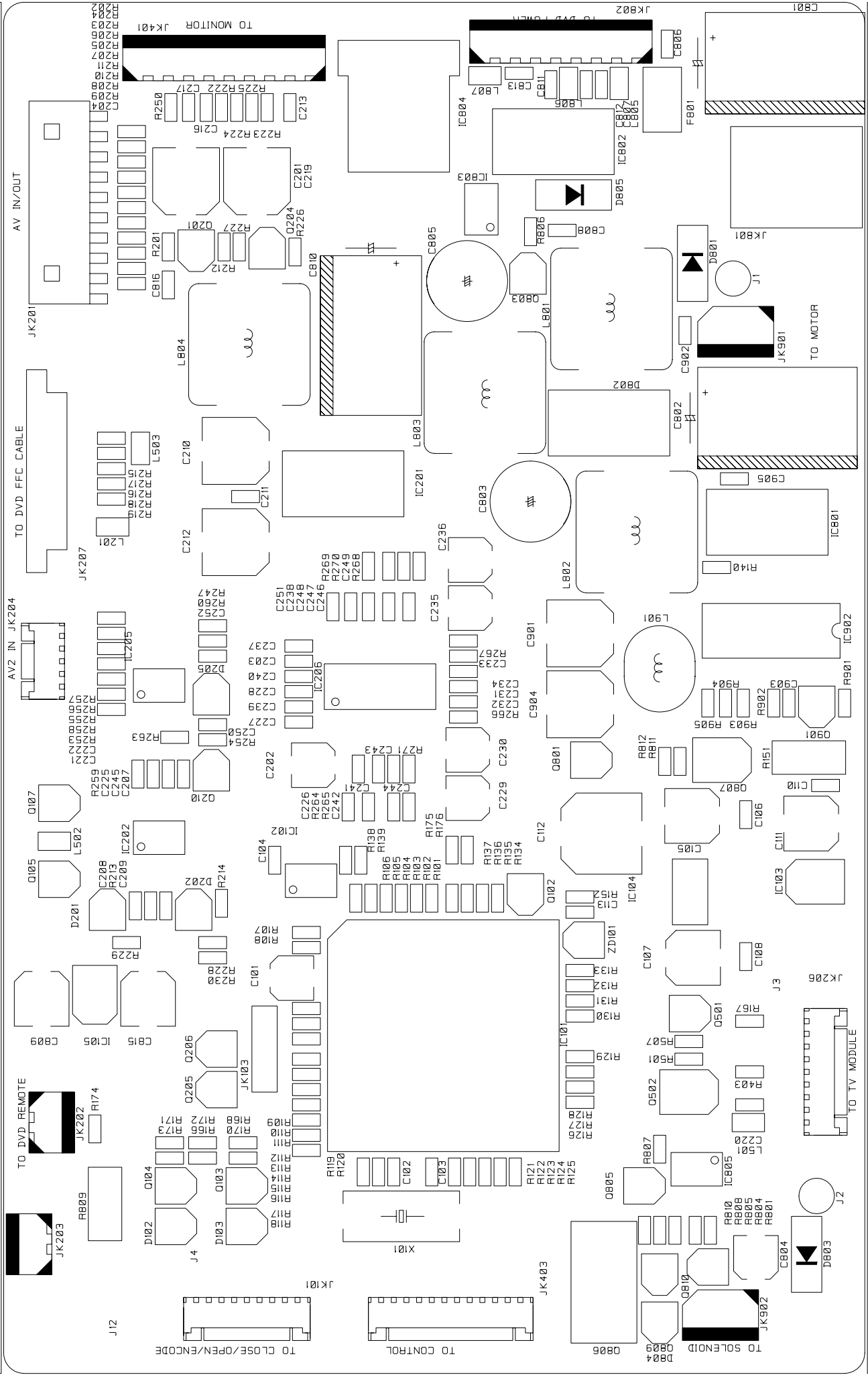
APPENDIX E
: SCHEMATIC DIAGRAM



DESIGNER'S NAME	JUNJANG ELECTRONICS CO., LTD.
DESIGNER'S NAME	YOUNGJAE KIM
DATE	2000.01.10
SIZE	100mm x 100mm
CHECKED	C
TESTED	
SCALE	1:1
SHEET	1

MODEL : XM-950(CBRIP.P)
DATA : 05.12. 27
REVISION : 2.0
DESIGN BY Y. H. SON

CVBS_IN
AV2_IN JK204



V-CU

V-CU