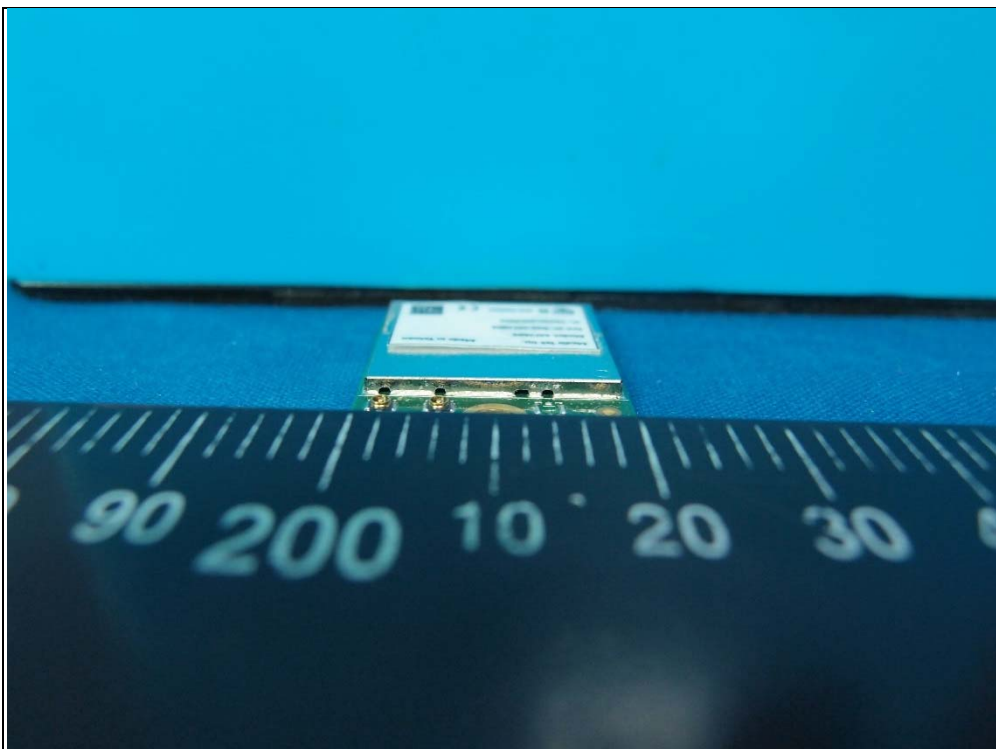
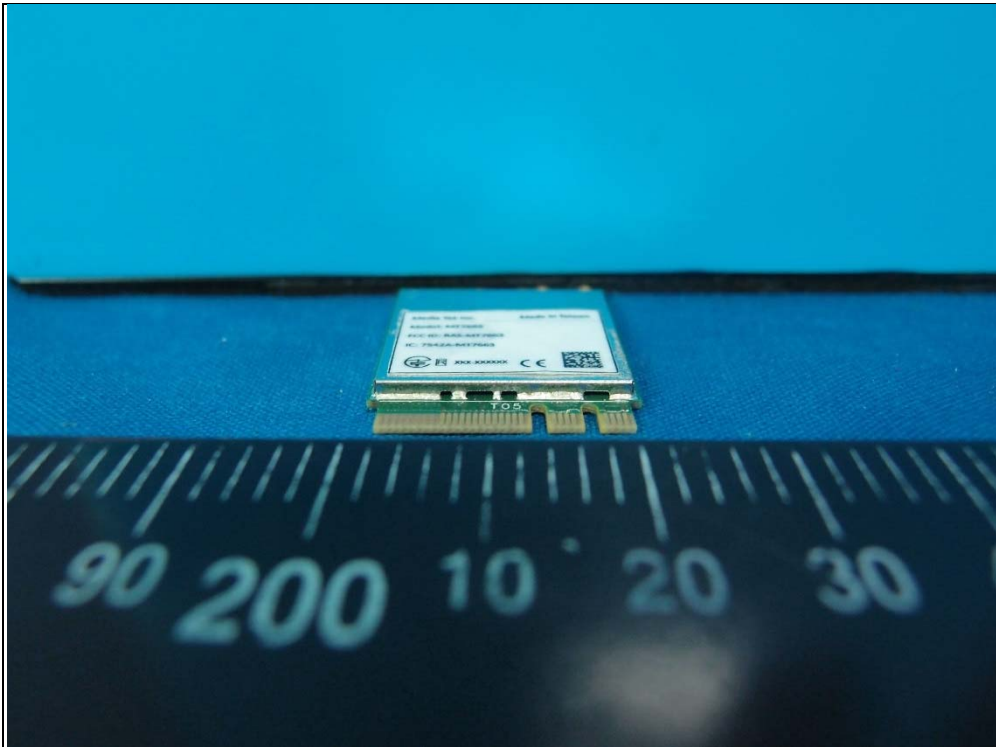
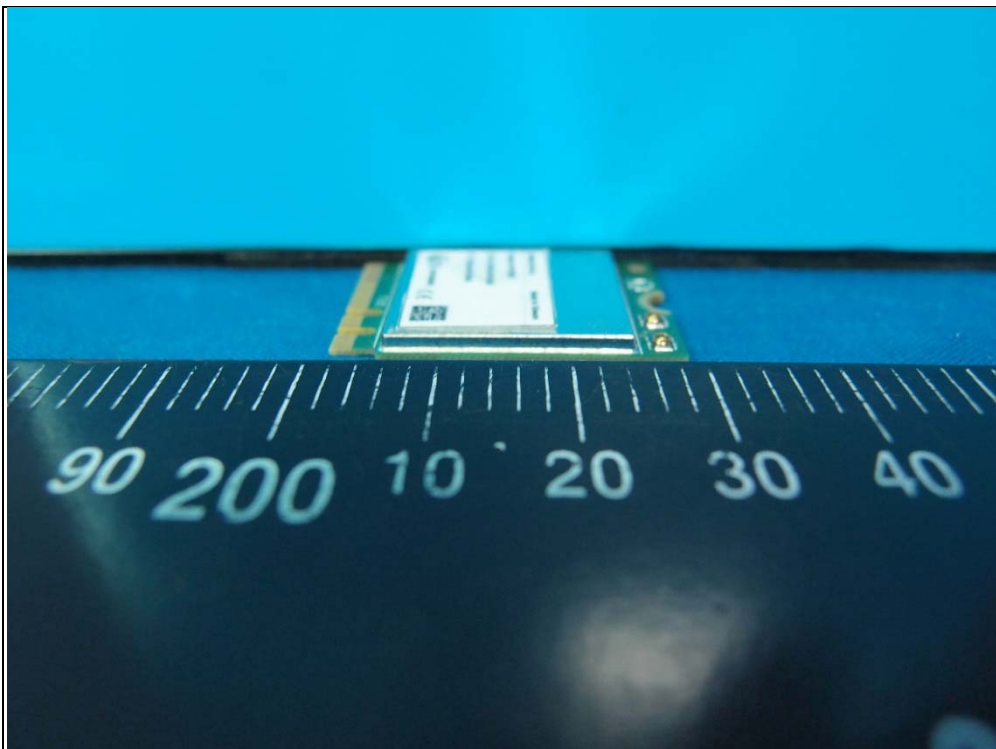
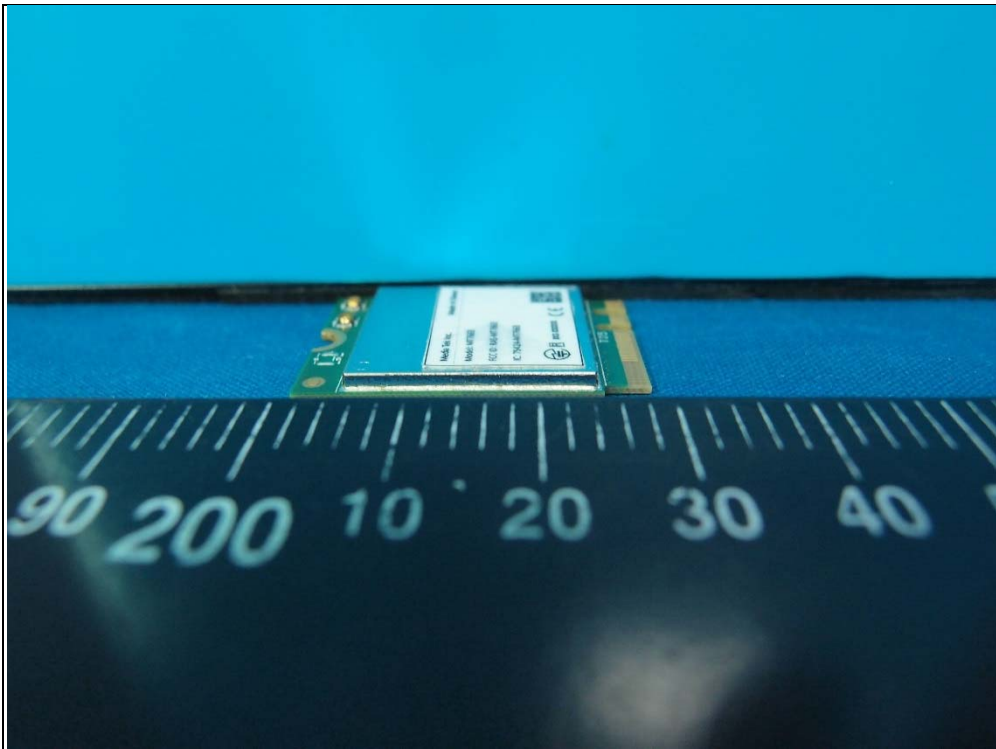
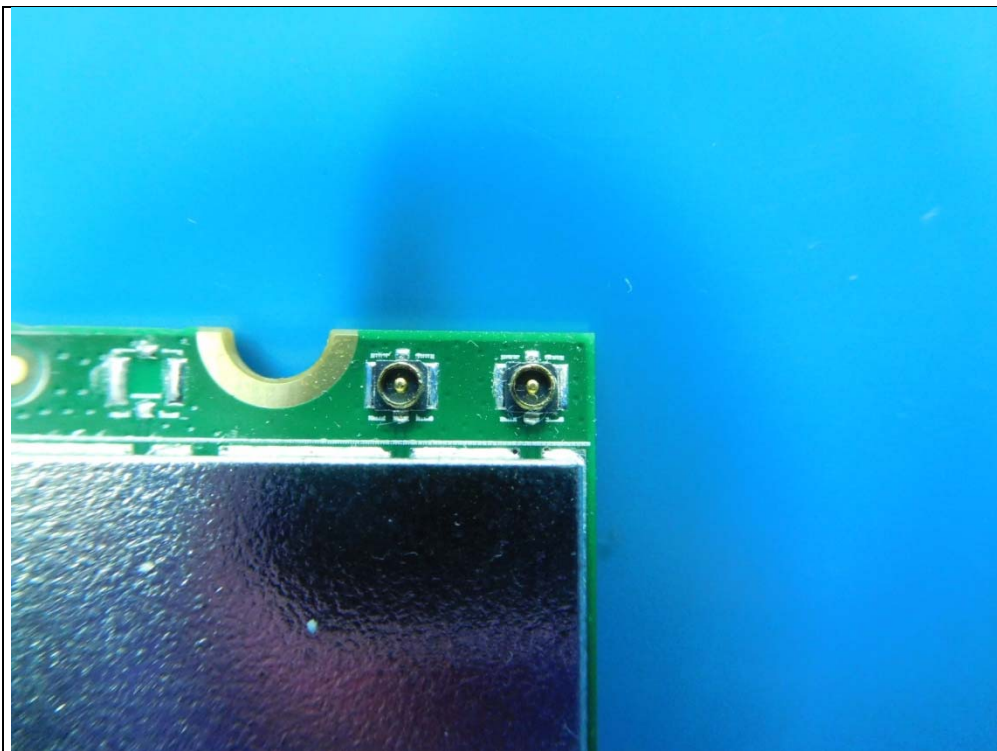


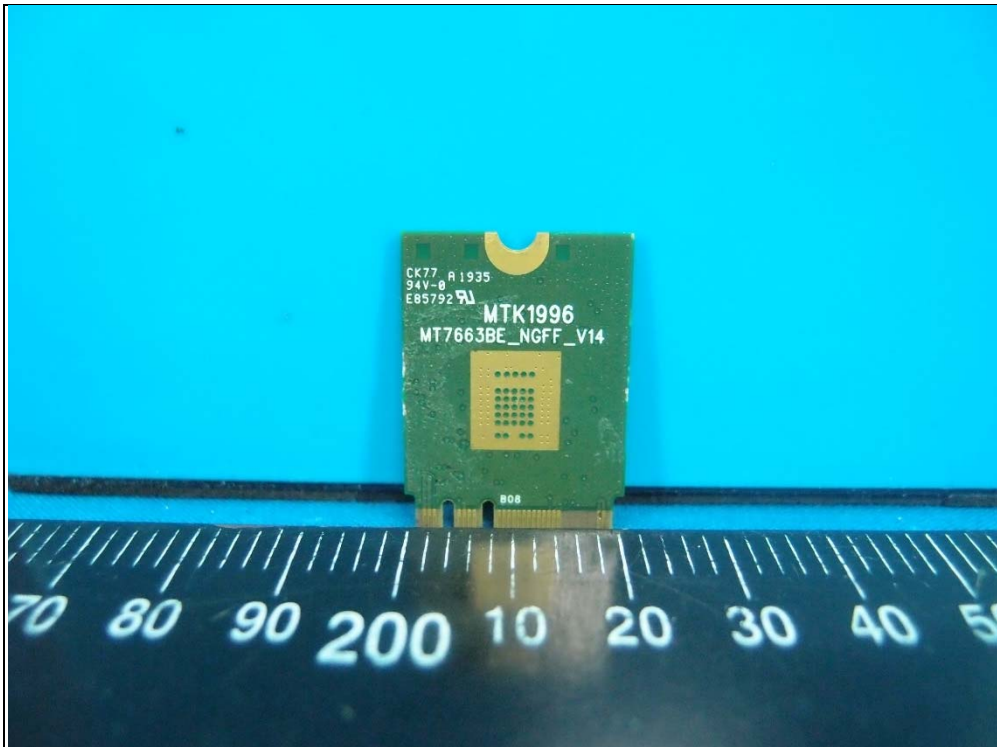
CONSTRUCTION PHOTOS OF EUT

Interface: PCIe

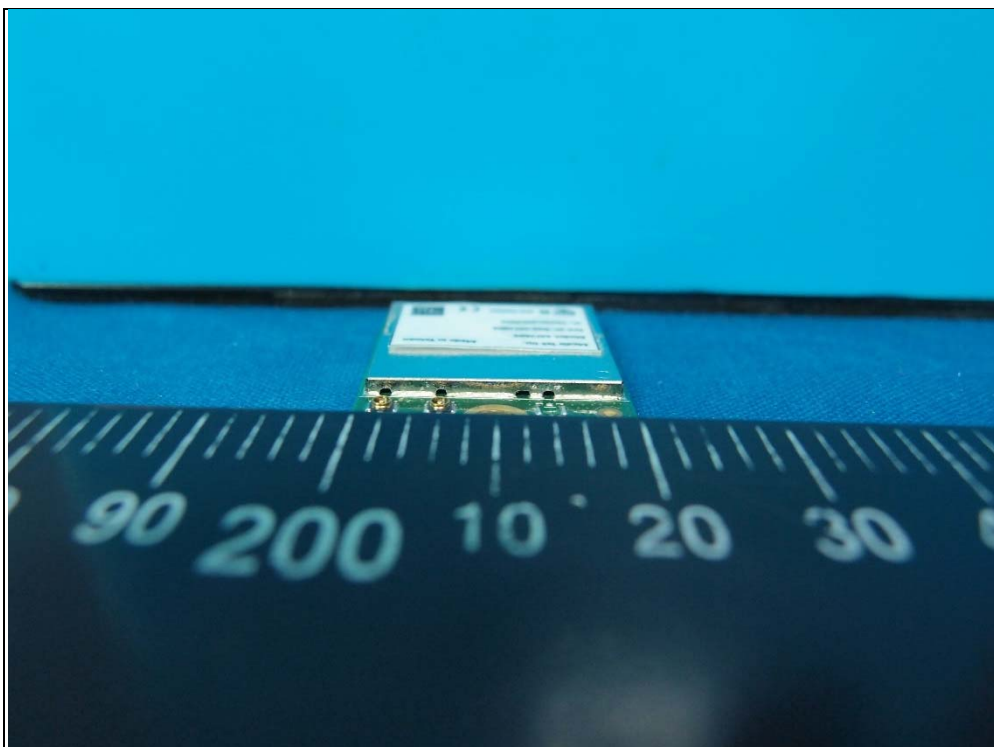
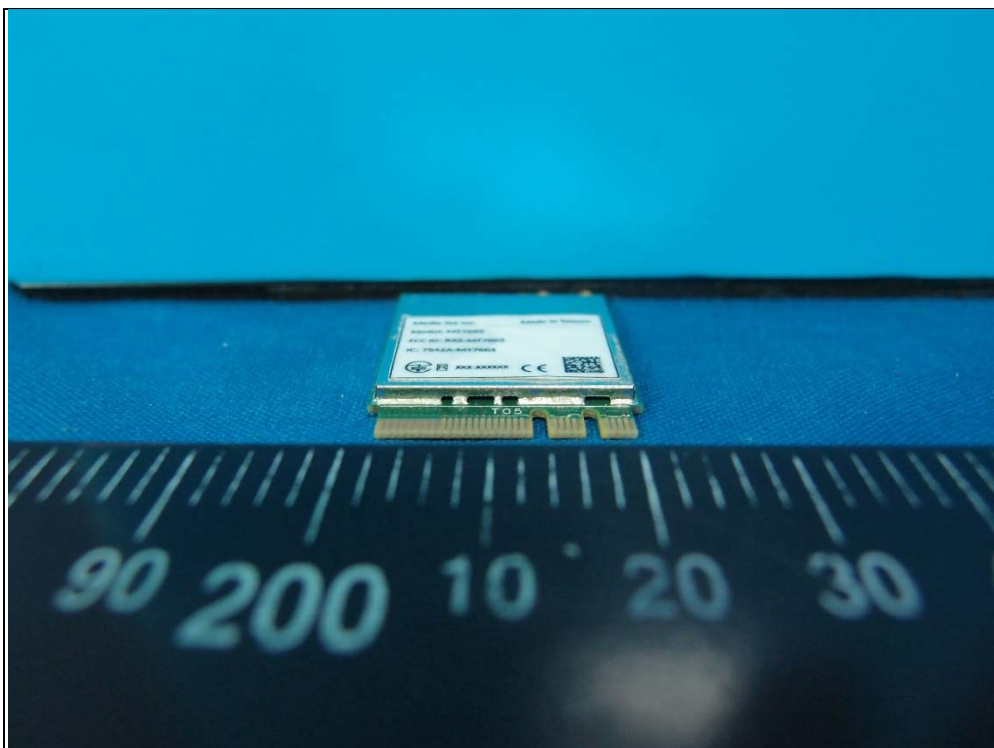


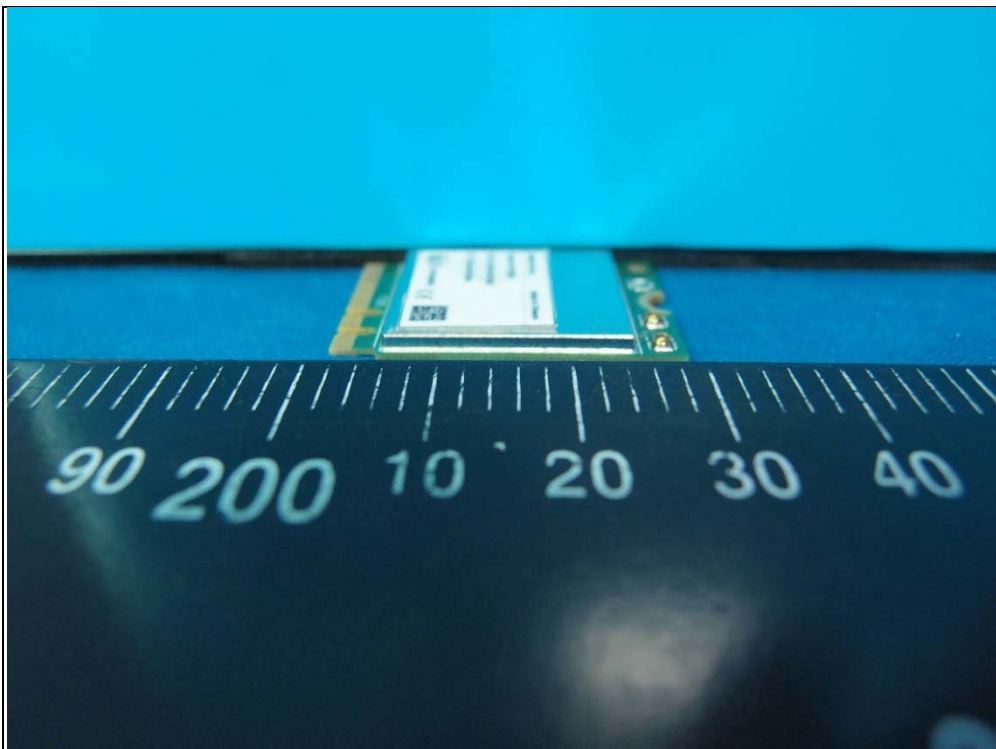
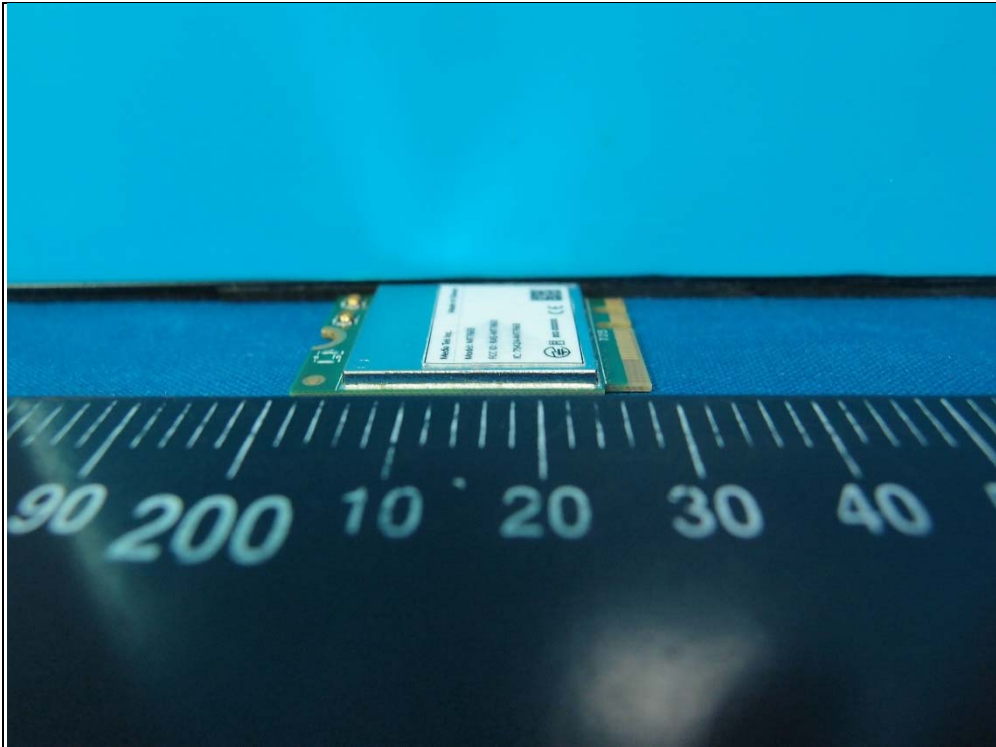


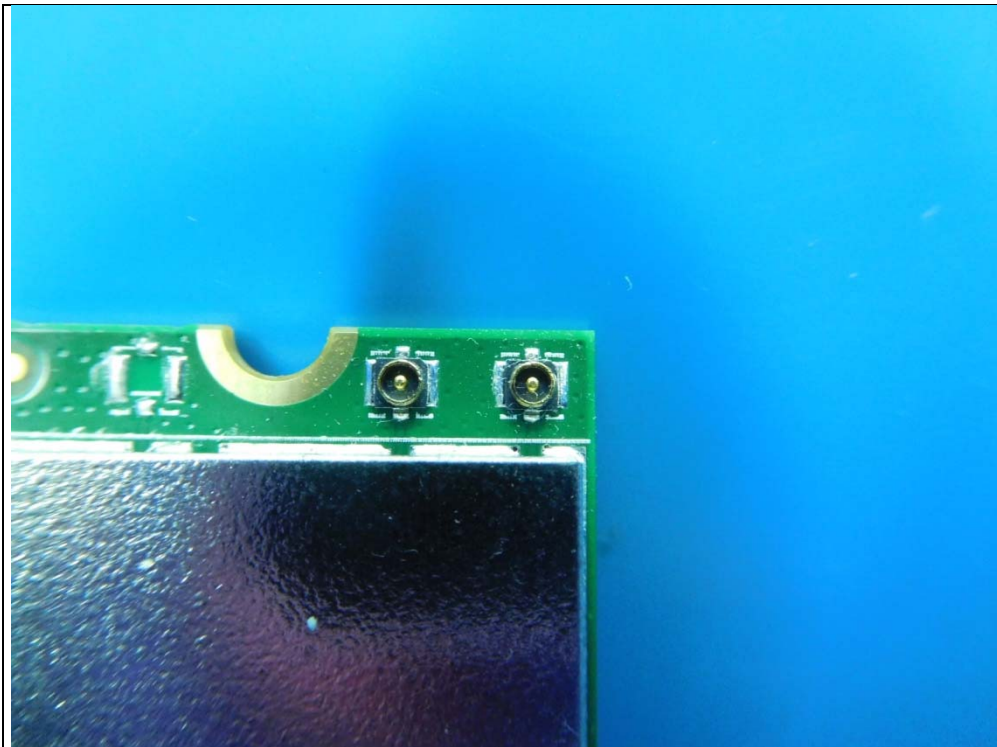


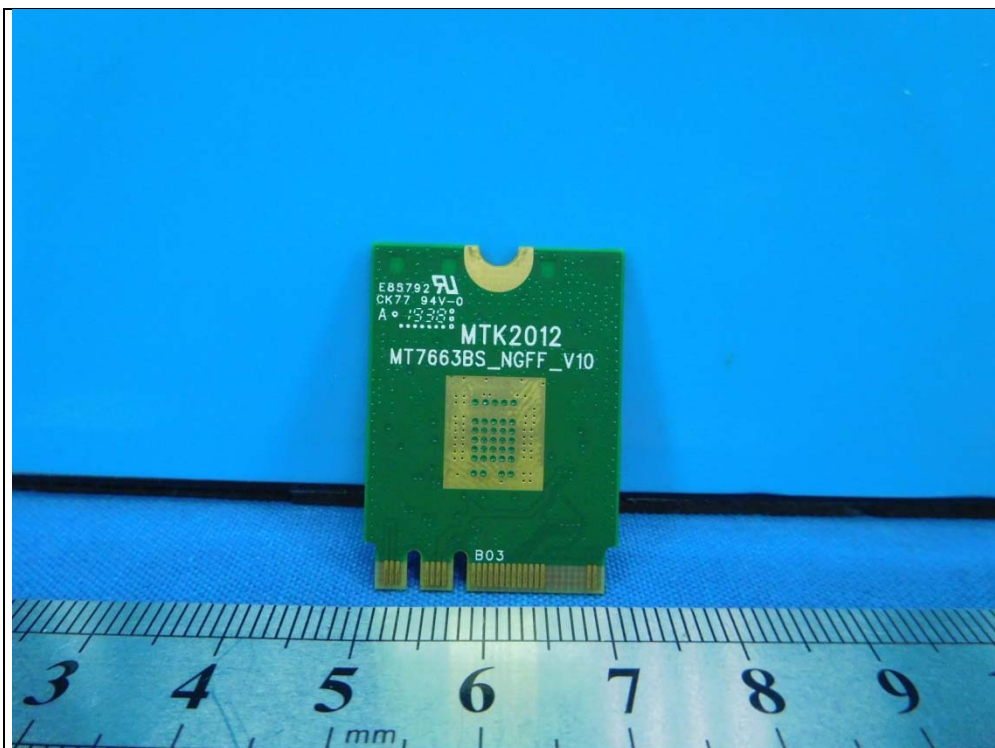


Interface: SDIO

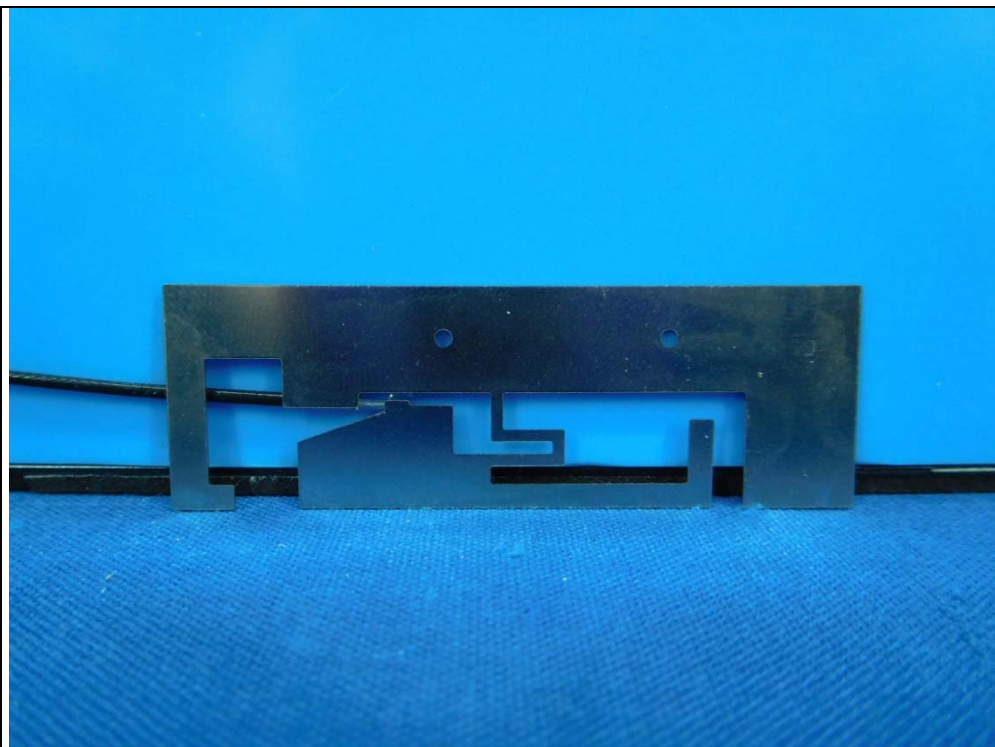
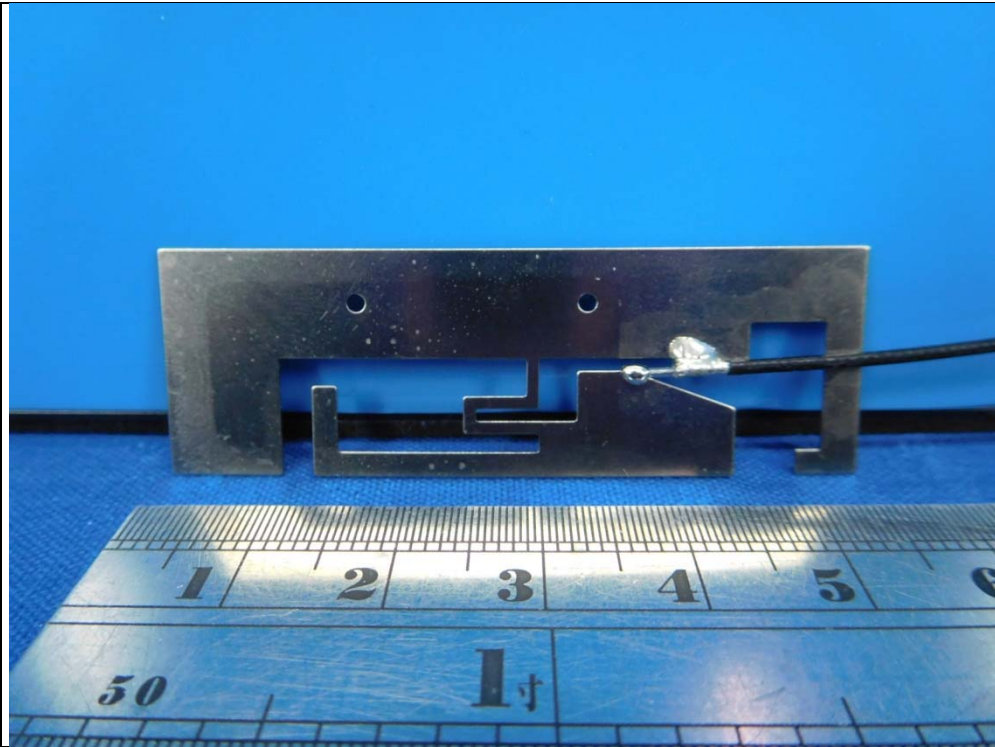


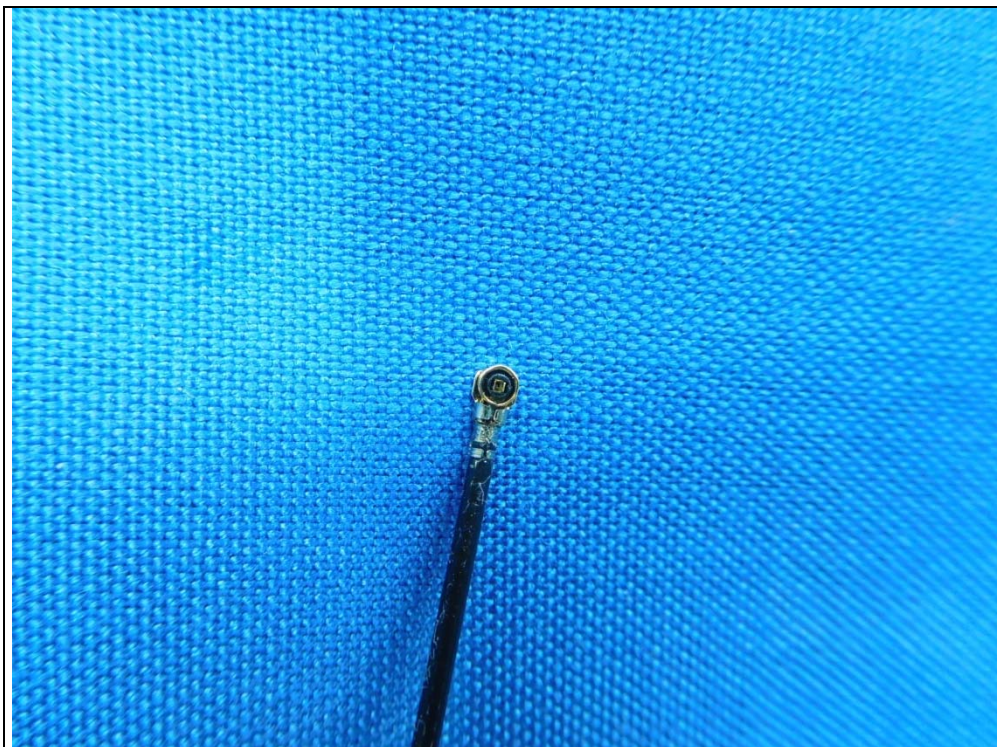




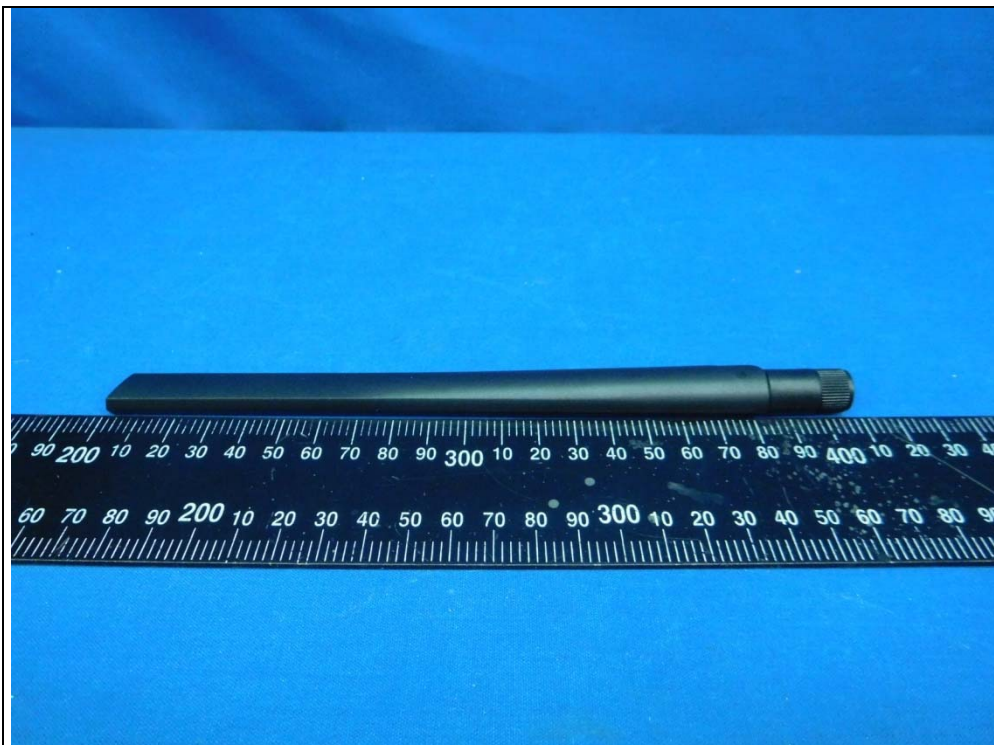


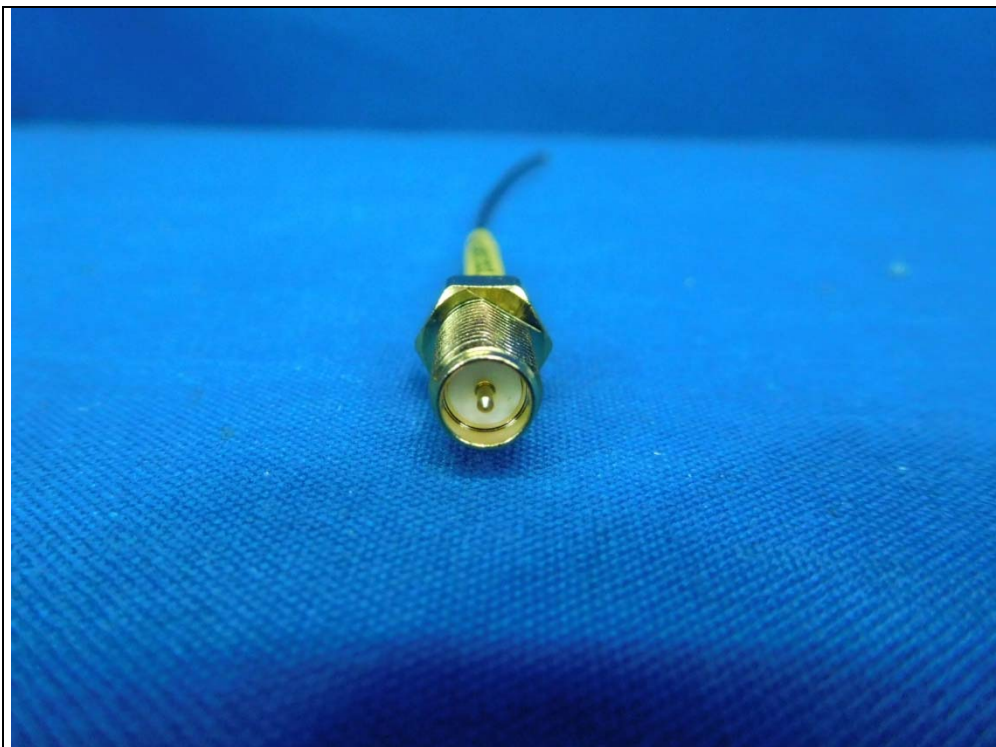
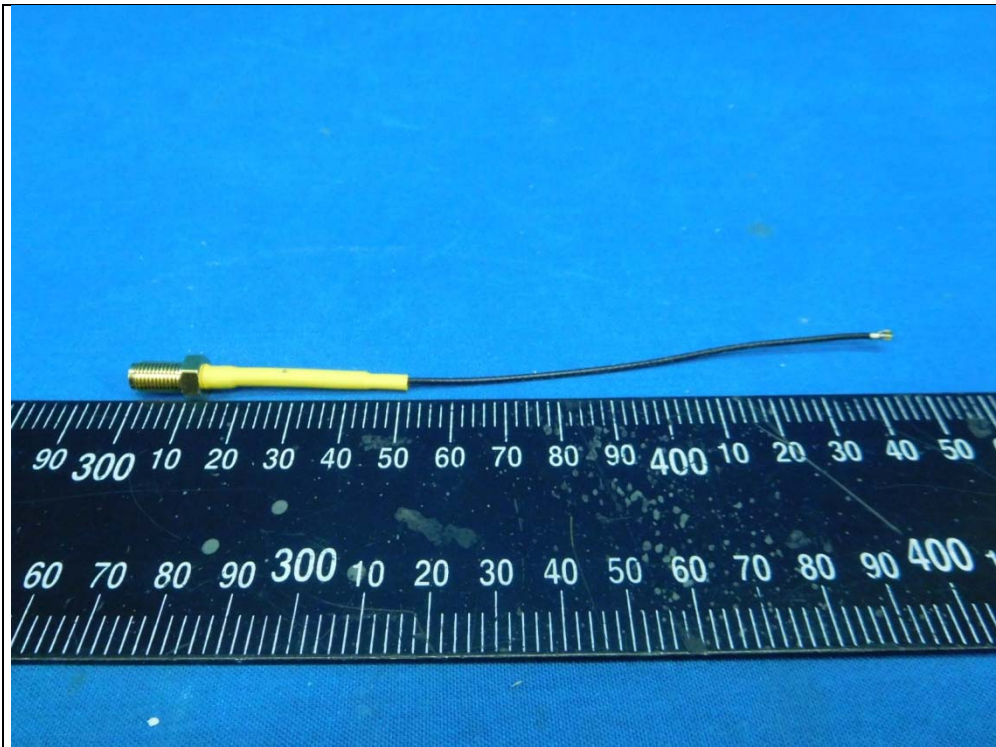
Antenna Set 1

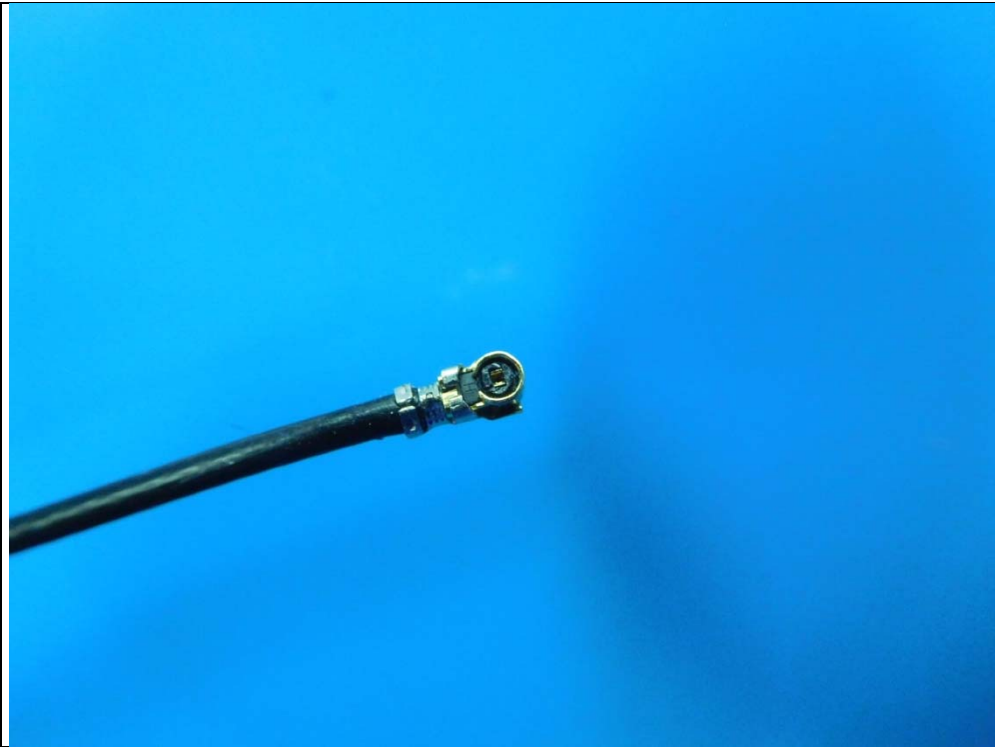




Antenna Set 2







Antenna Set 3

