

Block Diagram

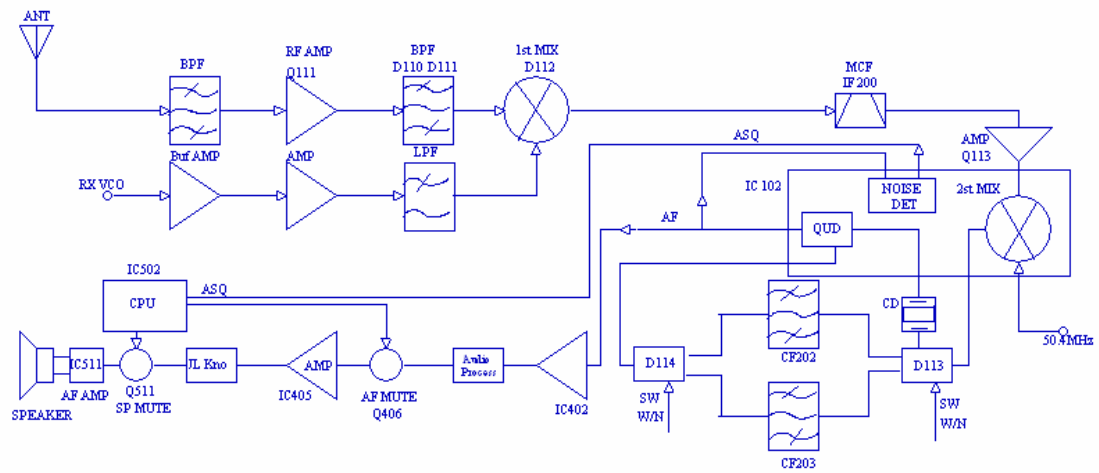


Fig 1 Receiver Circuit

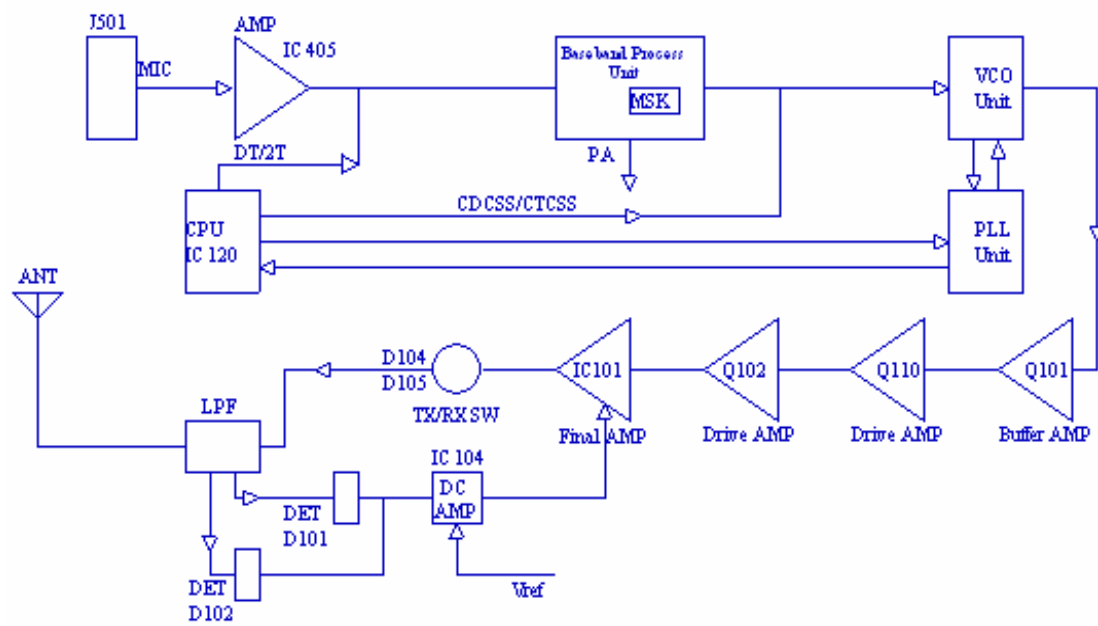


Fig 2 Transmitter Circuit

