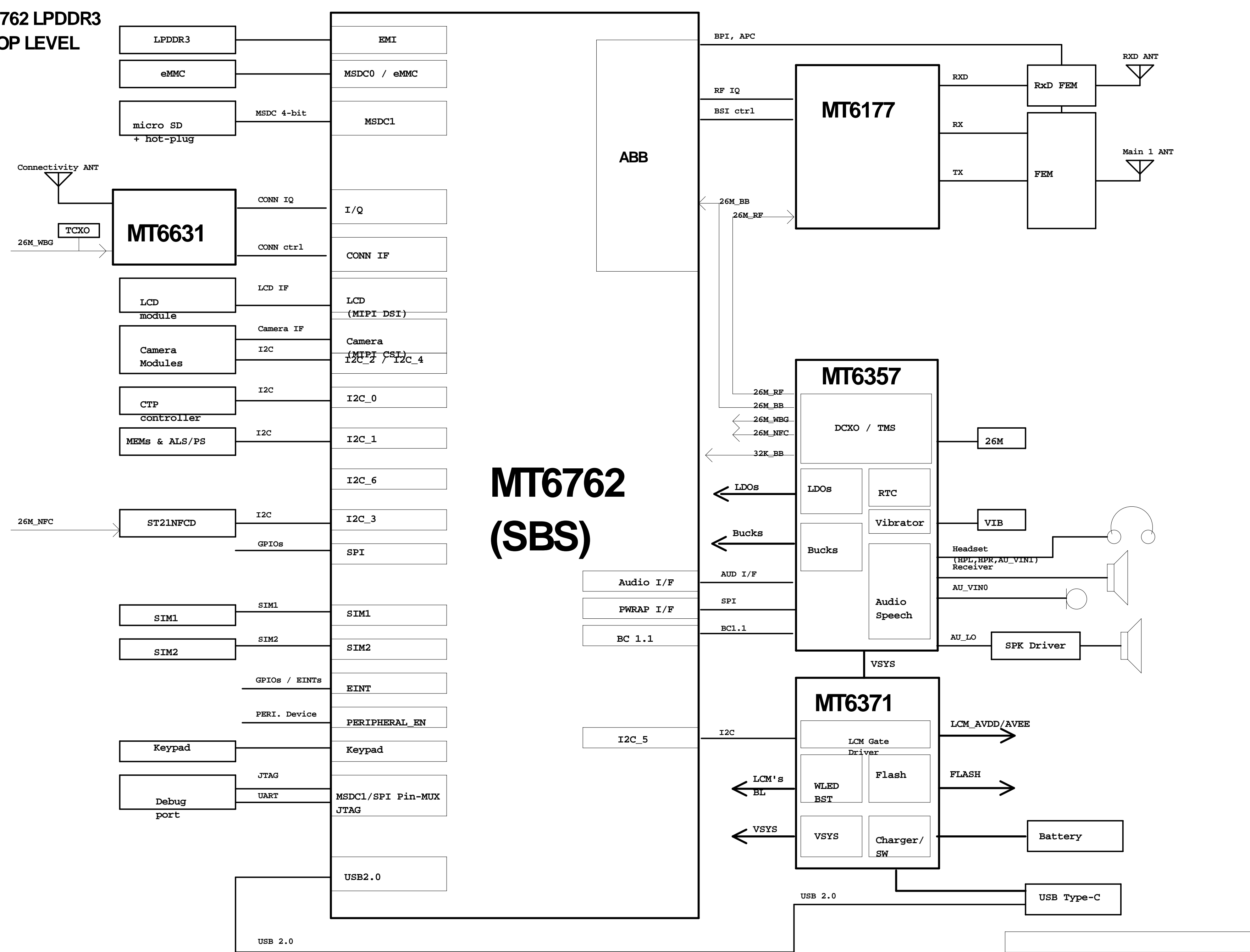
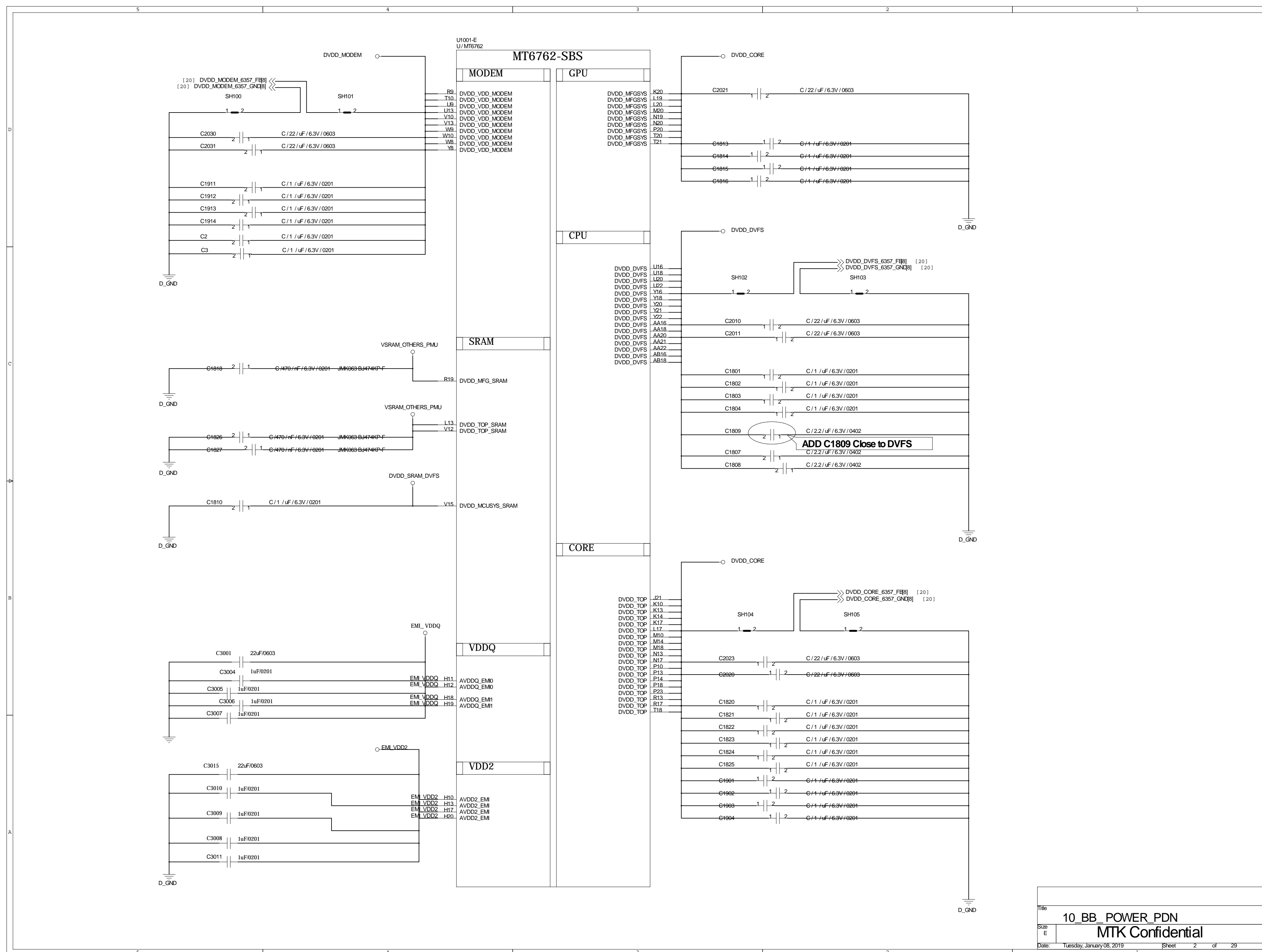
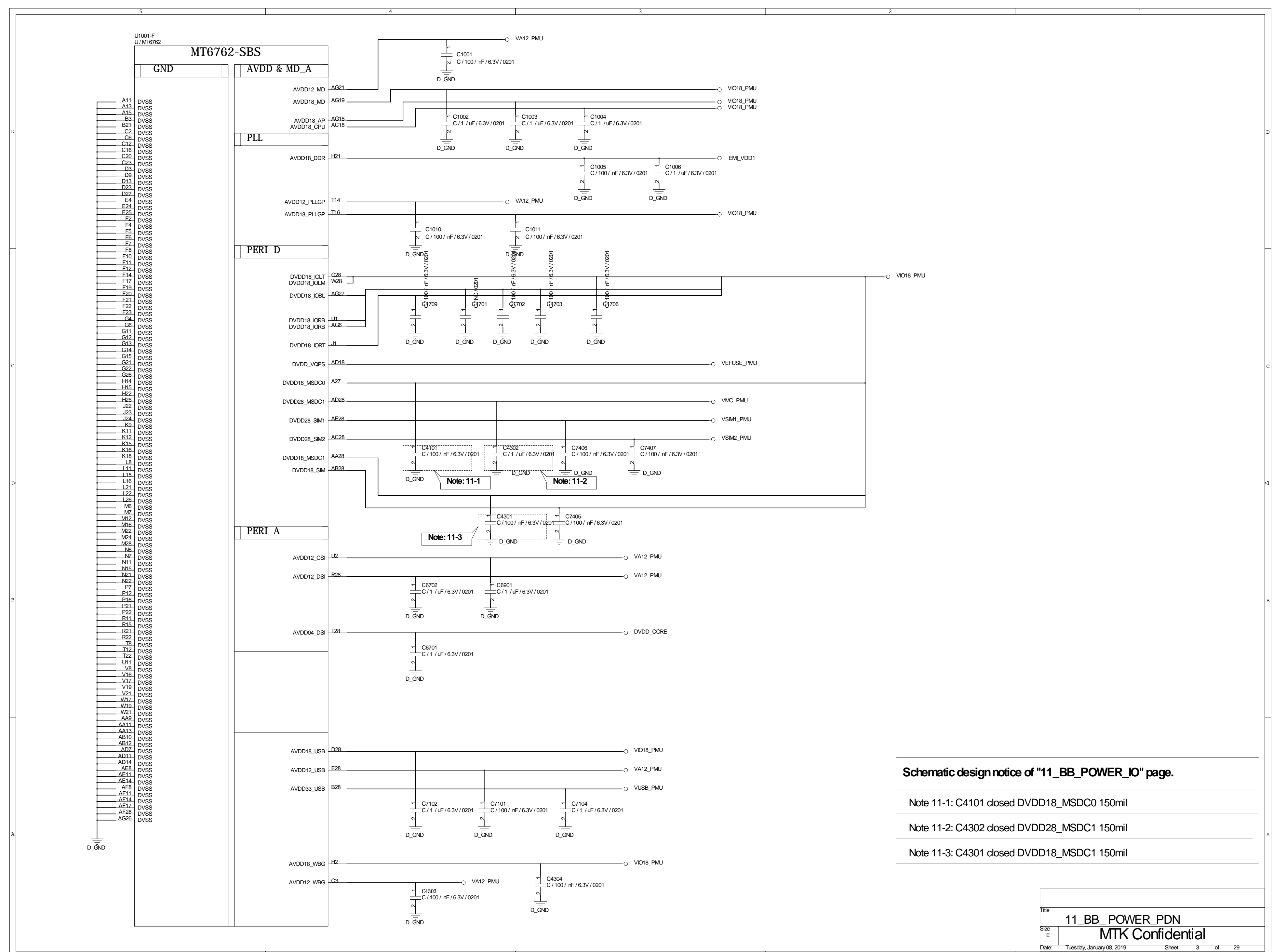


Project : MT6762 LPDDR3
REF_SCH TOP LEVEL





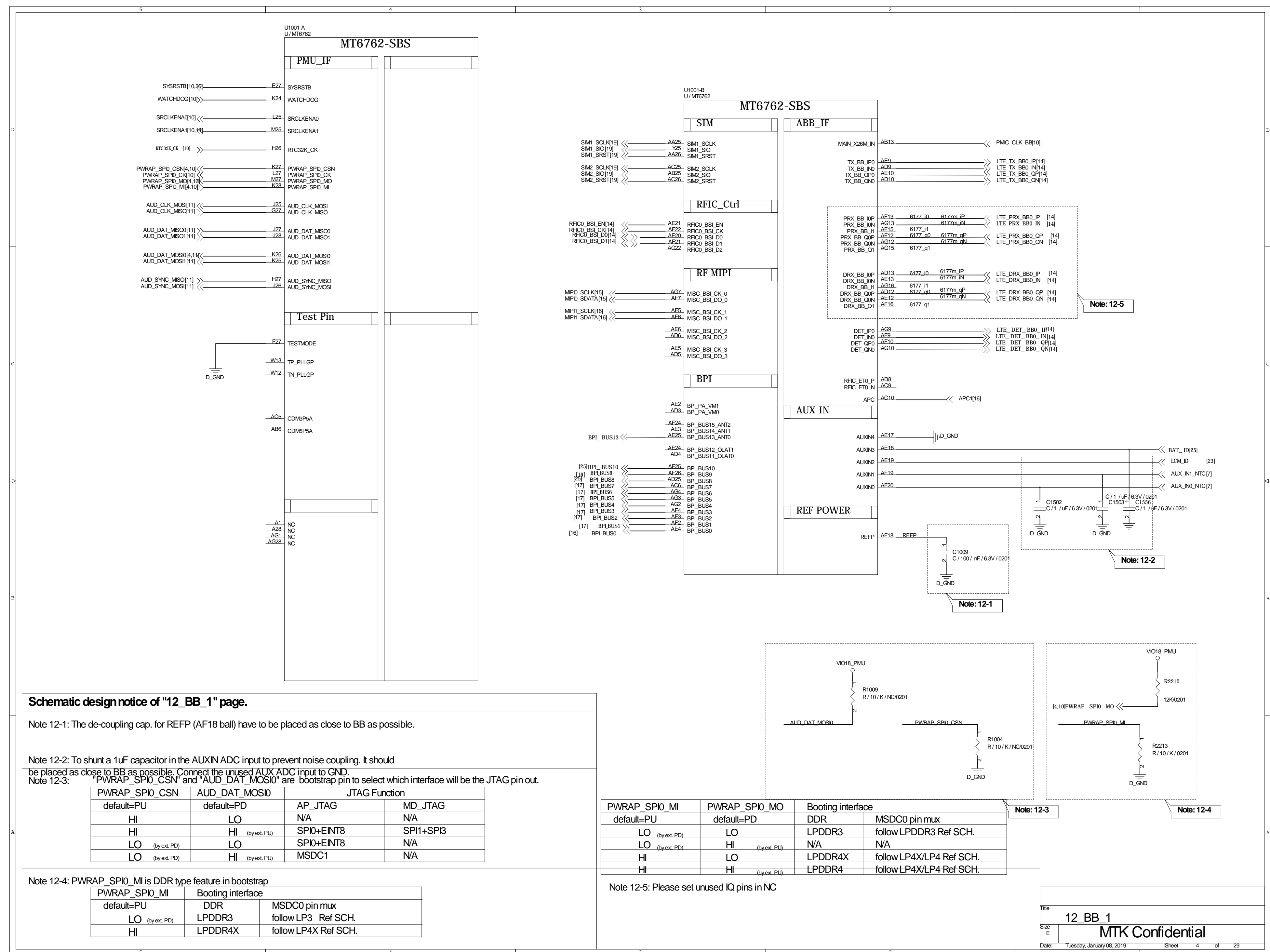


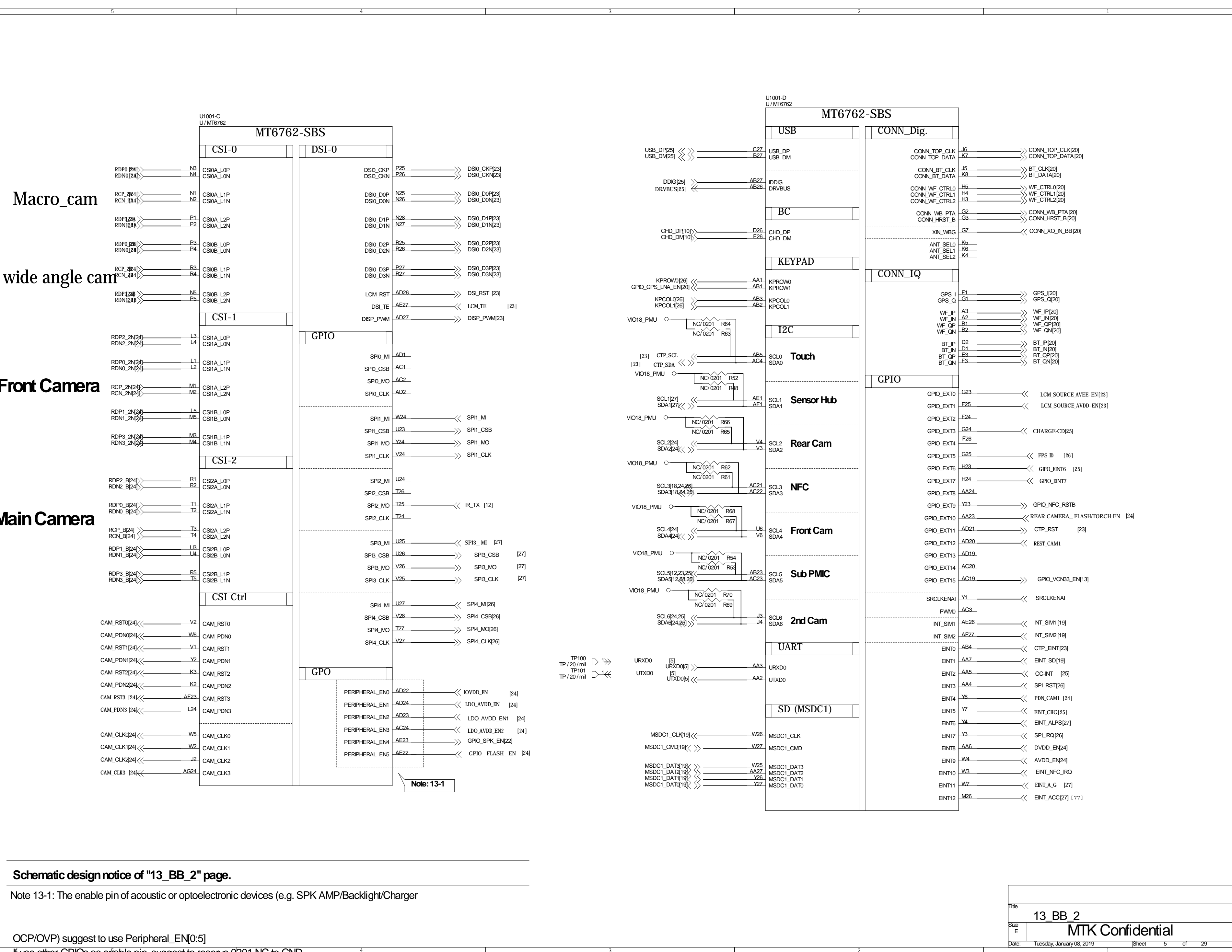
Schematic design notice of "11_BB_POWER_ID" page.

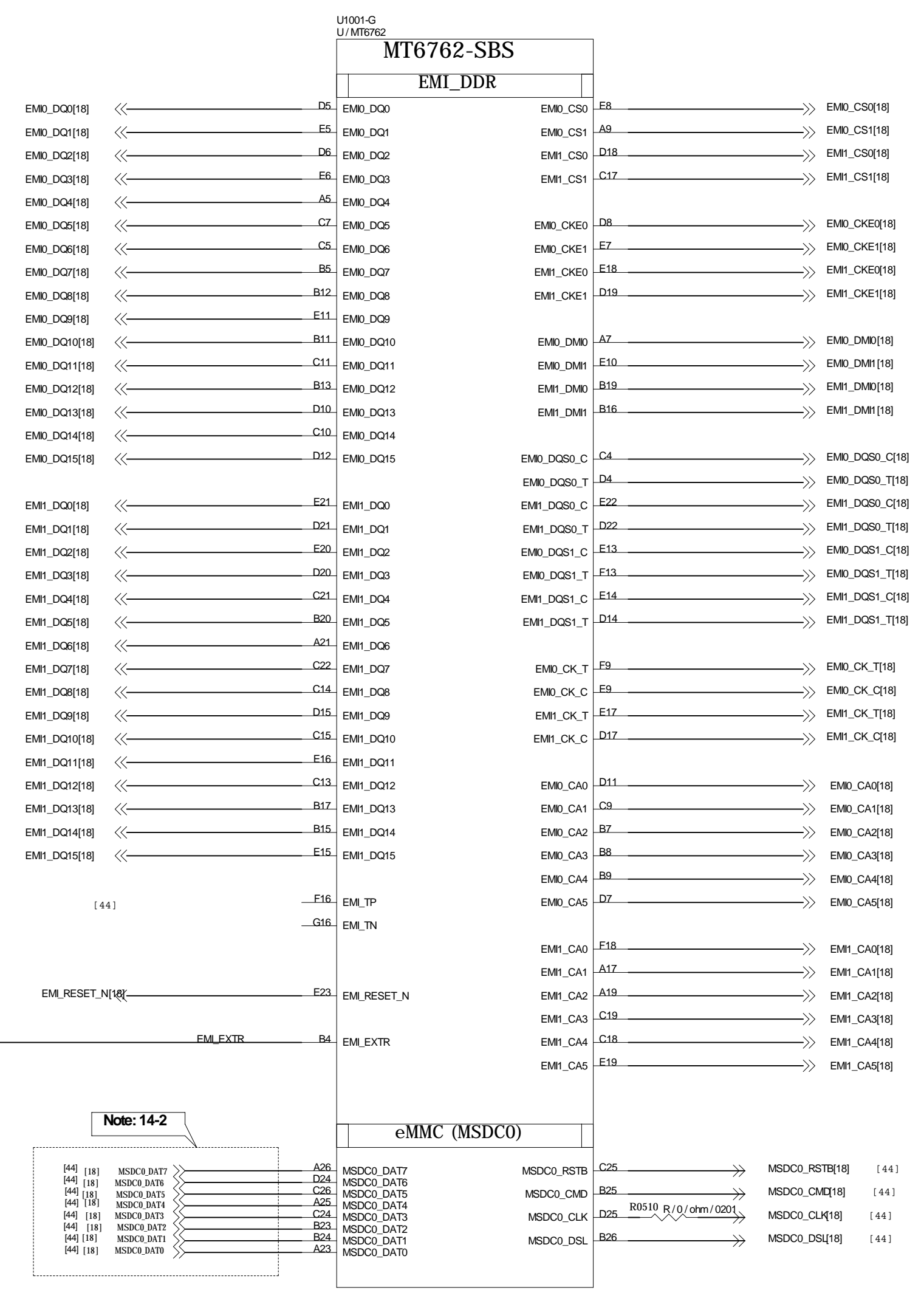
Note 11-1: C4101 closed DVDD18_MSDC0 150mil

Note 11-2: C4302 closed DVDD28_MSDC1 150mil

Note 11-3: C4301 closed DVDD18_MSDC1 150mil



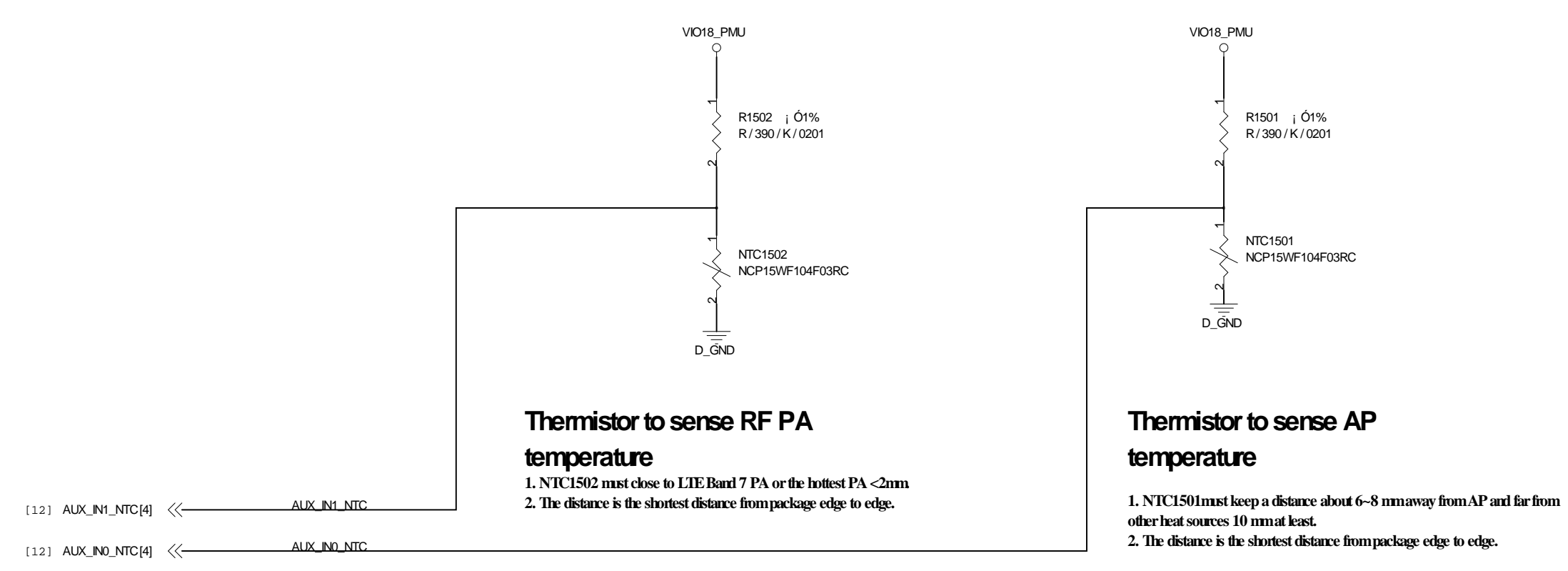


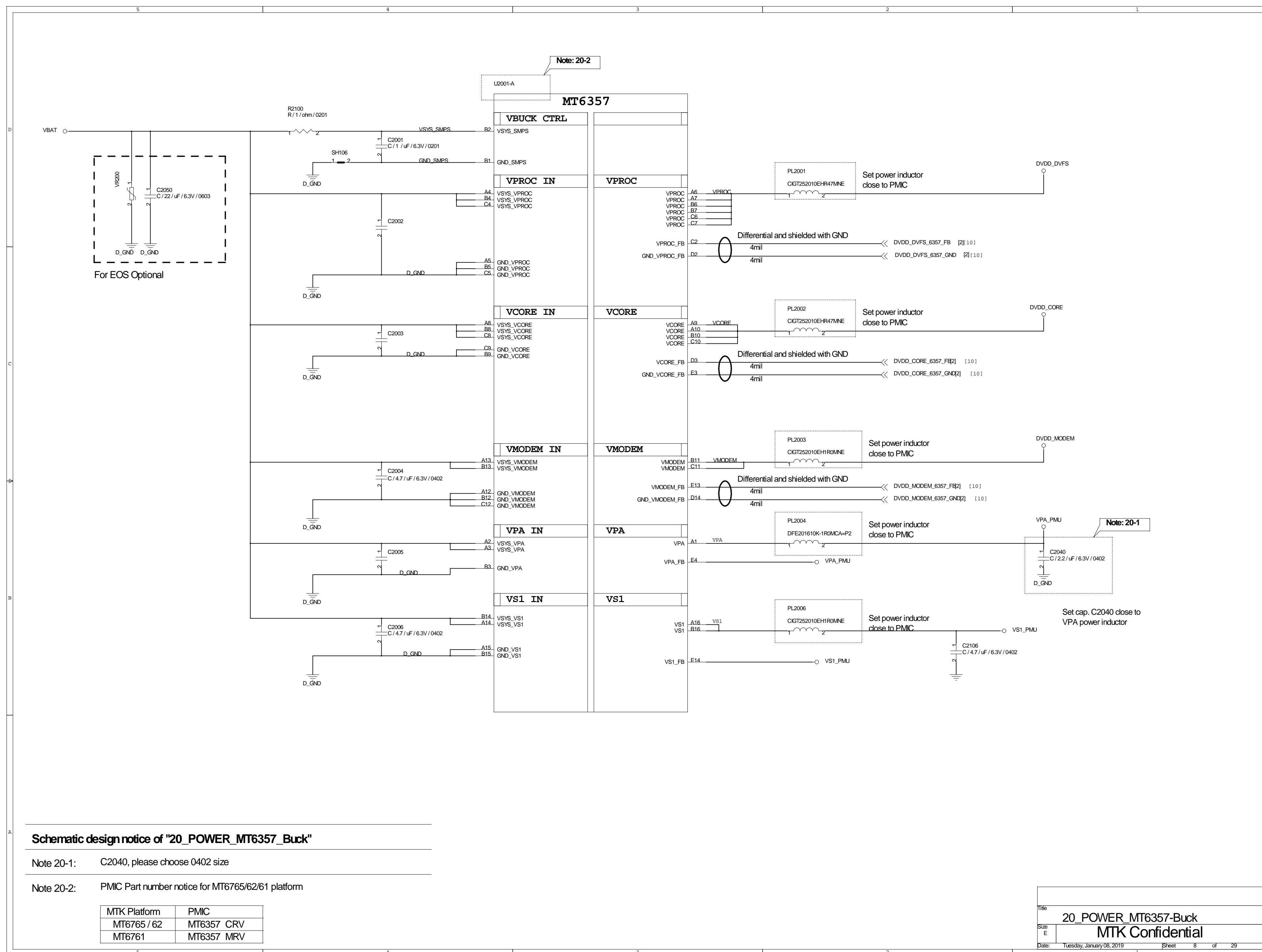


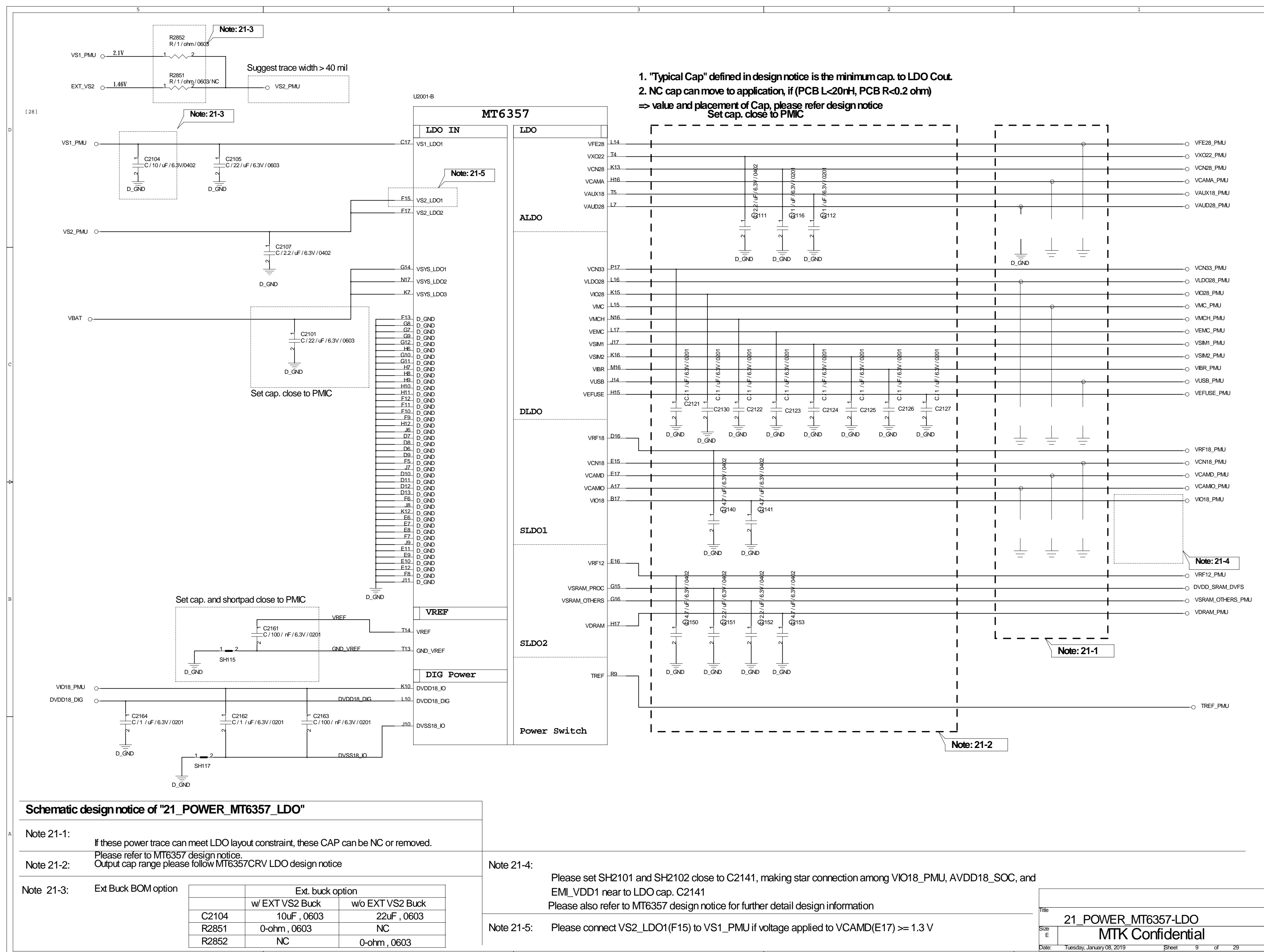
Schematic design notice of "14_BB_3" page.

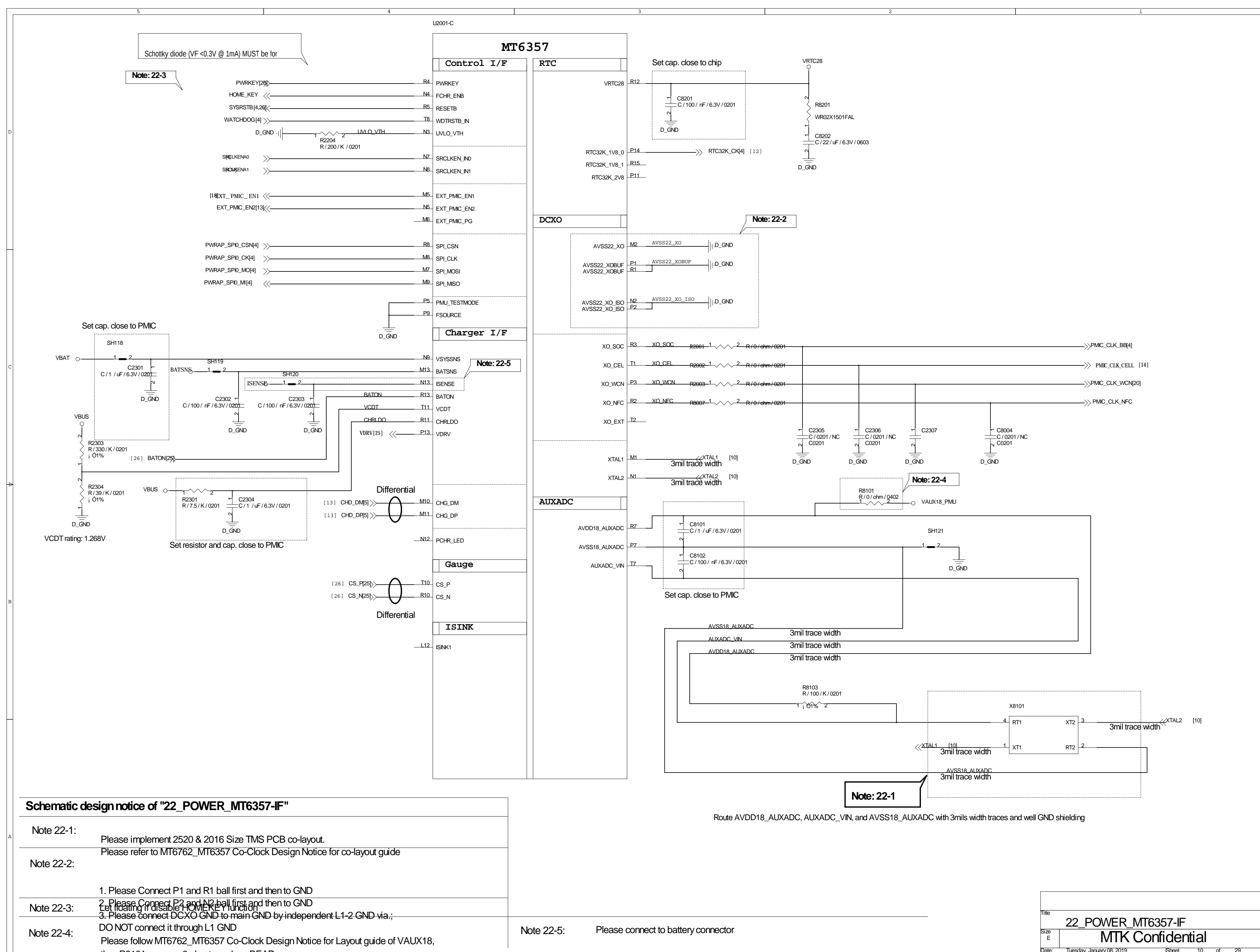
Note 14-1: R4001 please select 34.8 ohm (1%) resistor

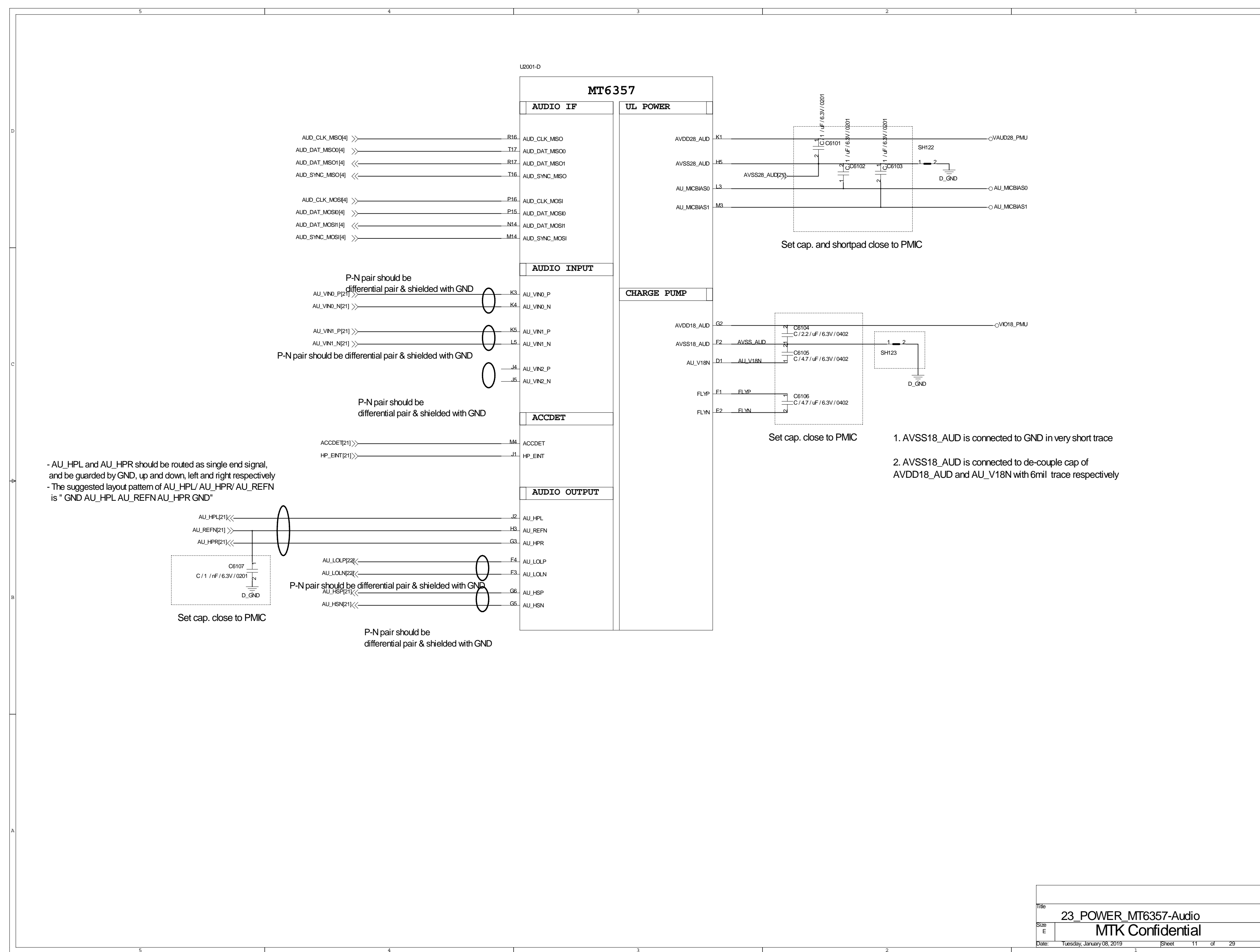
Note 14-2: Please check eMCP LP3 and eMCP LP4 pin mux



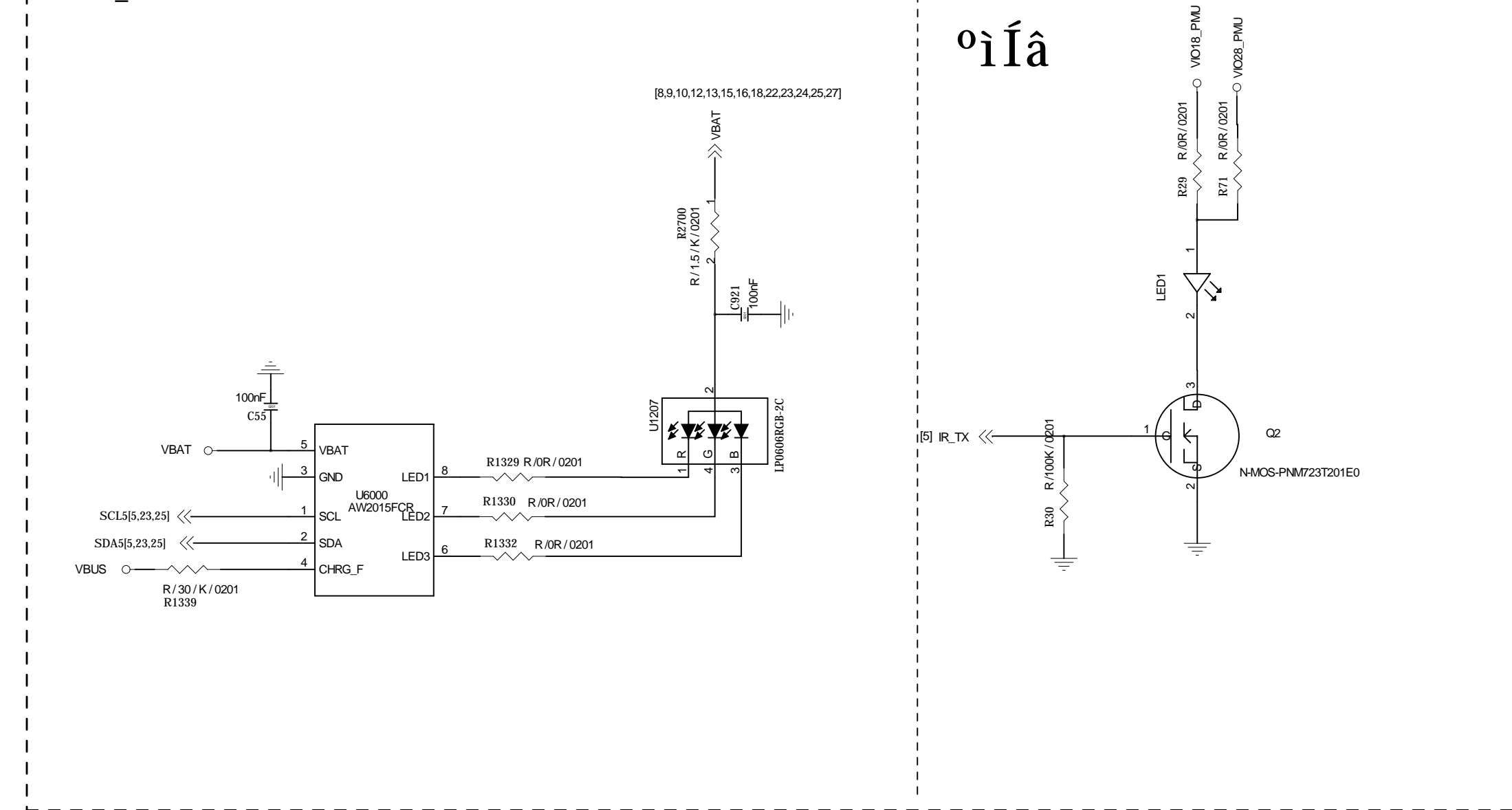




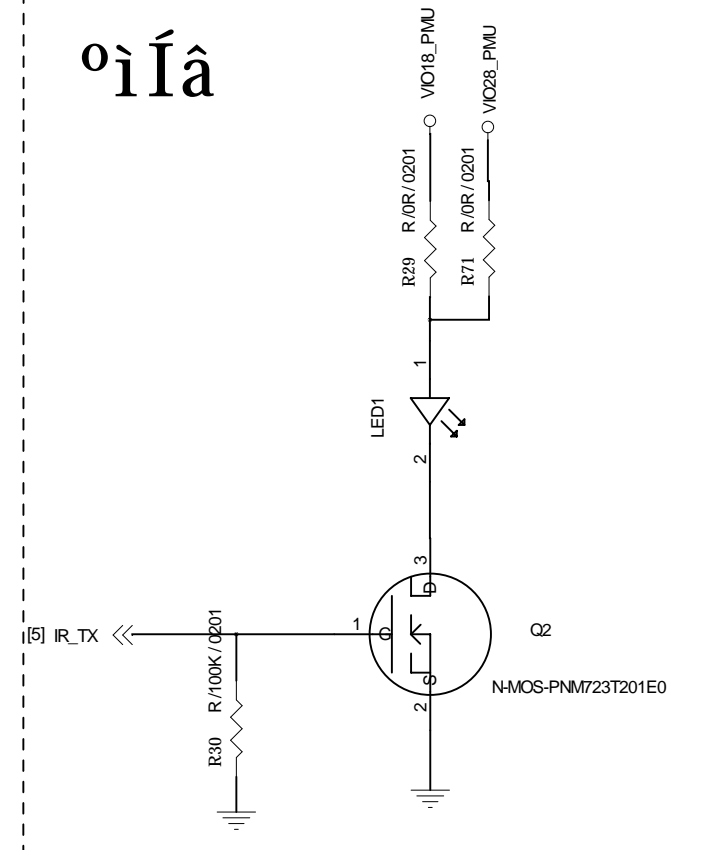




RGB_LED



011a

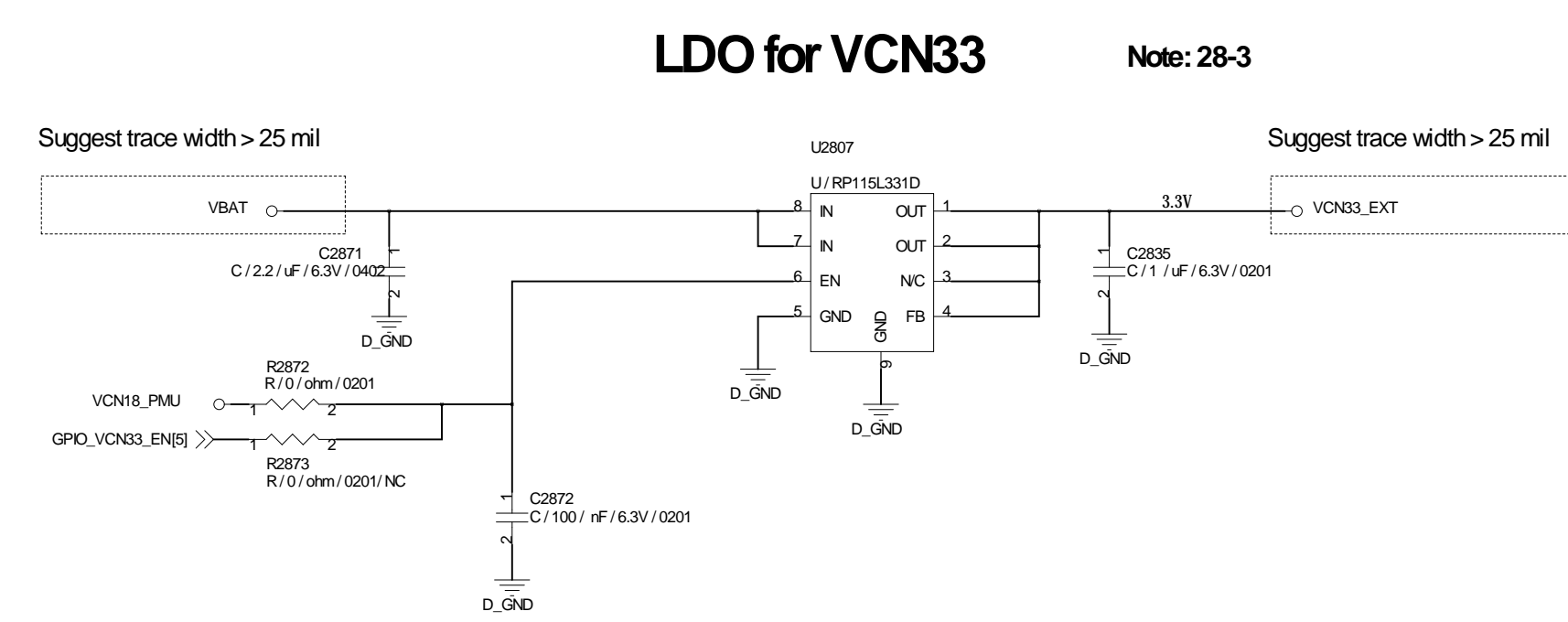
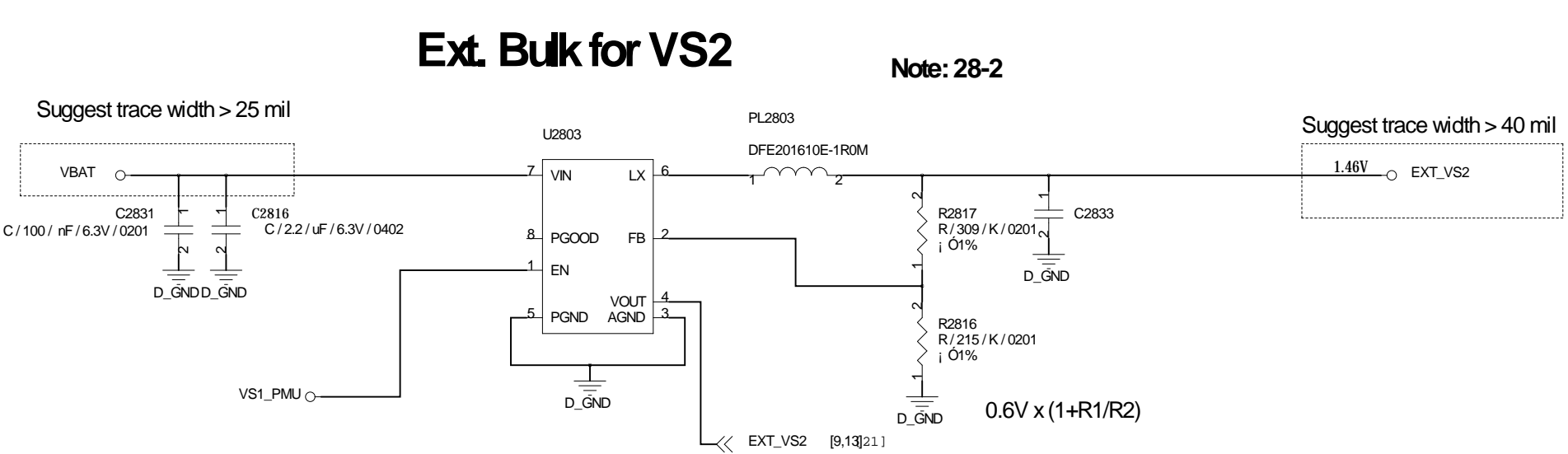
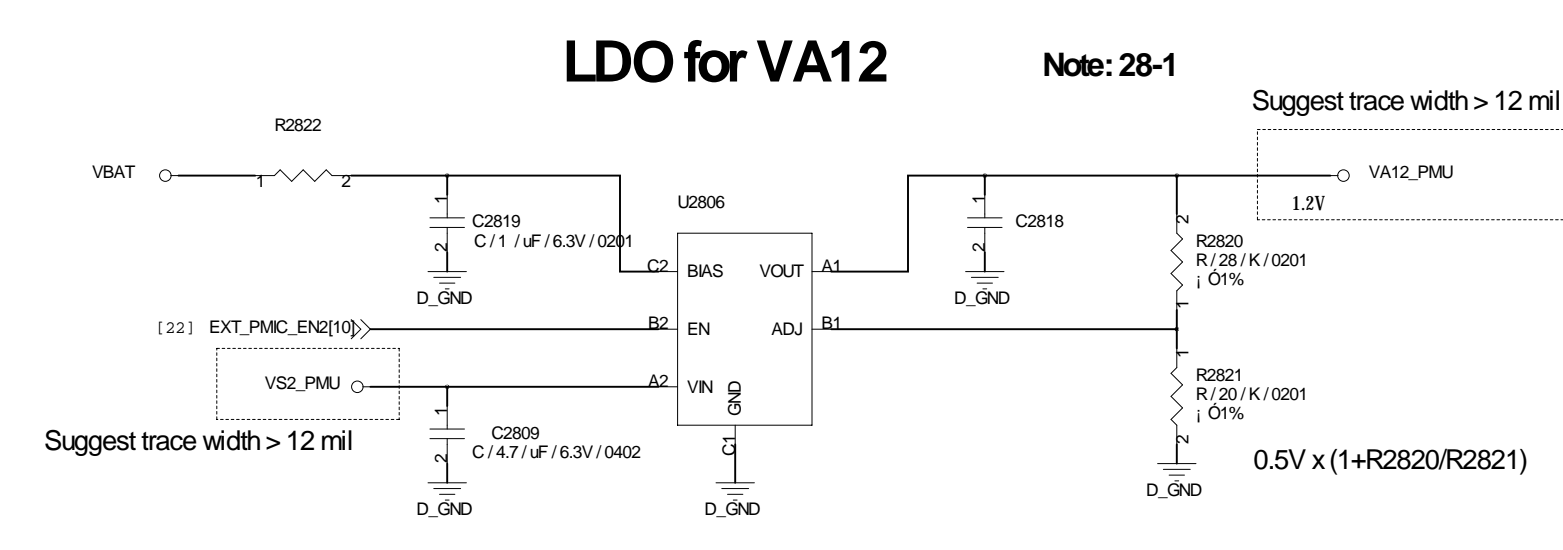


Schematic design notice of "27_POWER_SubPMIC-HV powers" page:

Note 27-1: To minimize RF de-sense, it is recommended to reserve 0-ohm and 0402 cap for BOM fine tuning.

Note 27-2: To minimize RF de-sense, it is recommended to reserve 0-ohm and 0201 cap. for BOM fine tuning.

Note 27-3: C2705 could be replaced with C / 1 / uF / 50V + C / 1 / uF / 50V

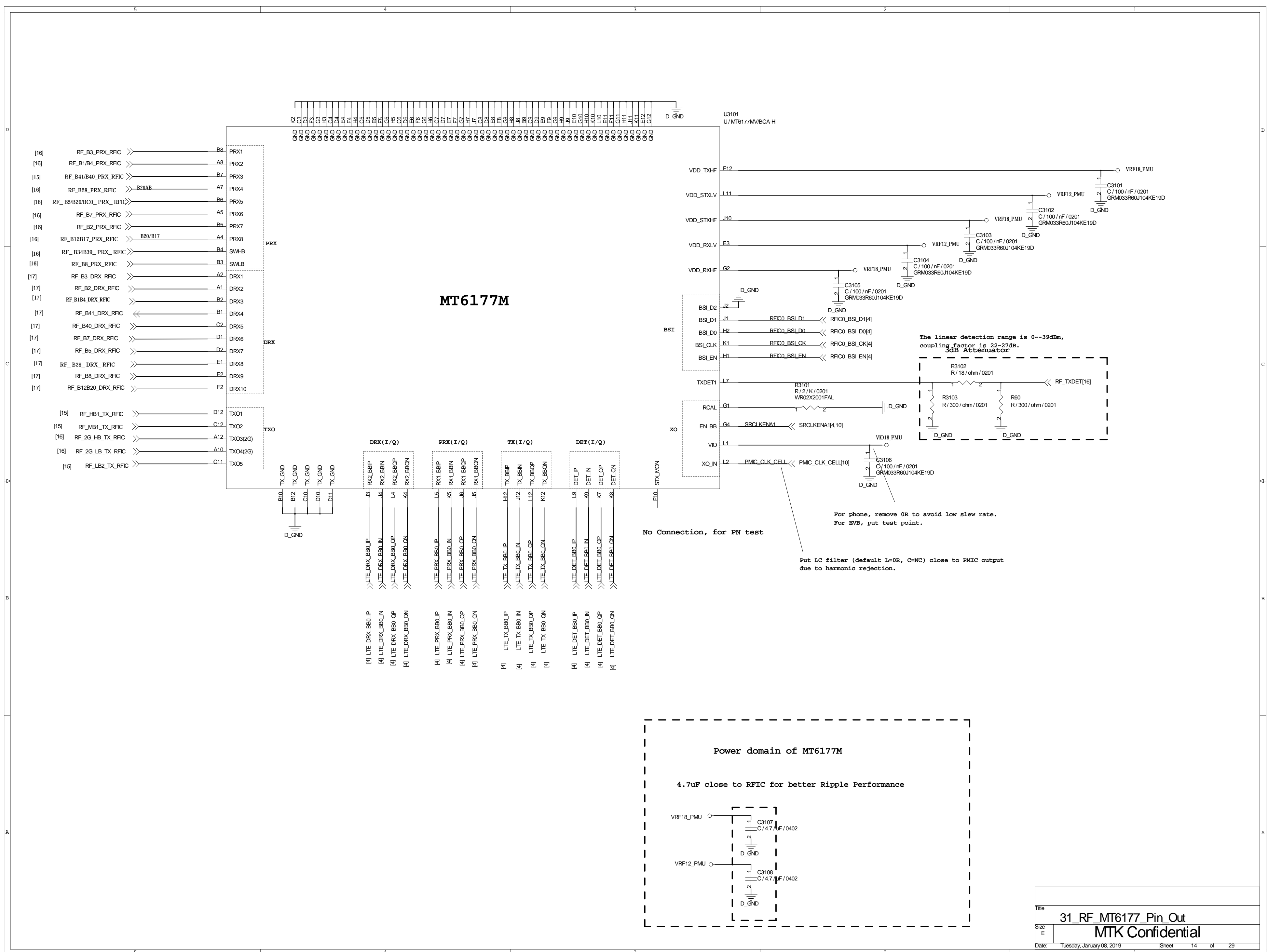


Schematic design notice of "28_POWER_ThirdParty-Power"

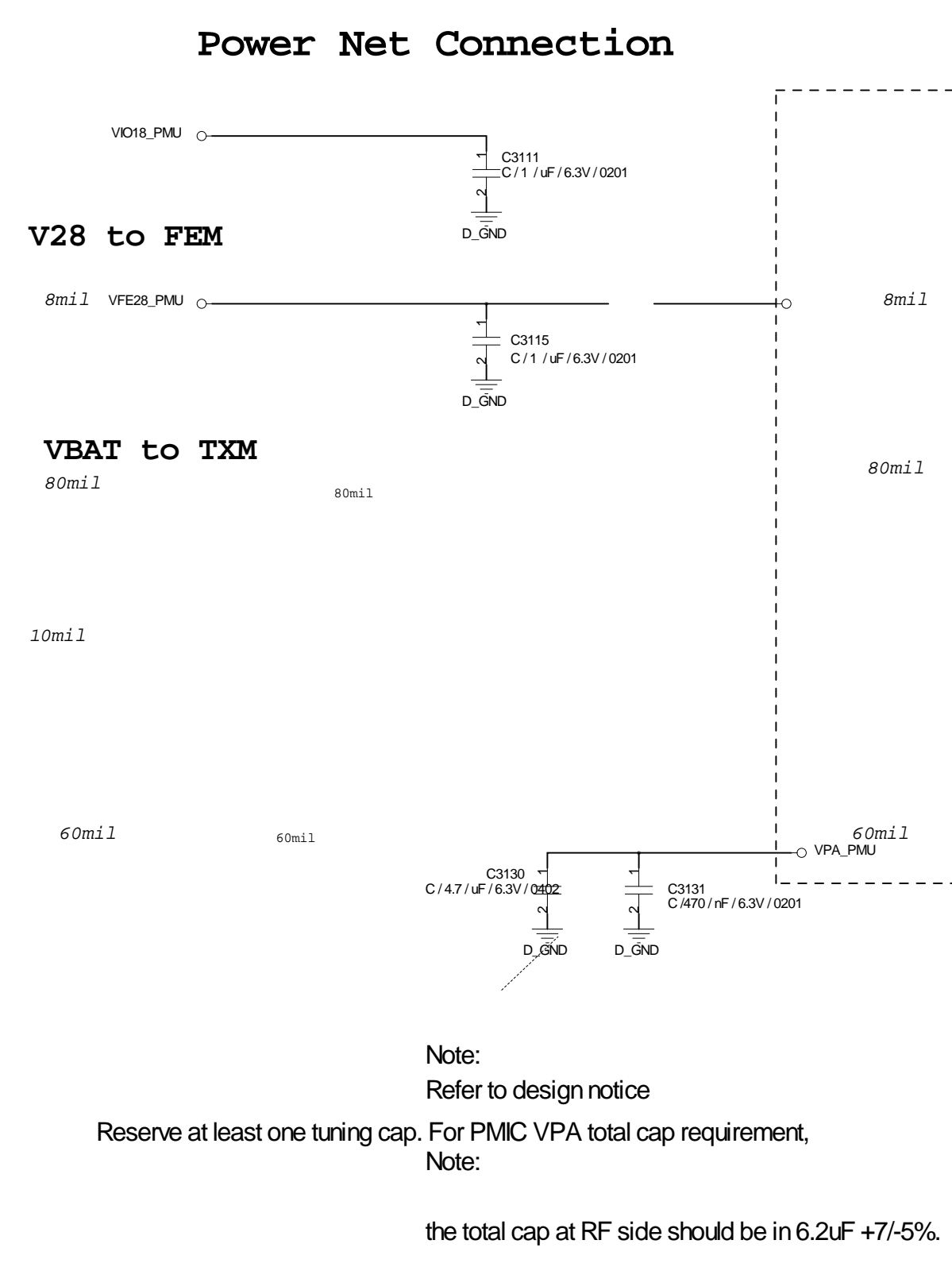
Note 28-1: VA12 Layout placement please close to AP

Note 28-2: VS2 Buck Layout placement please close to PMIC MT6357

Note 28-3: VCN33 LDO Layout placement please close to MT6631



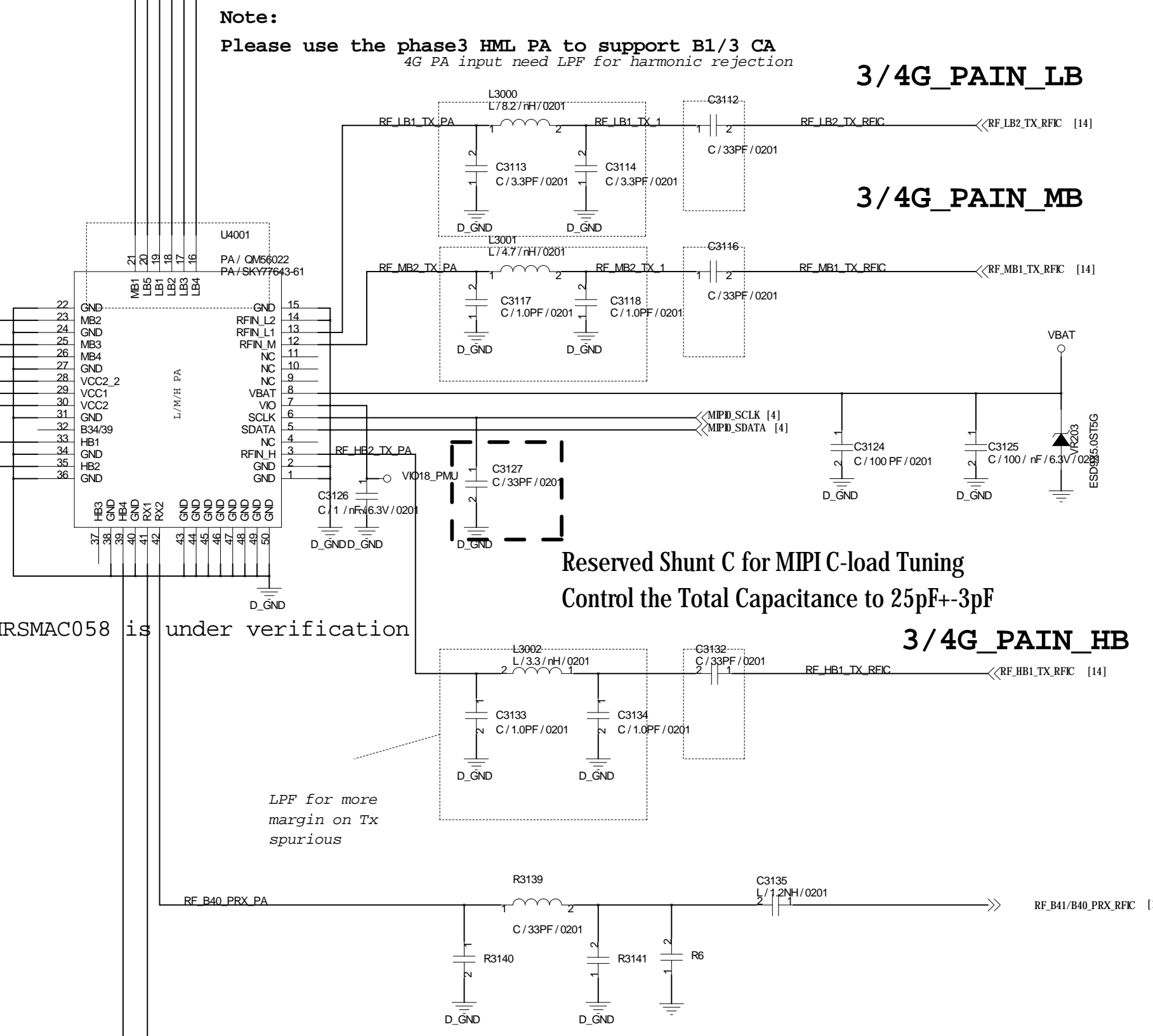
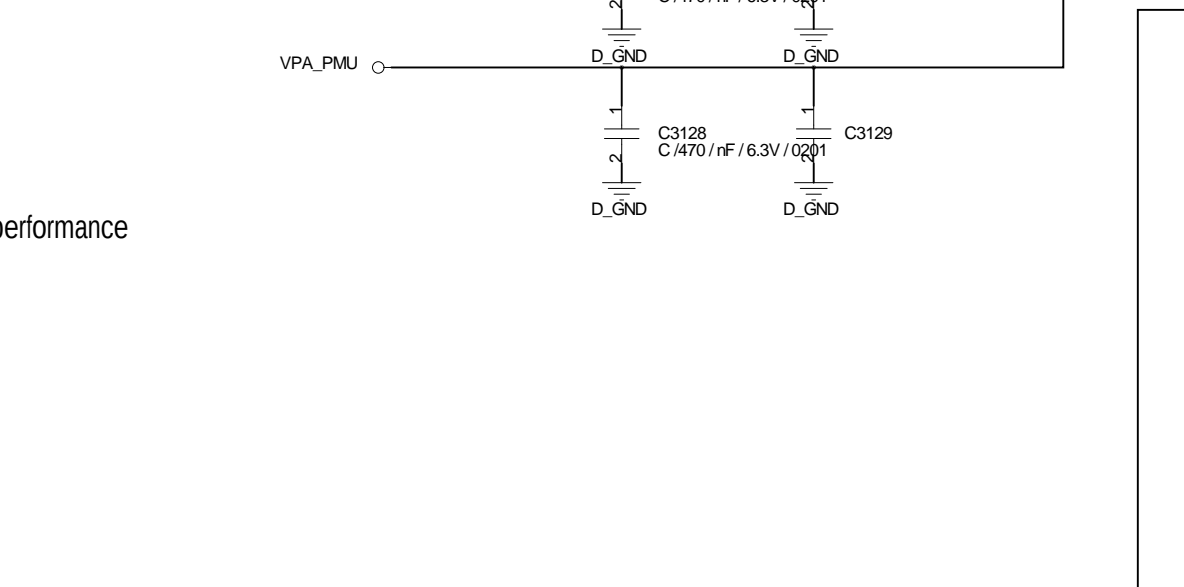
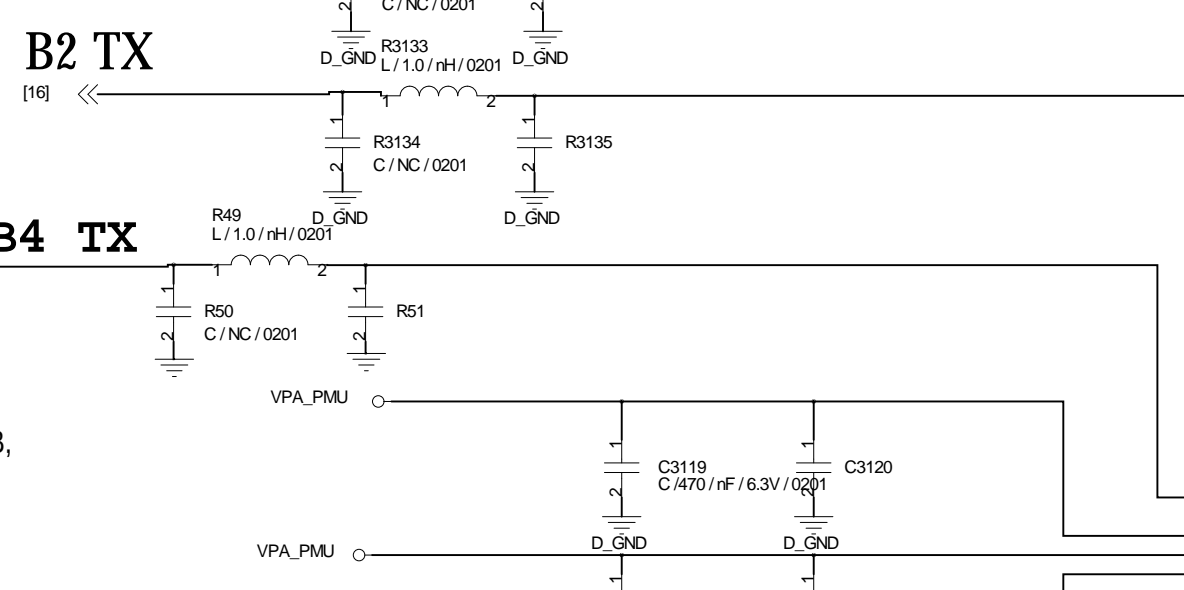
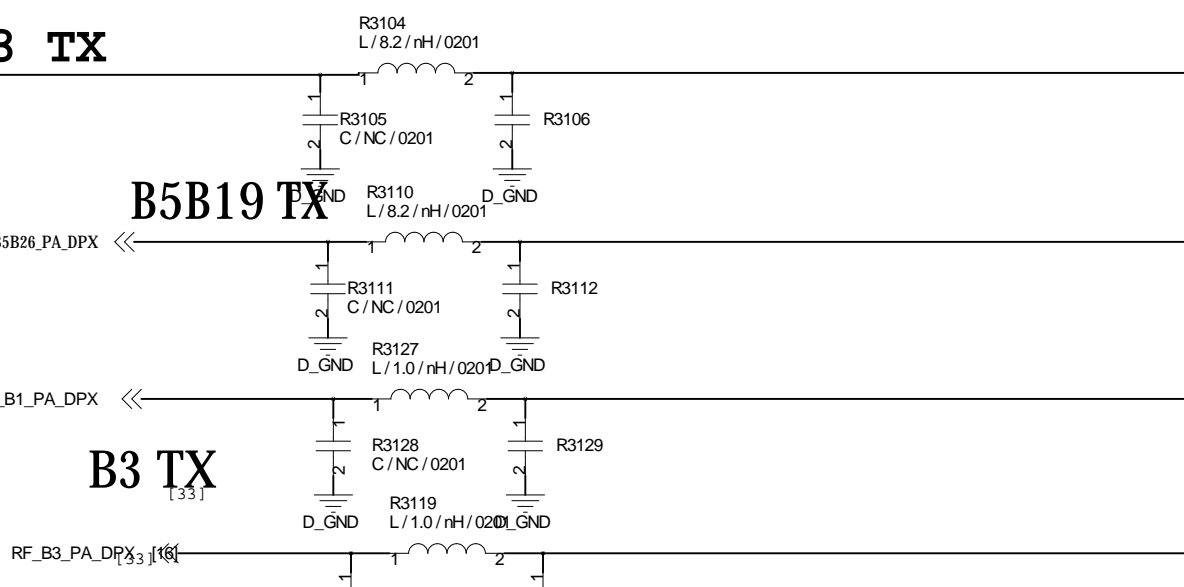
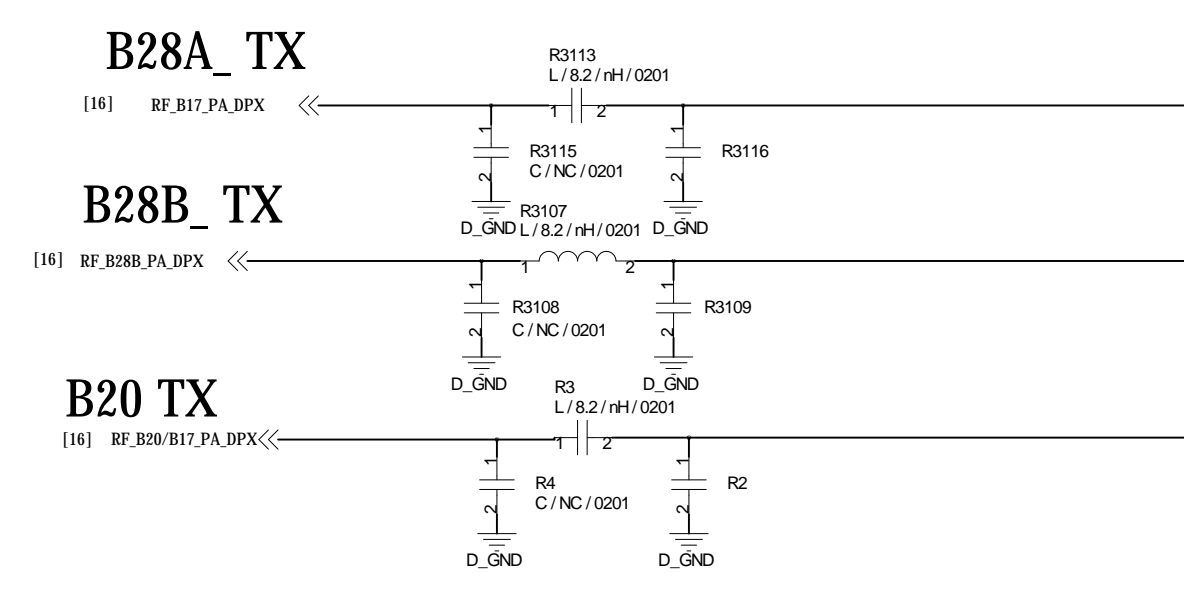
B1 TX



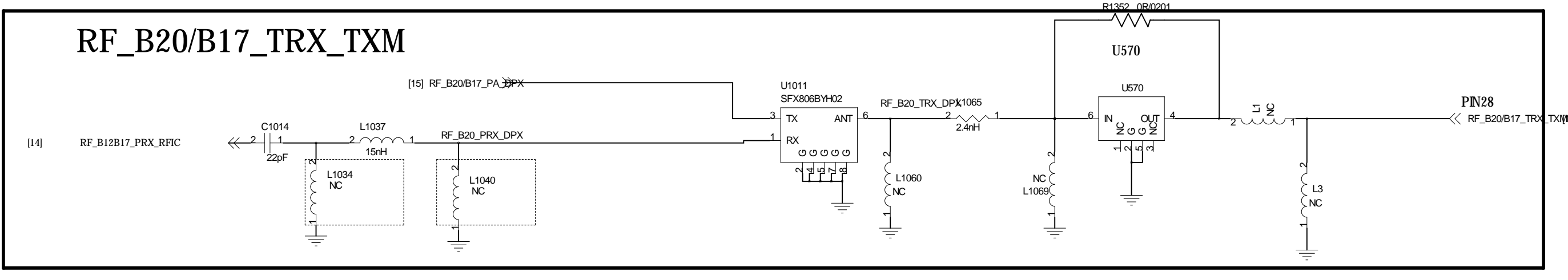
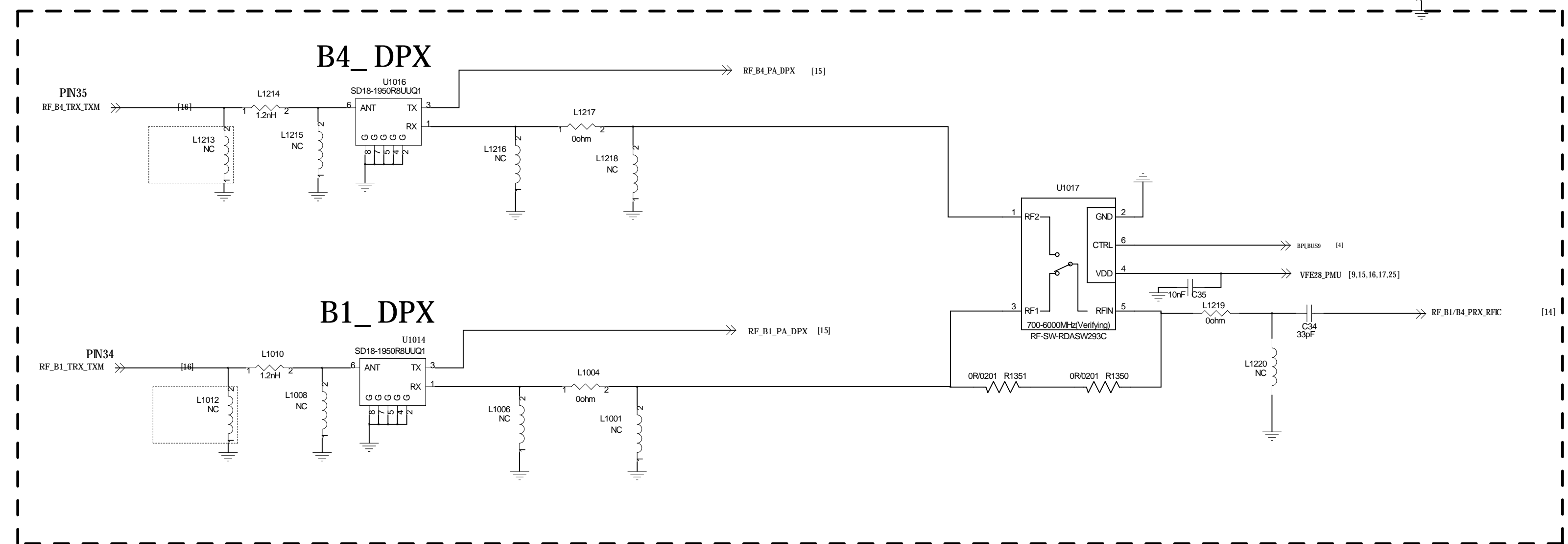
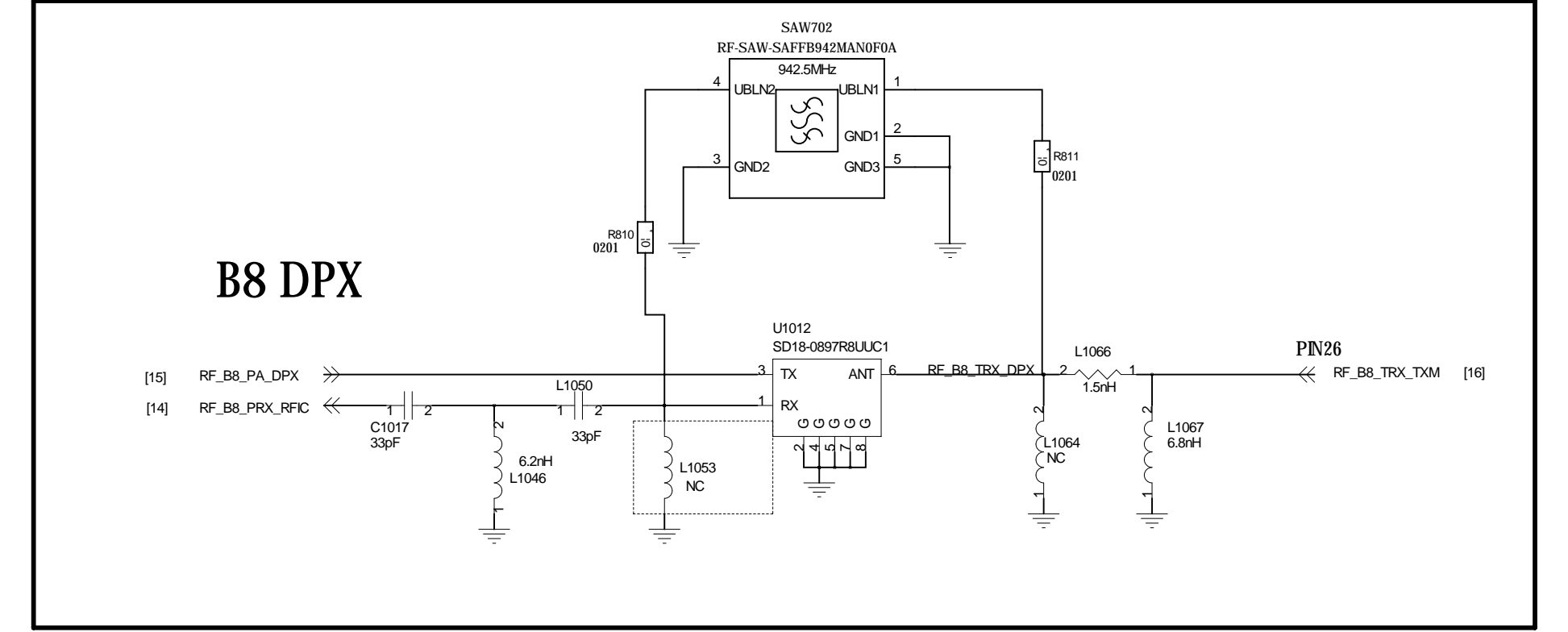
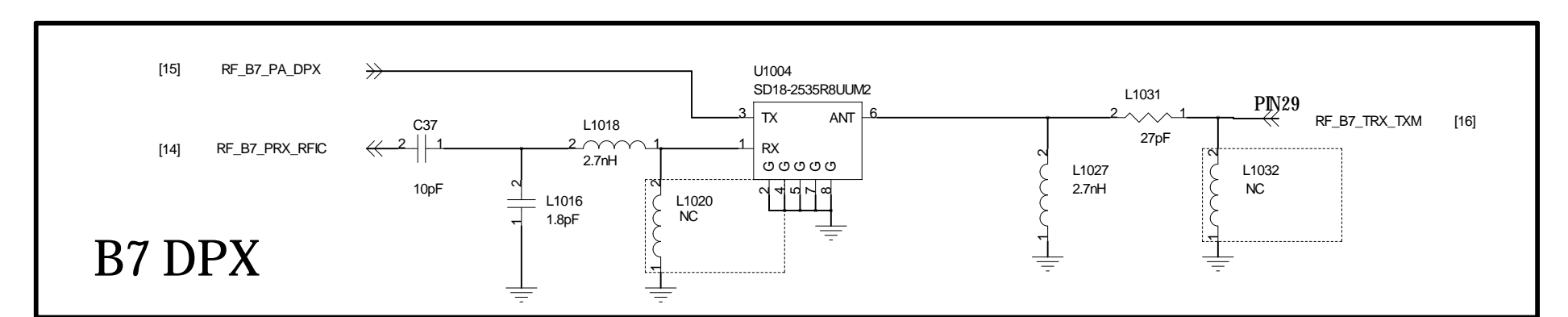
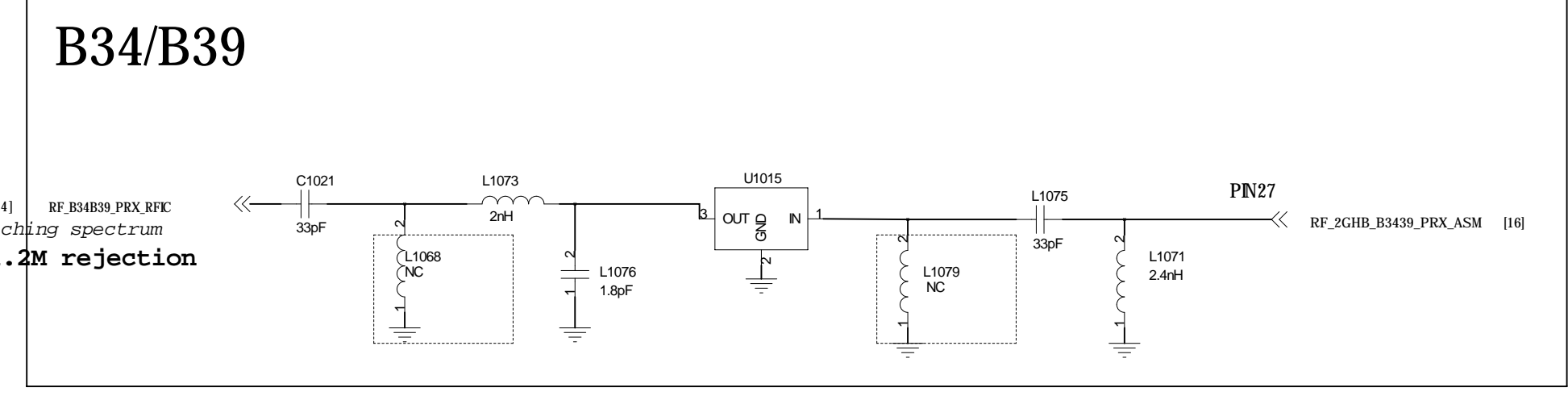
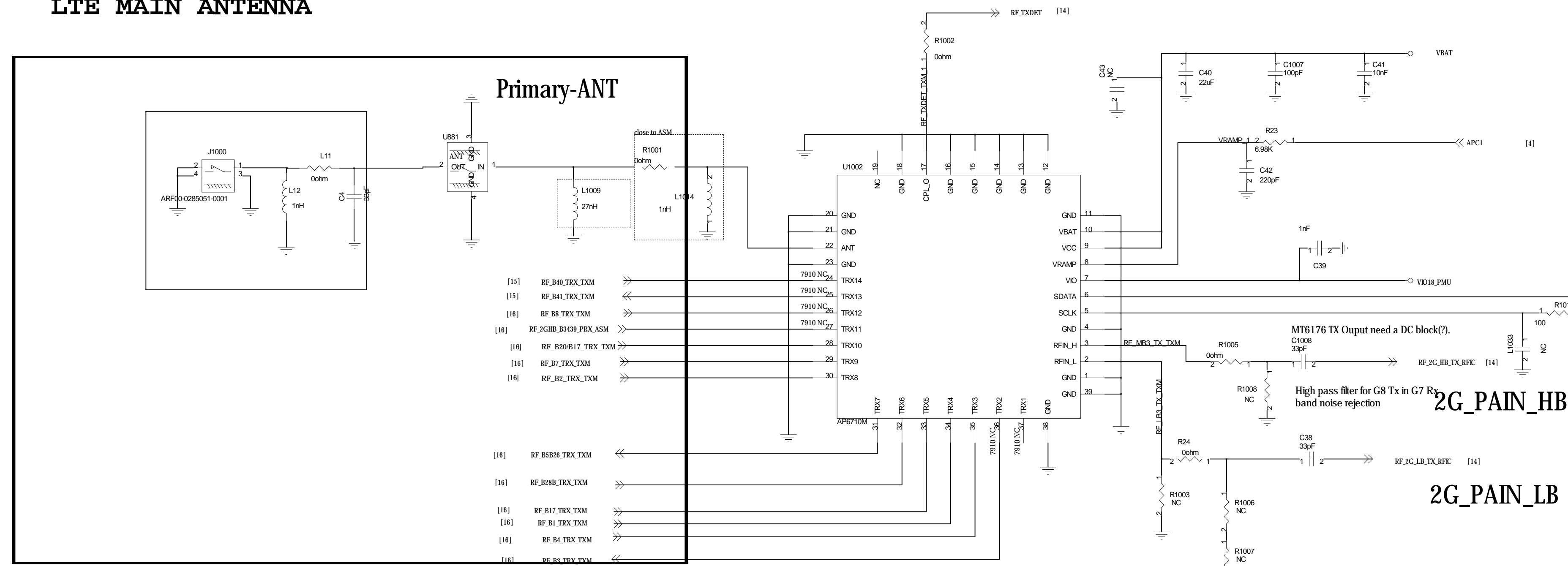
Note:
for GPS co-existence case and if the antenna isolation between GPS and B34 <30dB,
suggest not to use TXM reuse and use a BPF filter after B34 PA

From PMIC

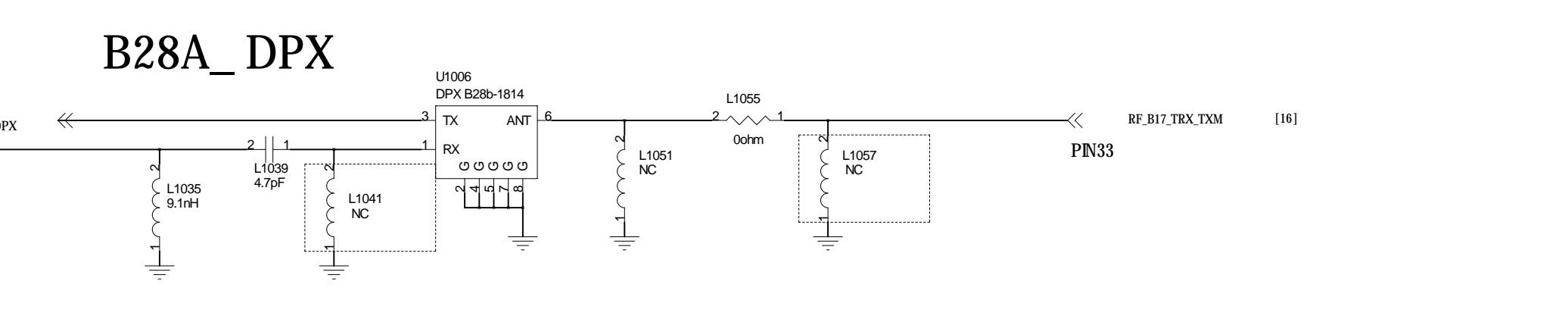
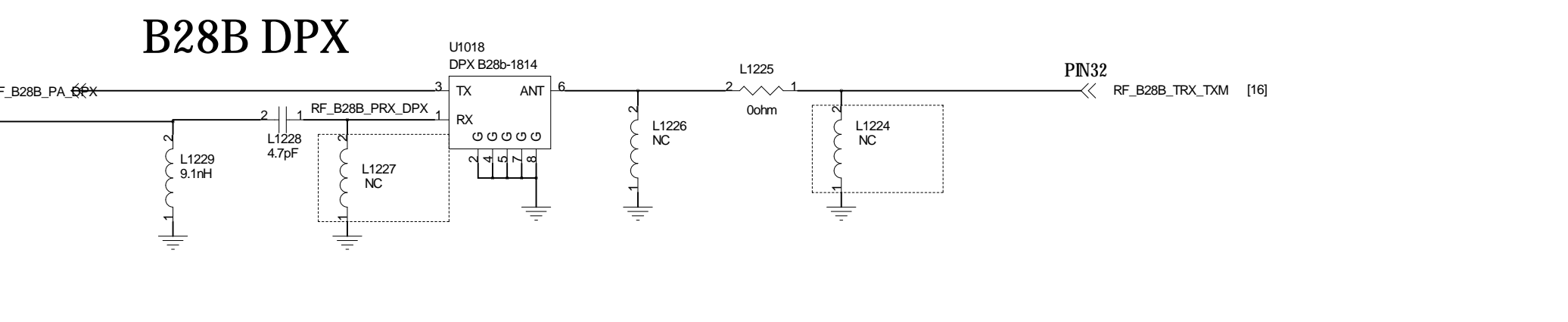
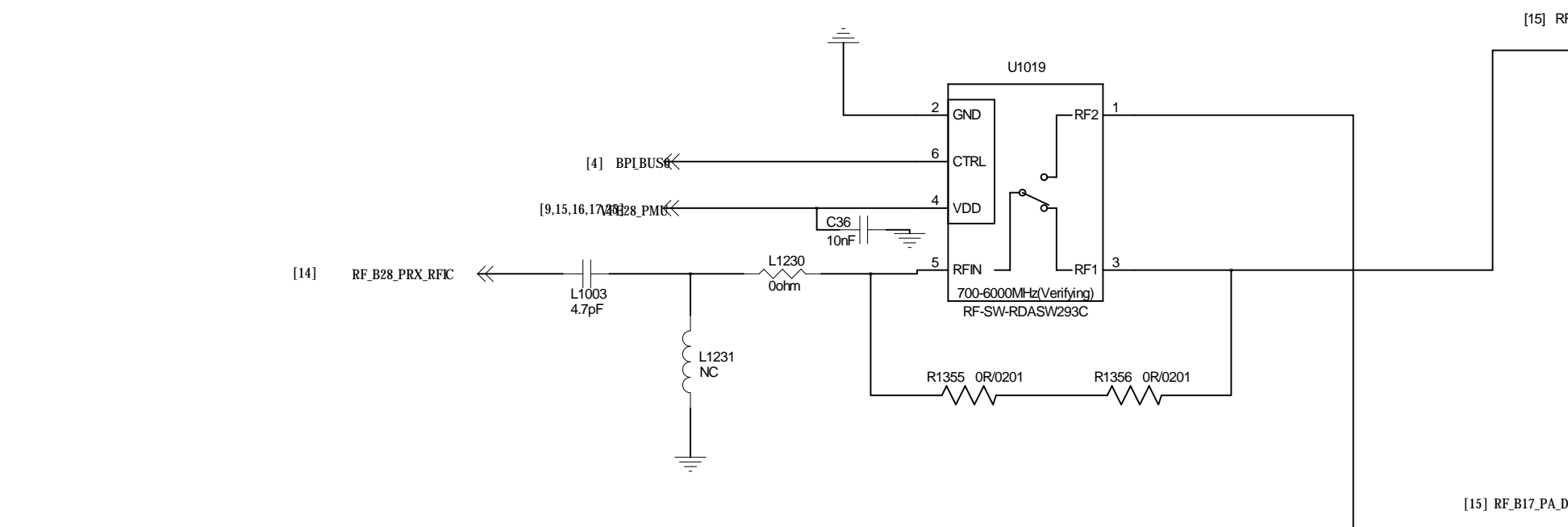
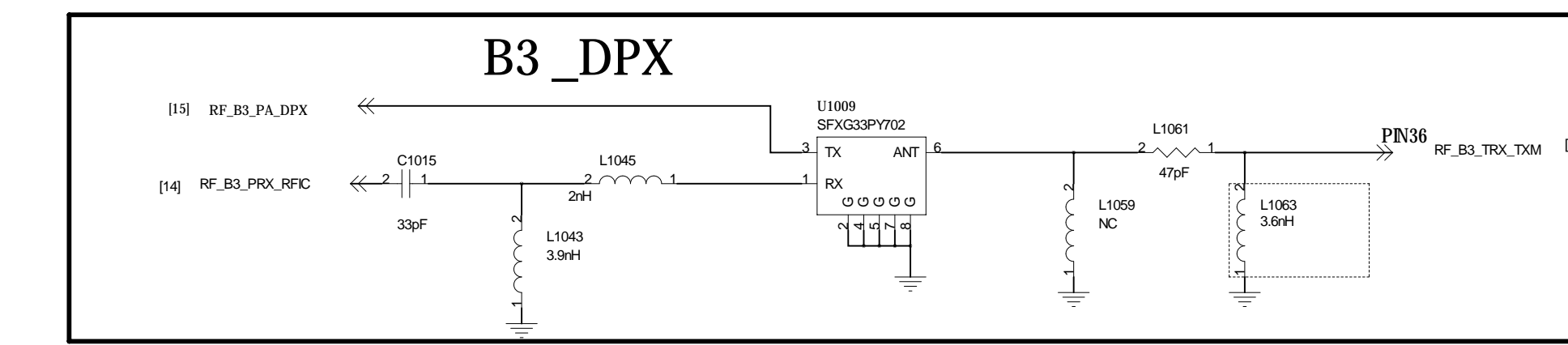
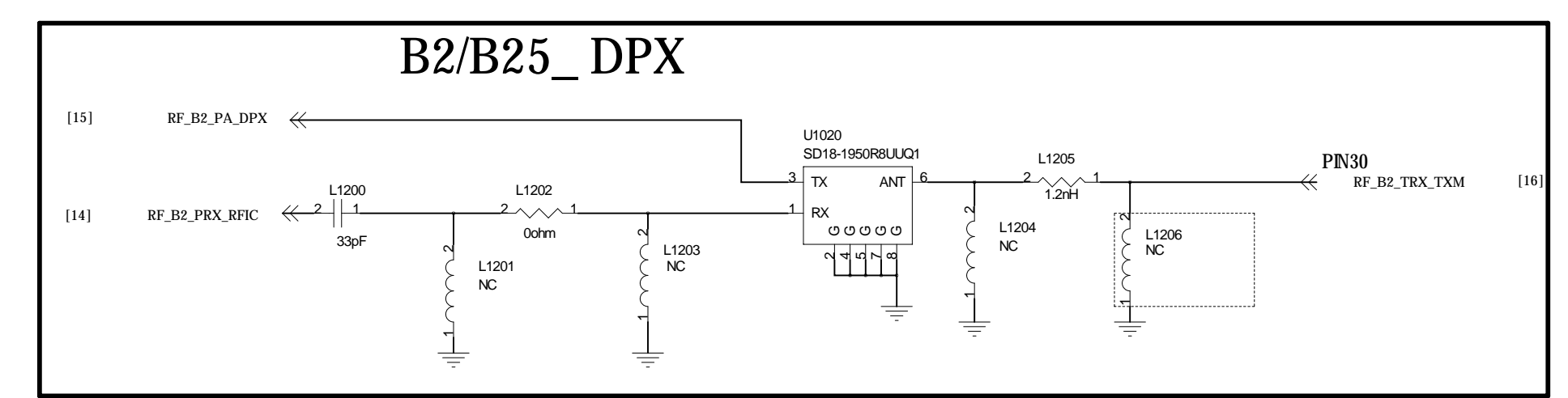
For B39 Multi-cluster Application, suggest to use B39 standalone TX SAW(U3225) for better SEM performance



LTE MAIN ANTENNA

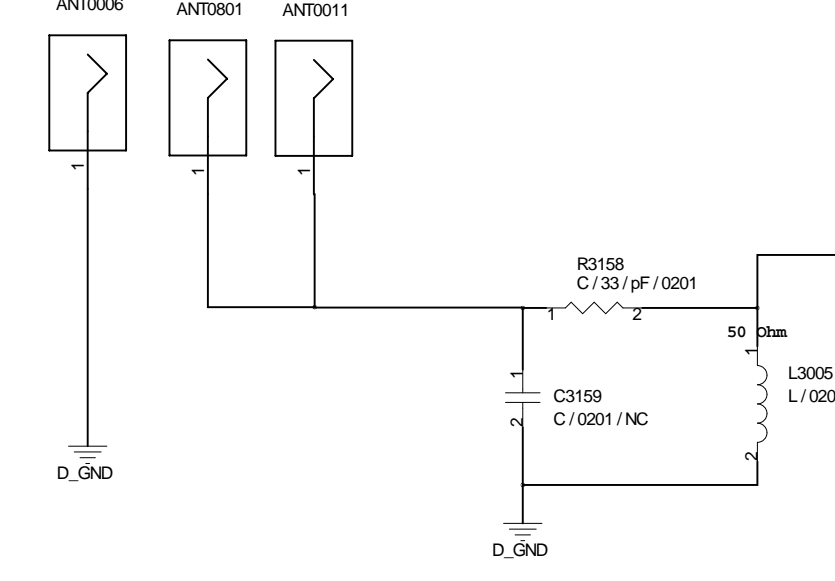


B17/28A/B28B I_{DC} = 1.00 ± 0.10 mA

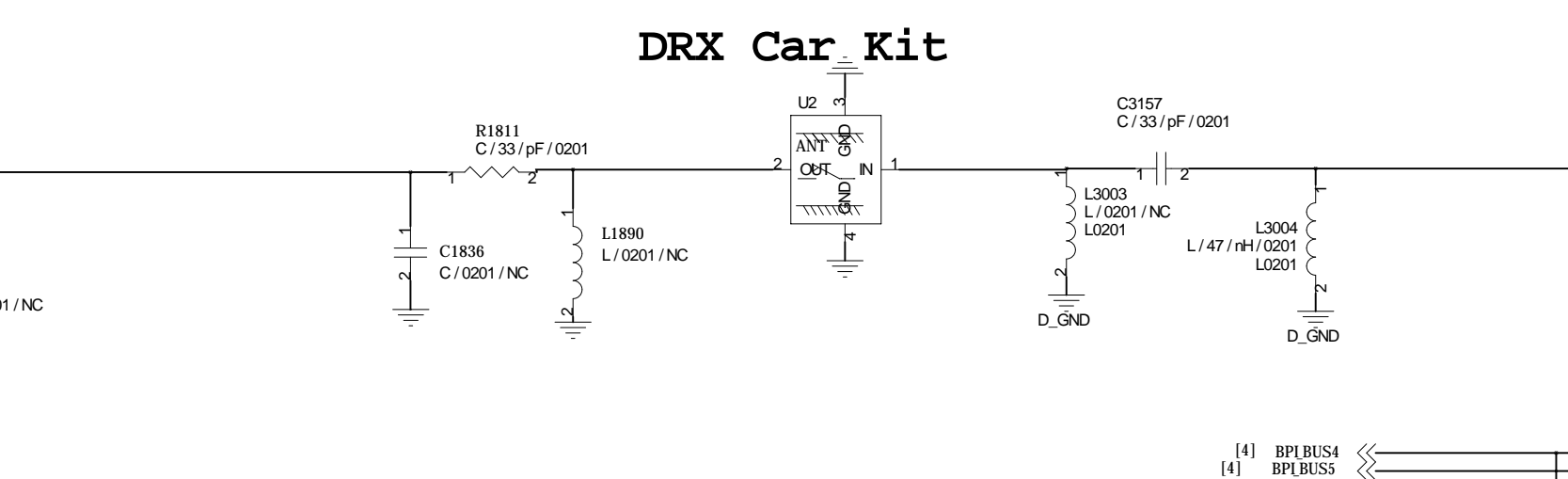


Supported Band Combination

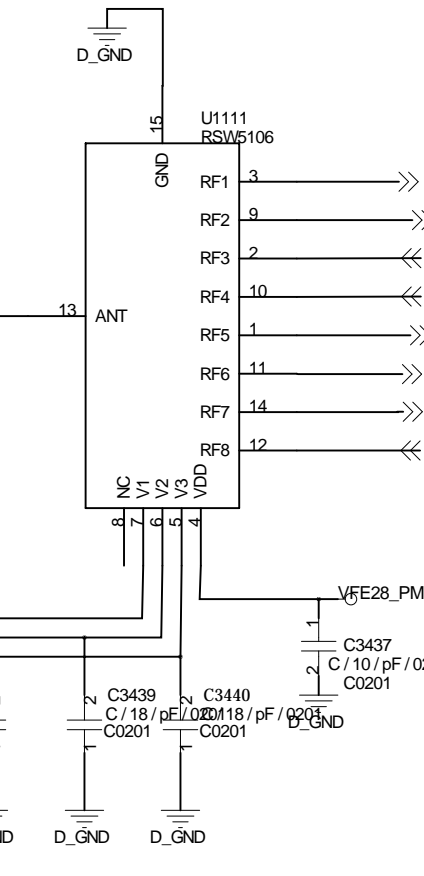
LTE DRX ANTENNA



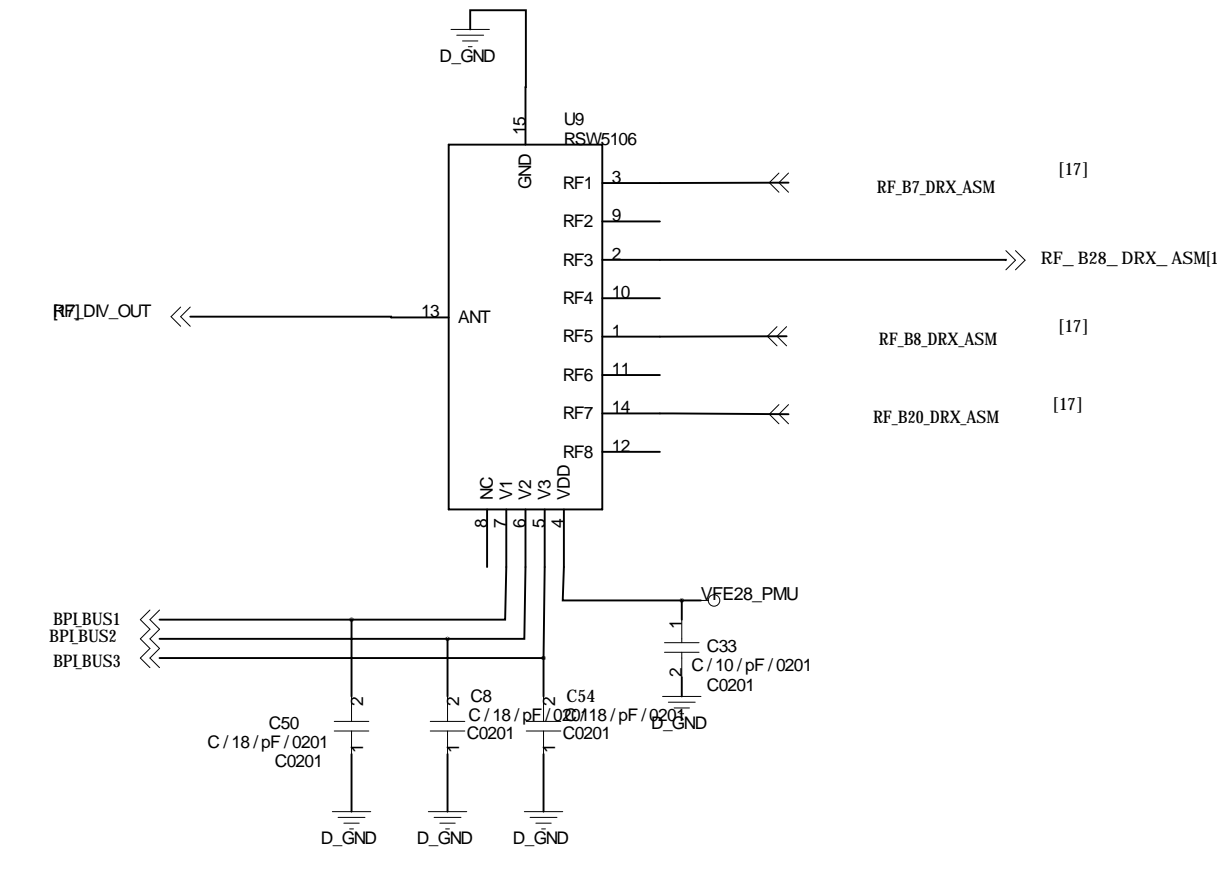
DRX Car Kit



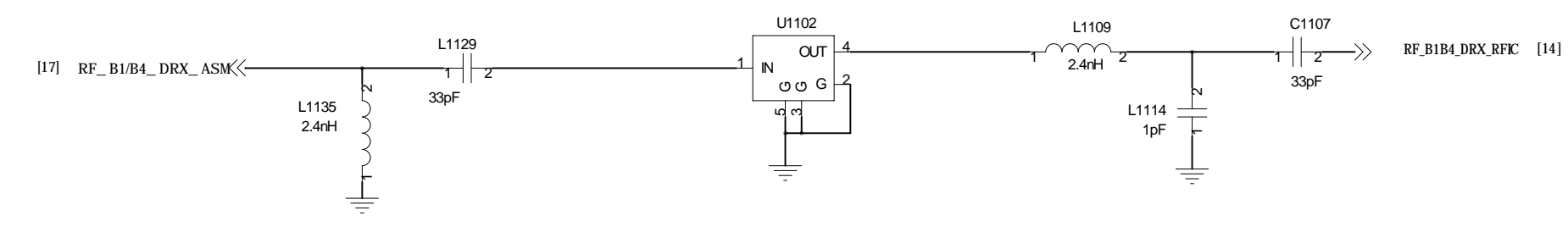
SP8T SKY13418



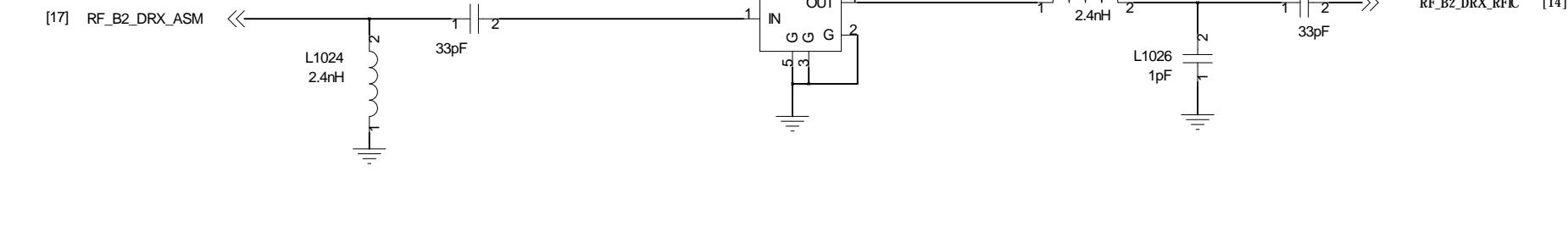
SP8T SKY13418



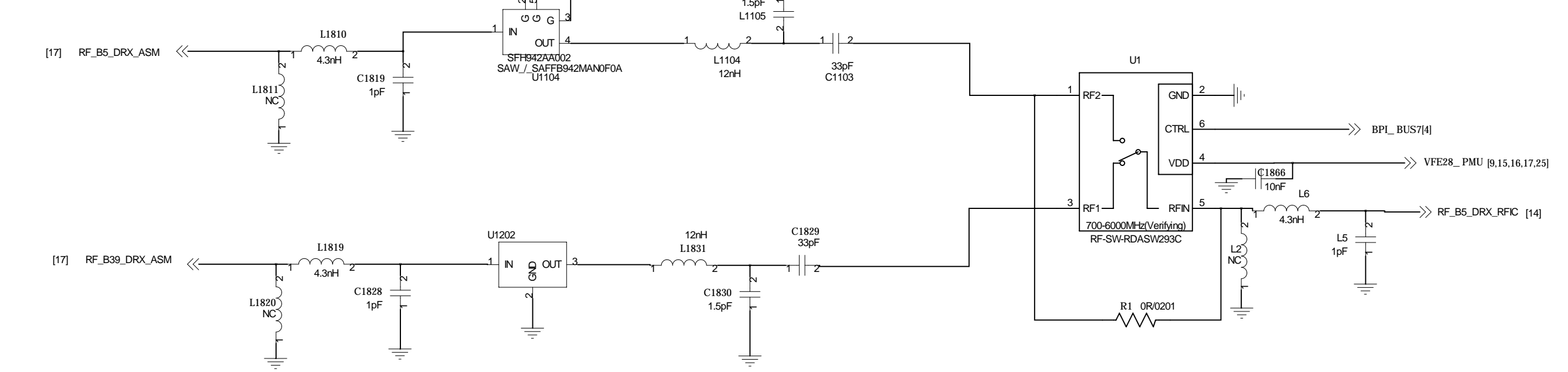
B1/B4_DRX



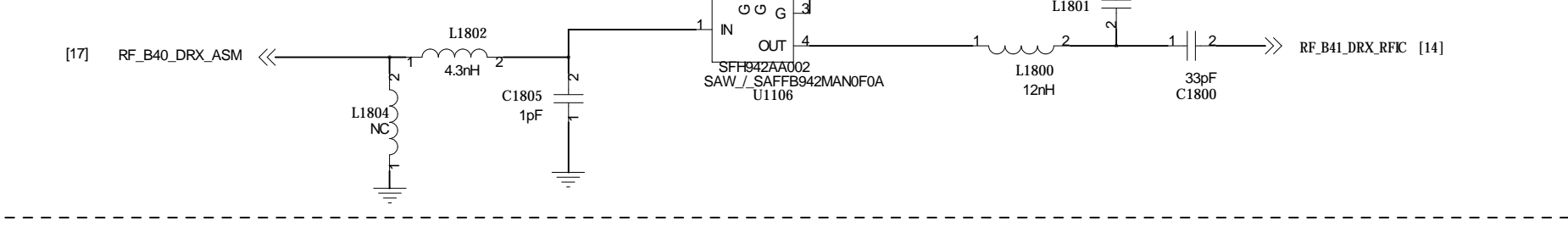
B2-DRX



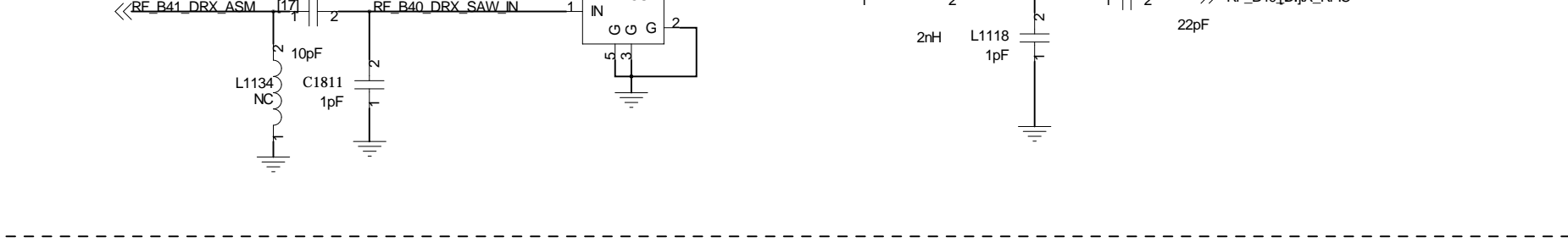
B5/B34/B39_DRX



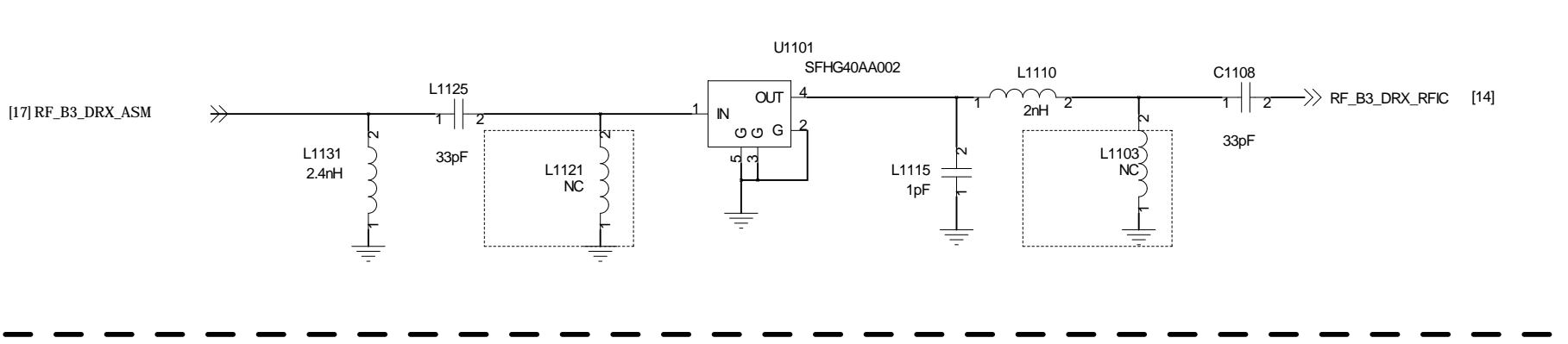
B40_DRX



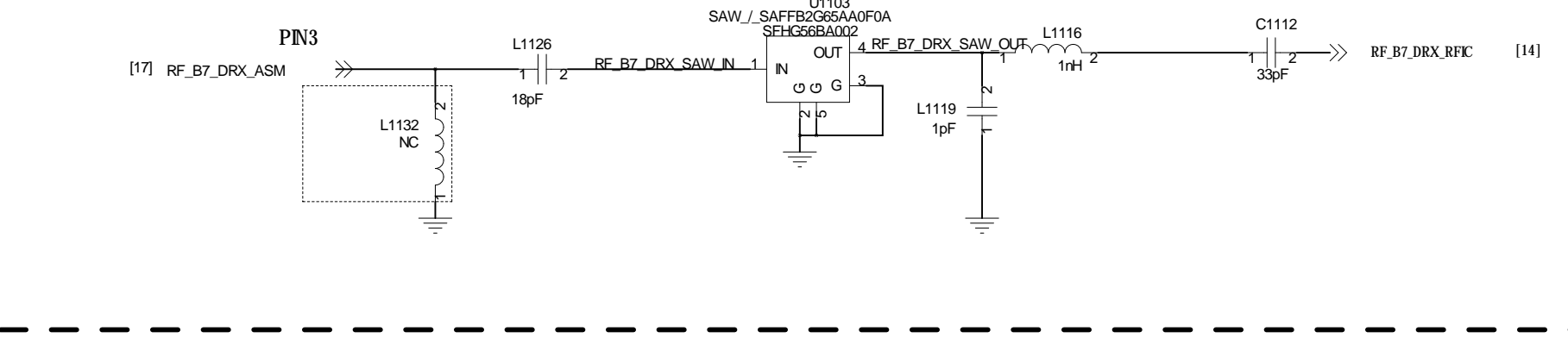
B41_DRX



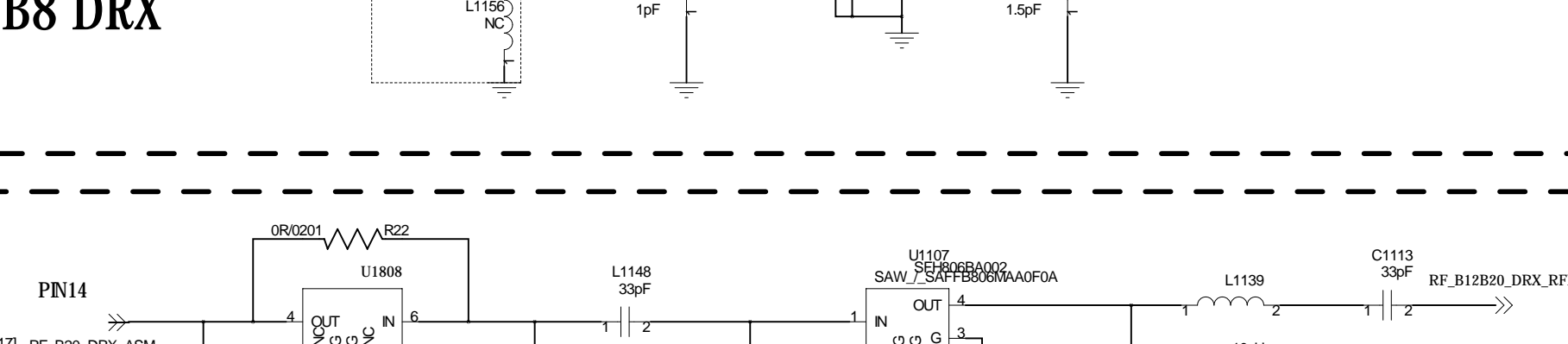
B3 DRX



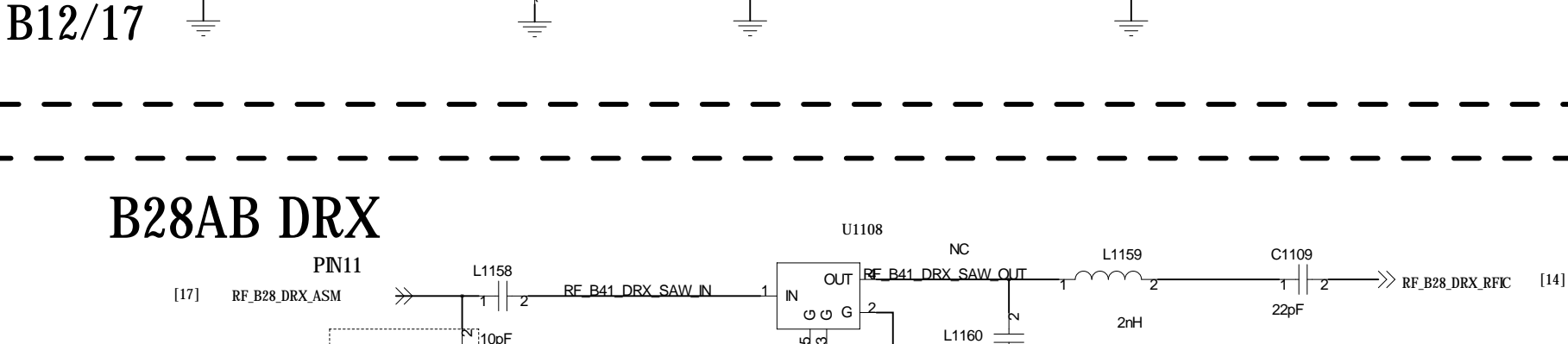
B7 DRX



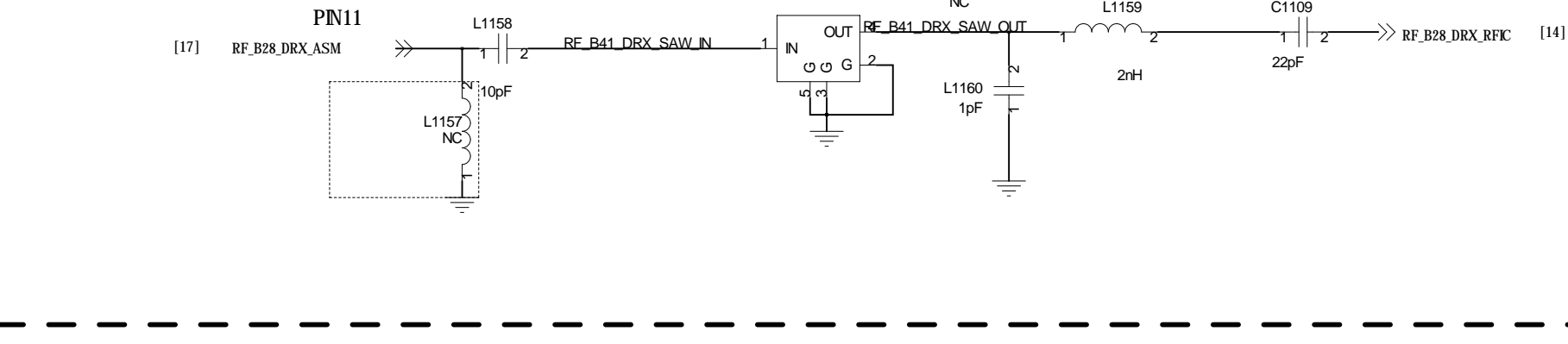
B8 DRX



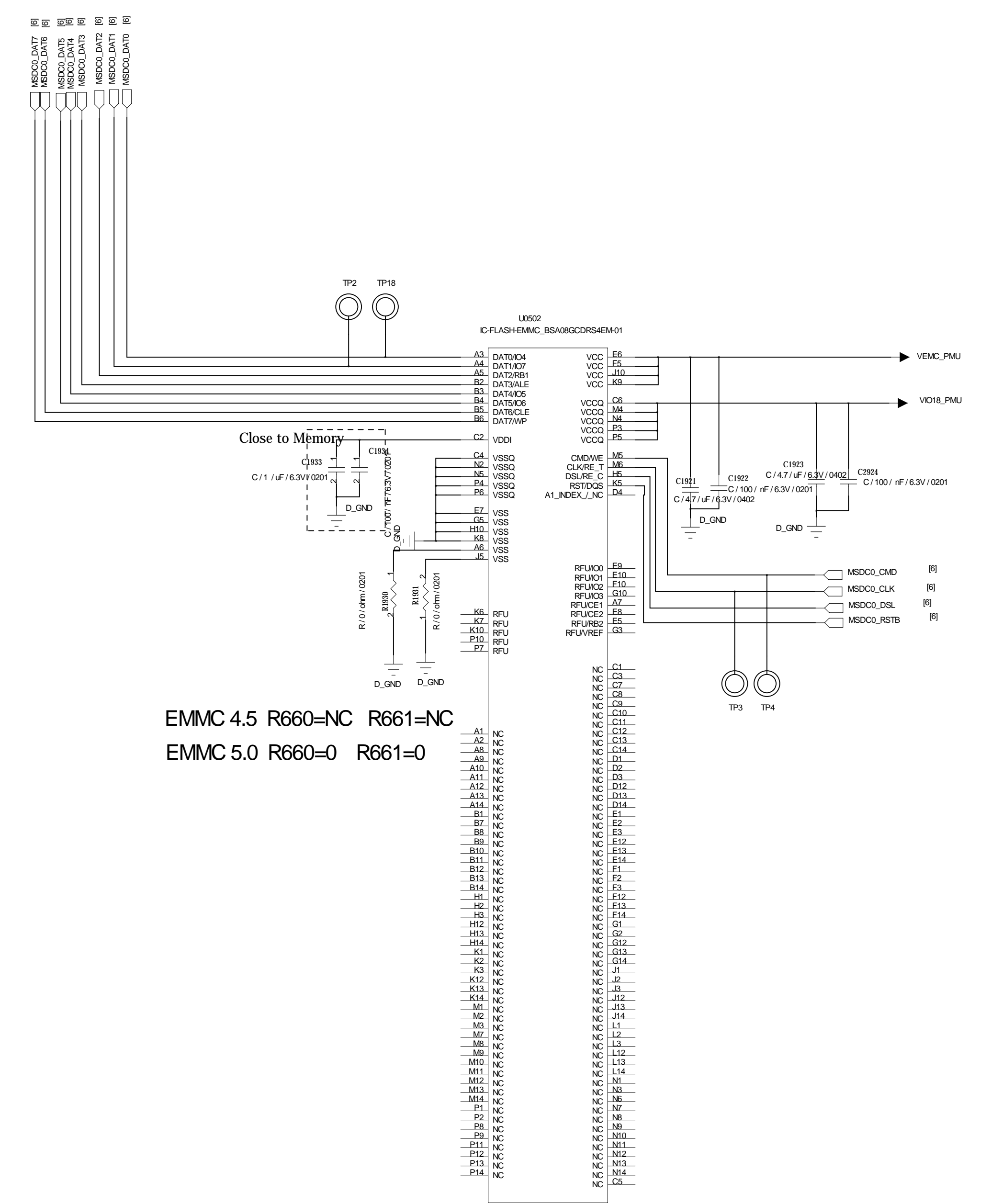
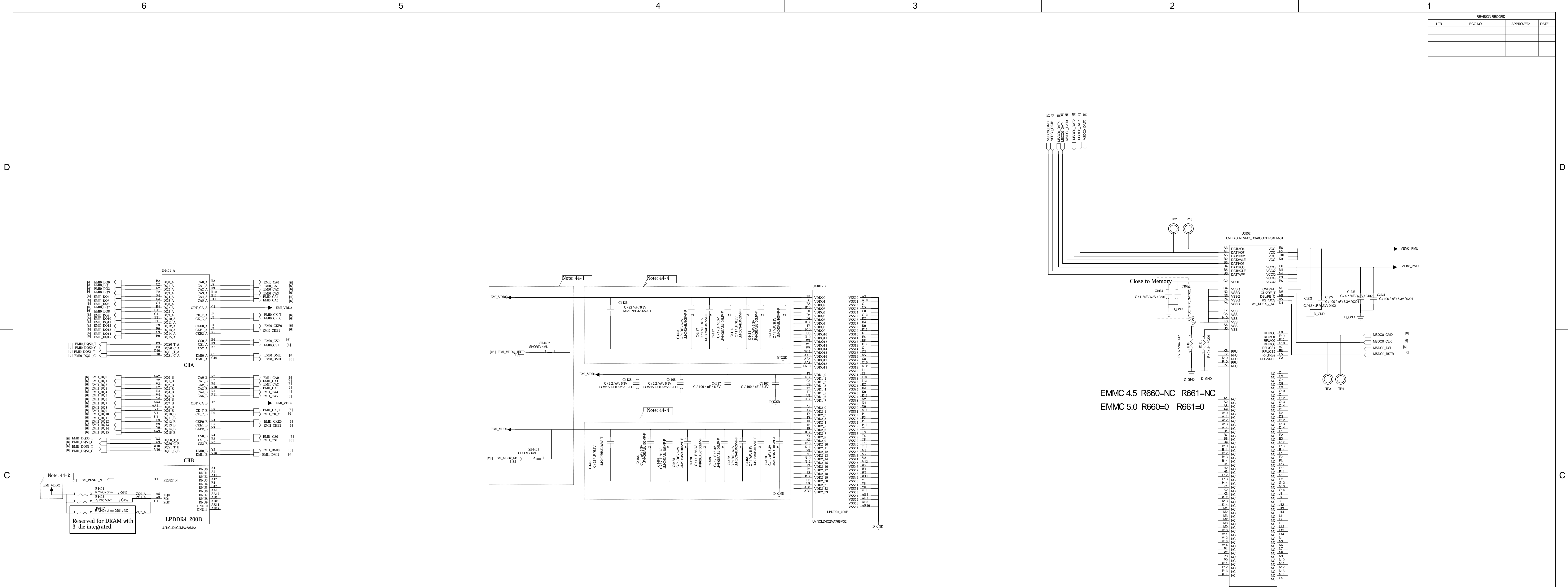
**B20 DRX
B12/17**



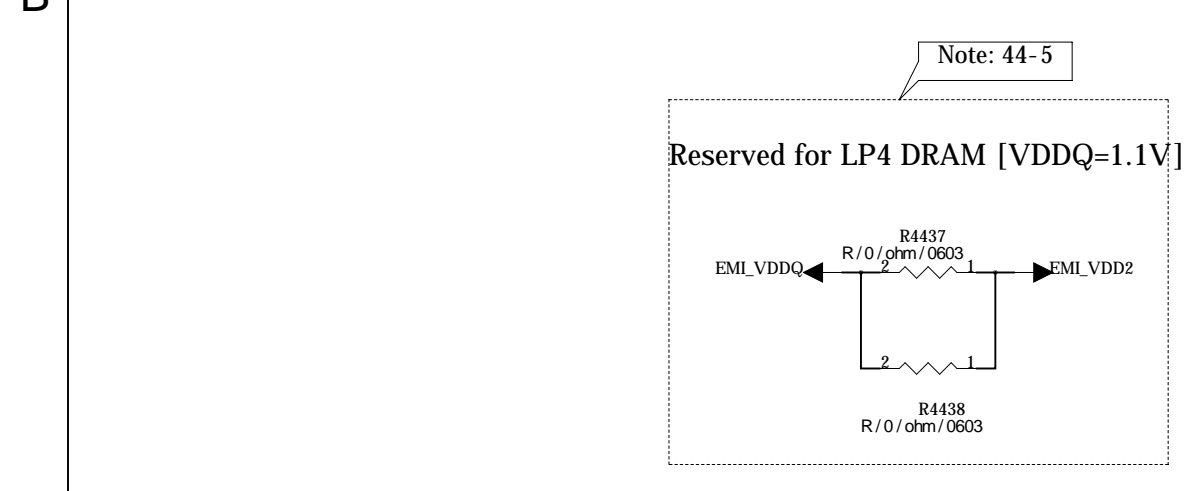
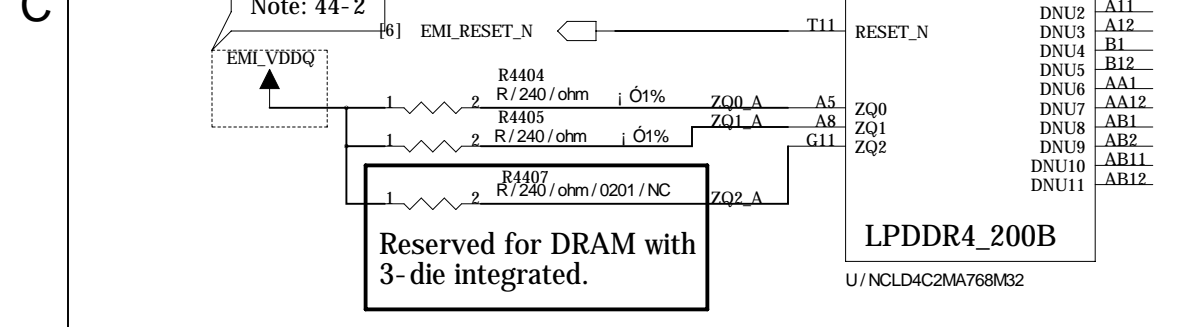
B28AB DRX



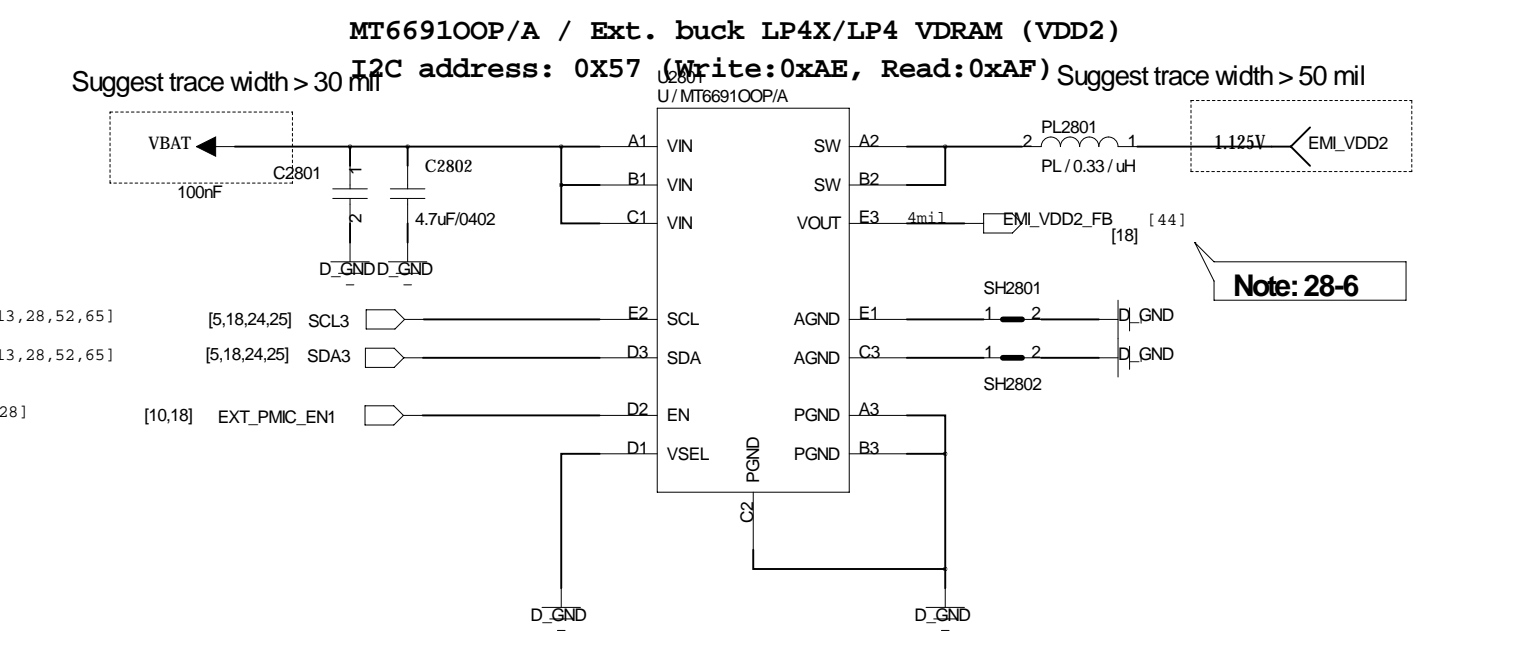
REVISION RECORD			
LTR	ECONO	APPROVED	DATE



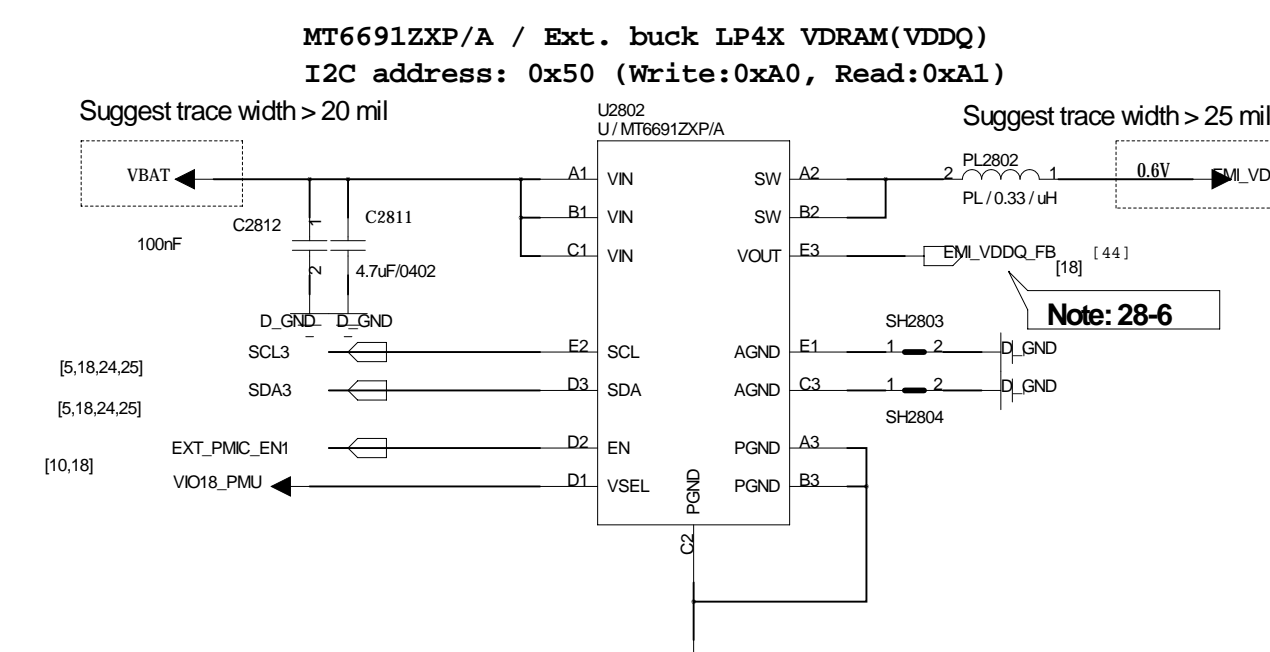
EMMC 4.5 R660=NC R661=NC
EMMC 5.0 R660=0 R661=0



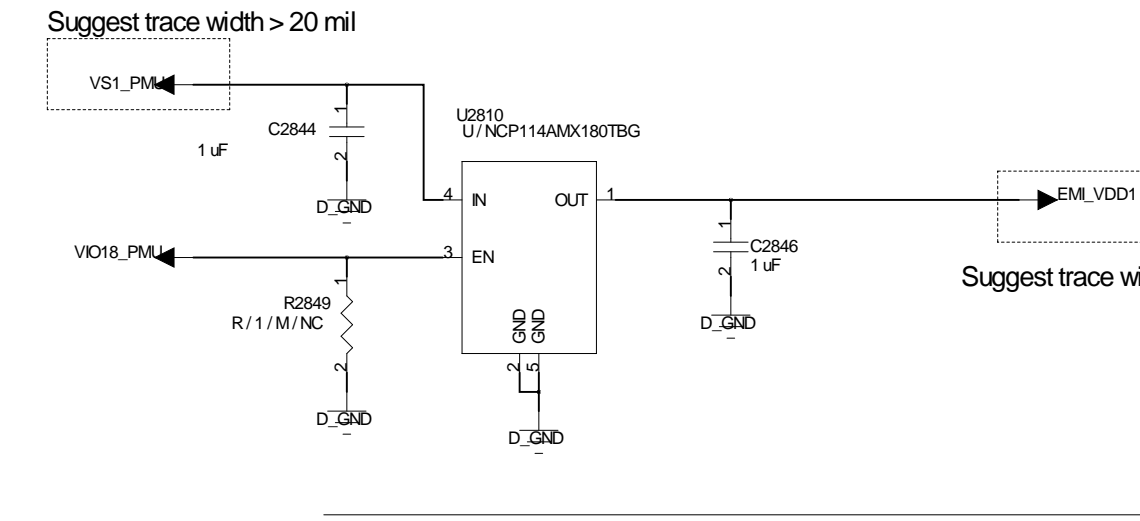
Ext. buck LPDDR4/LPDDR4 VDRAM Note: 28-4



Ext. buck LPDDR4X VDDQ Note: 28-4



LPDDR4X/LPDDR4 VDD1 1.8V LDO Note: 28-5



Schematic design notice of "44_Memory_eMMC_LPDDR4"

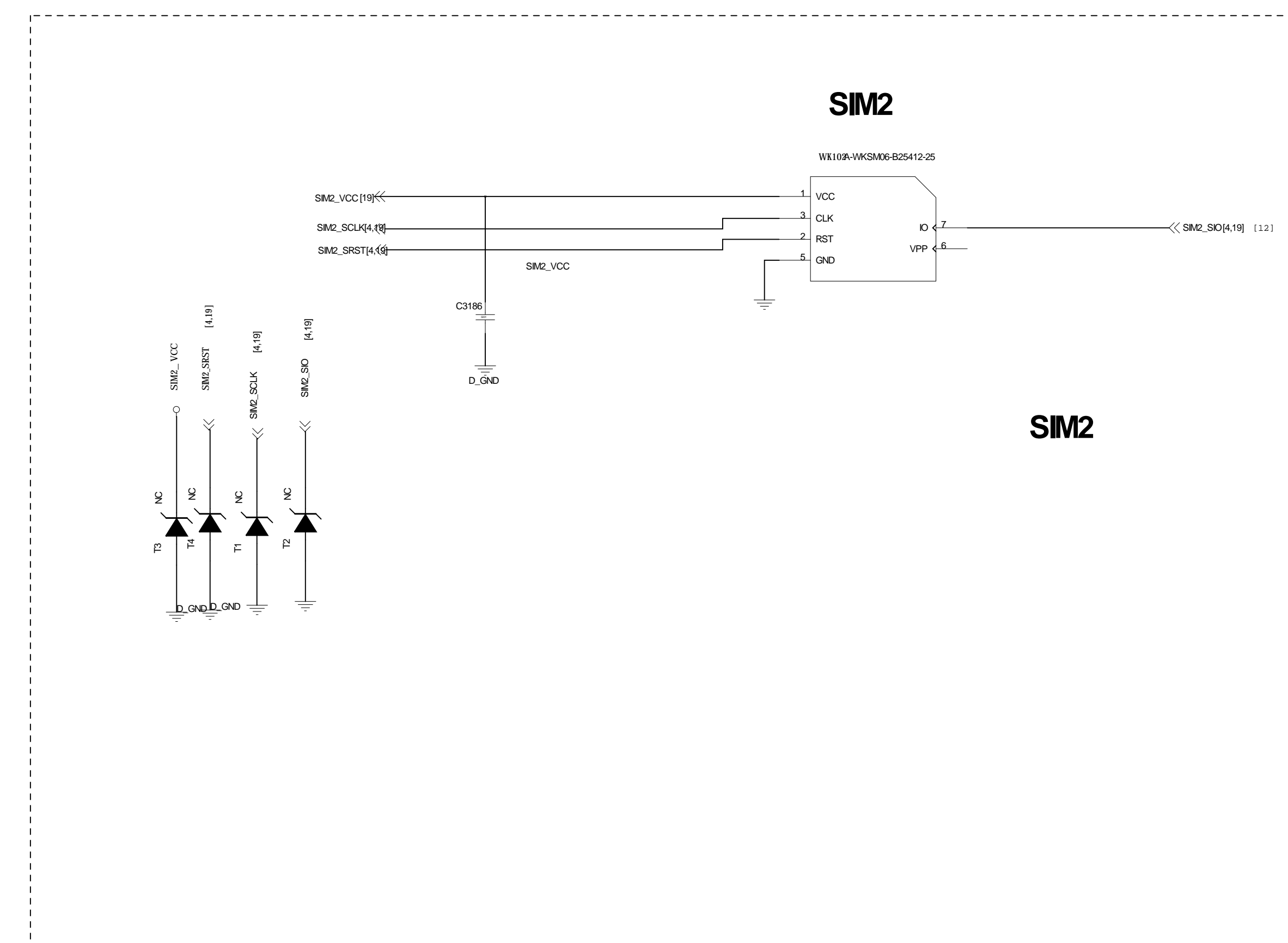
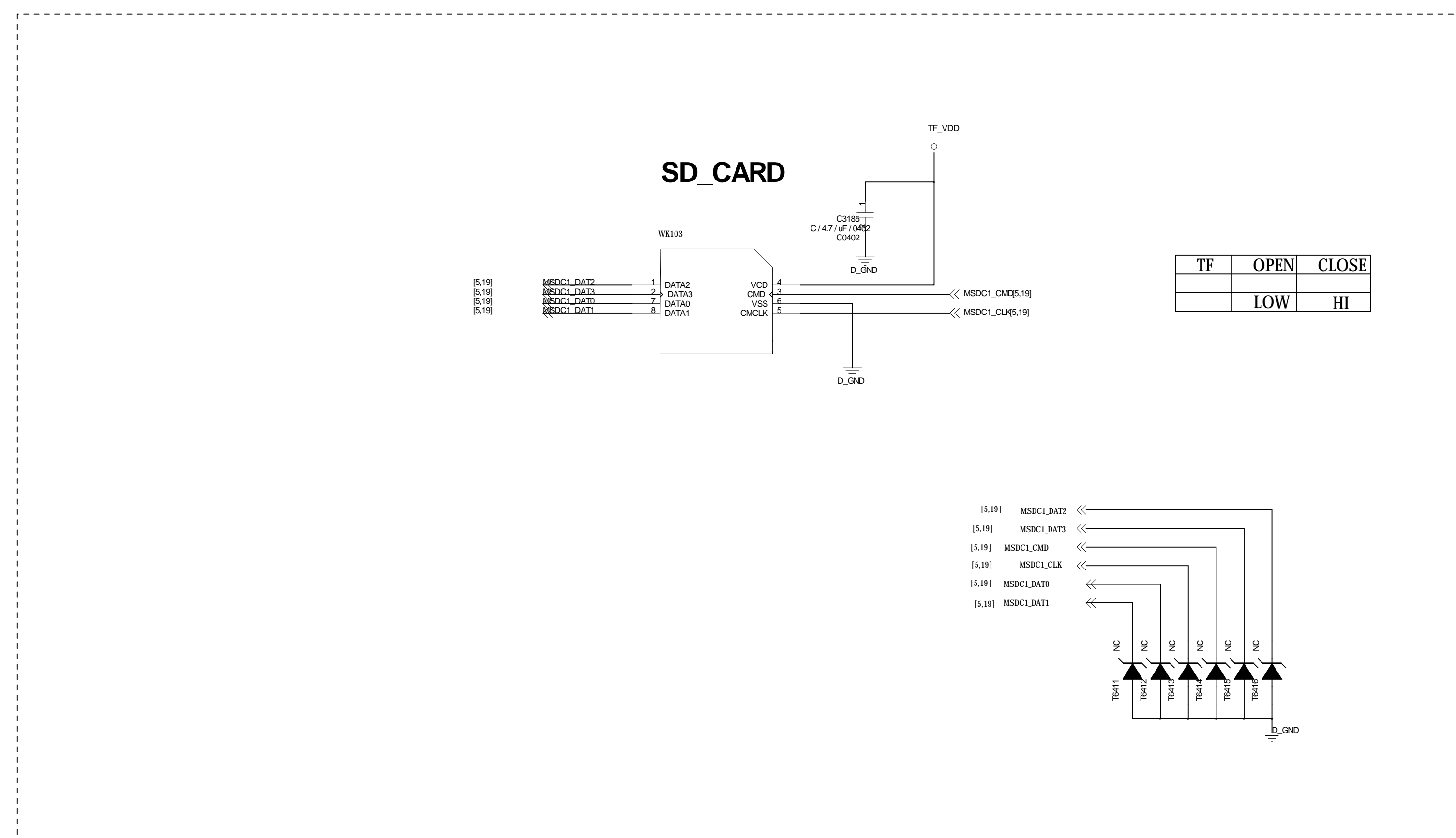
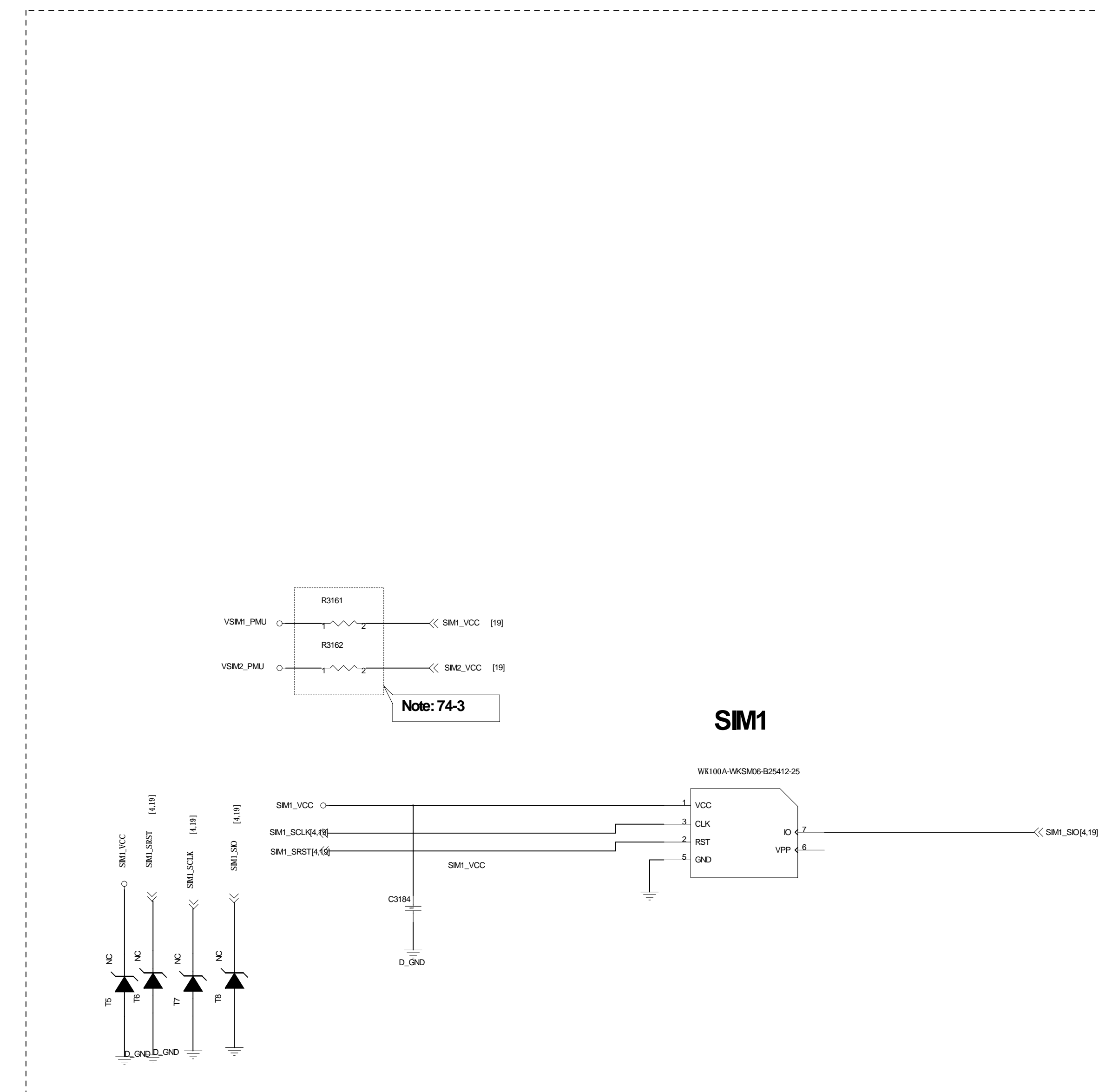
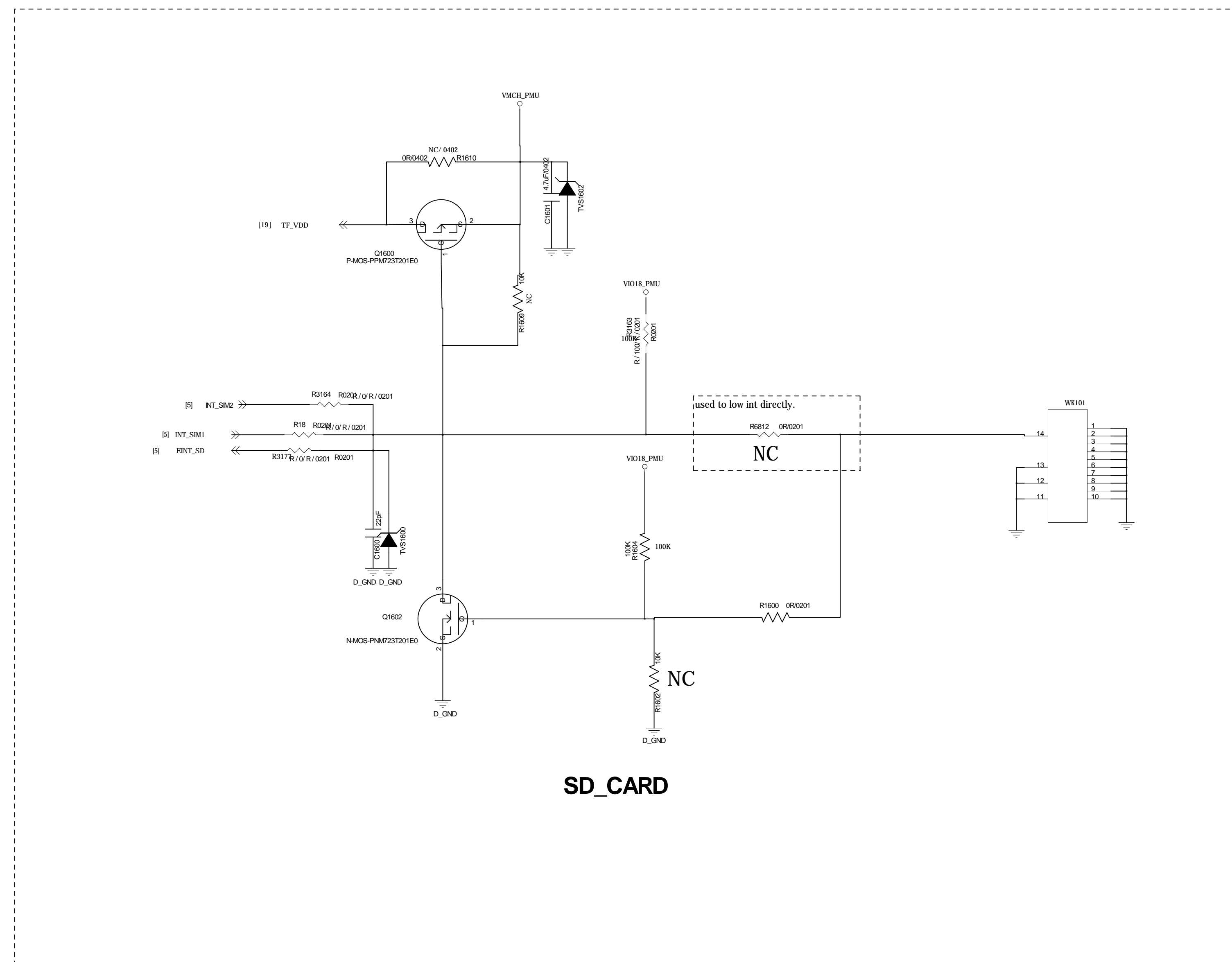
Note 44-1: Please refer to power supply related page select output voltage properly for LPDDR4

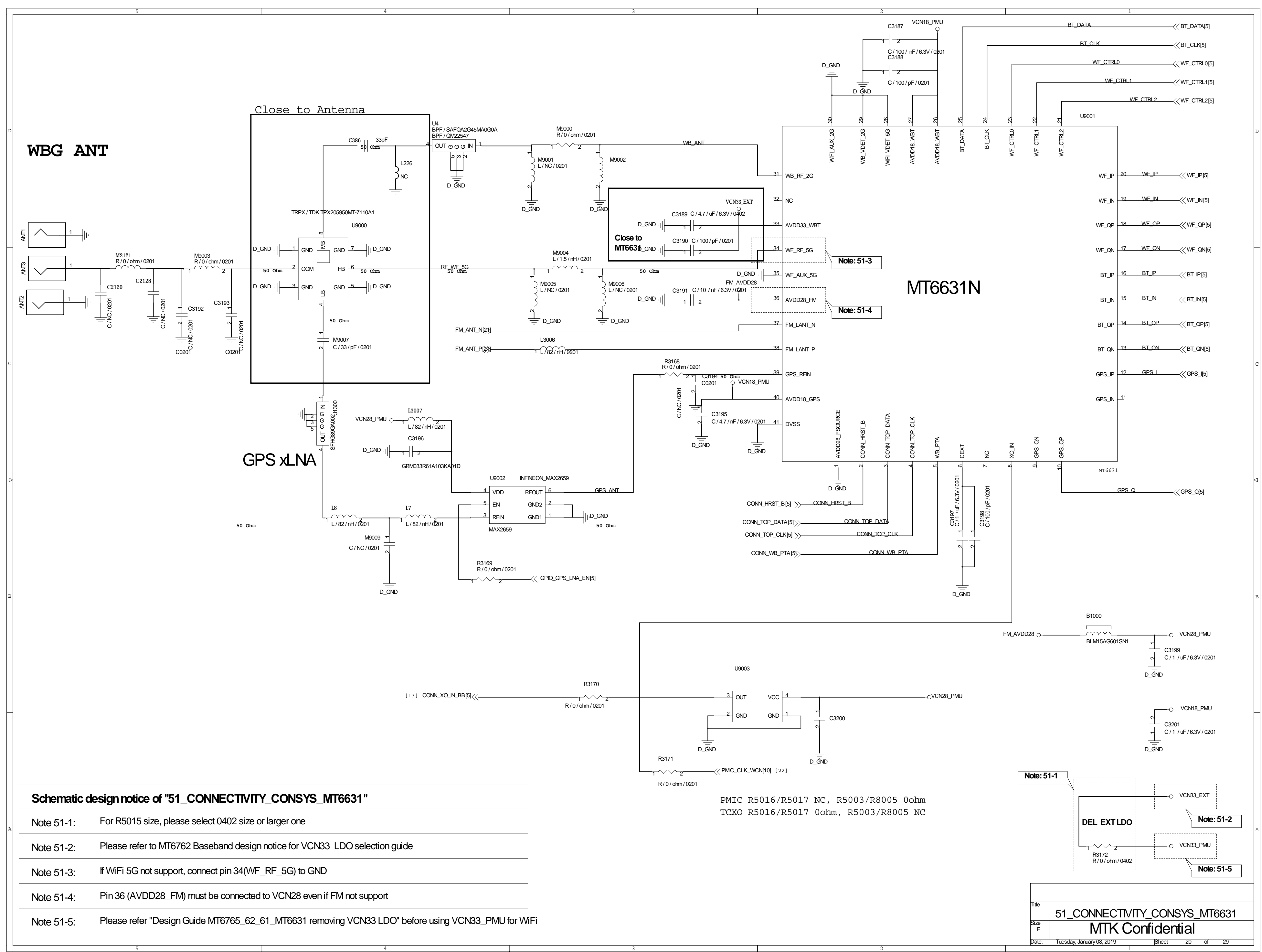
Note 44-2: DRAM ZQx resistor = 240ohm (1%) that must be connected to VDDQ.

Note 44-4: VDD2 VDDQ decoupling cap: closed to DRAM ball.
For other cap for PMIC [->10uF, at PMIC page]:
please also refer to MMD and layout guide for placement.

Note 44-5:	DRAM type	R4437, R4438
	LPDDR4	Mount
	LPDDR4X	NC

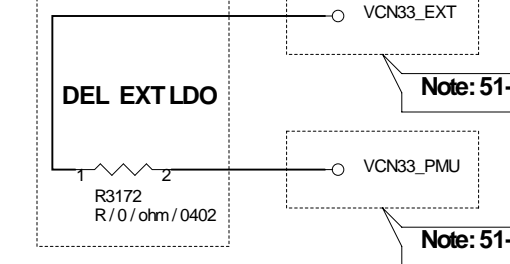
COMPANY: <Company Name>			
TITLE: <Title>			
DRAWN: <Drawn By>	DATE: <Drawn Date>	DESIGNED: <Checked Date>	REV: <Checked Date>
CHECKED: <Checked By>	DATE: <Checked Date>	QUALITY CONTROL: <QC Date>	RELEASED: <Released Date>
CODE: <Code>	SIZE: E	DRAWING NO: <Drawing Number>	REV: <Revision>
SCALE: <Scale>	SHEET 46	29	



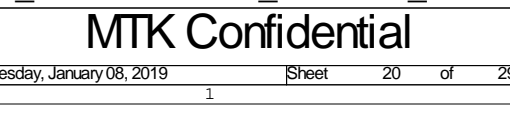


MT6631N

Note: 51-1



Note: 51-2

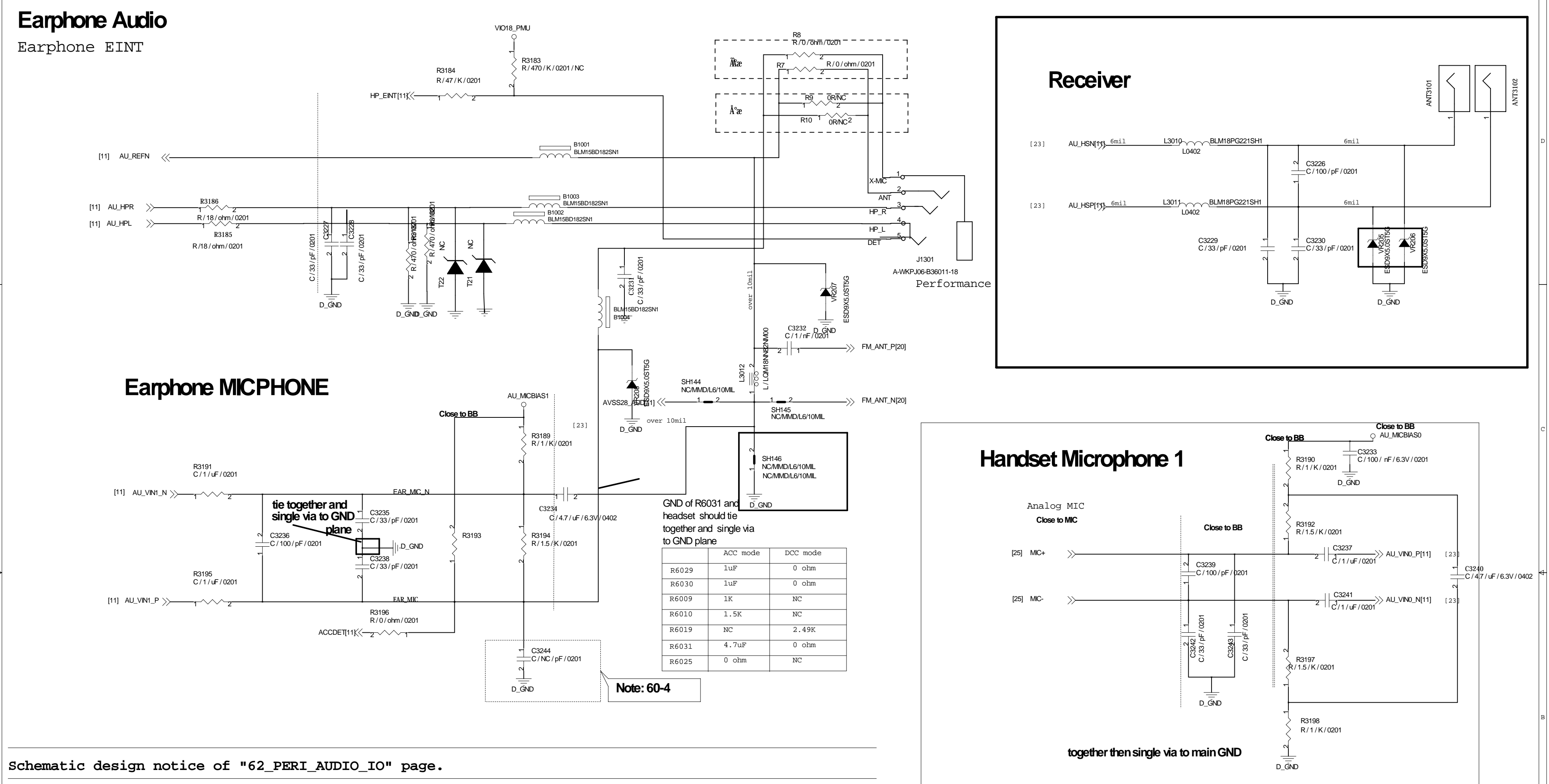


Note: 51-3



Note: 51-4





Schematic design notice of "62_PERI_AUDIO_IO" page.

Note 62-1:
Part # of BEAD6202, BEAD6203, BEAD6204 and BEAD6205 needs changed to "BHM18BD102SN1" for high THD performance (-90dB) but this BOM change will results in FM RSSI 10dB degraded.

Note 62-2: Reserved-Cap for CS/RS test, please double check multi-key-function when used.

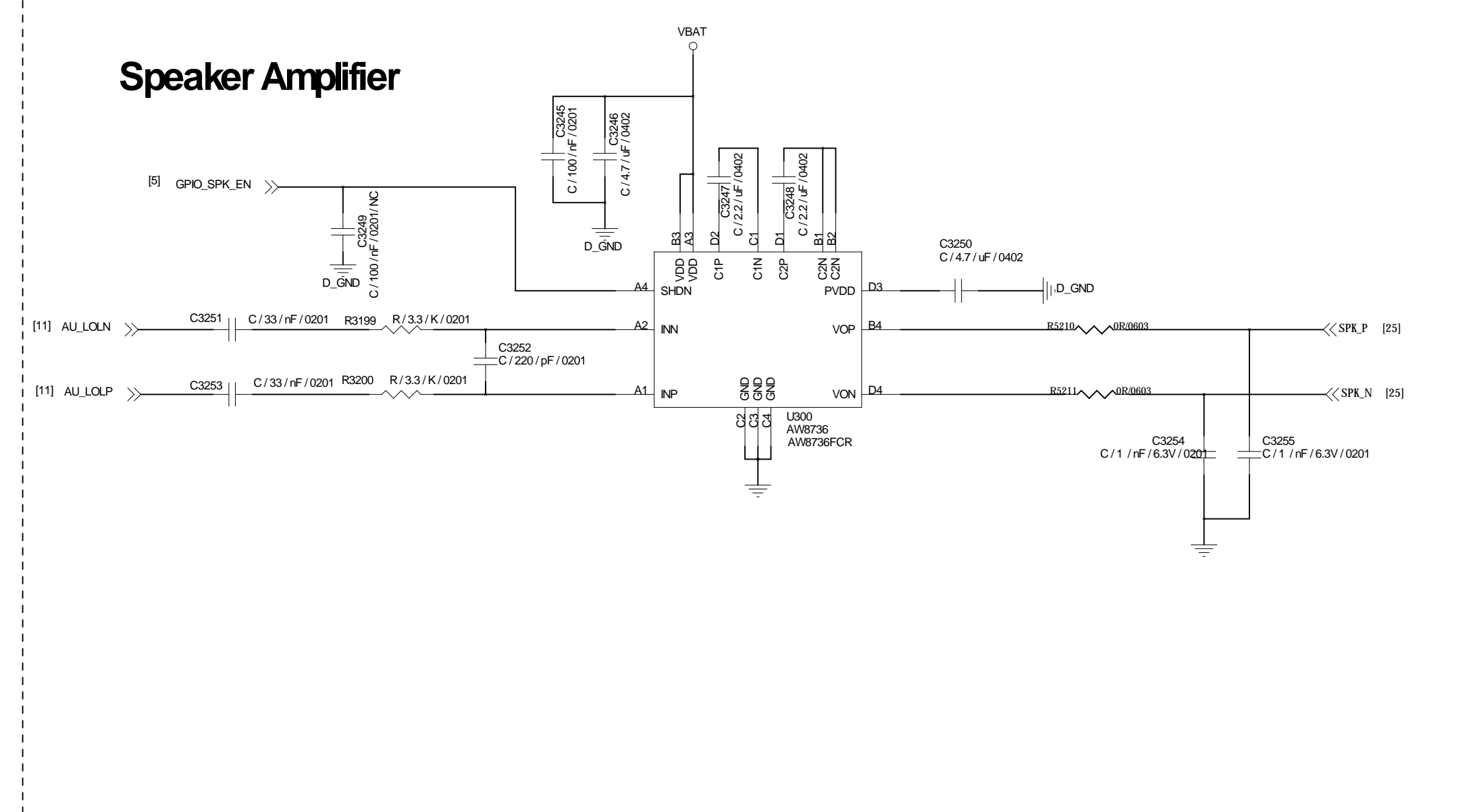
Part	Microphone Jack	Earphone Jack
R6613	0 ohm	100nF

Note 62-4:
1. Please Select ACC Mode for Operator Project to Pass Electrical MOS Test;
More Information refer to Audio/Speech Design Notice;
2. Need do SW configuration for ACC or DCC mode
3. Analog input impedance is as below, please customer check the detail requirement with mic vendor if the mic used has special requirement on the input impedance of analog input

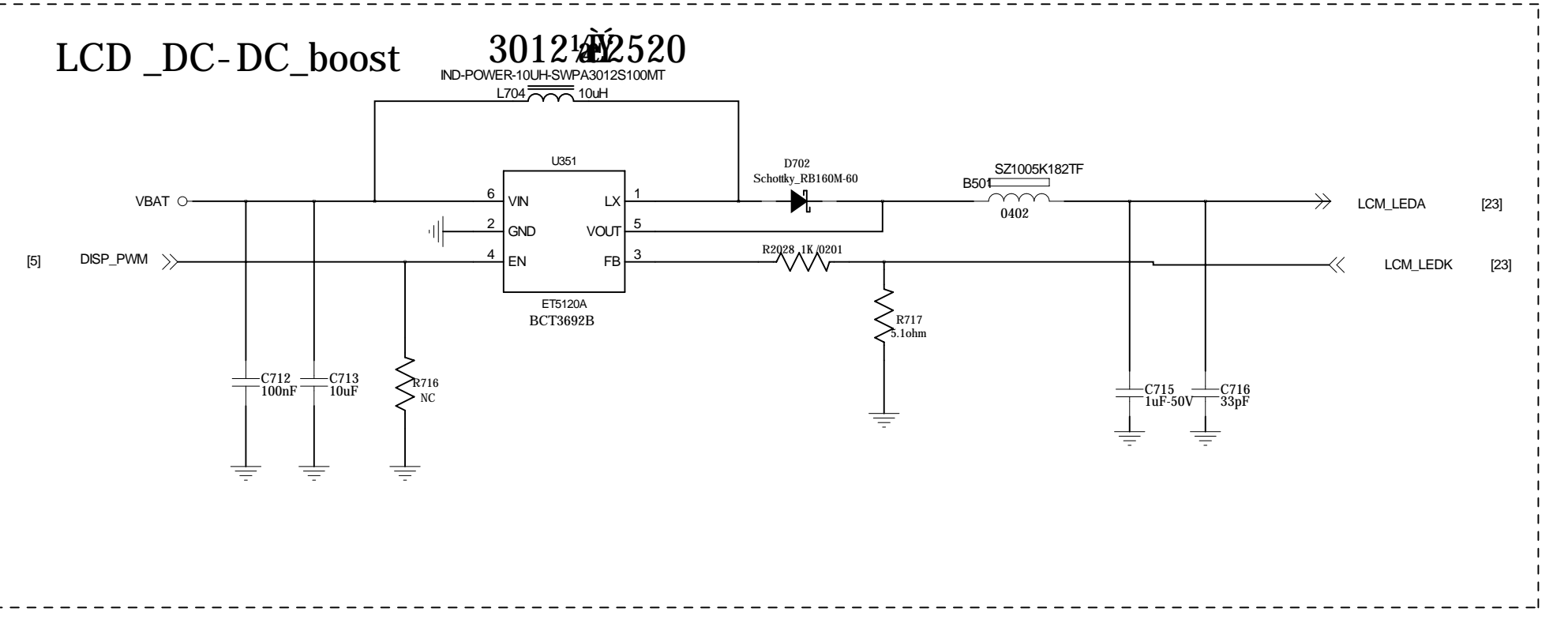
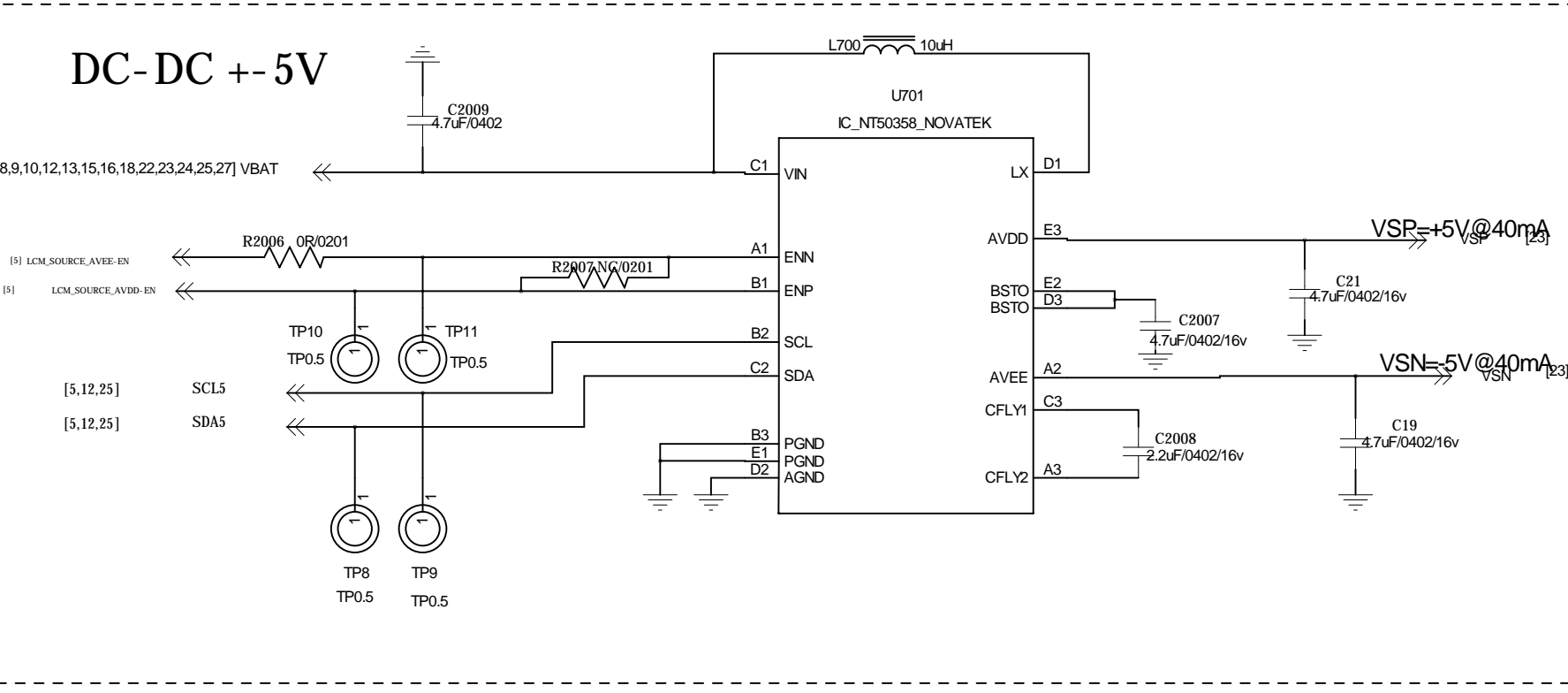
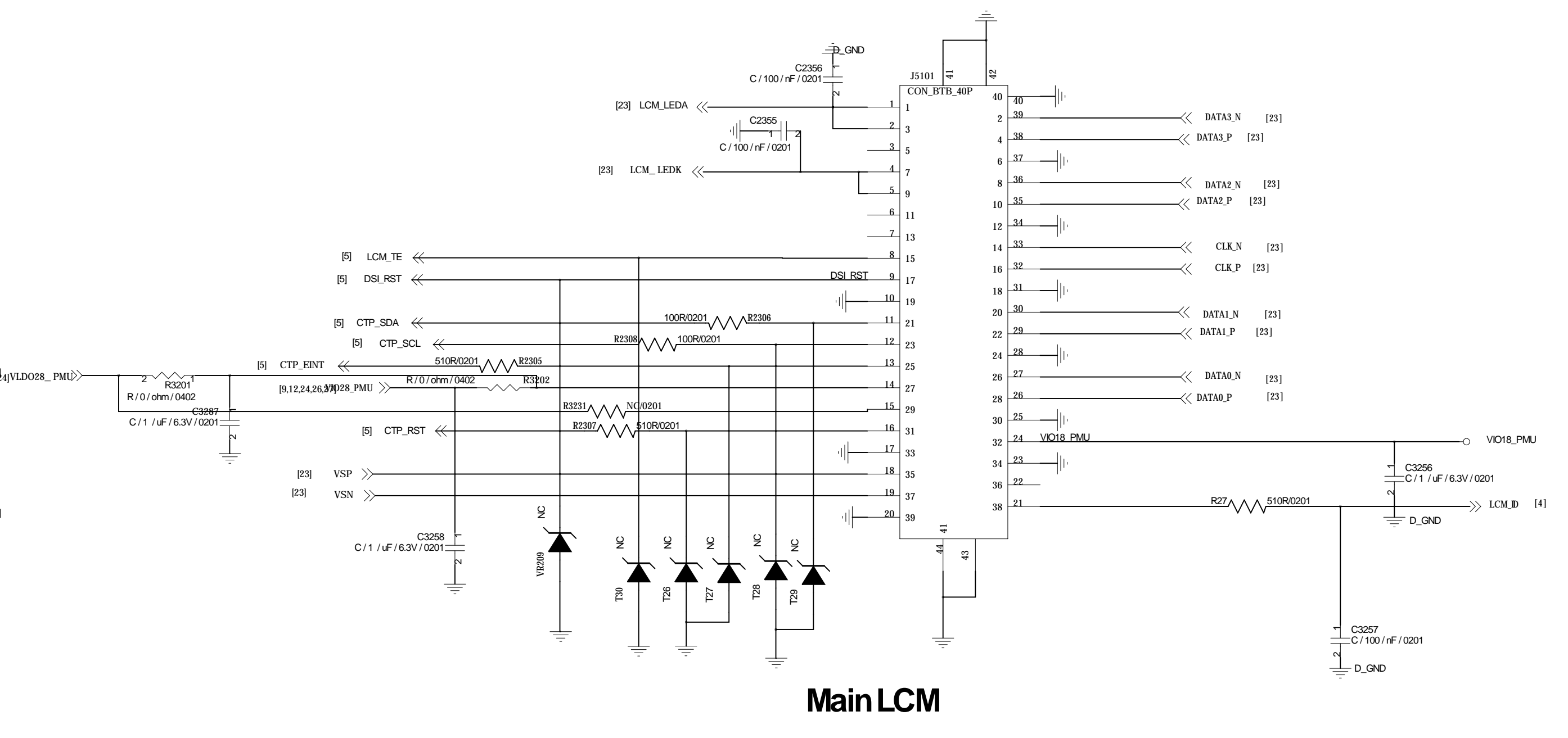
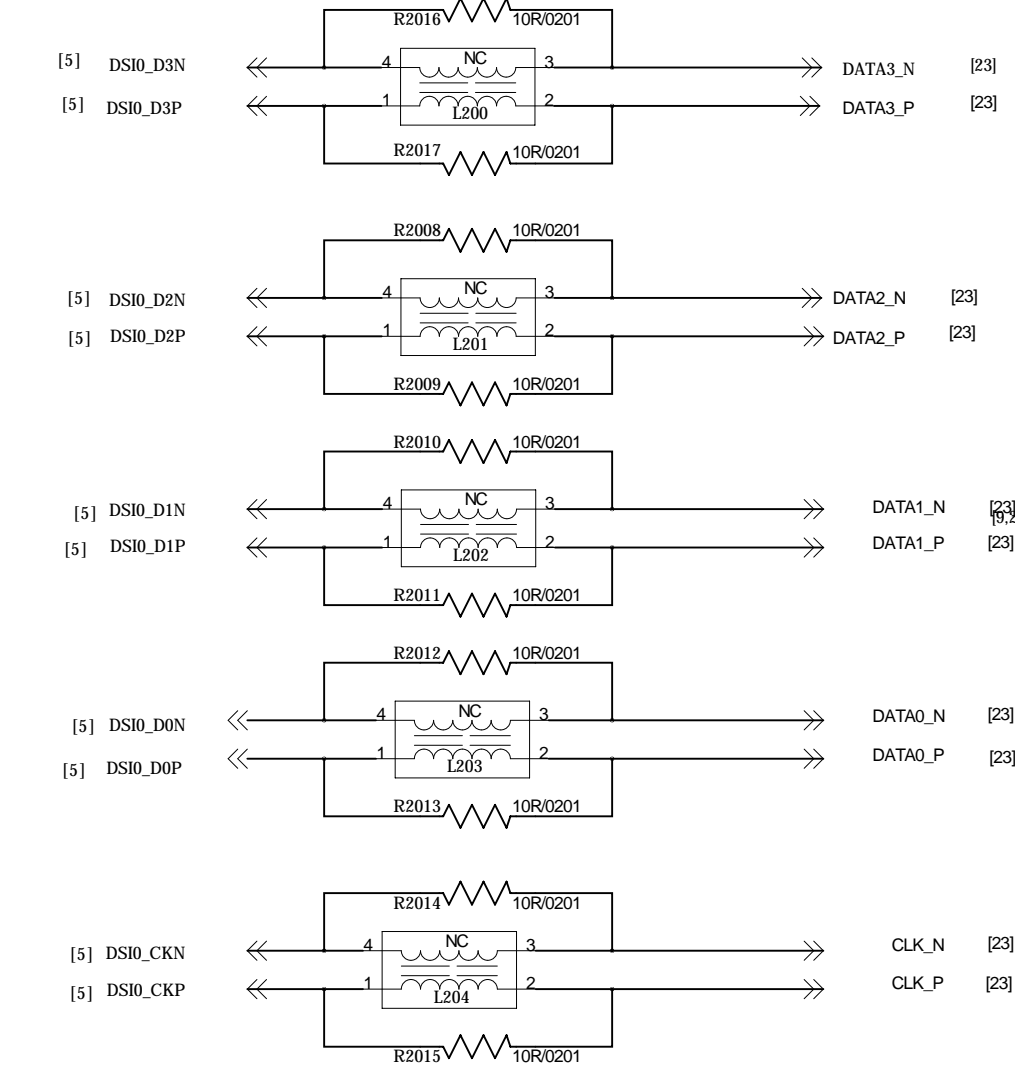
Part	Mode	Impedance	Typ	Max	Unit
R6613	ACC	7k	-	-	ohm
C623	DCC	-	-	11p	pF

Note 62-5: Please select C6231 with 0402 size

Speaker Amplifier

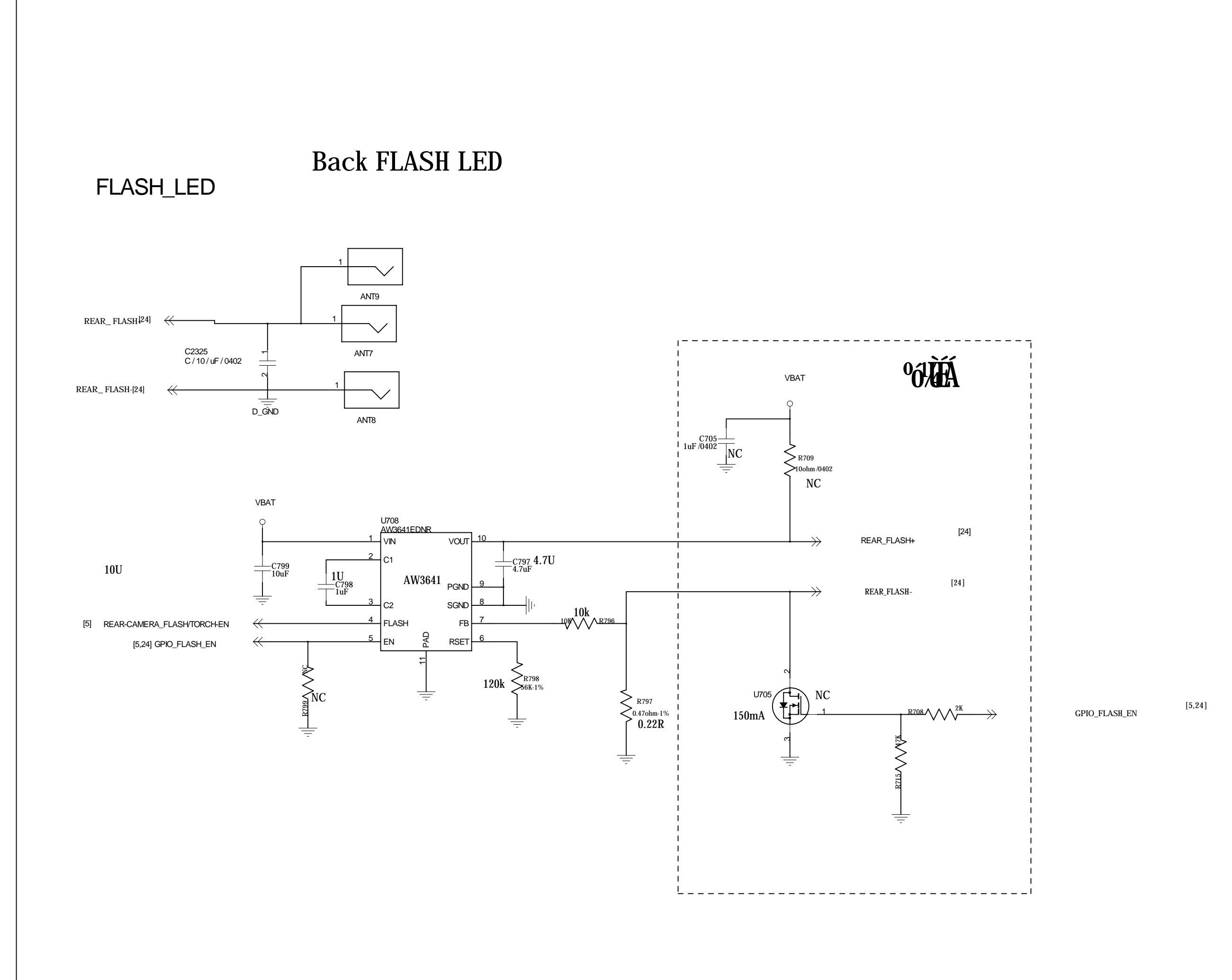
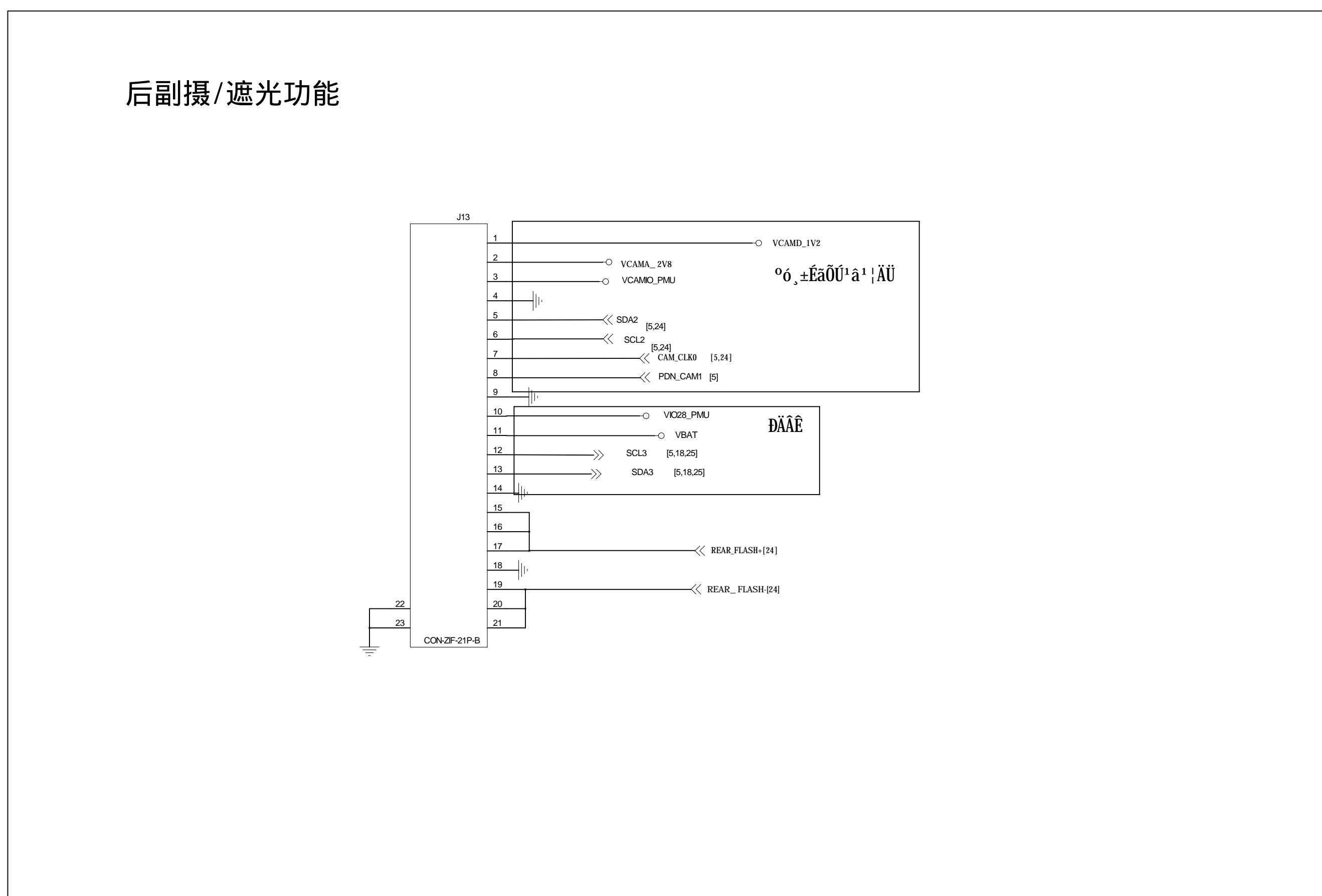
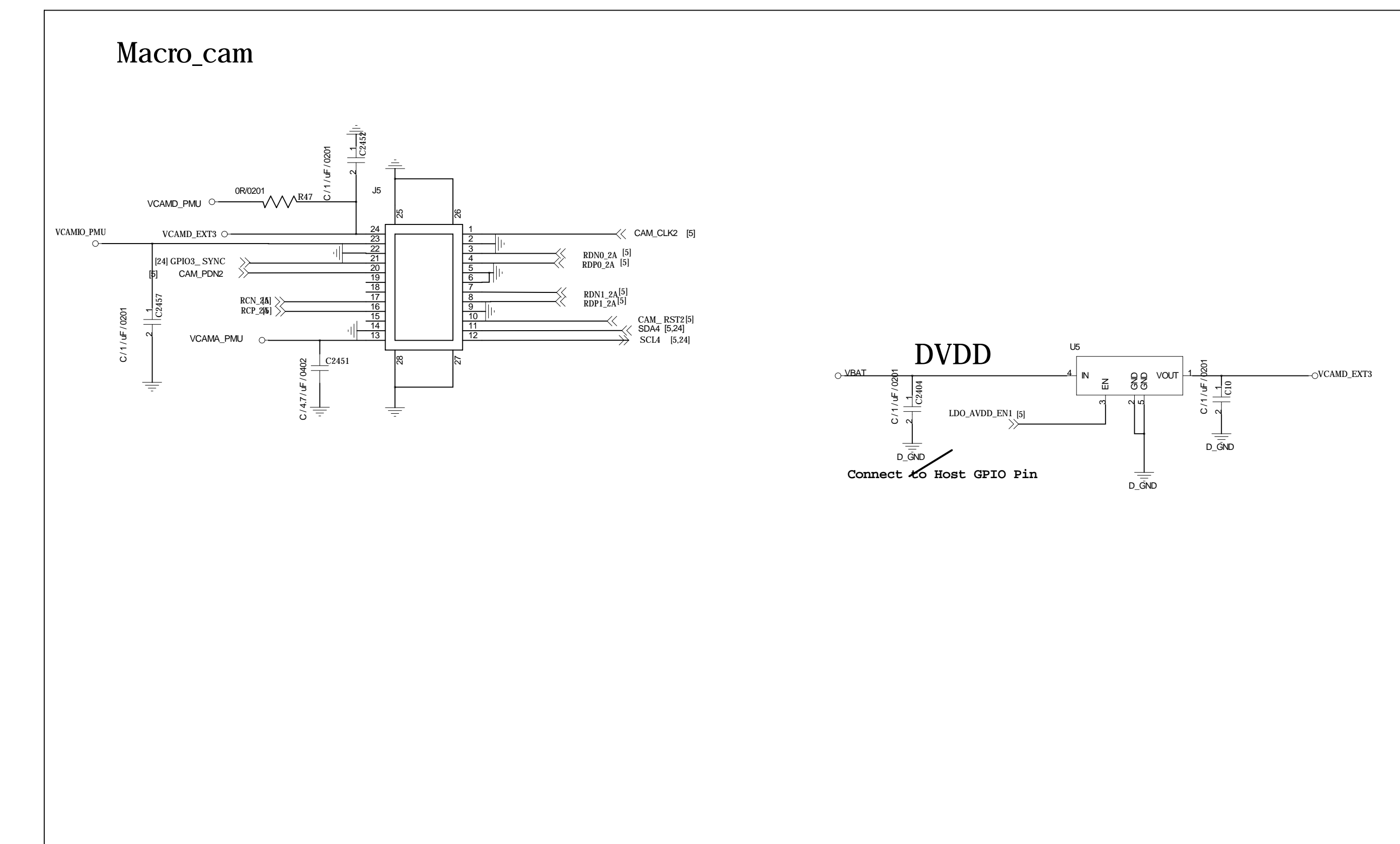
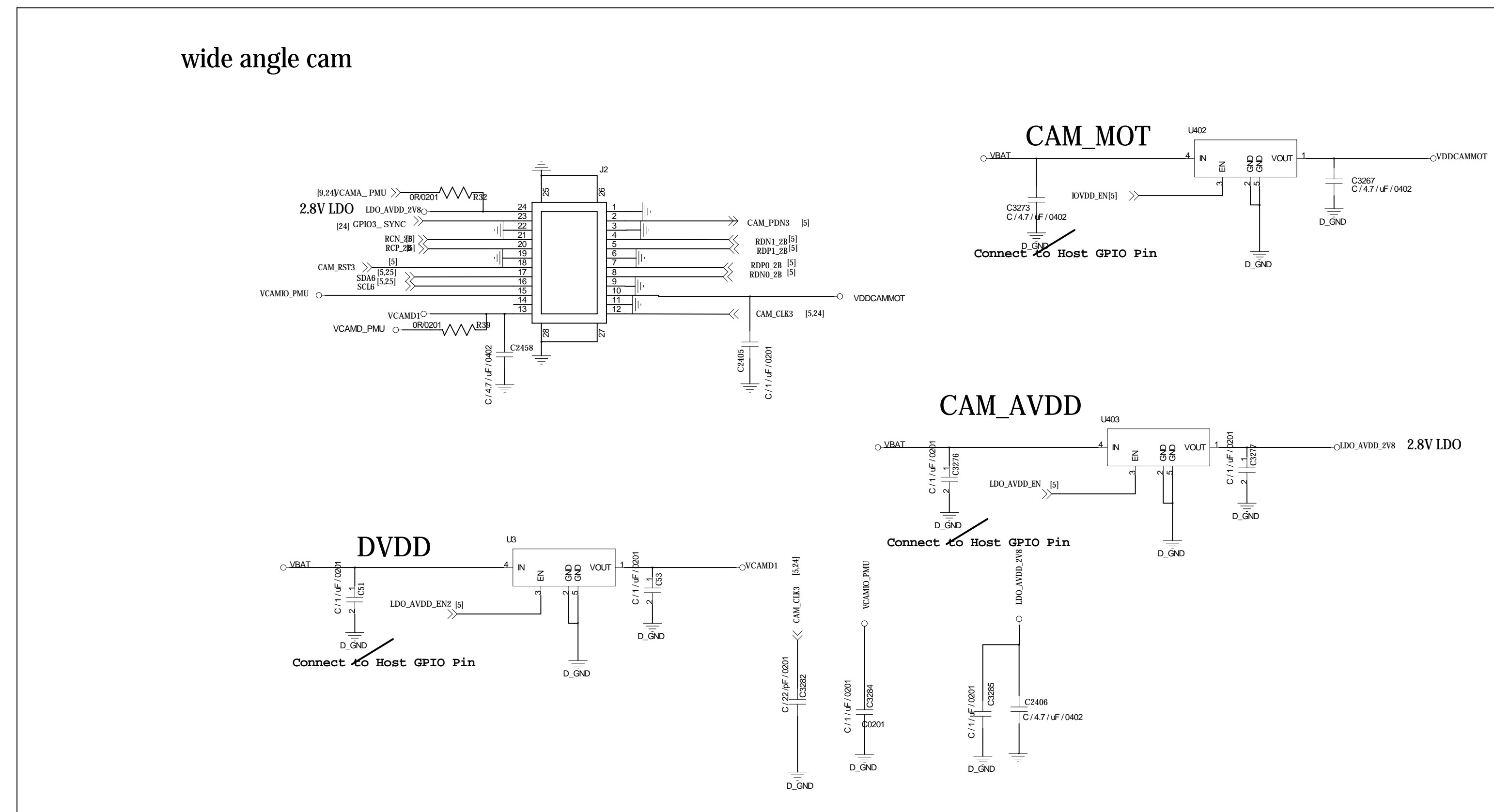
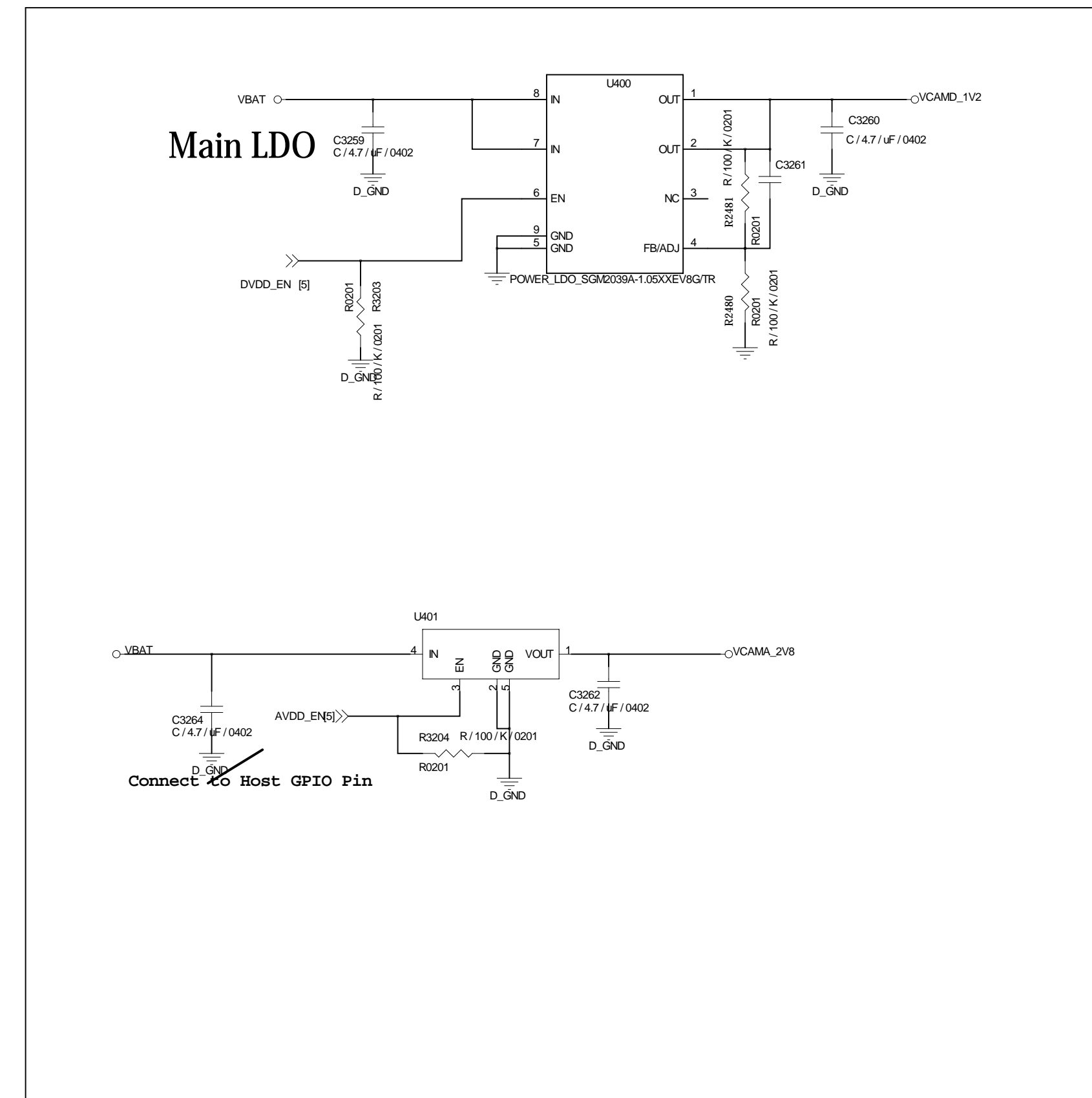
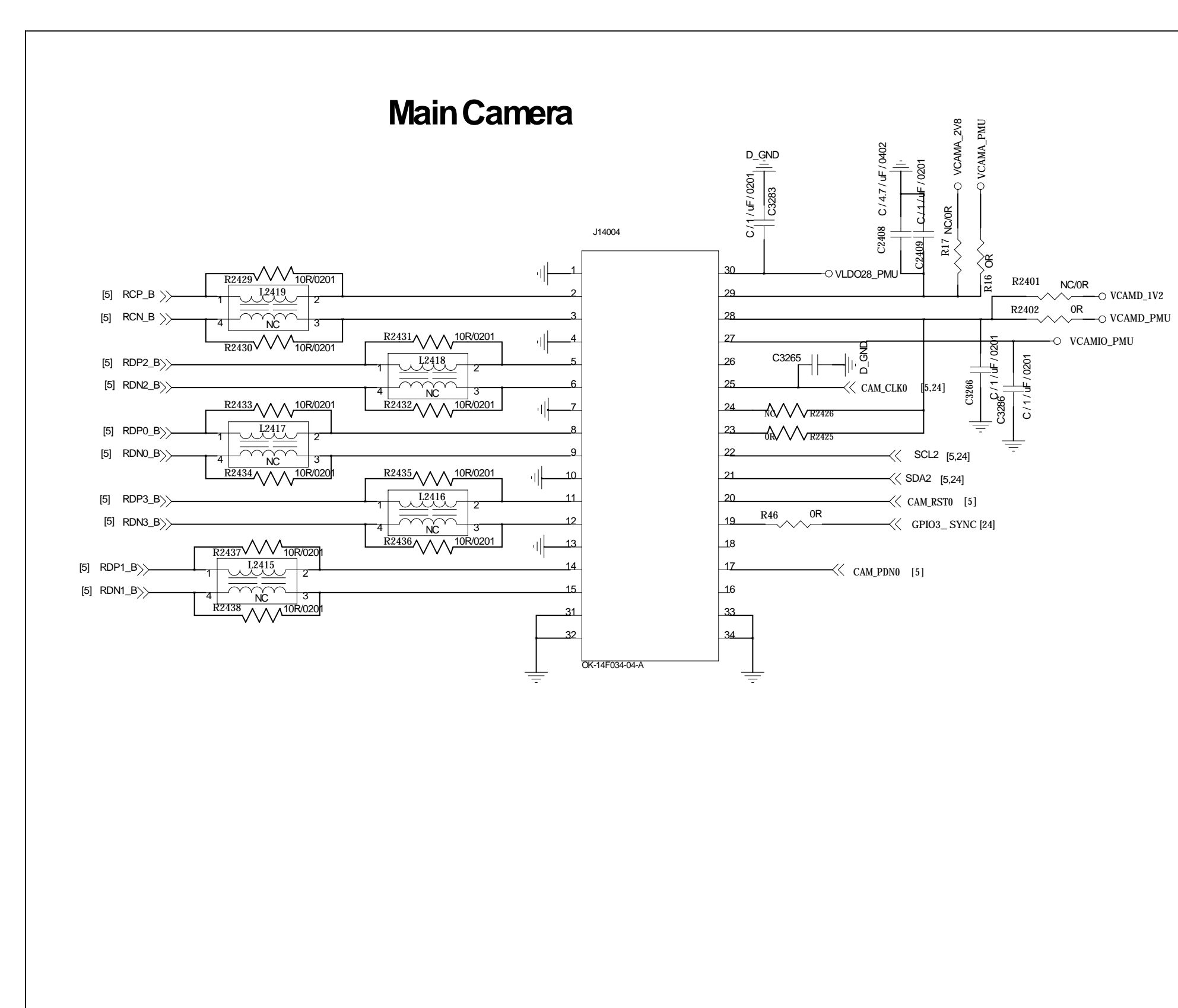
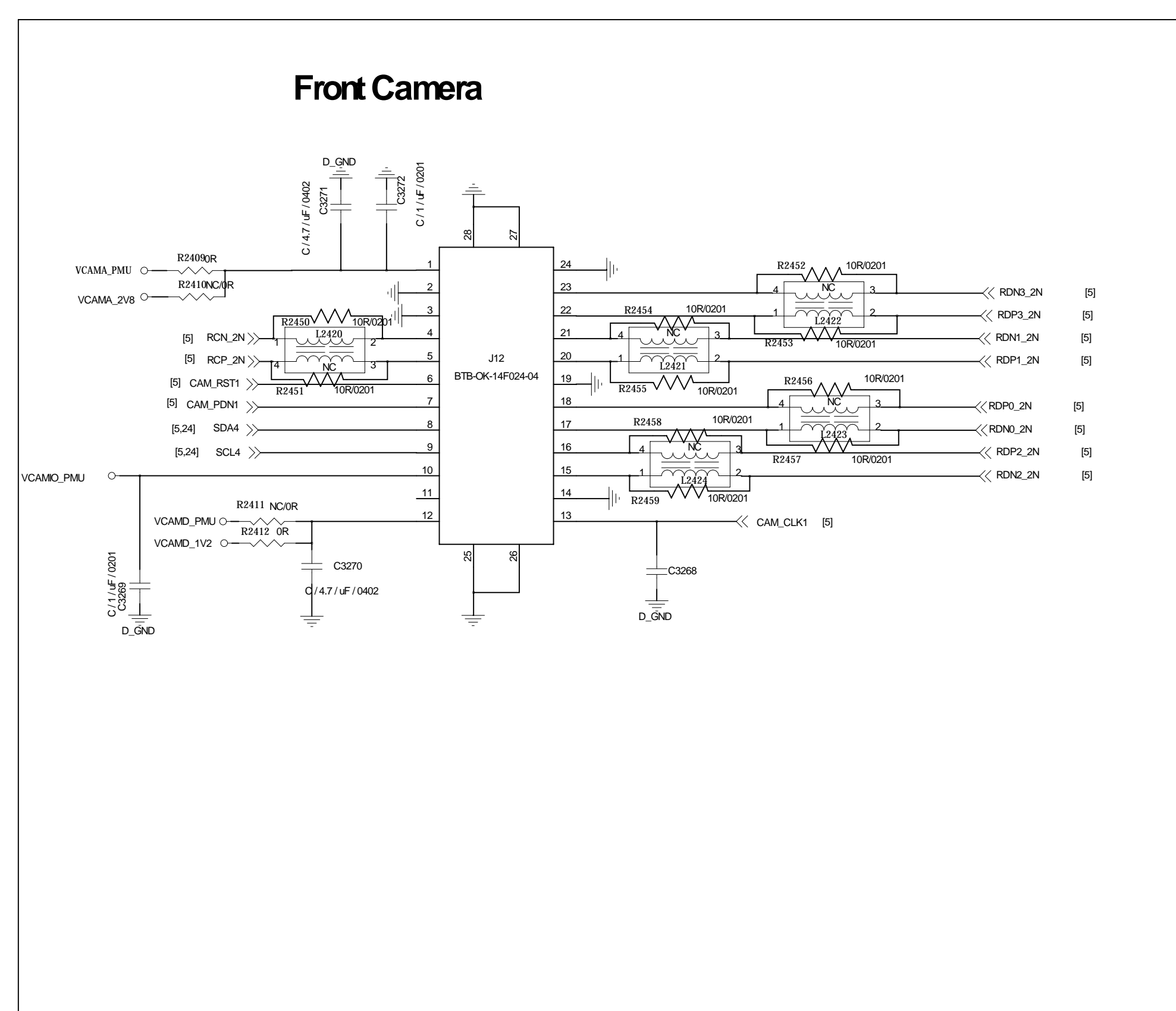


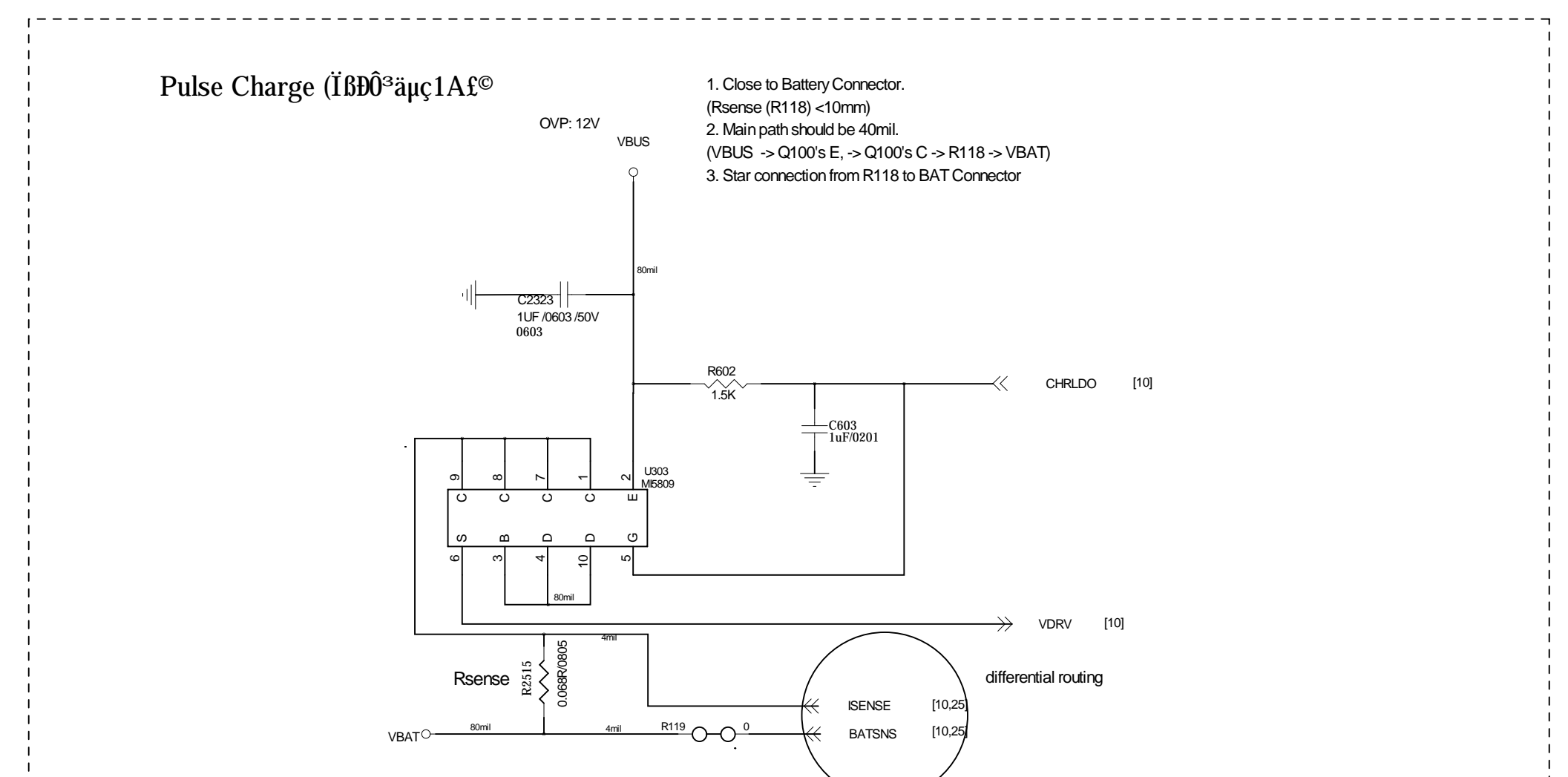
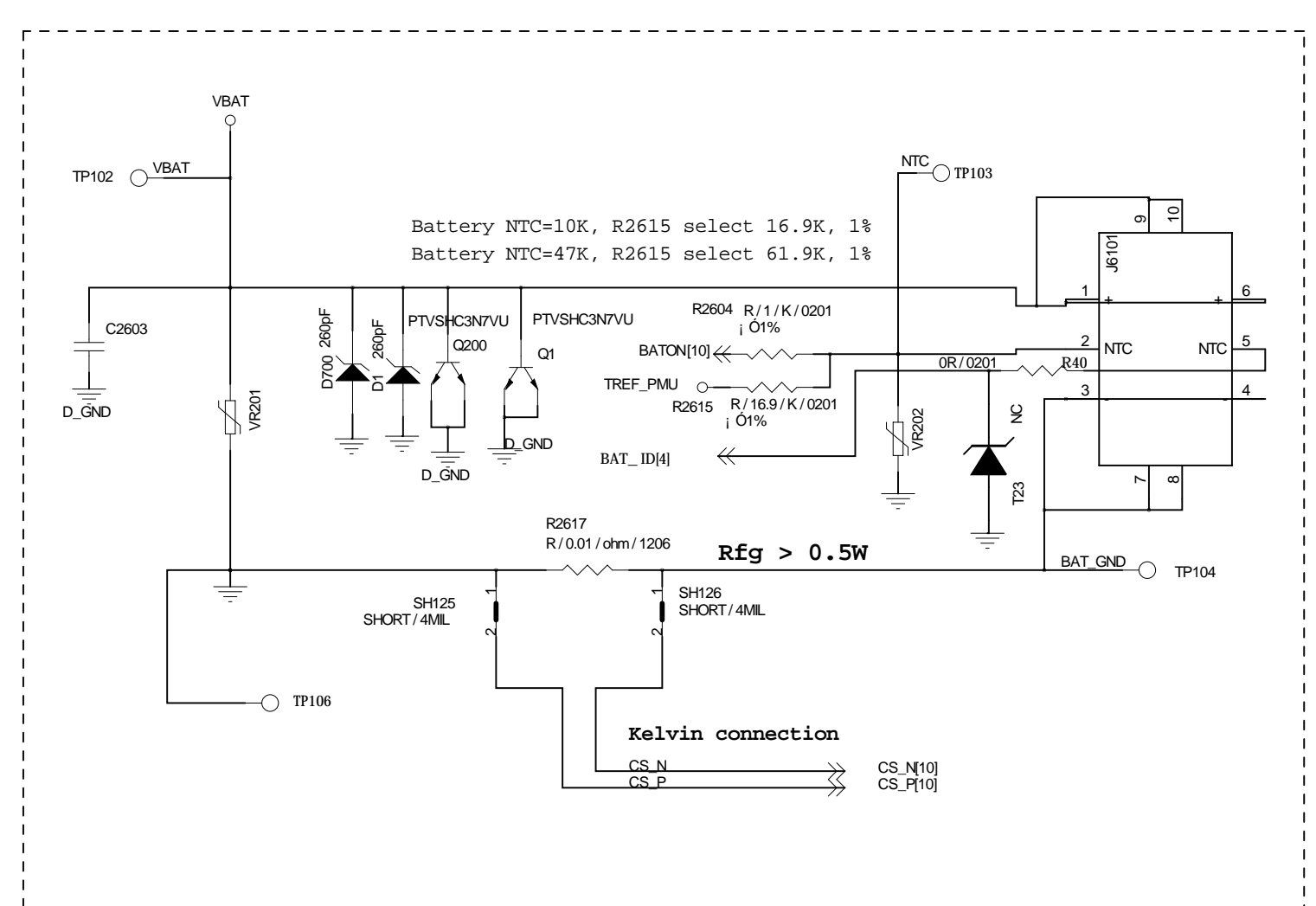
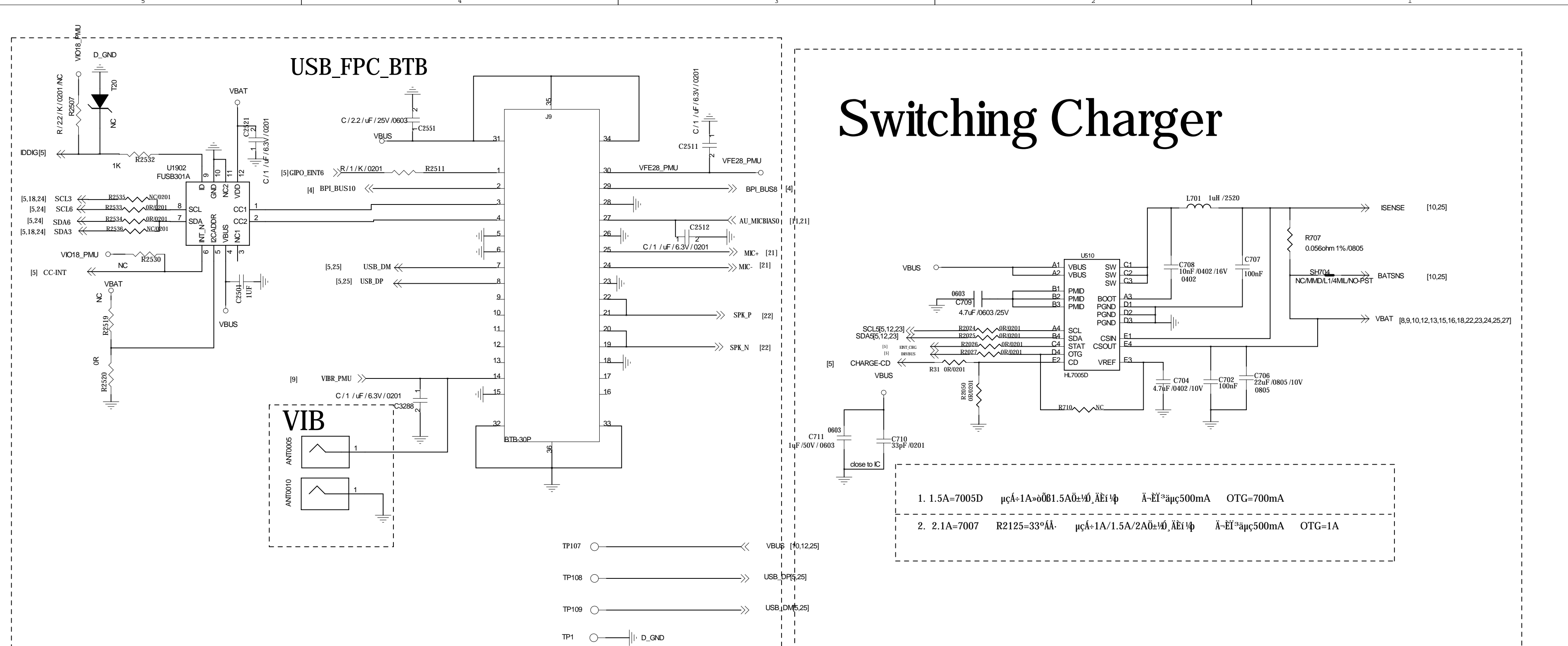
LCD EMI filter

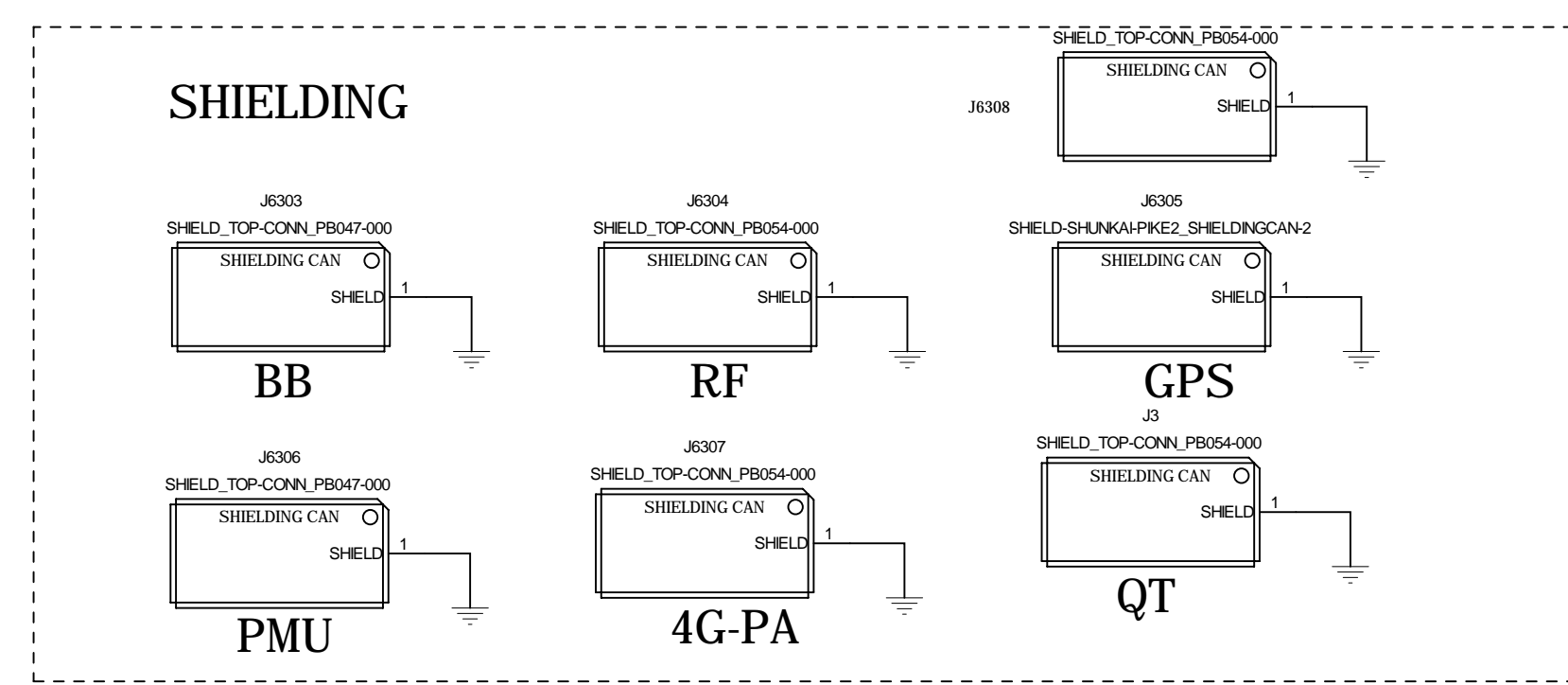
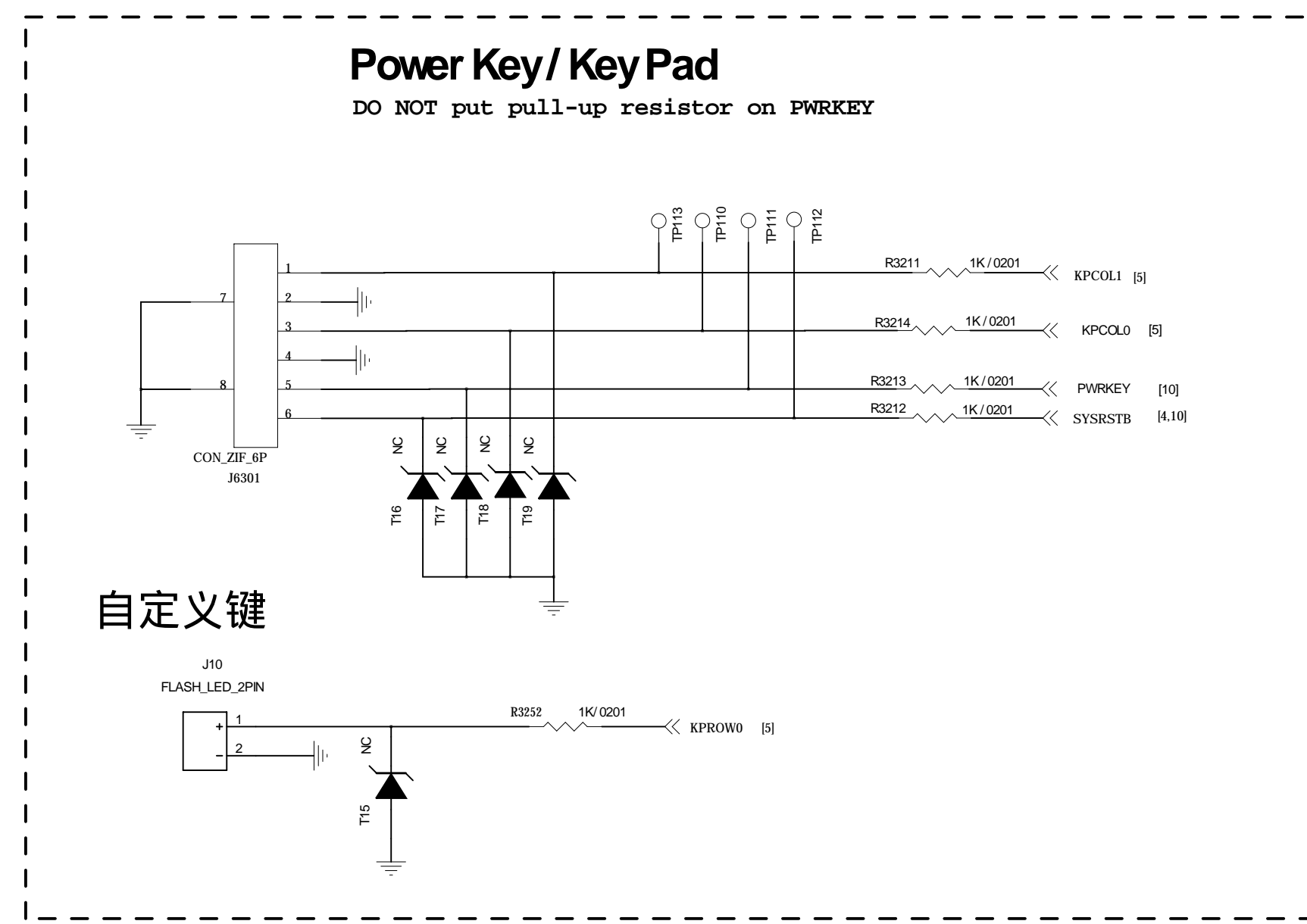


Schematic design notice of "67_PERI_LCD_CTP" page.

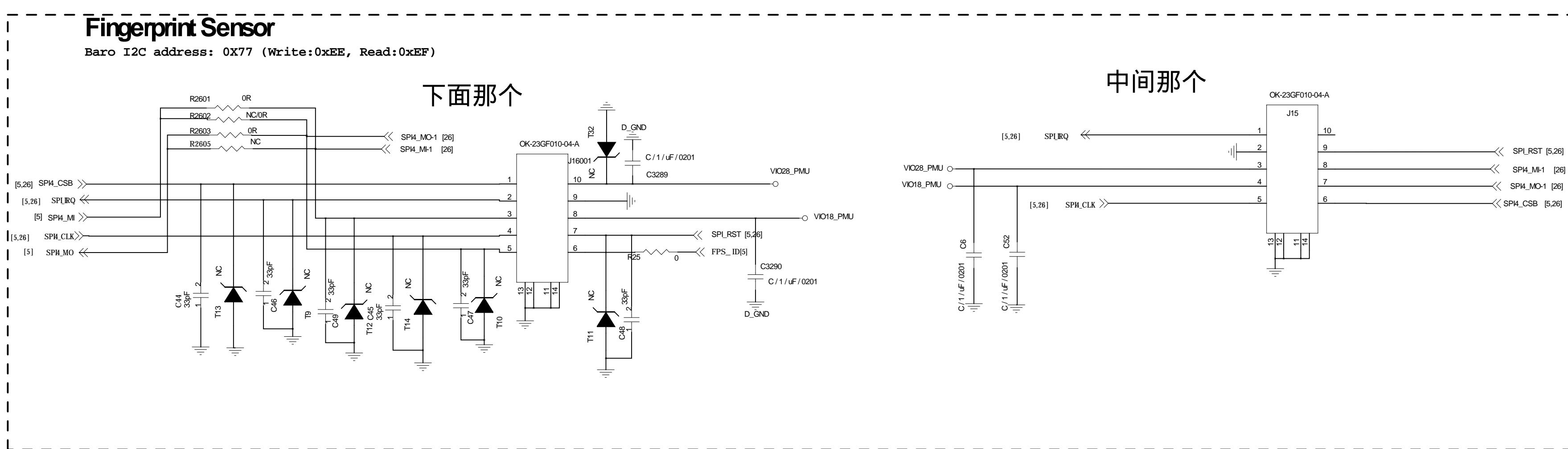
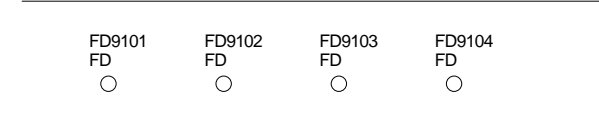
Note 67-1: It is recommended to reserve common-mode choke to prevent RF de-sense, the max. cap. loading of common-mode choke must be less than 3pF.







Fiducial mark



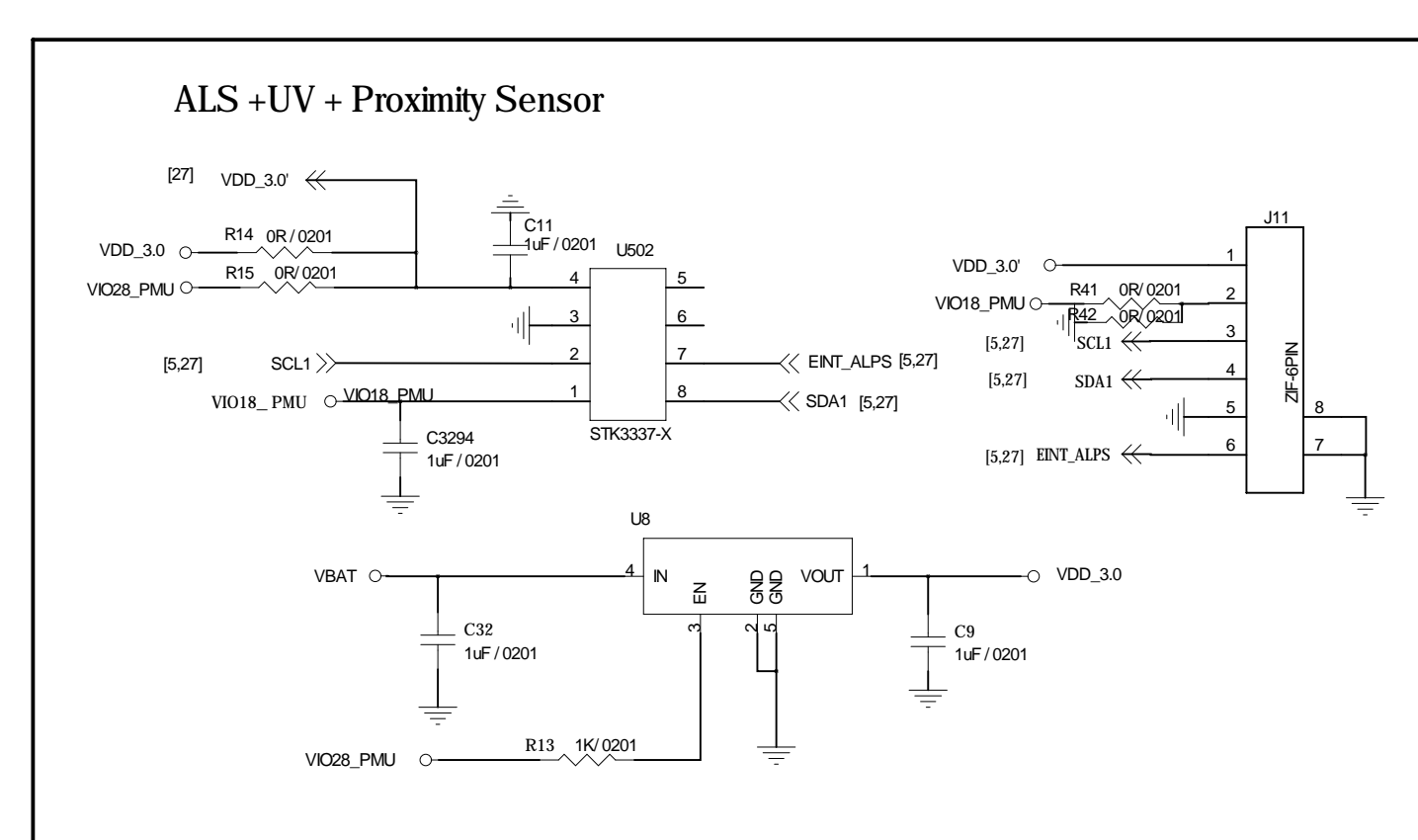
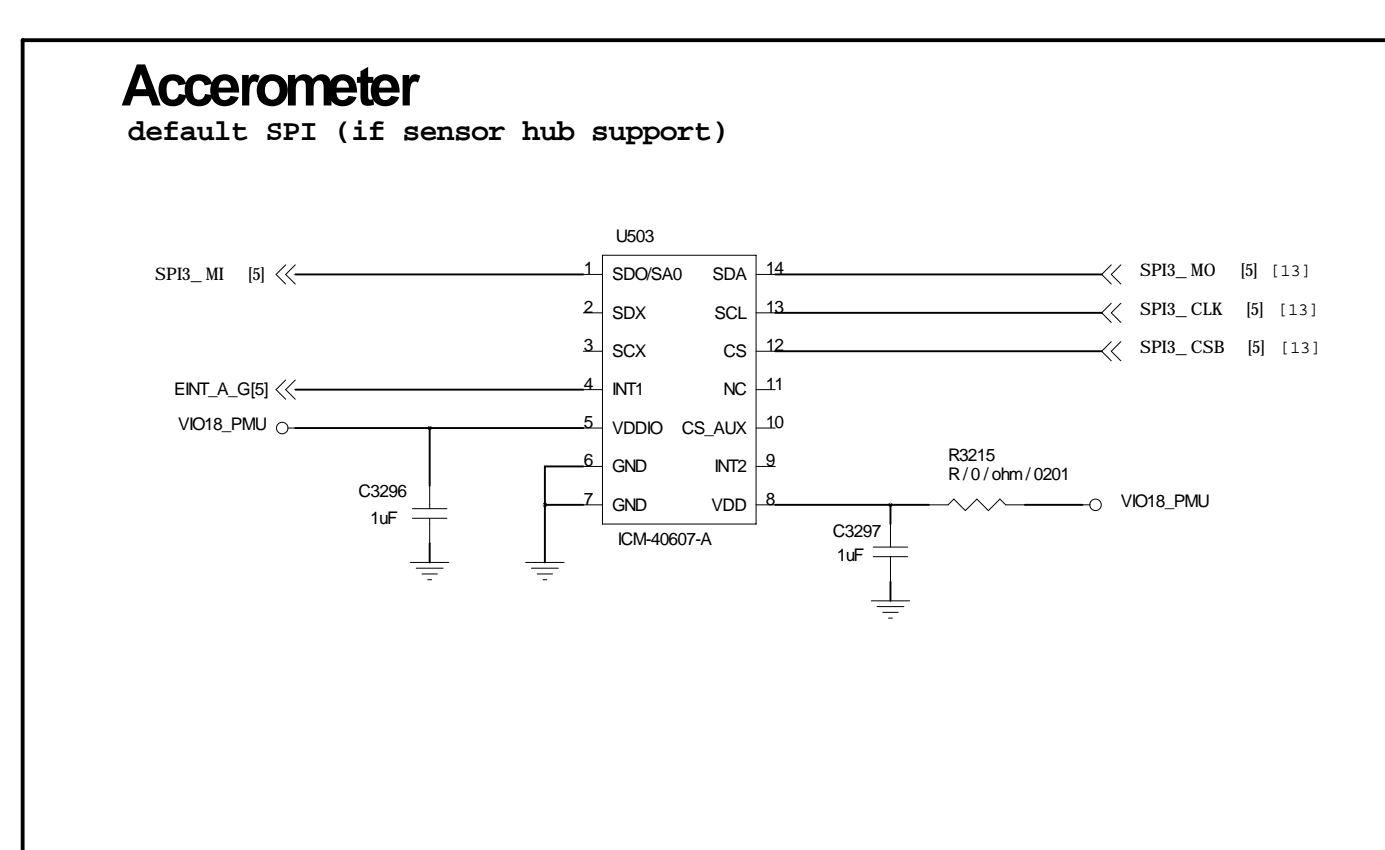
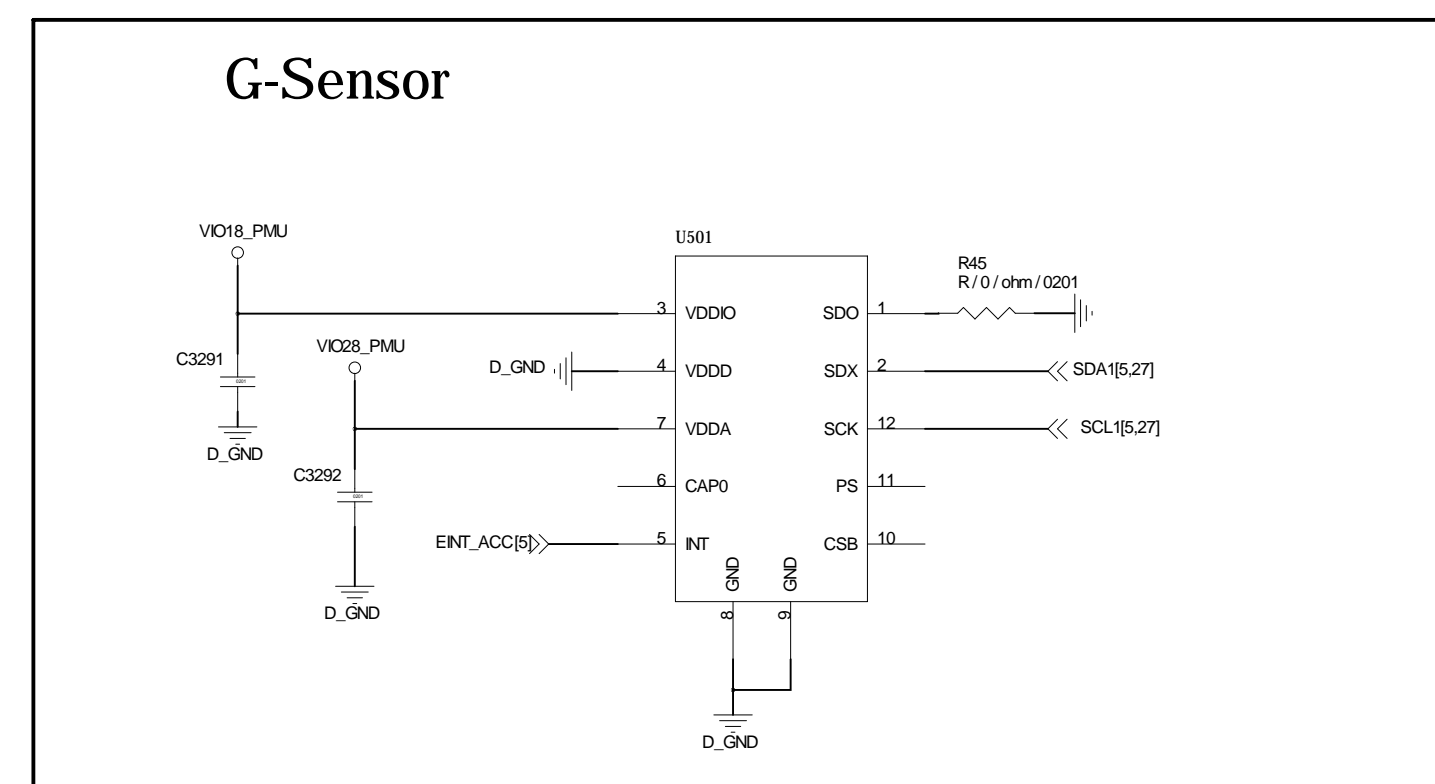
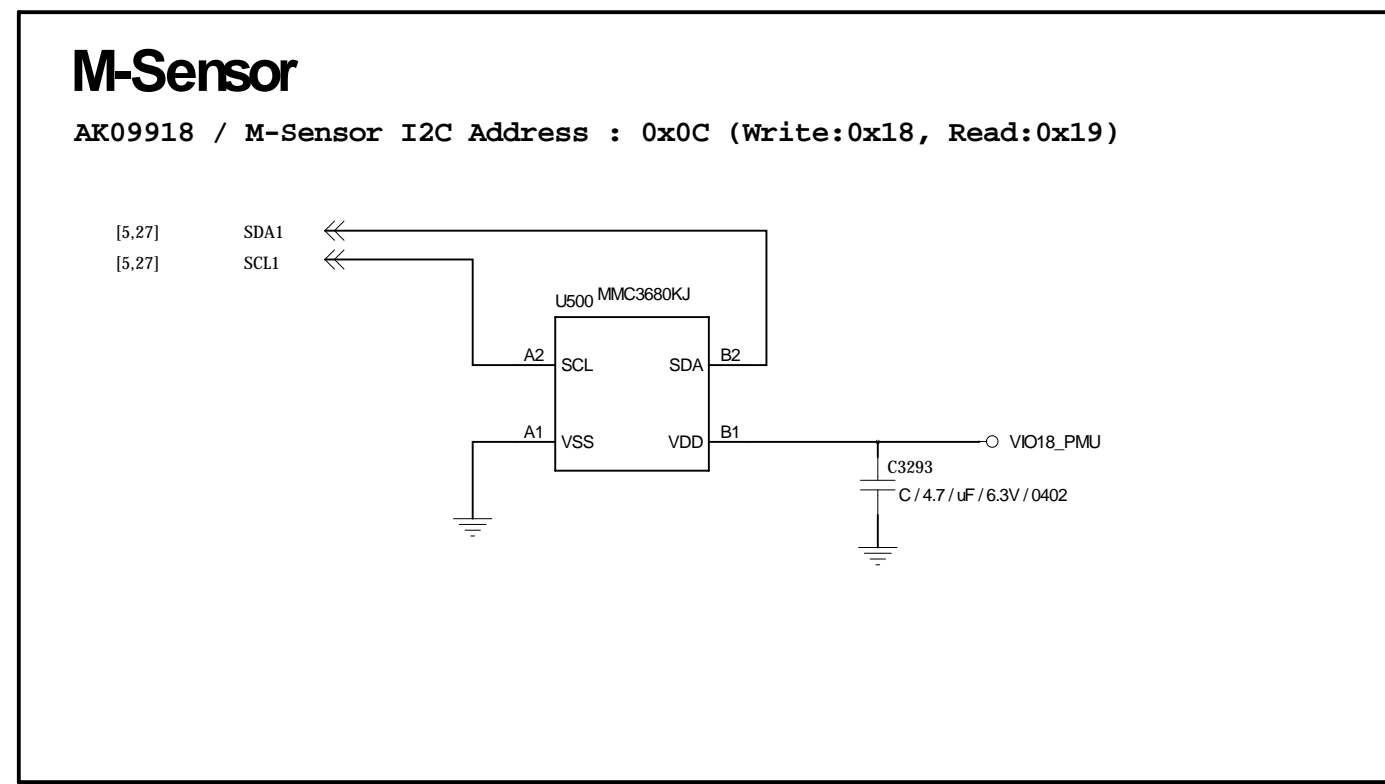
Schematic design notice of "65_PERI_Dual_SIM_ICUSB_KEYPAD" page.

Note 75-1: DO NOT put pull-up resistor on PWRKEY

Note 75-2: Volume Up : HOME Key/ GND

Volume Down: KPROJ/KPCOLO

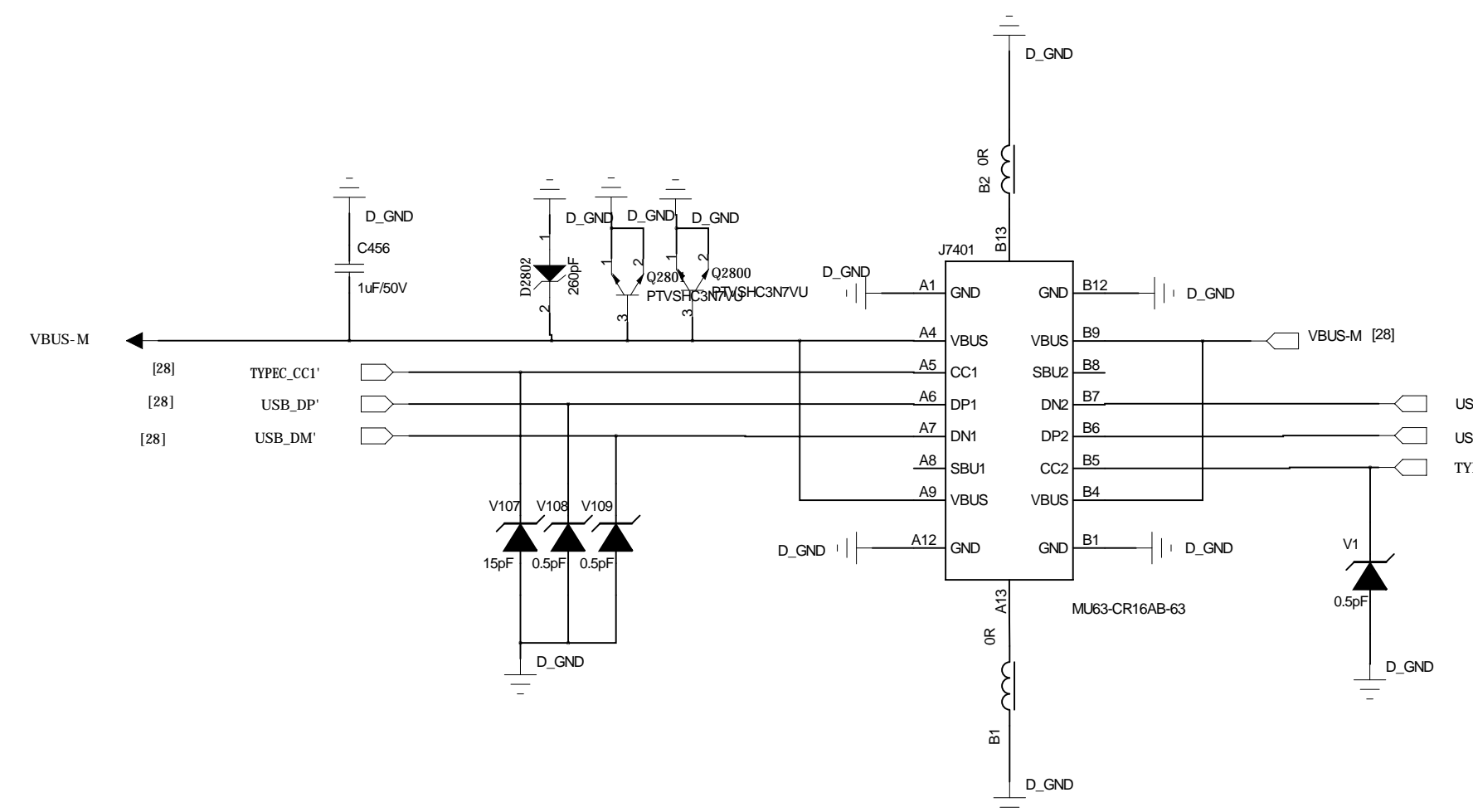
Note 75-3: For better ESD performance, please select suitable components for system protection.



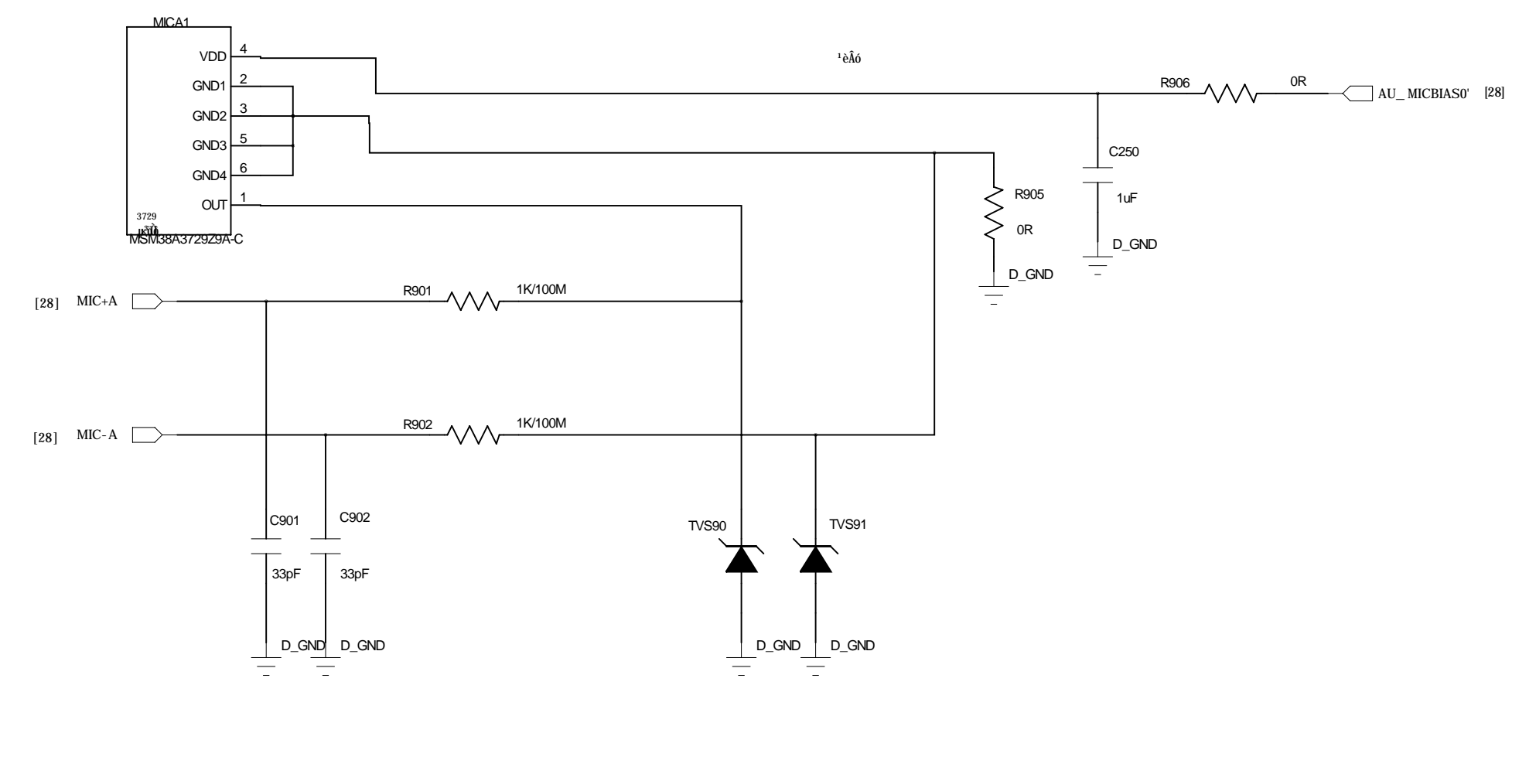
- Schematic design notice of "77_PERI_SENSORS_MEMS_ALS/PS" page.**
- Note 77-1: [M sensor] Keep a minimum distance of 15mm from power C's / PCB traces of more than 100mA / magnet component. Check HW design notice for more detail
 - Note 77-2: [A+G] For optimized GPS performance, please check HW design notice for Sensor selection guide
 - Note 77-3: [A+G] MUST use SPI for optimized sensor hub performance **DO NOT USE I2C**
 - Note 77-4: [A+G] Suggest choose sensor support FIFO watermark interrupt otherwise we cannot support Hifi-sensor, daydream VR. And Sensor-location accuracy will become worse.
 - Note 77-5: [Baro] Reserve Baro sensor for LPPe feature (Must for North America Operator / NA SKU)
 - Note 77-6: DO NOT share Sensor hub i2C to other non-SCP device
 - Note 77-7: Interrupt pin of MEMS sensor must be assign to ball EINT[12:0]

REVISION RECORD			
LTR	ECONO	APPROVED	DATE

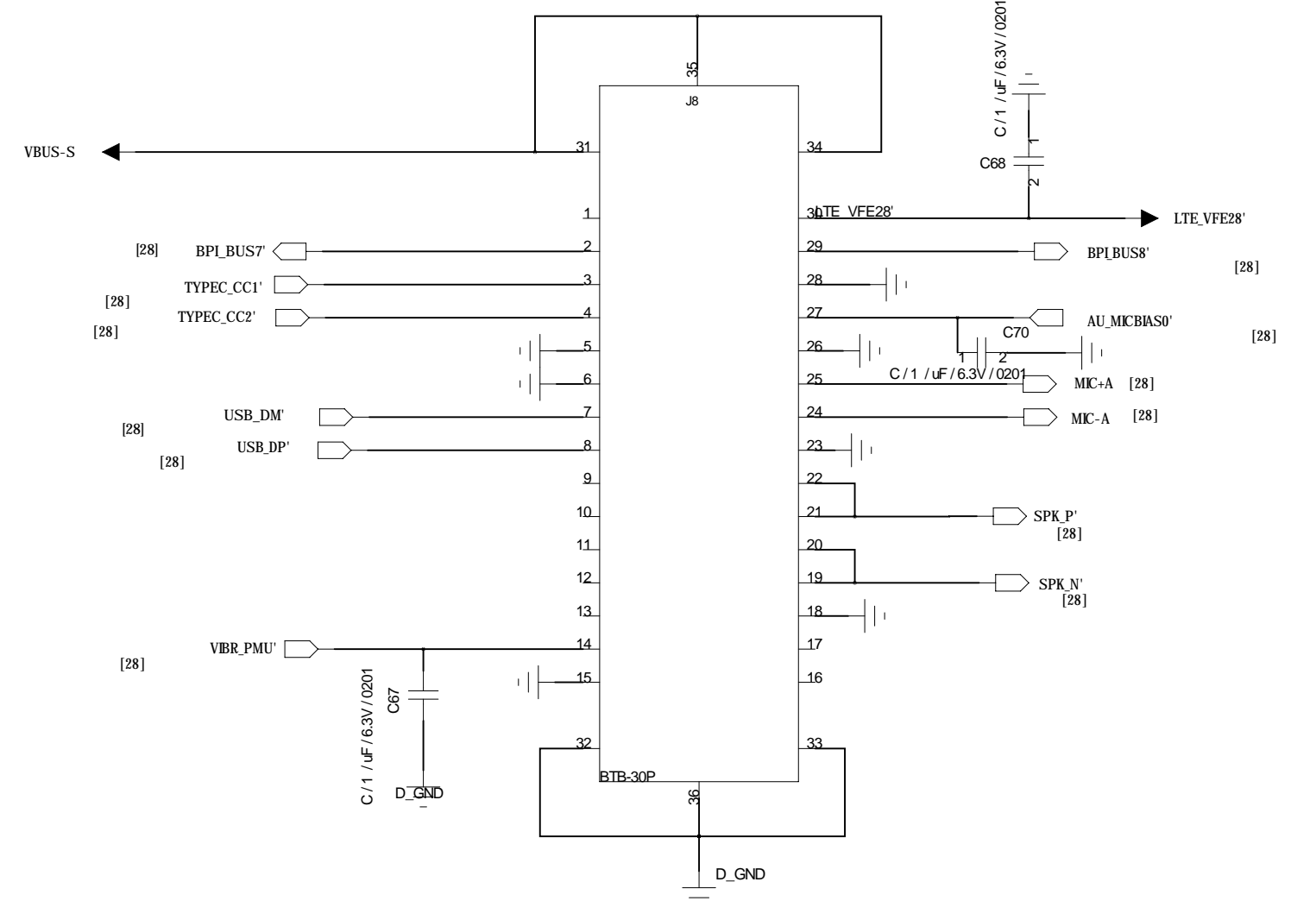
TYPE-C



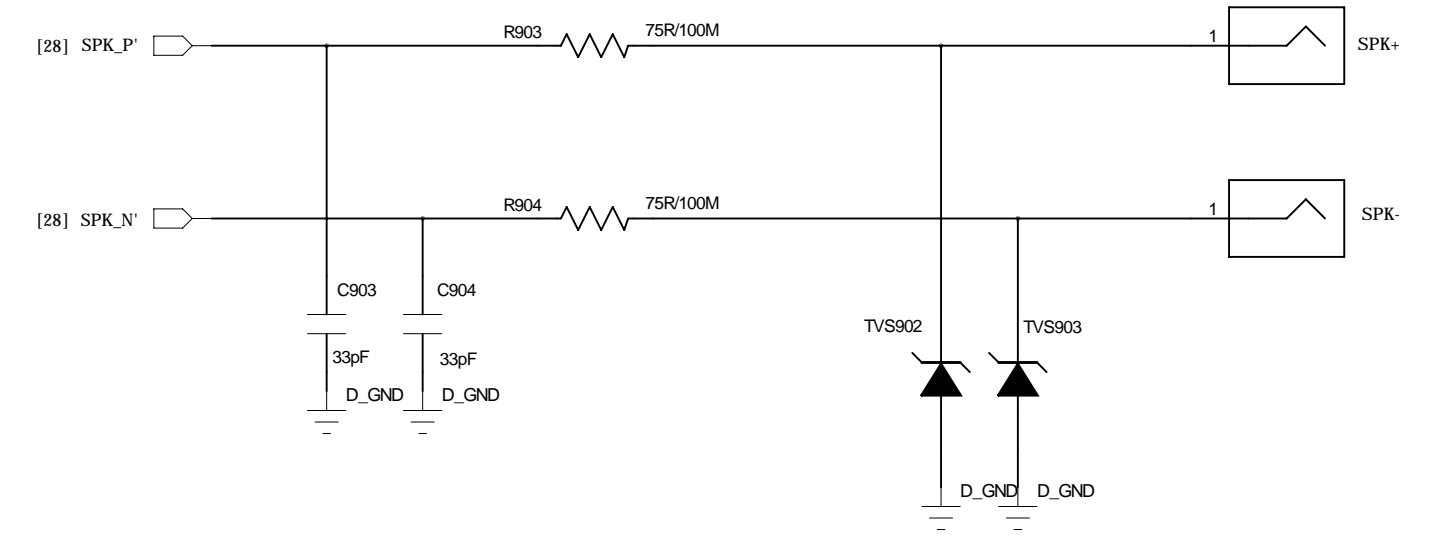
MIC10300



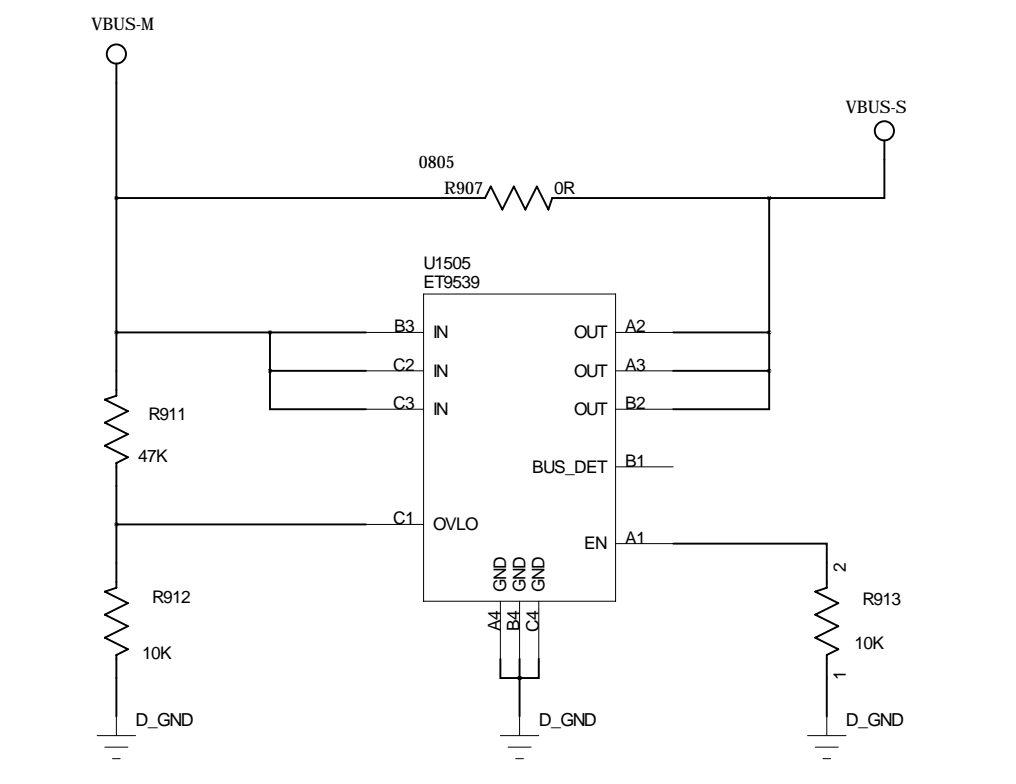
USB_FPC_BTBT



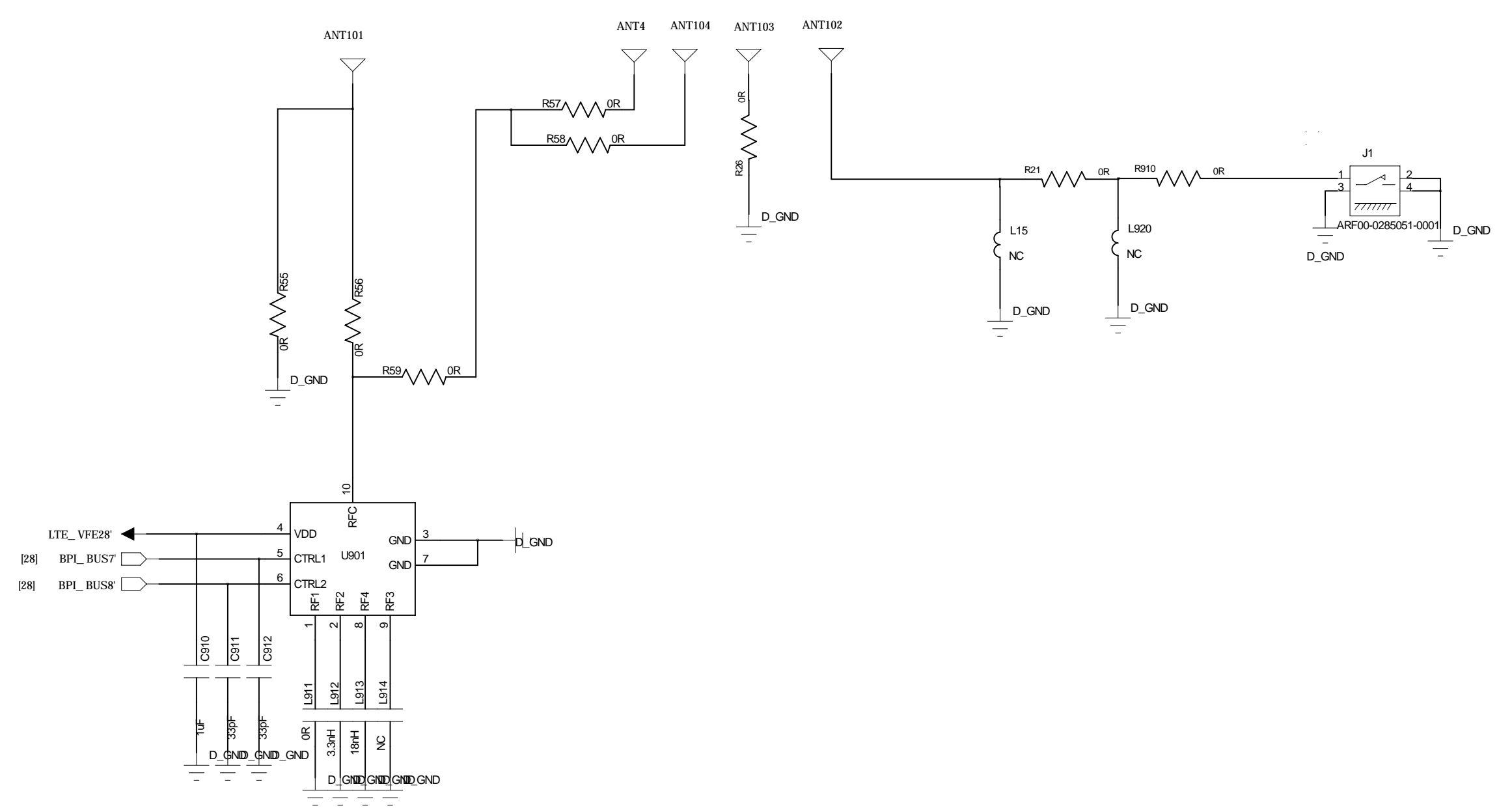
SPK



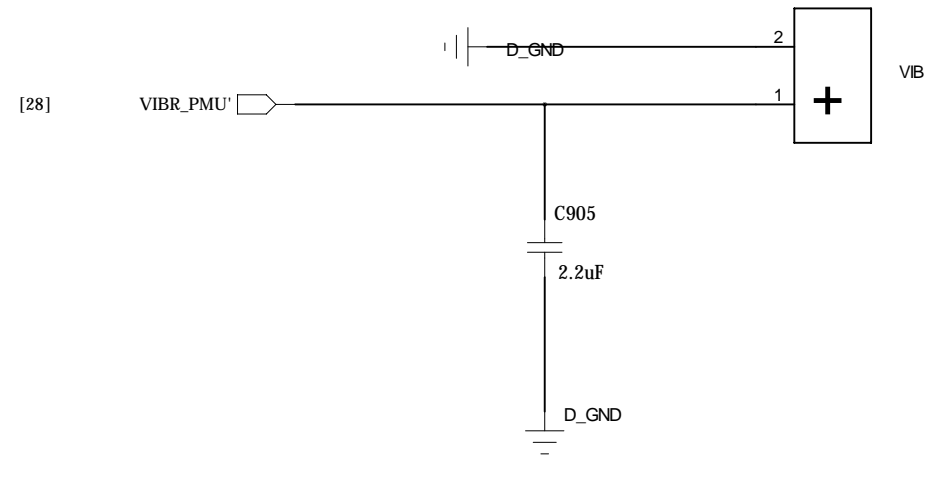
OVP



ANT



VIB



DRAWN		DATE		COMPANY	
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