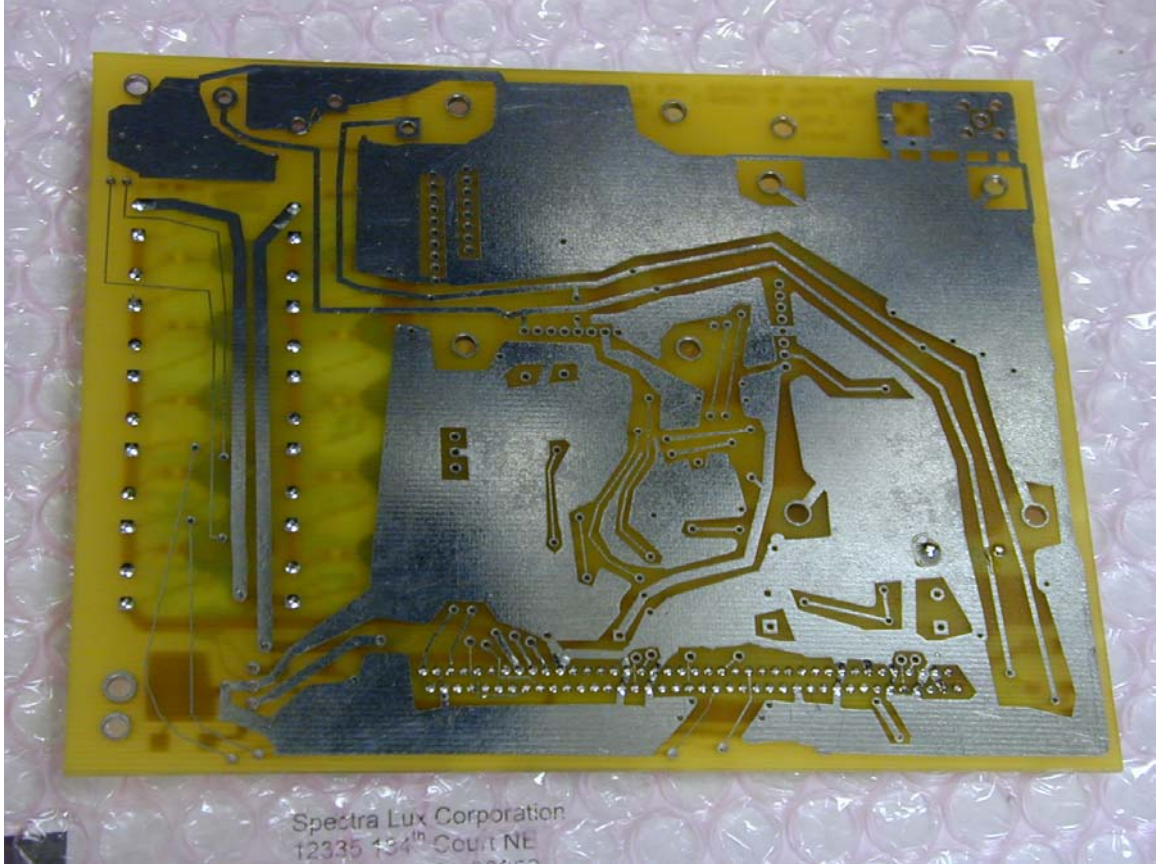
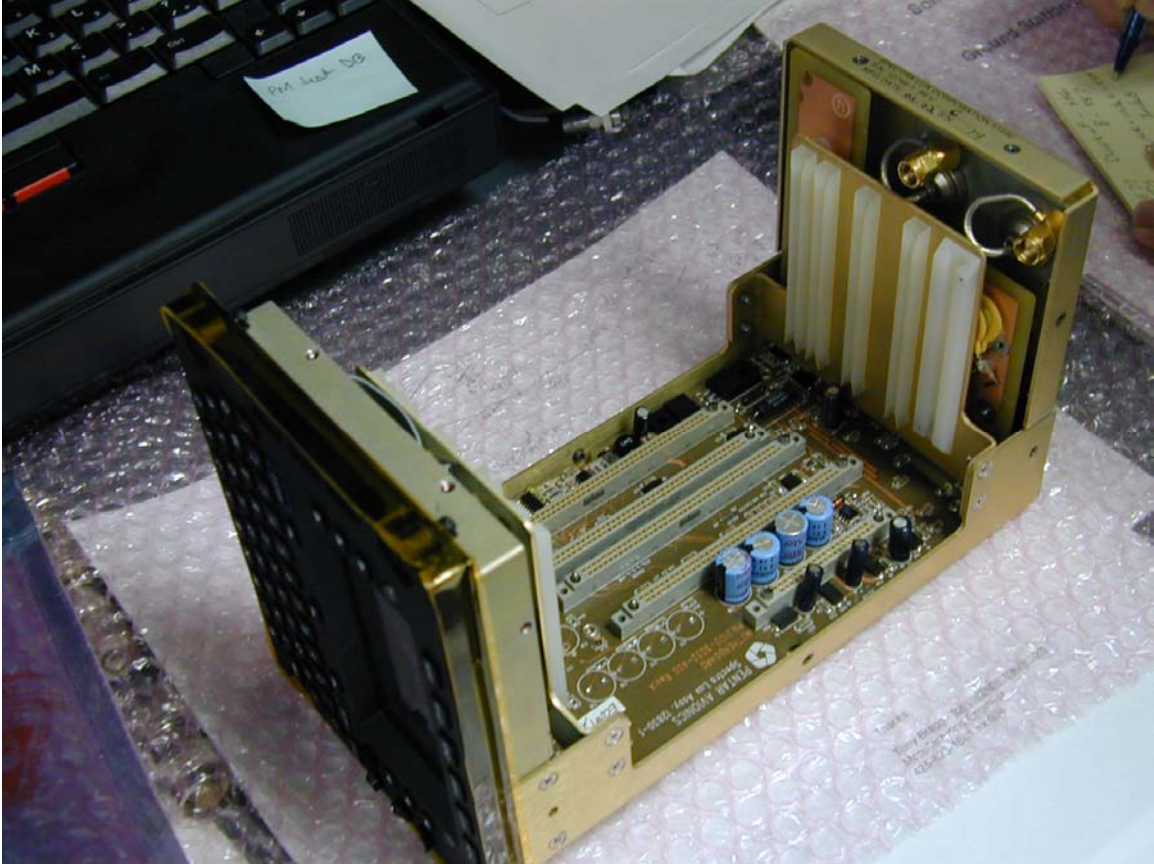


A-IO Front



A-IO Back





DLink+ Inside Back



DU-PS Front



DU-PS Back