

Digitech International Transmitter Circuit Description 1420-001

Referring to the schematic:

In the quiescent condition switches S1, S2 and S3 are biased in various state combinations by an externally positioned magnet(s). When the state of any switch changes, pin 8 of micro-controller, U1 pulls low turning on Q1 which enables the TX6000 hybrid, U2. Pulse code data is then issued from U1, pin 6, level shifted through divider R14, R16, R17 and applied to the Data input (pin 8) of U2. The rf signal appearing at pin 20 of the TX6000 is matched and coupled to the helical antenna through inductive network, L1, L3.