

Figure 1 - Bottom of Device

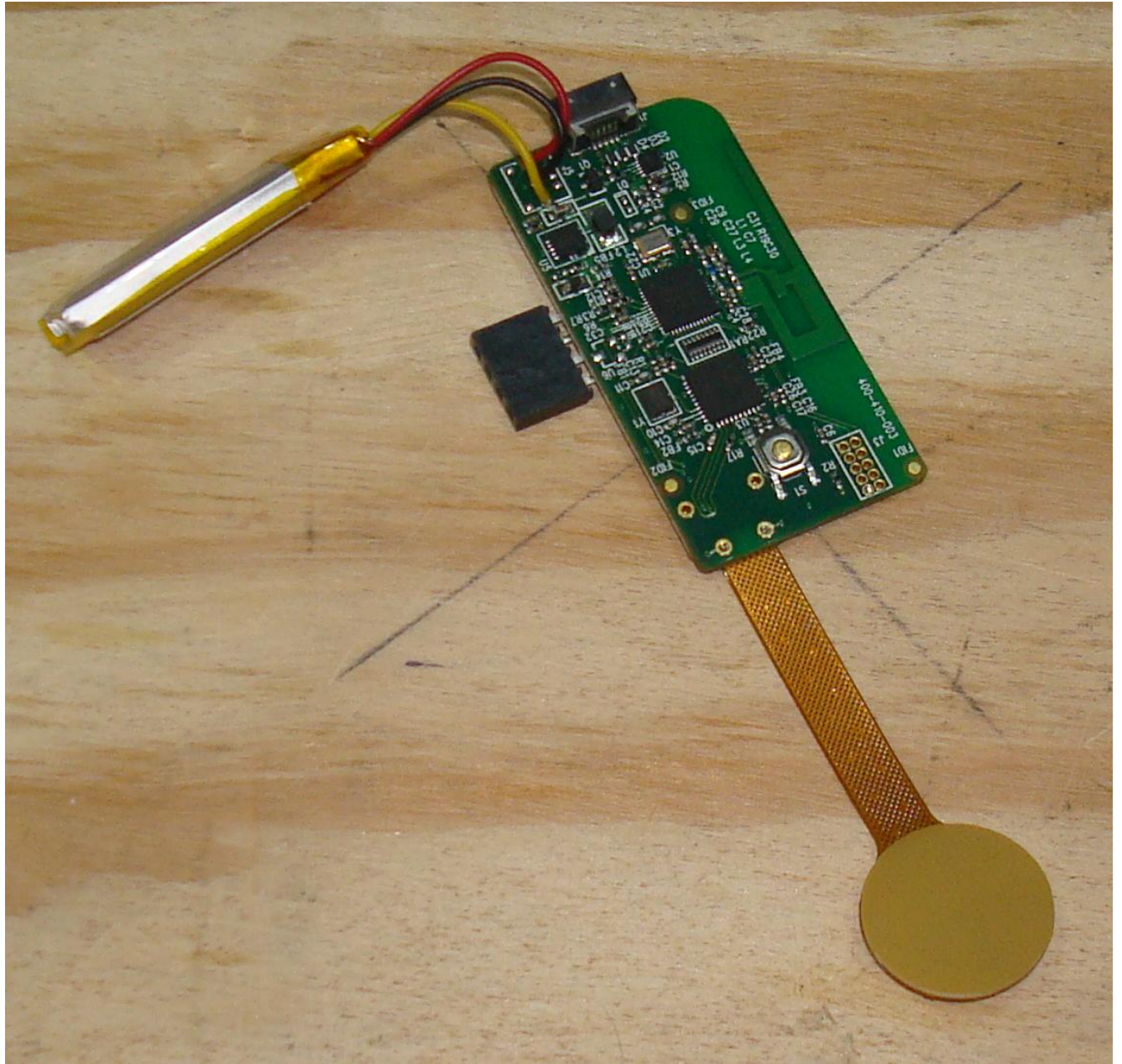


Figure 2 - Top of Device

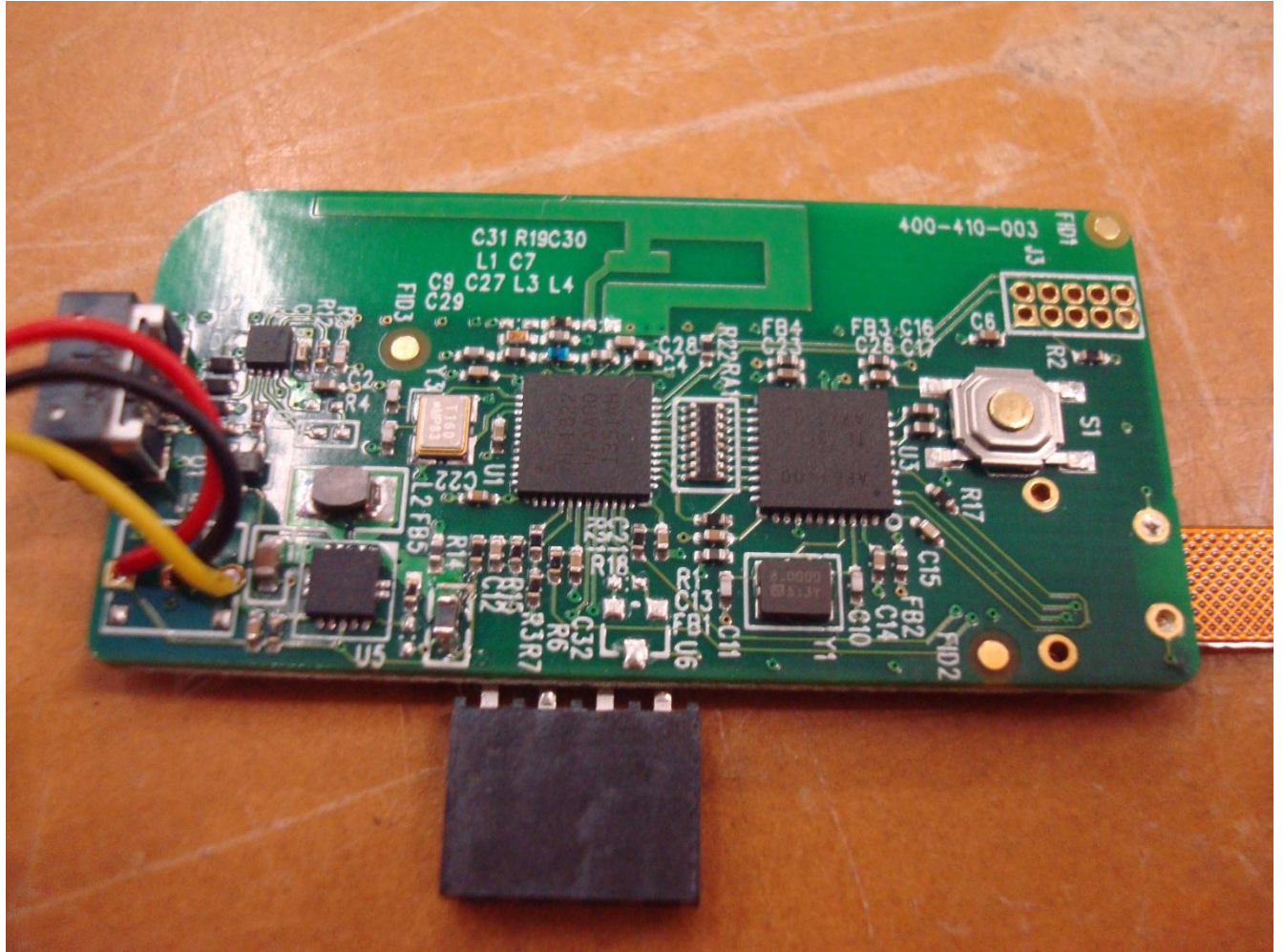


Figure 3 - PCB Detail