

EXHIBIT 8 C

FCC ID: QKGRDX-U3

HARMONIC SUPPRESSION CIRCUIT

Circuit Description:

Attenuation for harmonics is provided by a low pass filter in the RF Power Amplifier and the Antenna Switch. The low pass filter in the RF PA is composed of L1, L2, C1 and C2. The low pass filter in the Antenna Switch is composed of L3, L4, C3 and C4. There also exist a zero at $2f_0$ (L3,C3) in the antenna switch.

Circuit diagram:

