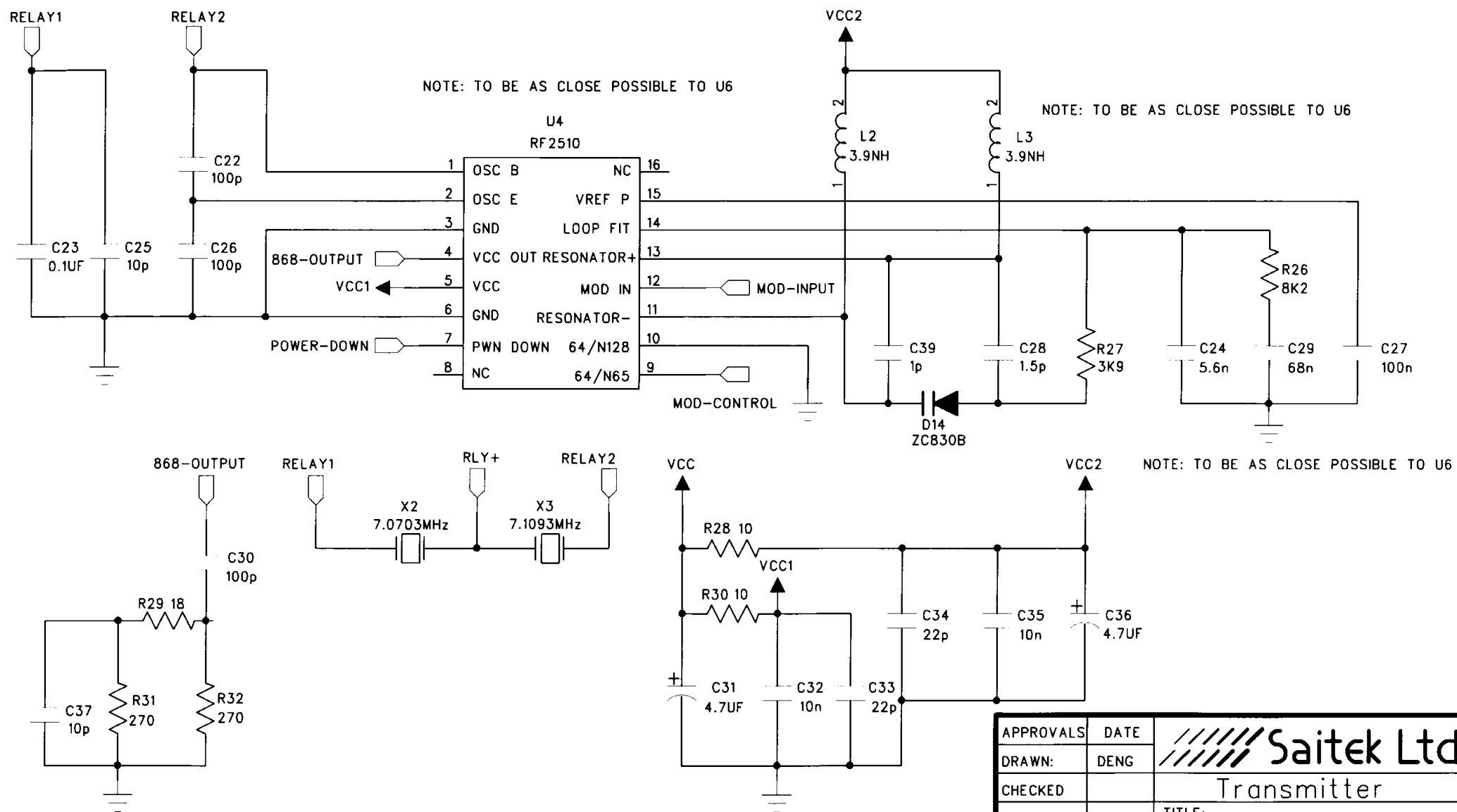


NOTE: ALL LAYED OUT TRANSMITTER SECTION SHOULD BE LAYED OUT
ON DOUBLE SIDED PCB WITH GROUND PLANE ON BOTH SIDES

NOTE: TO BE LOCATED
ON ANALOGUE CIRCUIT

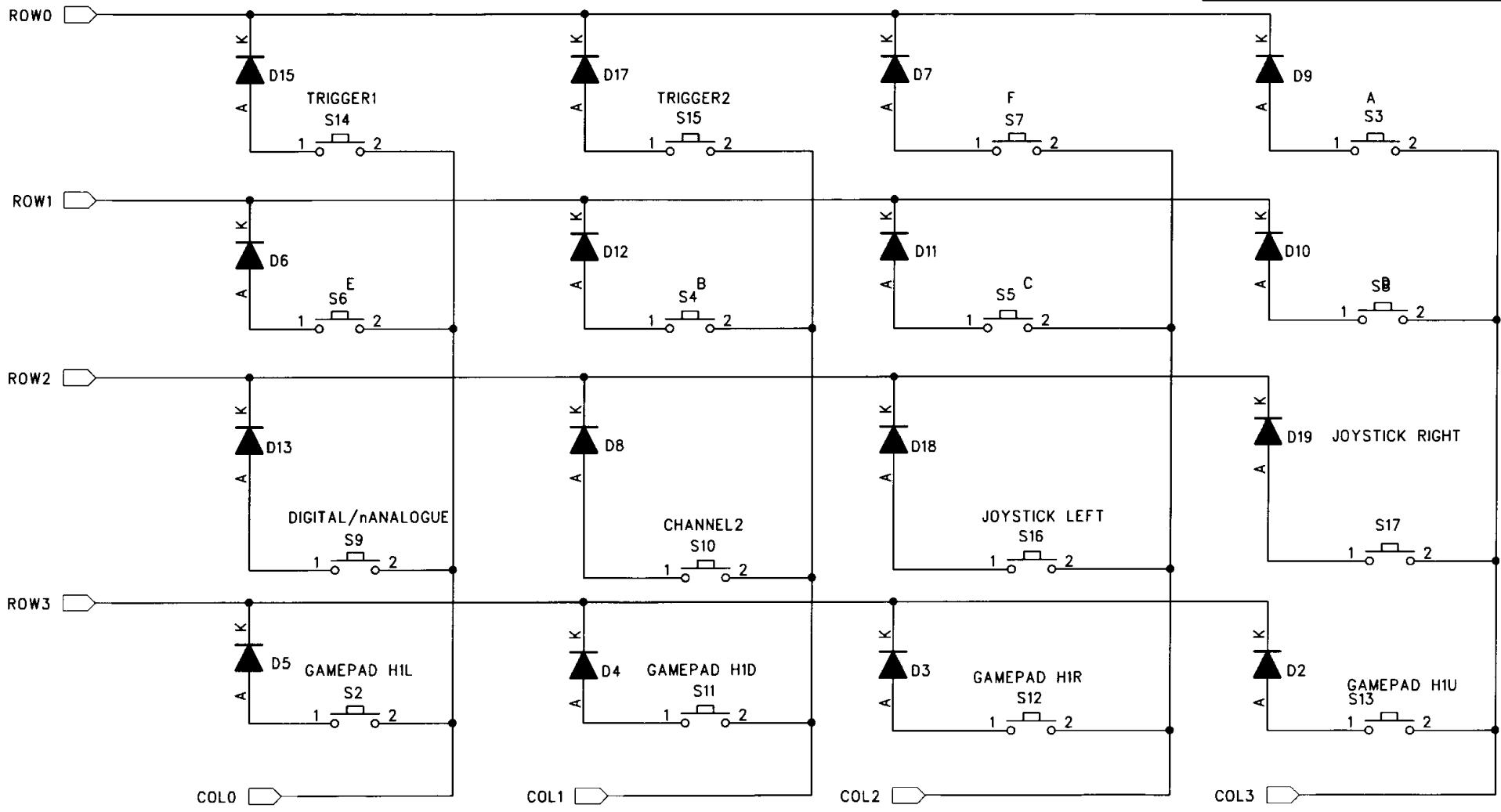
REVISION

DATE	DESCRIPTION	ECN NO.	REV.NO.

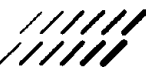


APPROVALS	DATE	 Saitek Ltd.
DRAWN:	DENG	
CHECKED		Transmitter
B.D.D.		TITLE: PP9W2
Sheet 1 of 7		REV.

SWITCH MATRIX

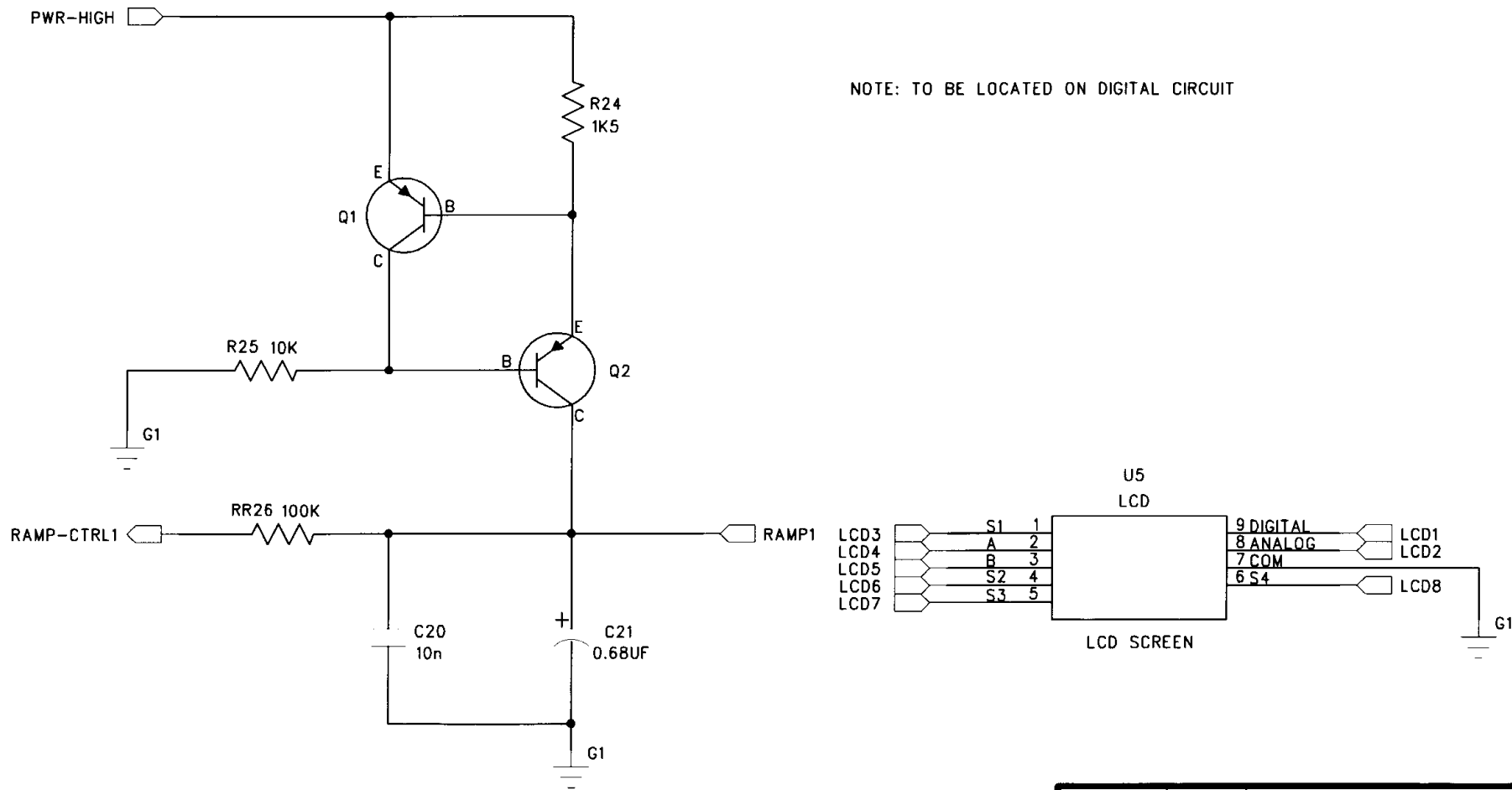


REVISION			
DATE	DESCRIPTION	ECN NO.	REV.NO.

APPROVALS	DATE	 Saitek Ltd.	
DRAWN:	DENG		
CHECKED		Switch Matrix	
B.D.D.		TITLE: PP9W2	
		Sheet 2 of 7	REV.

REVISION			
DATE	DESCRIPTION	ECN NO.	REV.NO.

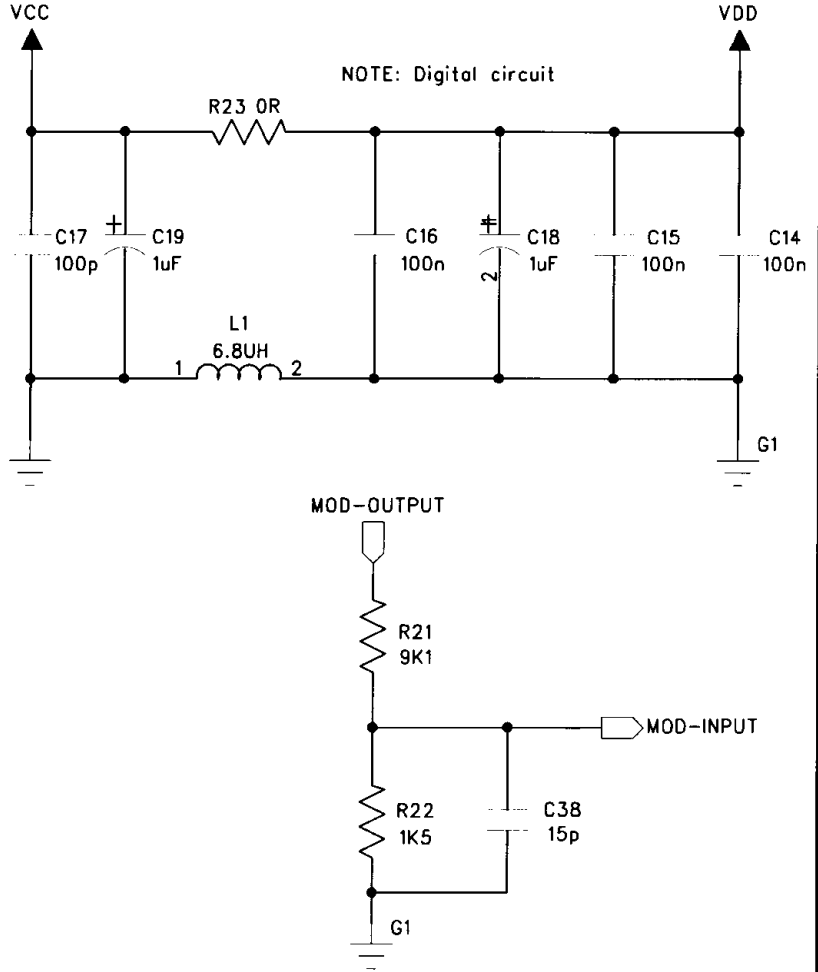
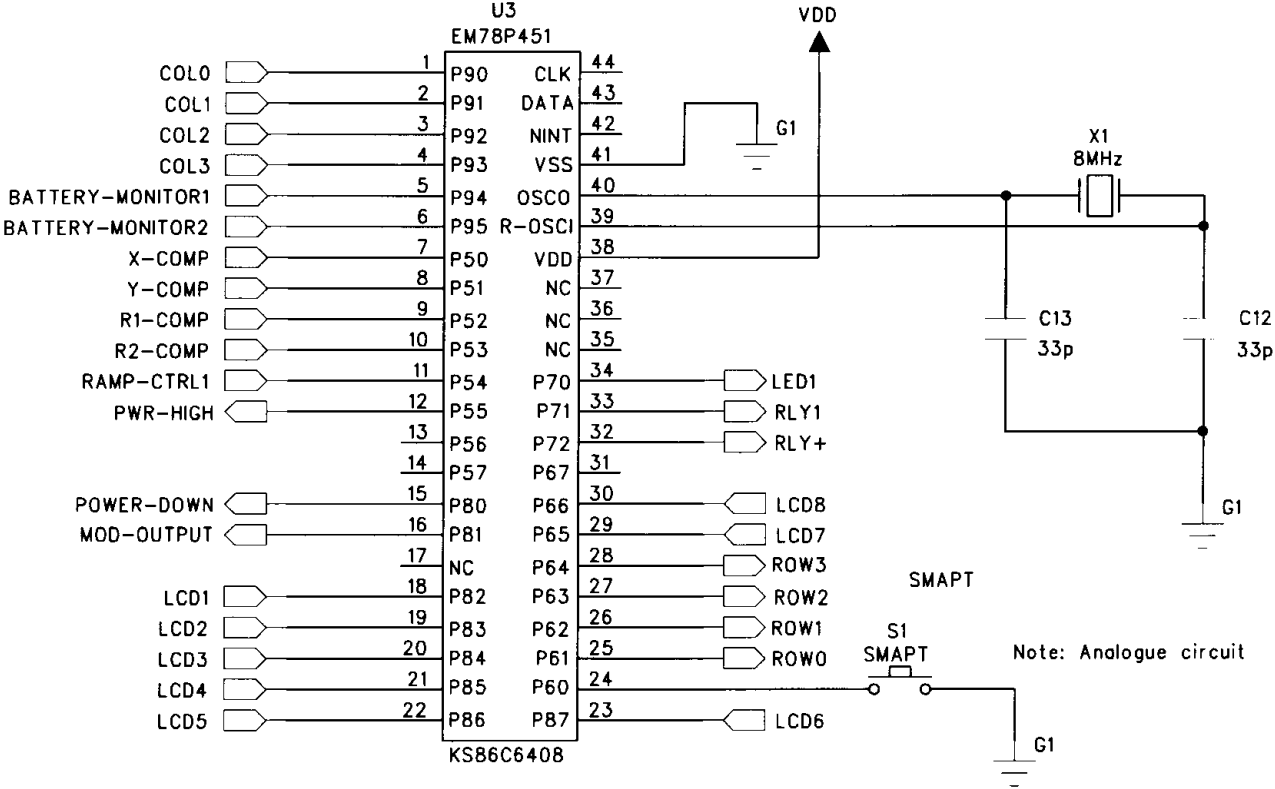
RAMP GENERATOR



APPROVALS	DATE	Saitek Ltd.	
DRAWN:	DENG		
CHECKED		Ramp Generator	
B.D.D.		TITLE: PP9W2	
		Sheet 3 of 7	REV.

REVISION			
DATE	DESCRIPTION	ECN NO.	REV.NO.

NOTE: TO BE LOCATED AWAY FROM THE
TRANSMITTER CIRCUIT
NOTE: TO BE LOCATED ON DIGITAL CIRCUIT

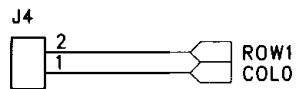


APPROVALS	DATE	Saitek Ltd.	
DRAWN:	DENG		
CHECKED		Micro Controller	
B.D.D.		TITLE: PP9W2	
		Sheet 4 of 7	REV.

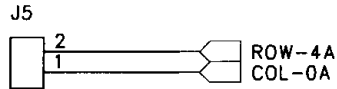
REVISION			
DATE	DESCRIPTION	ECN NO.	REV.NO.

COMPARATORS

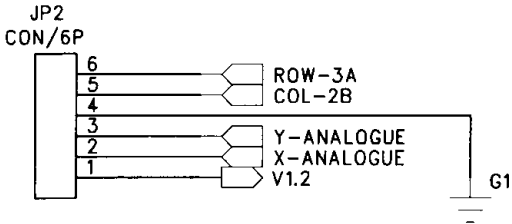
TO SWITHES ON TRIG RHT PCB



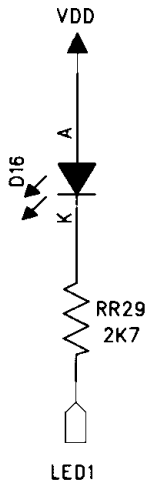
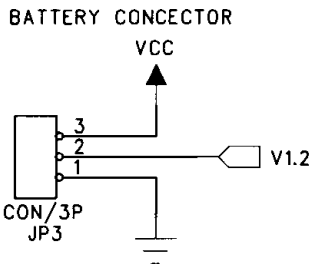
TO SWITHES ON TRIG LEFT PCB



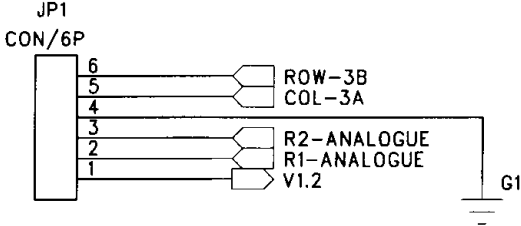
TO LEFT JOY PCB



NOTE: TO BE LOCATED ON DIGITAL CIRCUIT



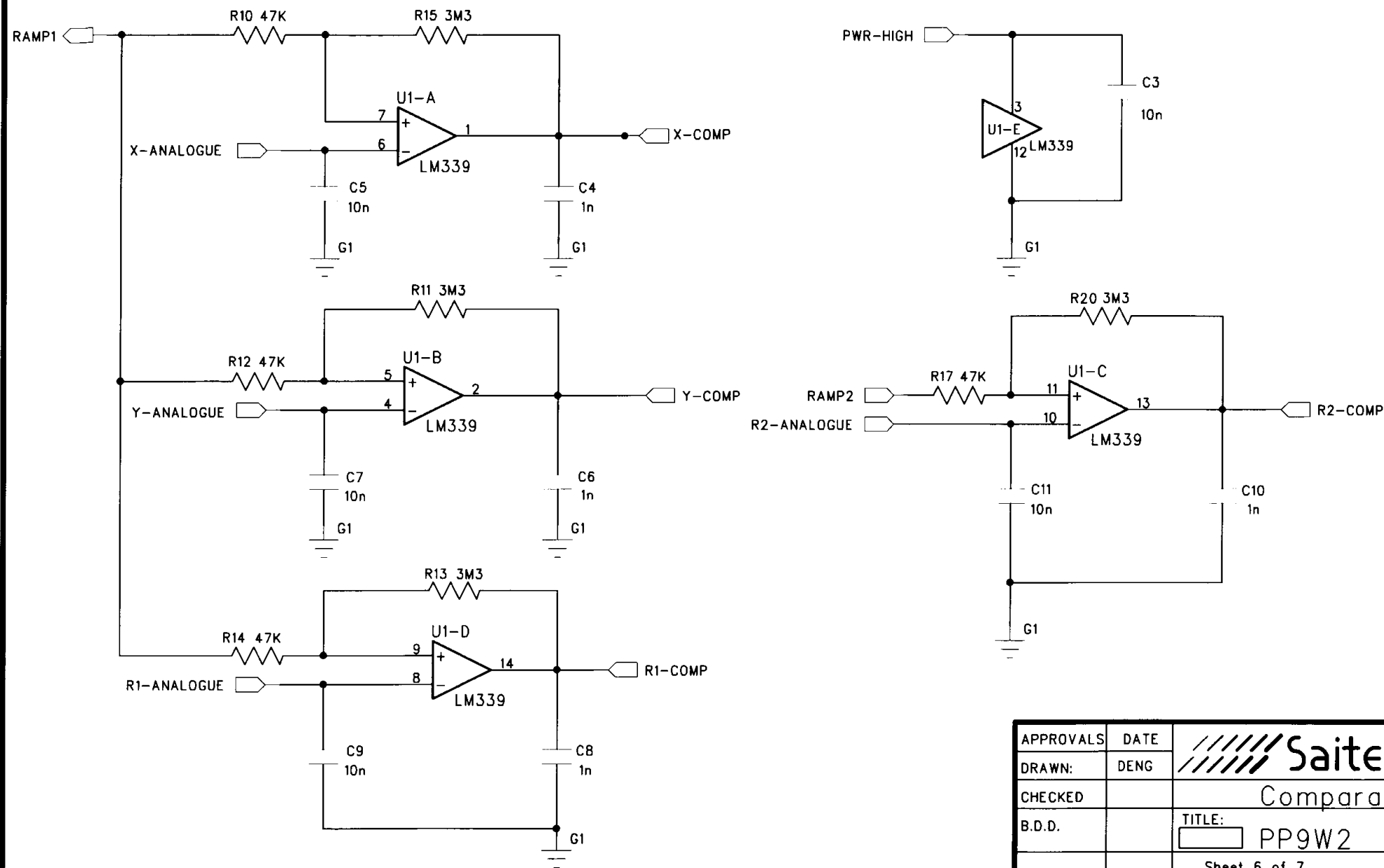
TO RIGHT JOY PCB



APPROVALS	DATE	Saitek Ltd.	
DRAWN:	DENG		
CHECKED		Connectors	
B.D.D.		TITLE: PP9W2	
		Sheet 5 of 7	REV.

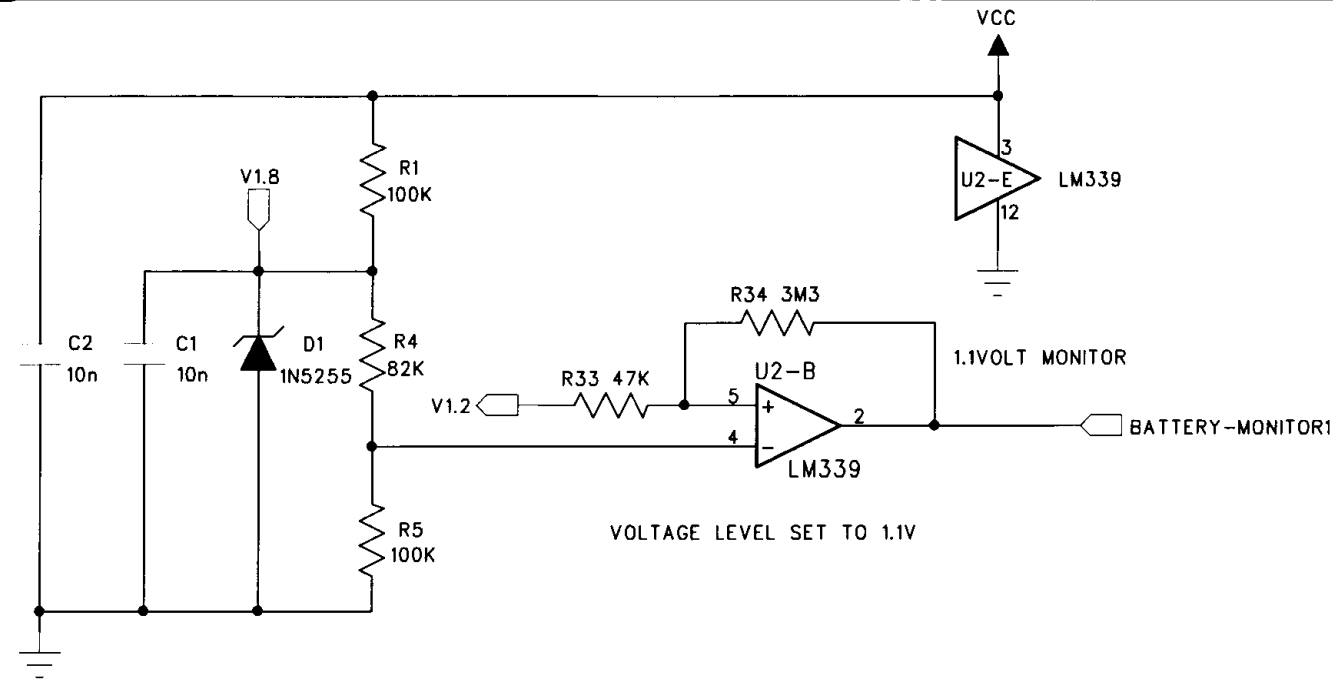
COMPARATORS

REVISION			
DATE	DESCRIPTION	ECN NO.	REV.NO.

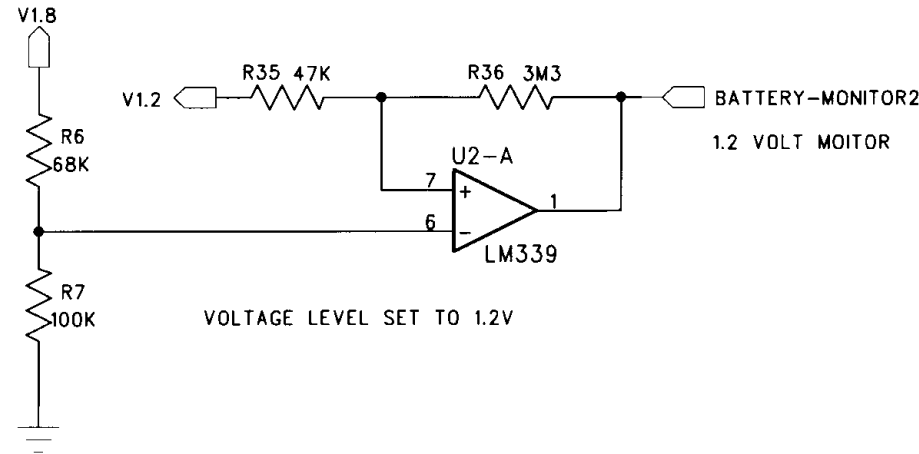


APPROVALS	DATE	 Saitek Ltd.	
DRAWN:	DENG		
CHECKED		Comparators	
B.D.D.		TITLE: PP9W2	
		Sheet 6 of 7	REV.

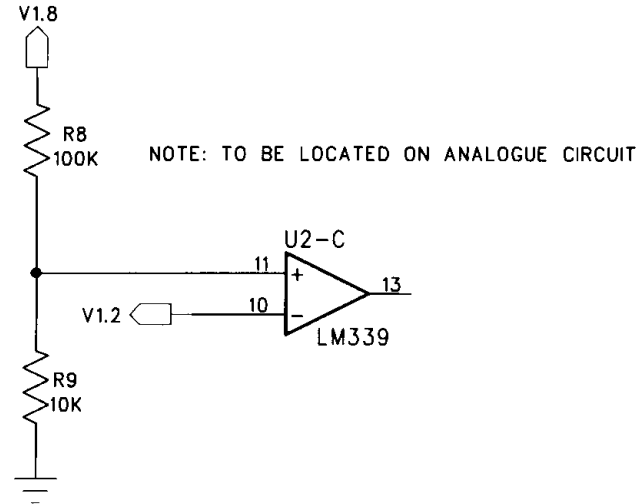
REVISION			
DATE	DESCRIPTION	ECN NO.	REV.NO.



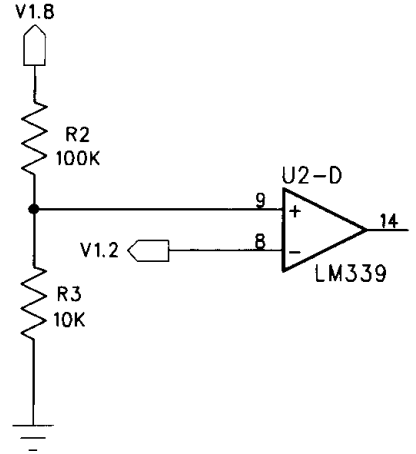
VOLTAGE LEVEL SET TO 1.1V



VOLTAGE LEVEL SET TO 1.2V



NOTE: TO BE LOCATED ON ANALOGUE CIRCUIT



APPROVALS	DATE	Saitek Ltd.	
DRAWN:	DENG		
CHECKED		Battery Monitor	
B.D.D.		TITLE: PP9W2	
		Sheet 7 of 7	REV.