



Appendix A: 20dB Emission Bandwidth (EBW)



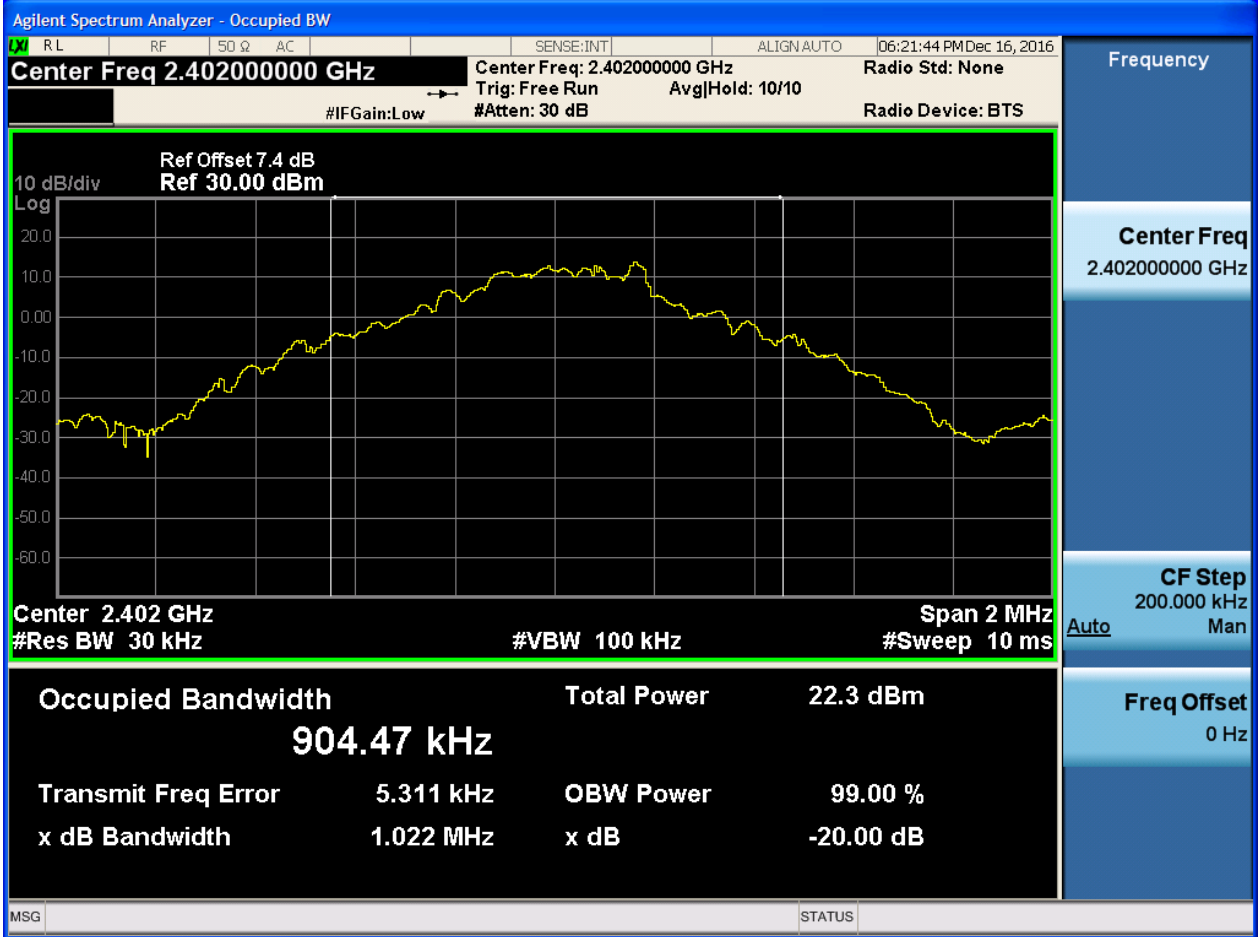
1 Result Table

EUT Conf.	EBW [MHz]	Verdict
TM1_DH5_Ch0	1.02	Pass
TM1_DH5_Ch39	1.01	Pass
TM1_DH5_Ch78	1.02	Pass
TM2_2DH5_Ch0	1.35	Pass
TM2_2DH5_Ch39	1.35	Pass
TM2_2DH5_Ch78	1.35	Pass
TM3_3DH5_Ch0	1.35	Pass
TM3_3DH5_Ch39	1.35	Pass
TM3_3DH5_Ch78	1.35	Pass



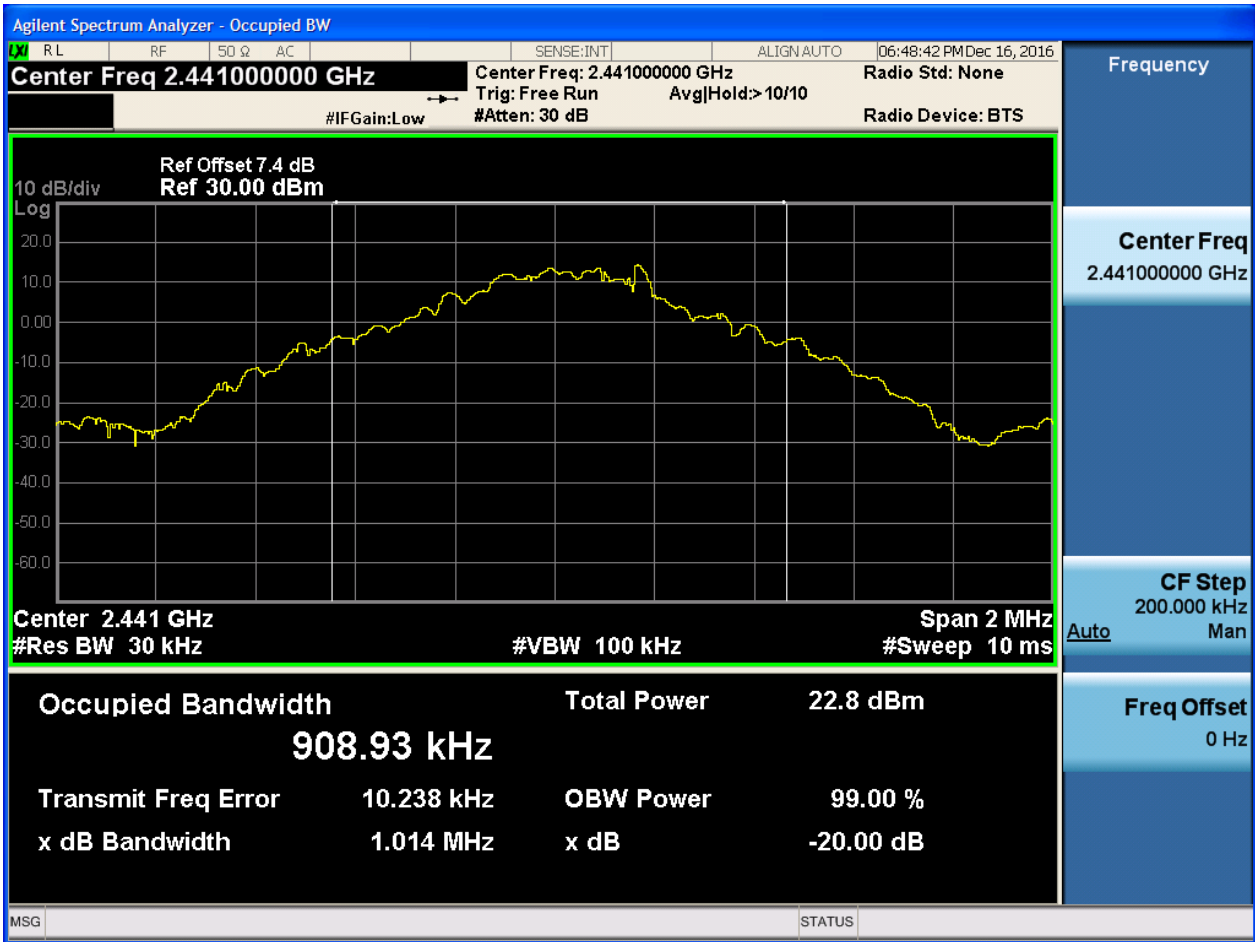
2 Test Plot

2.1 TM1_DH5_Ch0



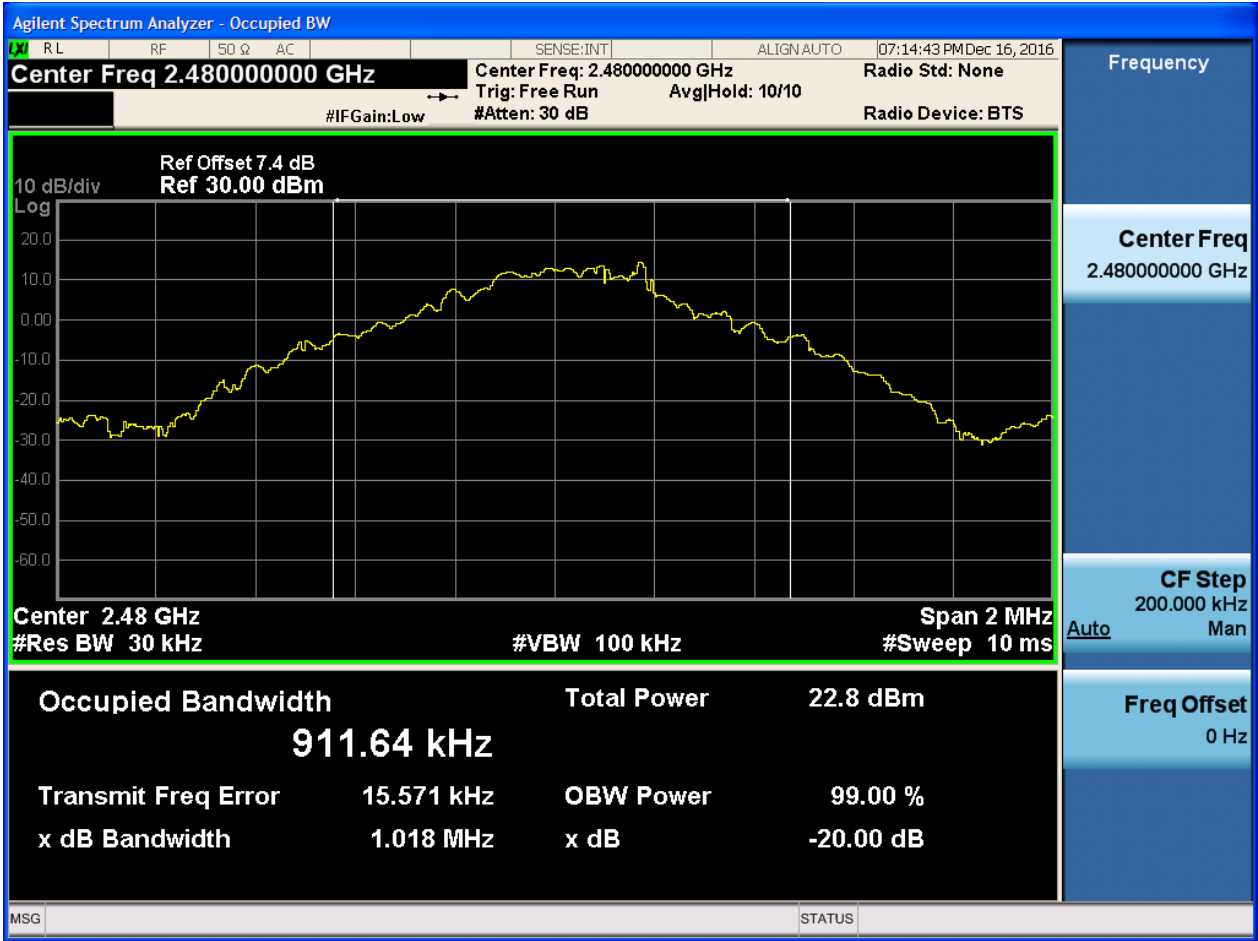


2.2 TM1_DH5_Ch39



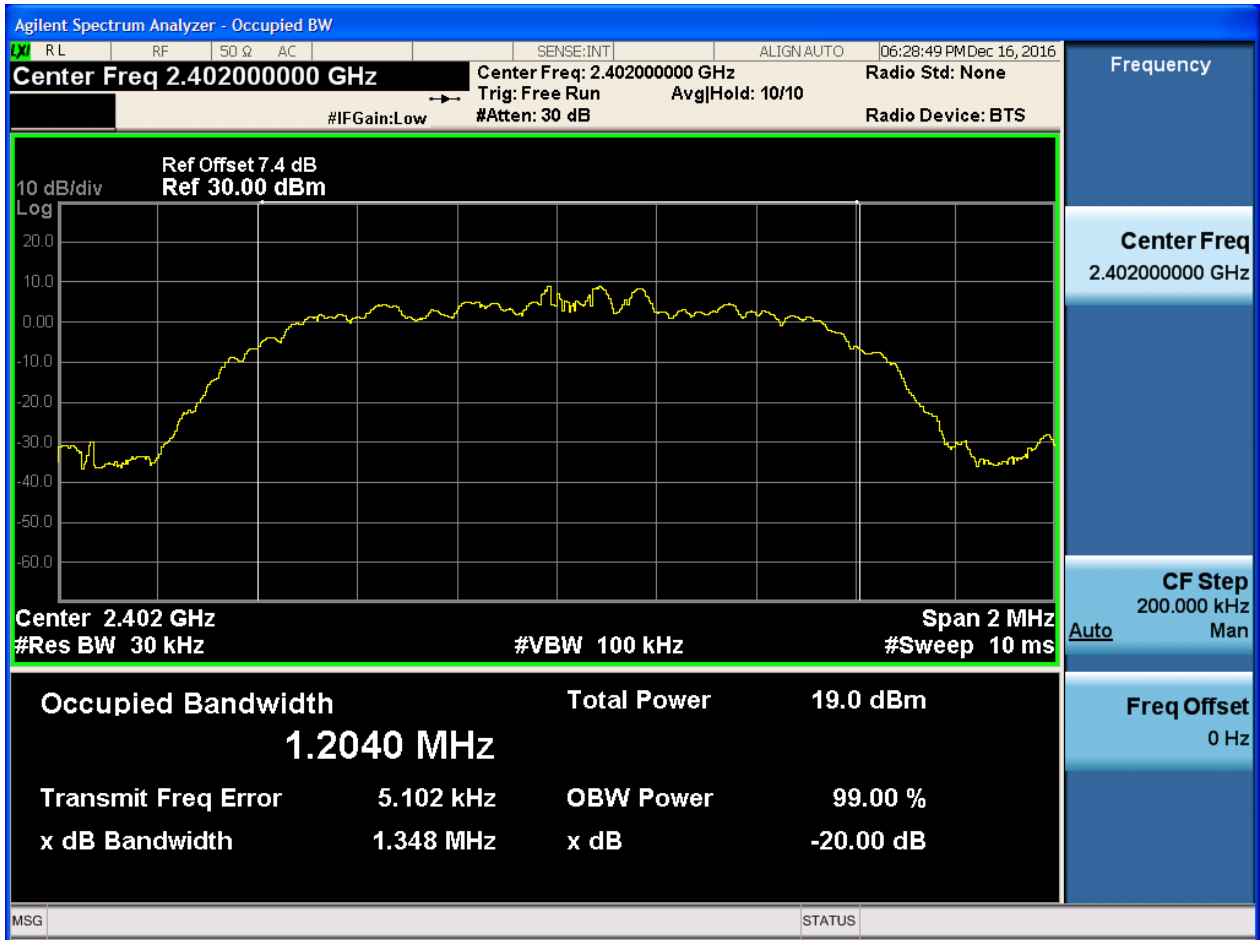


2.3 TM1_DH5_Ch78



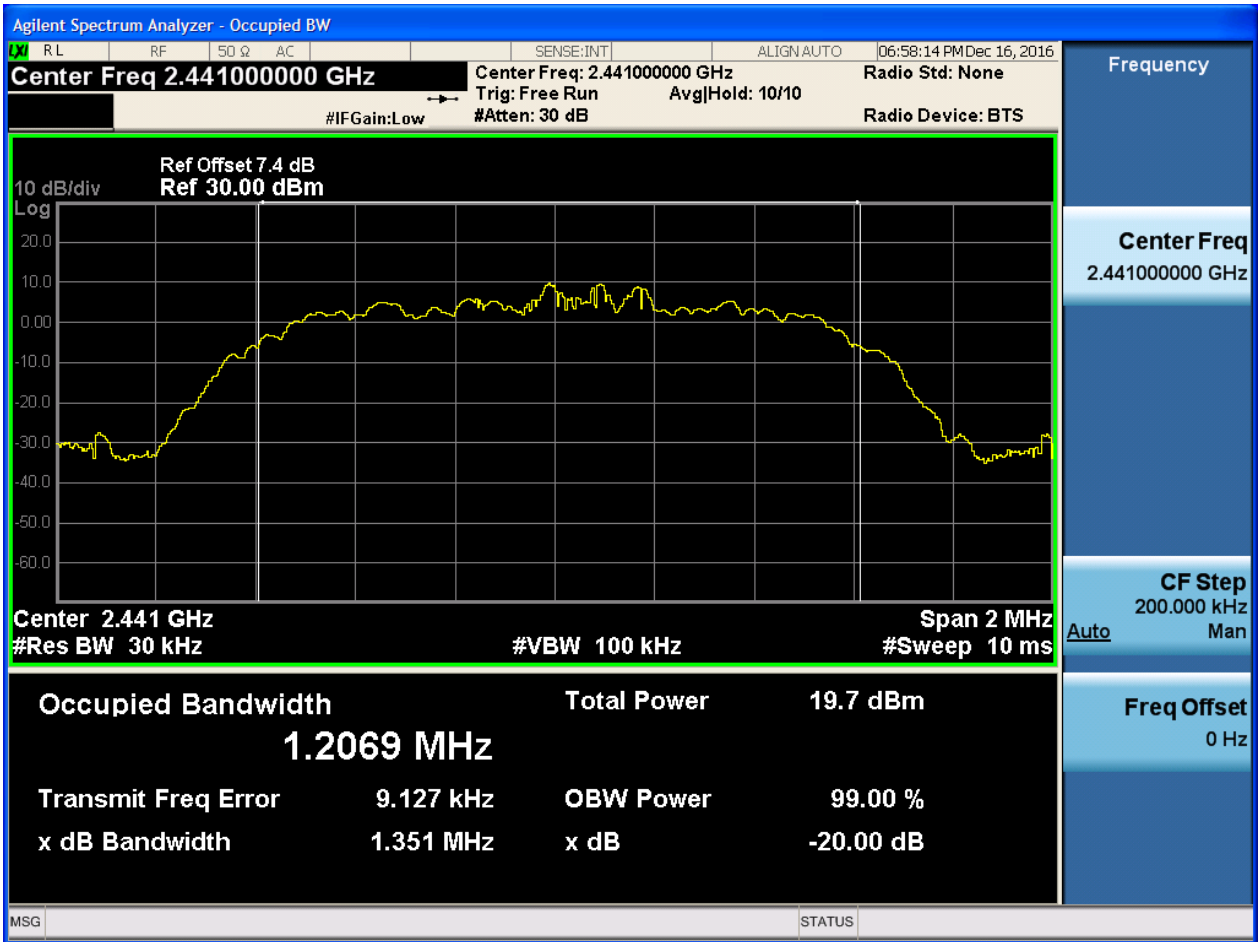


2.4 TM2_2DH5_Ch0



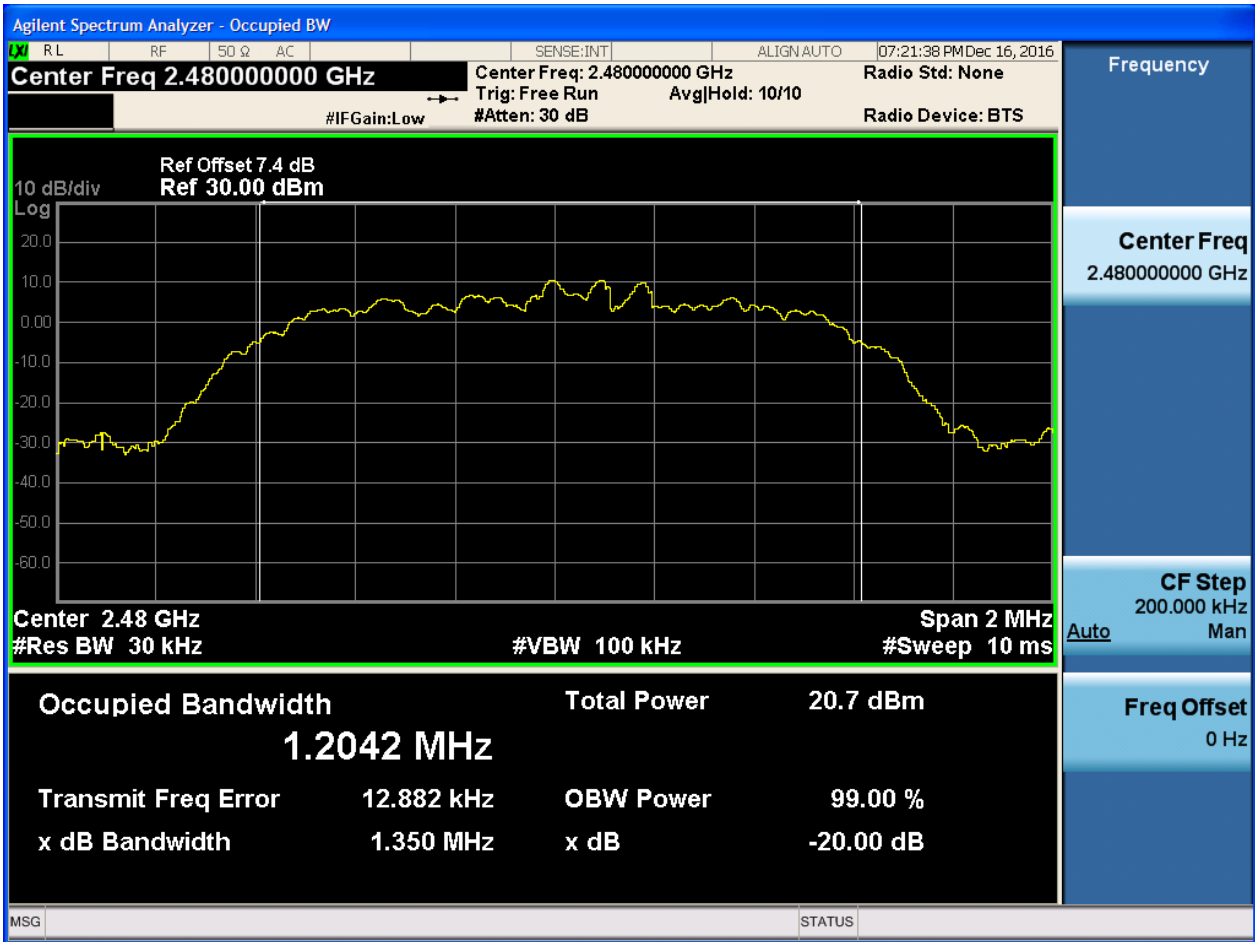


2.5 TM2_2DH5_Ch39



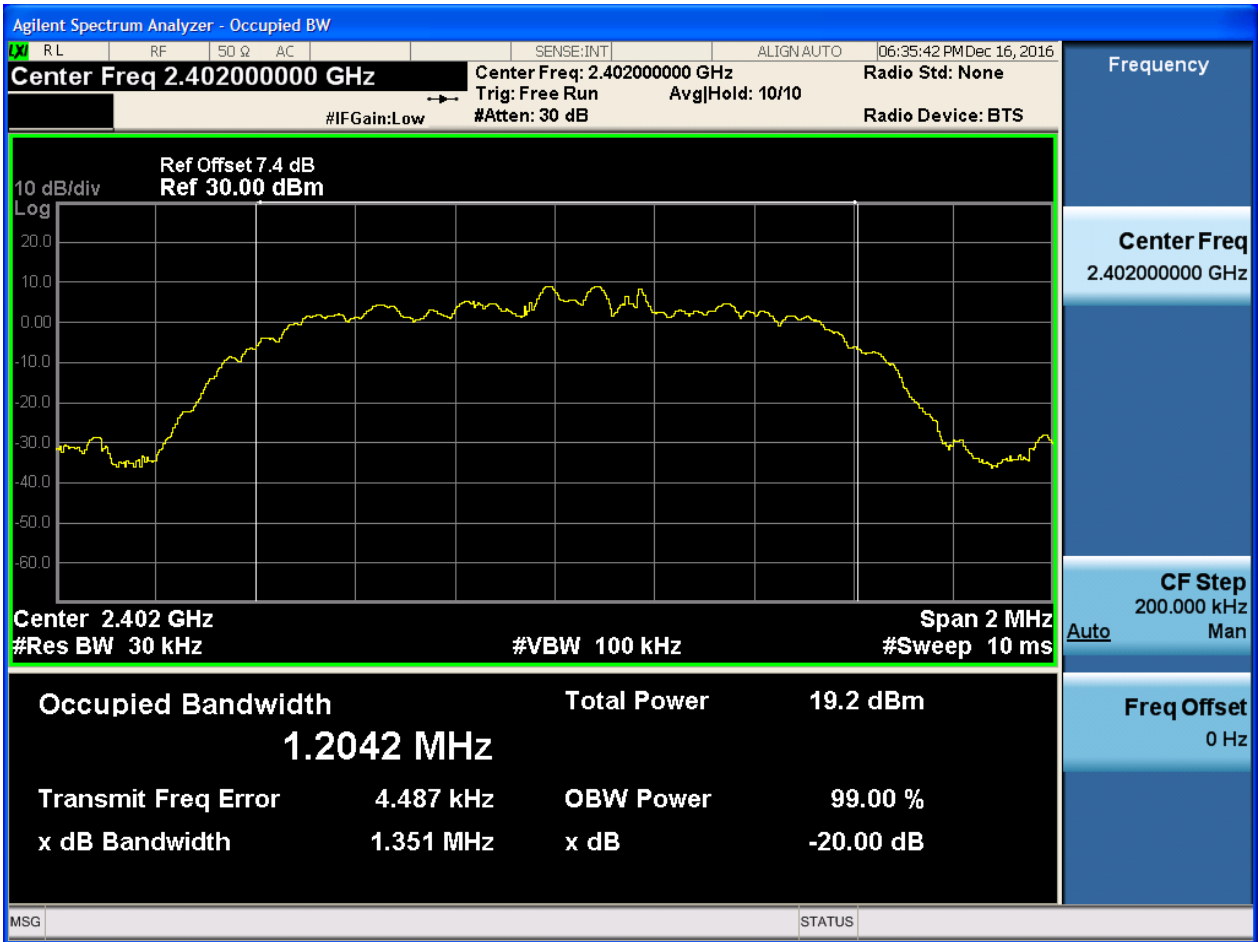


2.6 TM2_2DH5_Ch78



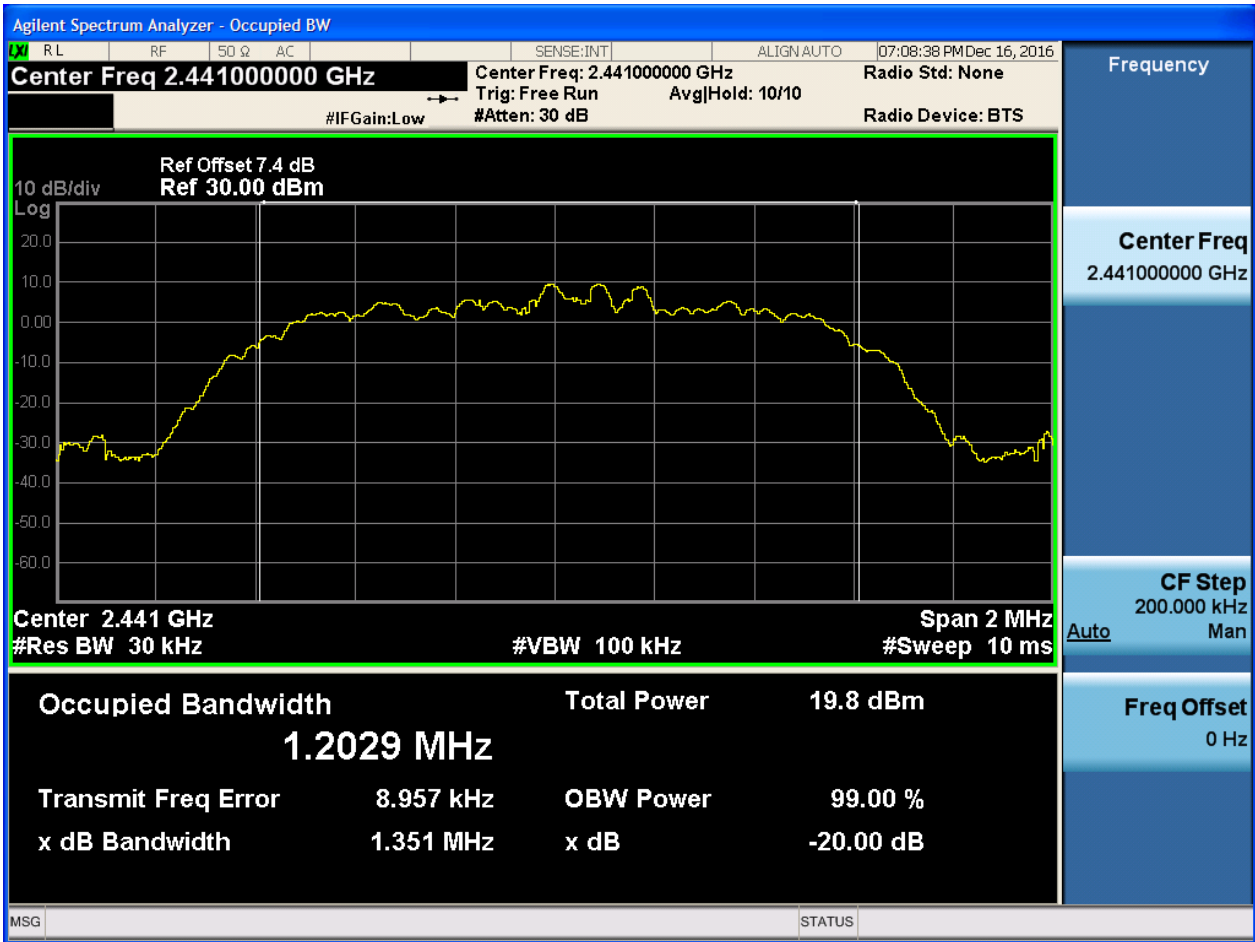


2.7 TM3_3DH5_Ch0



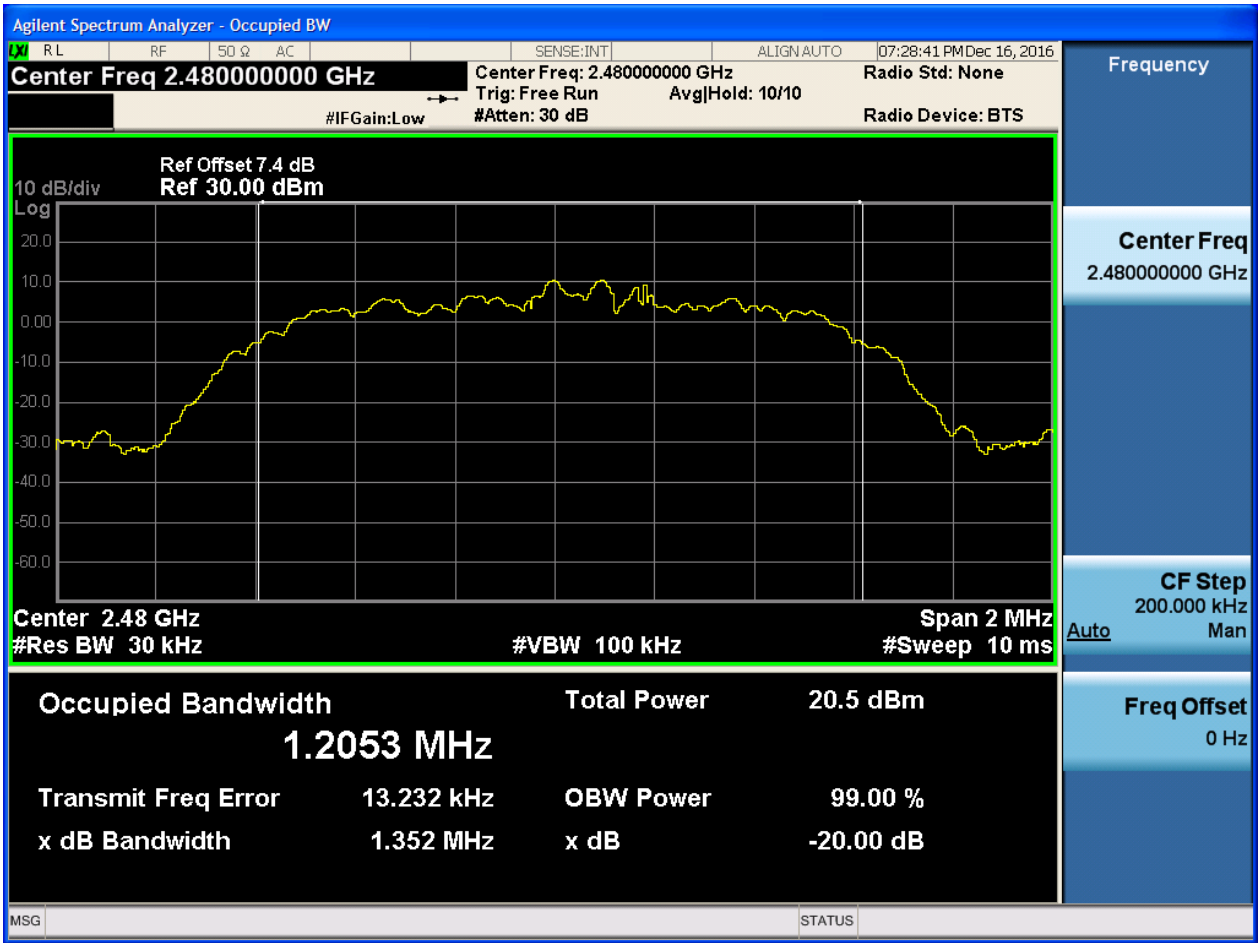


2.8 TM3_3DH5_Ch39





2.9 TM3_3DH5_Ch78





Appendix B: Carrier Frequency Separation



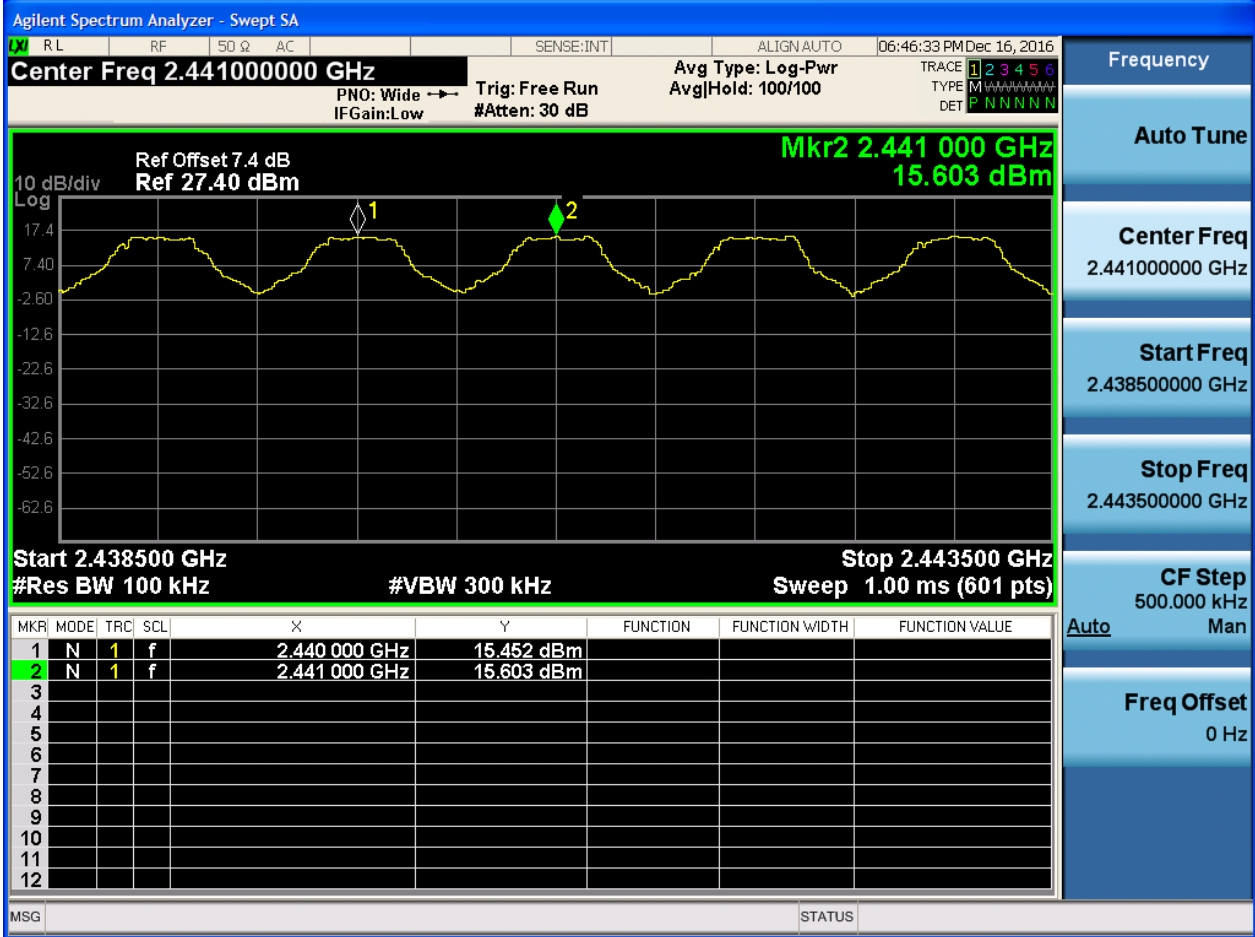
1 Result Table

EUT Conf.	Carrier Frequency Separation [MHz]	Verdict
TM1_DH5_Hop	1	Pass
TM2_2DH5_Hop	1.1	Pass
TM3_3DH5_Hop	0.95	Pass



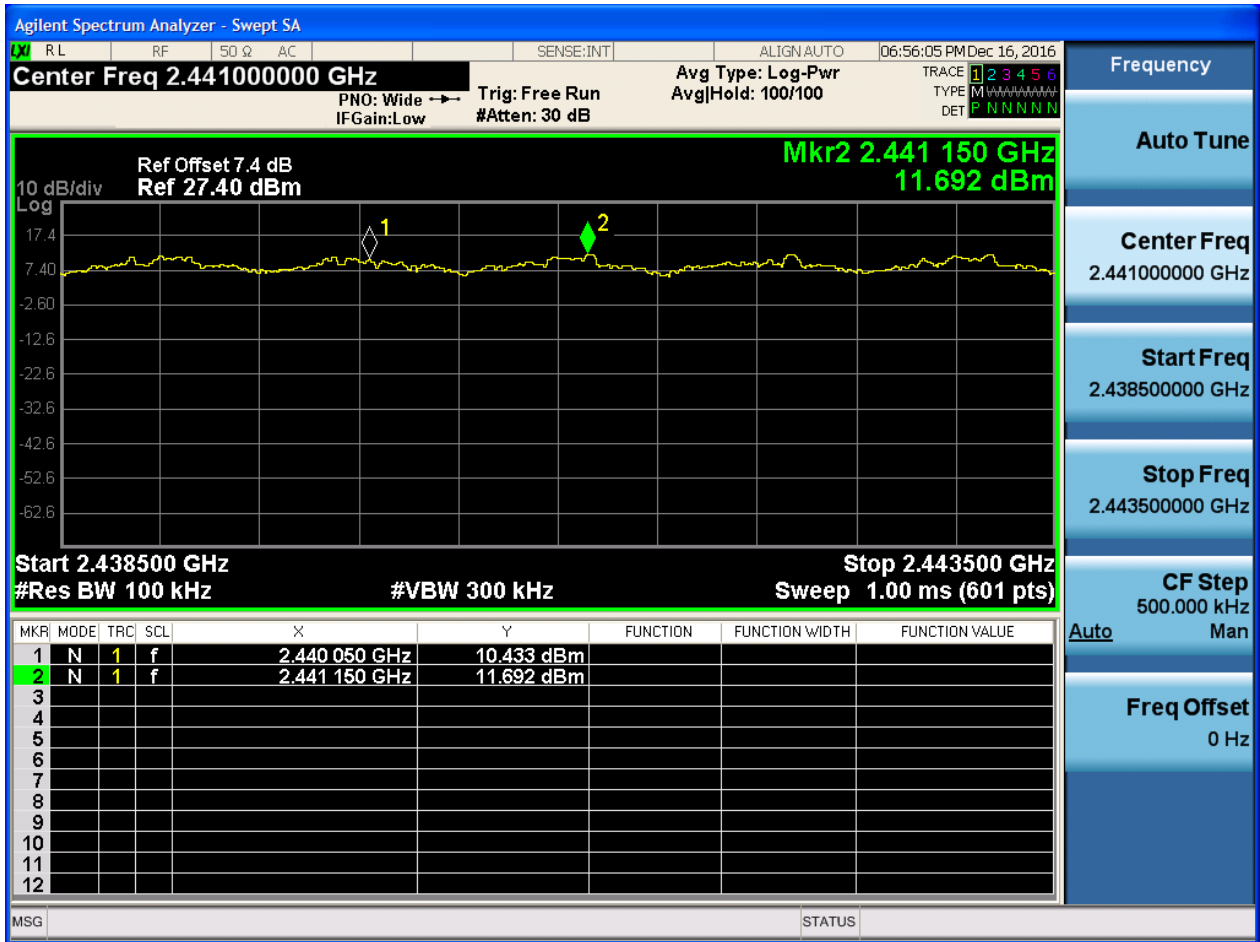
2 Test Plot

2.1 TM1_DH5_Hop



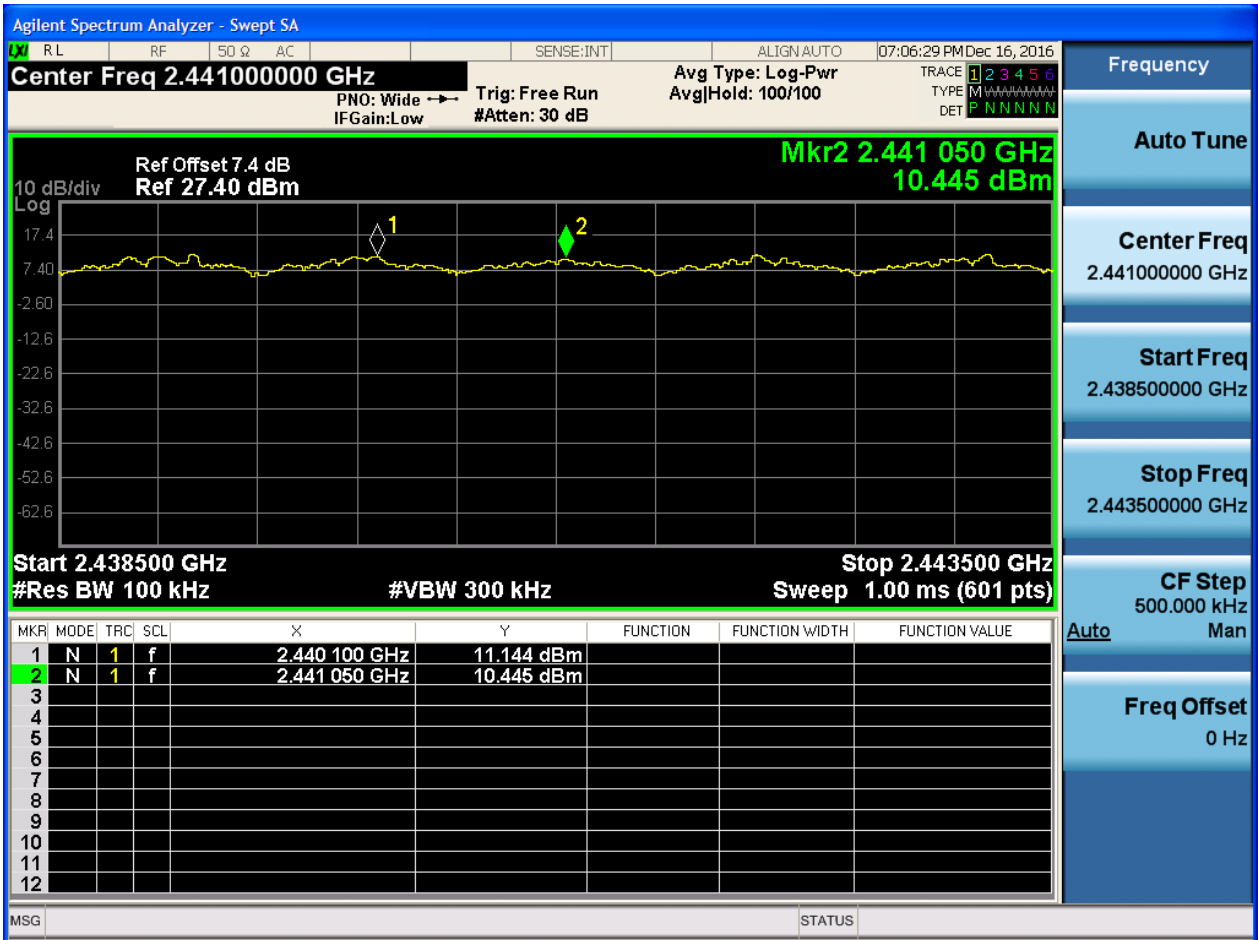


2.2 TM2_2DH5_Hop





2.3 TM3_3DH5_Hop





Appendix C: Number of Hopping Channel



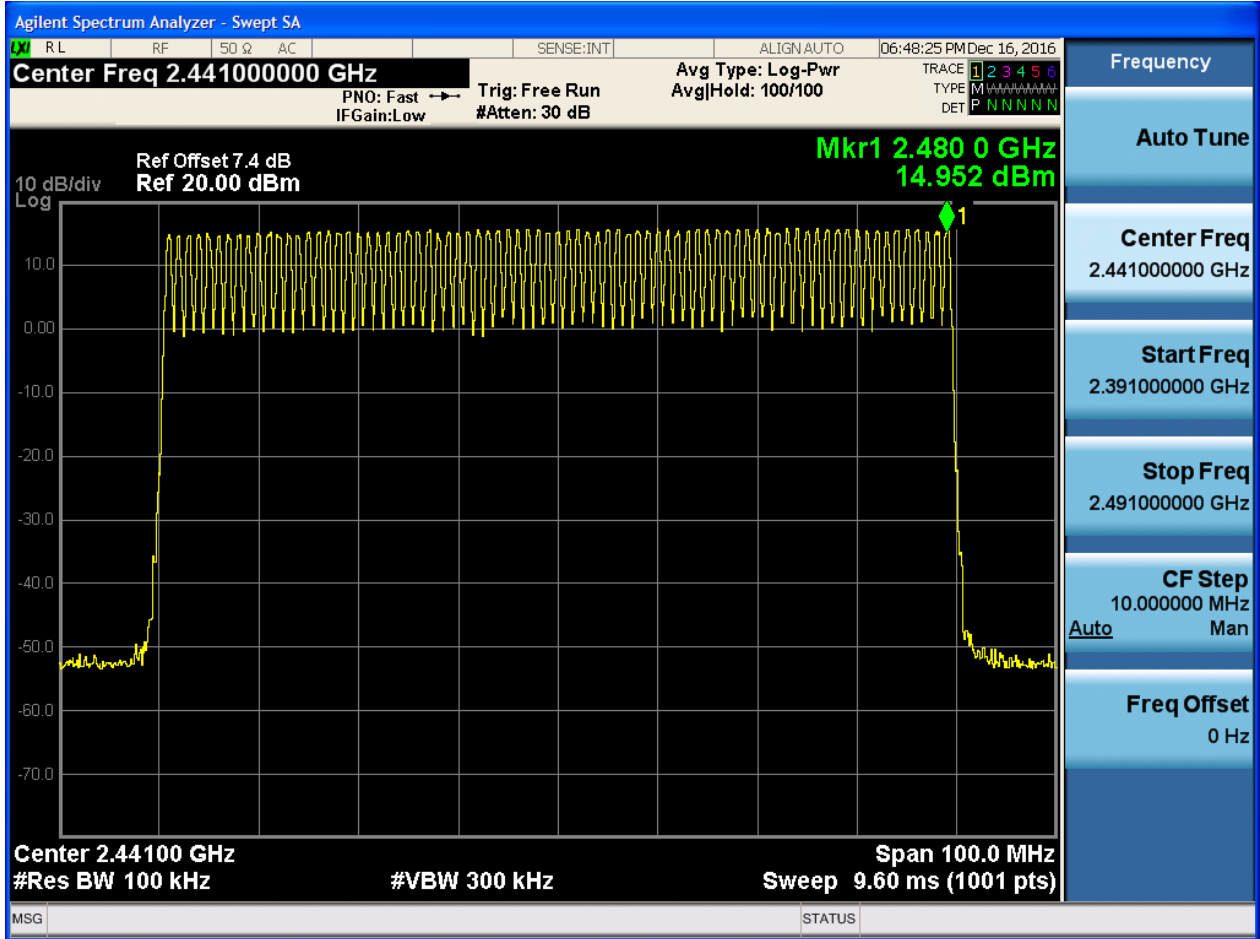
1 Result Table

EUT Conf.	Number of Hopping Channel	Verdict
TM1_DH5_Hop	79	Pass
TM2_2DH5_Hop	79	Pass
TM3_3DH5_Hop	79	Pass



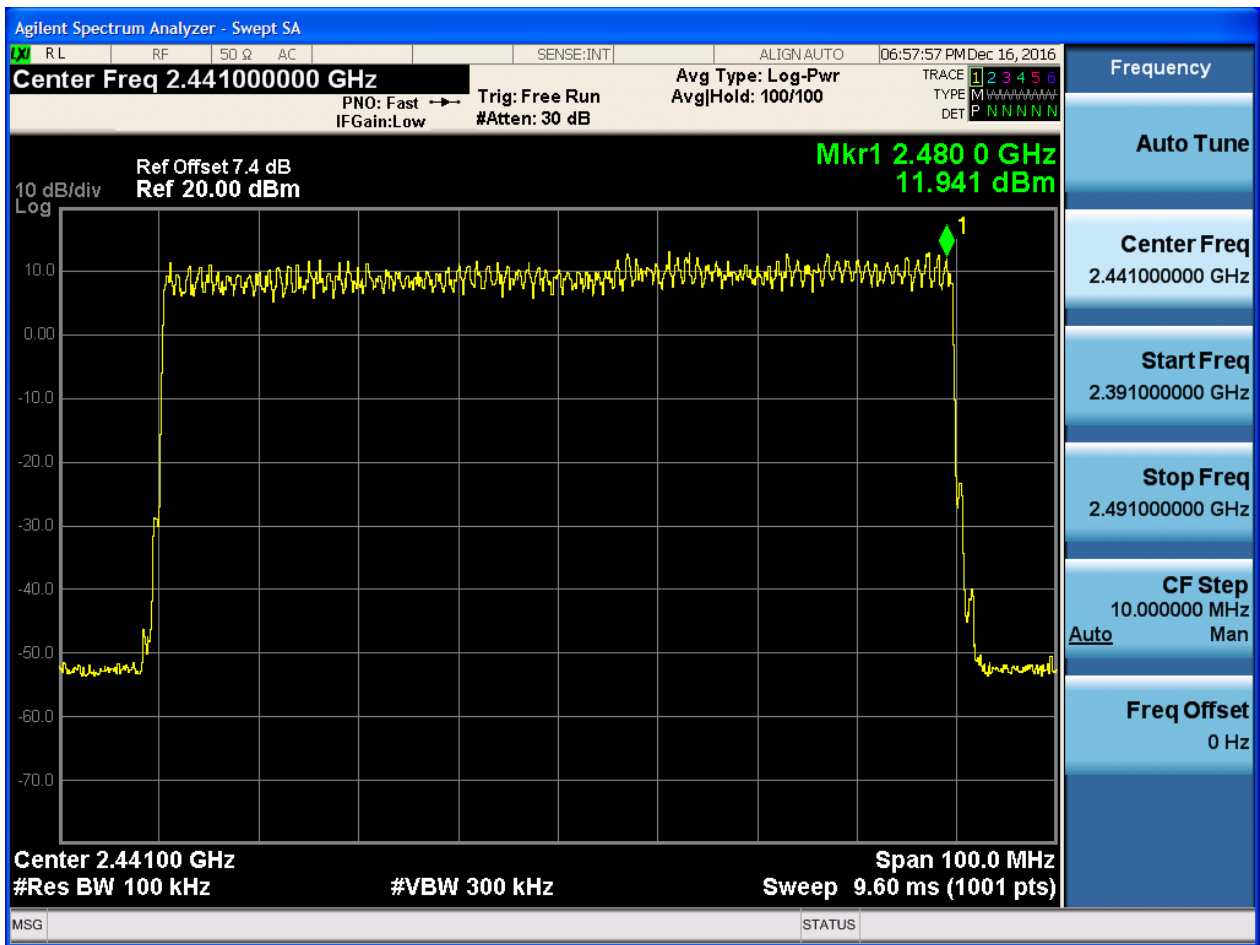
2 Test Plot

2.1 TM1_DH5_Hop



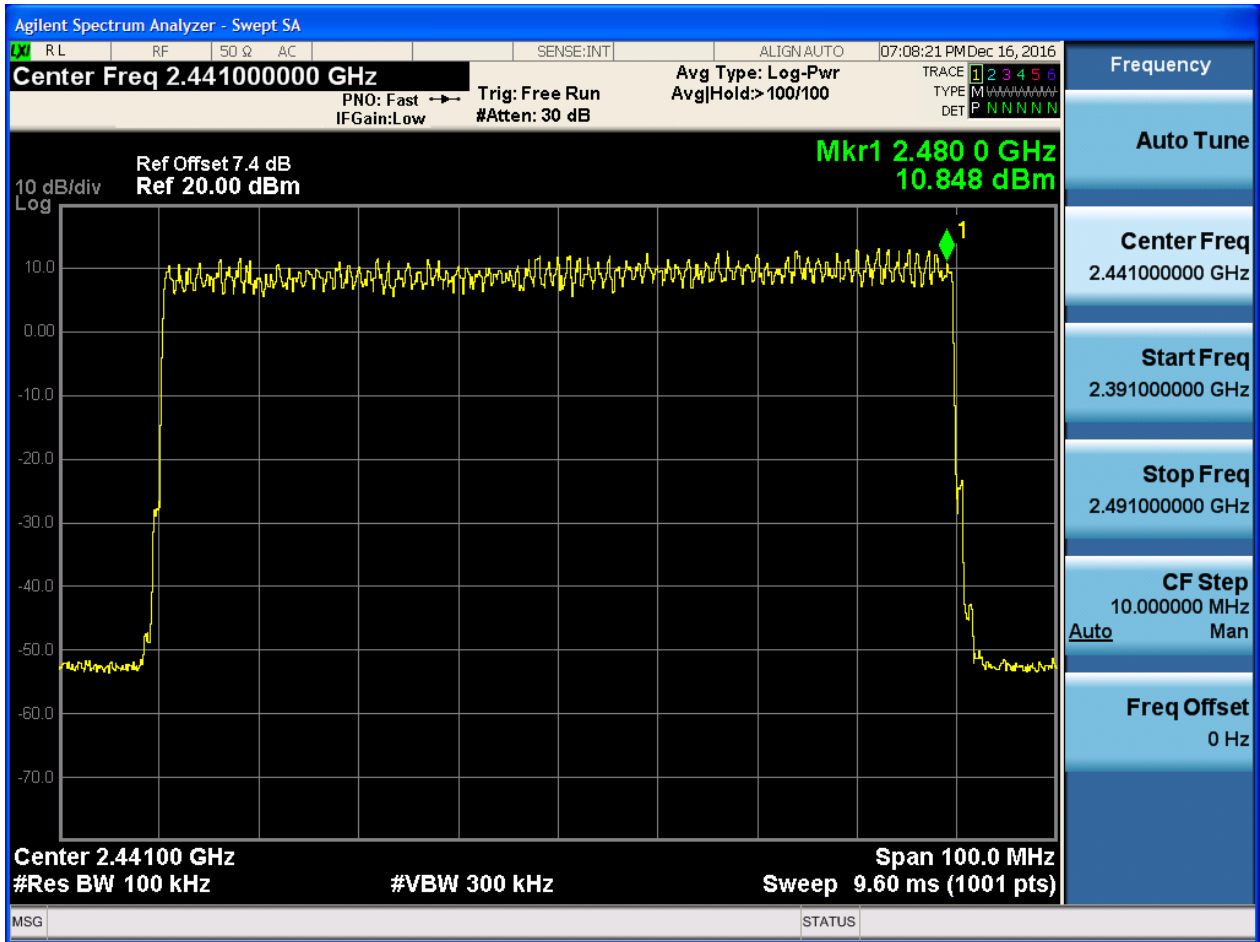


2.2 TM2_2DH5_Hop





2.3 TM3_3DH5_Hop





Appendix D: Time of Occupancy (Dwell Time)

1 Result Table

The Dwell Time = Burst Width * Total Hops. The detailed calculations are showed as follows:

- The duration for dwell time calculation: $0.4 \text{ [s]} * \text{hopping number} = 0.4 \text{ [s]} * 79 \text{ [ch]} = 31.6 \text{ [s*ch]}$;
- The burst width [ms/hop/ch], which is directly measured, refers to the duration on one channel hop.
- The hops per second for all channels: The selected EUT Conf uses a slot type of 5-Tx&1-Rx and a hopping rate of 1600 [ch*hop/s] for all channels. So the final hopping rate for all channels is $1600 / 6 = 266.67 \text{ [ch*hop/s]}$;
- The hops per second on one channel: $266.67 \text{ [ch*hop/s]} / 79 \text{ [ch]} = 3.38 \text{ [hop/s]}$;
- The total hops for all channels within the dwell time calculation duration: $3.38 \text{ [hop/s]} * 31.6 \text{ [s*ch]} = 106.67 \text{ [hop*ch]}$;
- The dwell time for all channels hopping: $106.67 \text{ [hop*ch]} * \text{Burst Width [ms/hop/ch]}$.

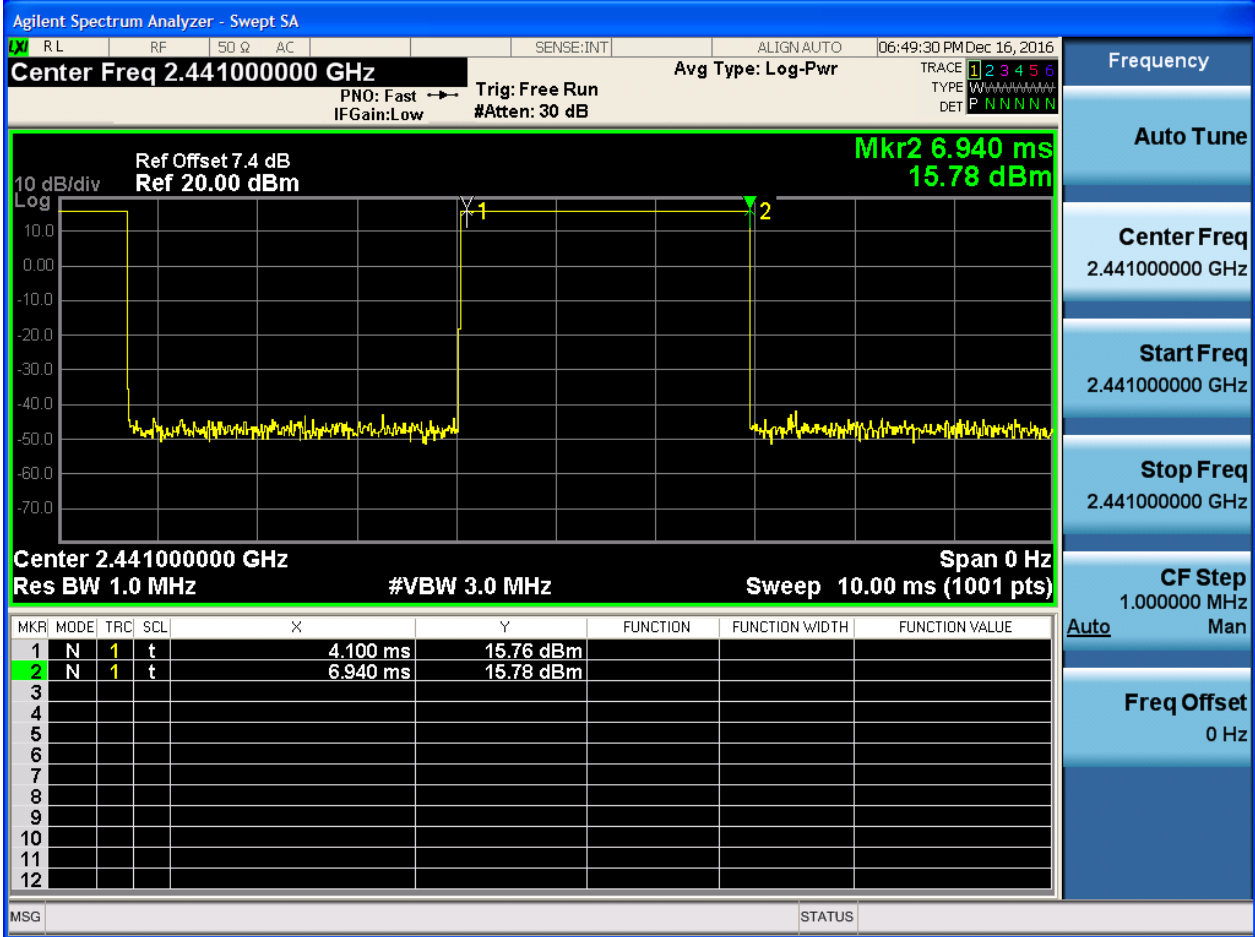
EUT Conf.	Burst Width [s/hop/ch]	Total Hops [hop*ch]	Dwell Time [s]	Verdict
TM1_DH5_Ch39	0.00284	106.67	0.303	Pass
TM2_2DH5_Ch39	0.00284	106.67	0.303	Pass
TM3_3DH5_Ch39	0.00288	106.67	0.307	Pass



2 Test Plot

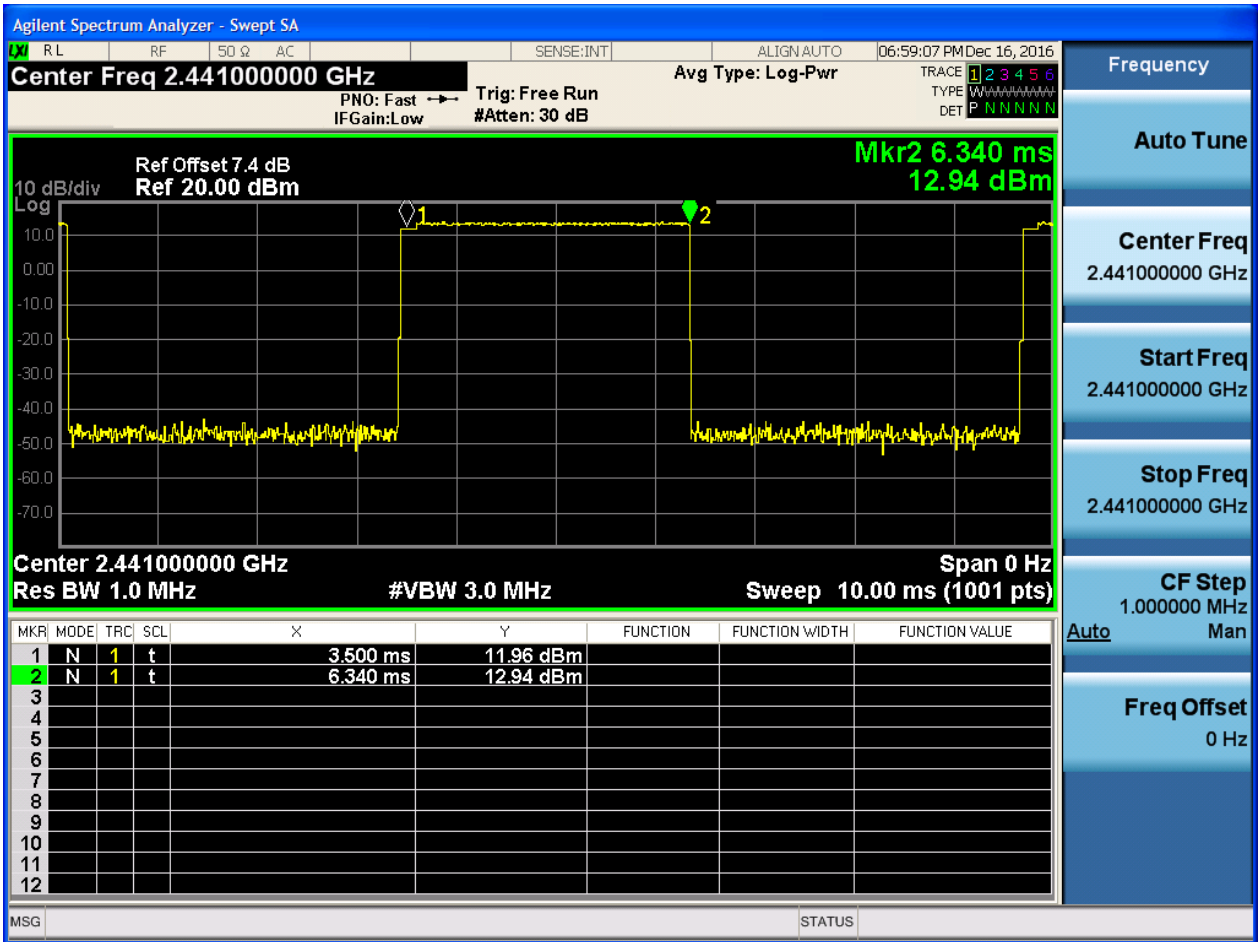
NOTE: The test plots are only for Burst Width measurements.

2.1 TM1_DH5_Ch39



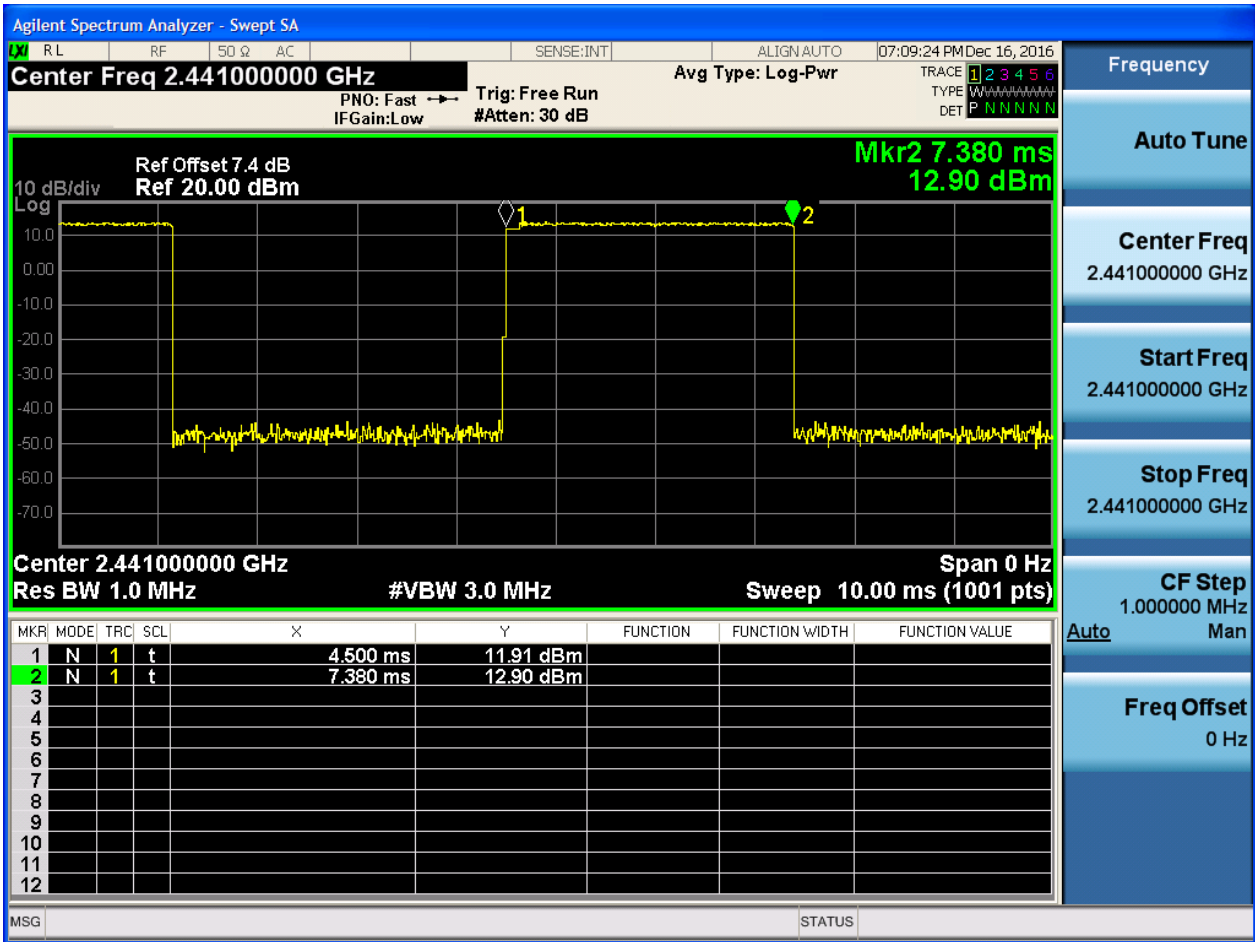


2.2 TM2_2DH5_Ch39





2.3 TM3_3DH5_Ch39





Appendix E: Maximum Peak Conducted Output Power



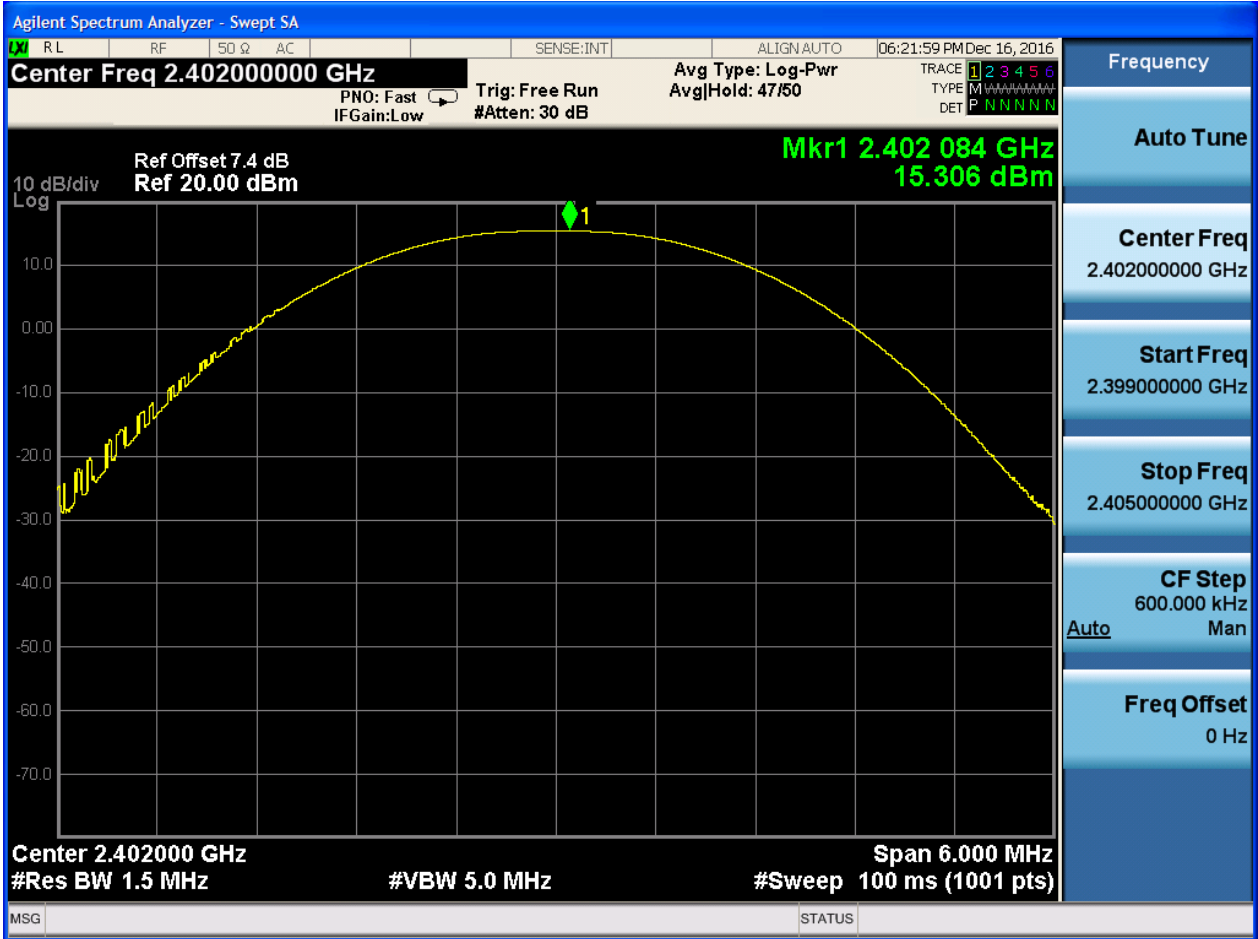
1 Result Table

EUT Conf.	Max. Peak Power [dBm]	Verdict
TM1_DH5_Ch0	15.306	Pass
TM1_DH5_Ch39	15.908	Pass
TM1_DH5_Ch78	15.951	Pass
TM2_2DH5_Ch0	13.583	Pass
TM2_2DH5_Ch39	14.261	Pass
TM2_2DH5_Ch78	15.011	Pass
TM3_3DH5_Ch0	13.671	Pass
TM3_3DH5_Ch39	14.209	Pass
TM3_3DH5_Ch78	14.91	Pass



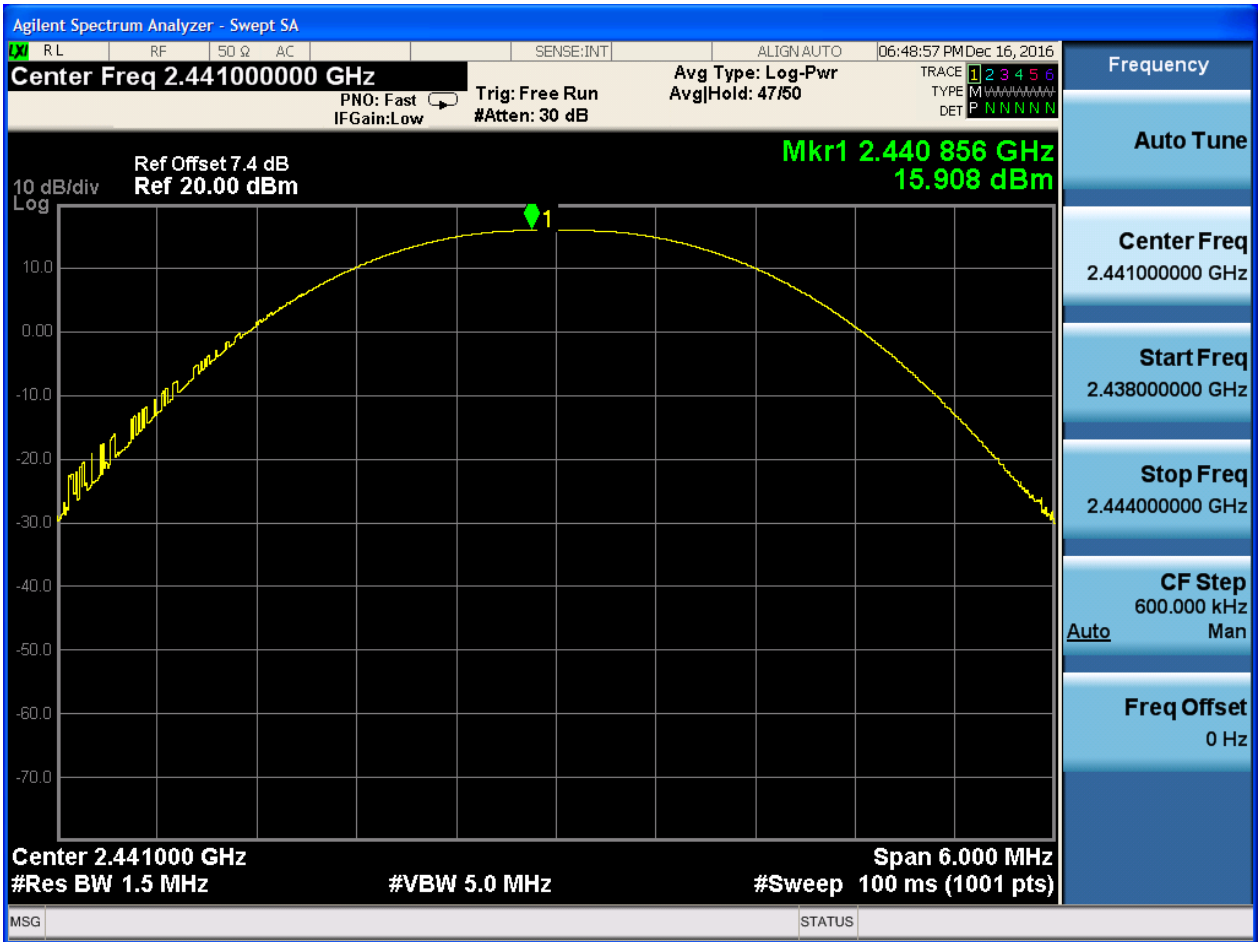
2 Test Plot

2.1 TM1_DH5_Ch0



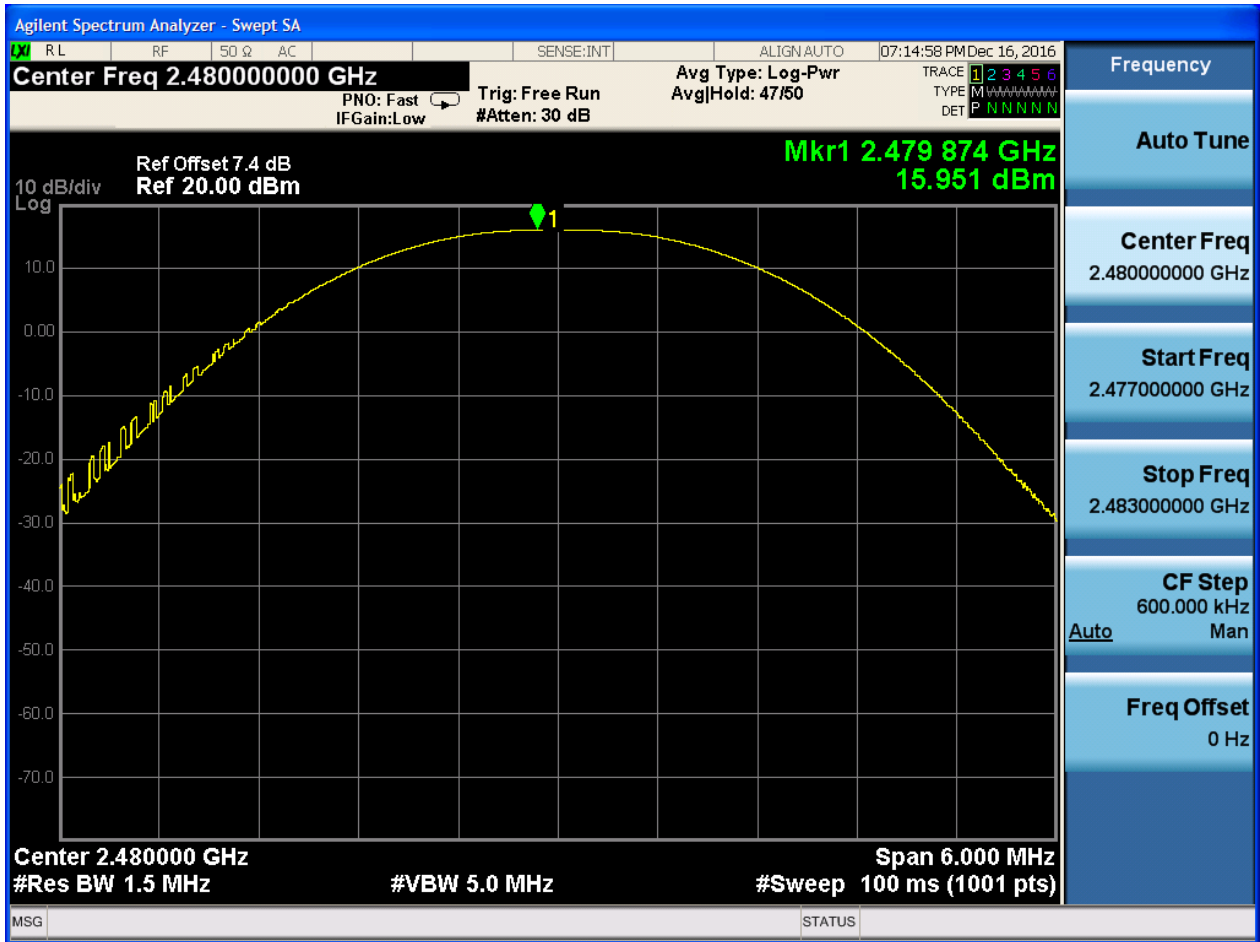


2.2 TM1_DH5_Ch39



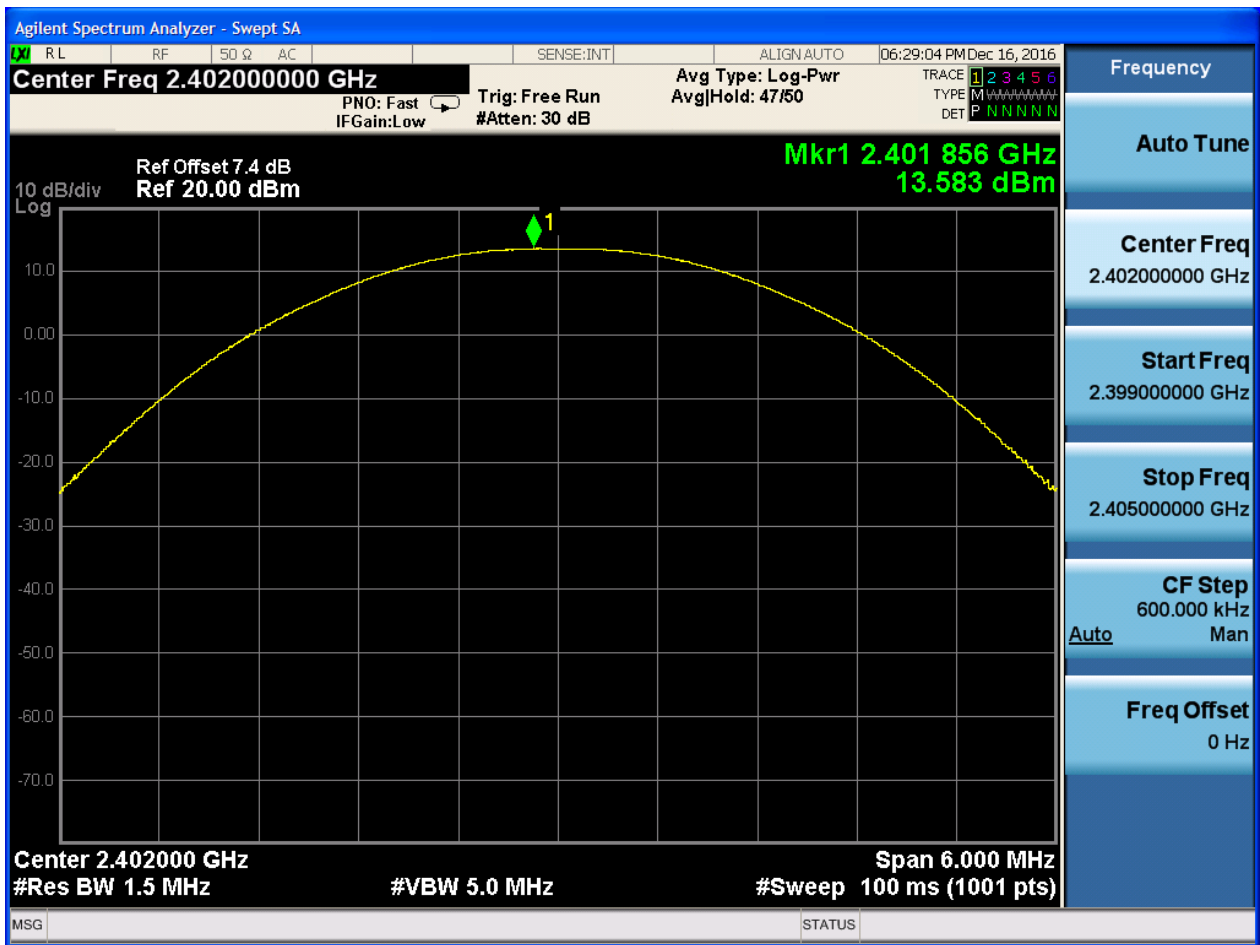


2.3 TM1_DH5_Ch78

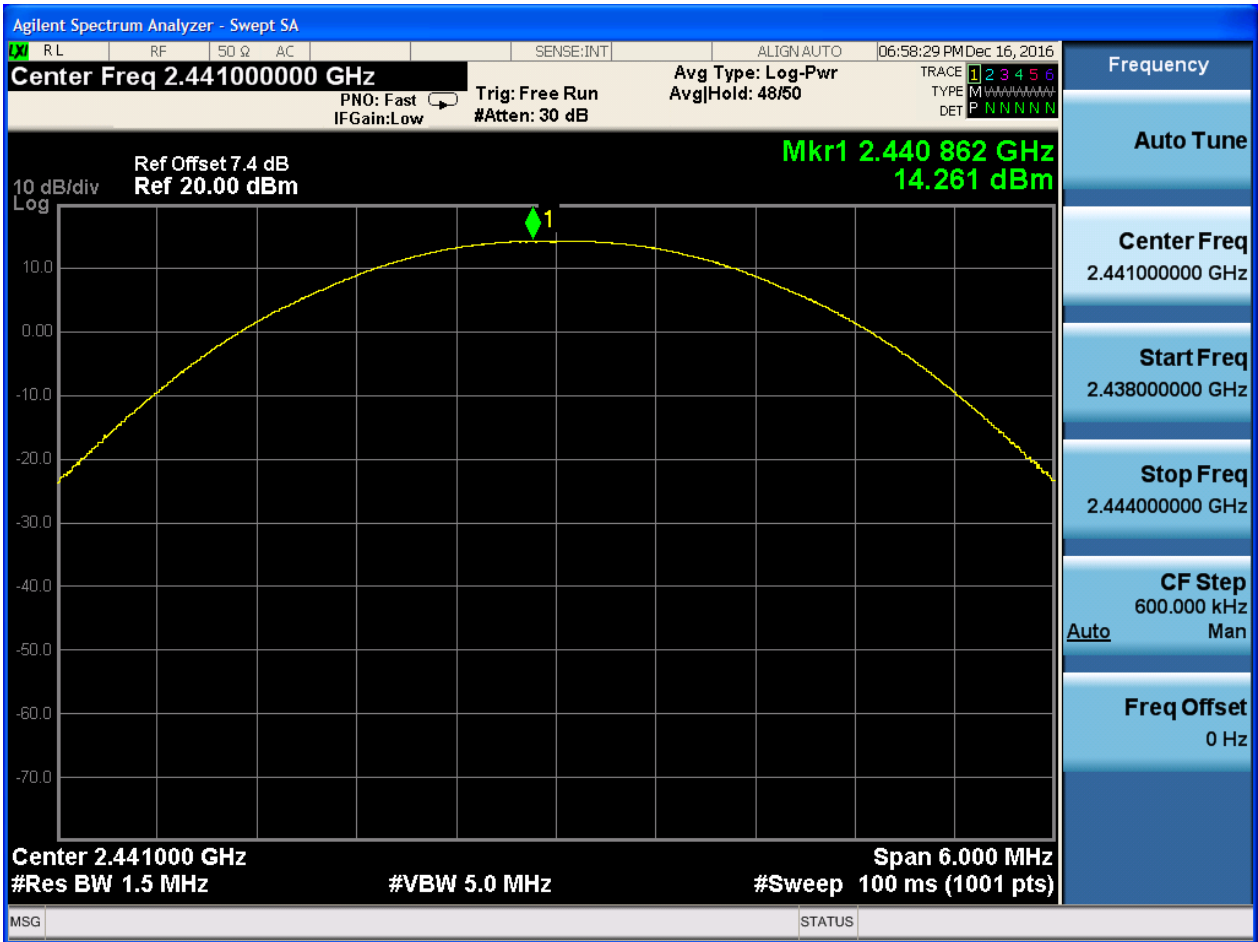




2.4 TM2_2DH5_Ch0

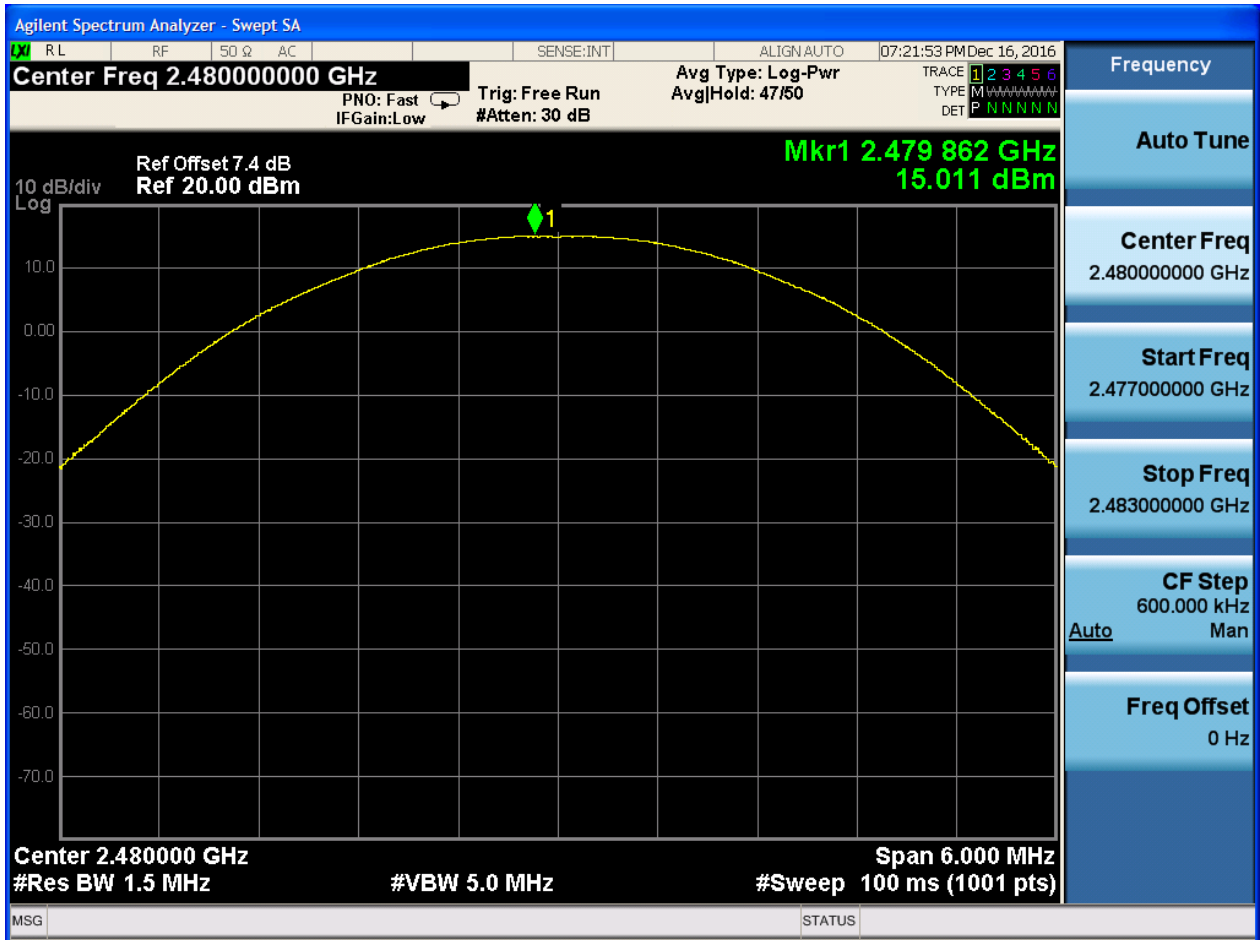


2.5 TM2_2DH5_Ch39



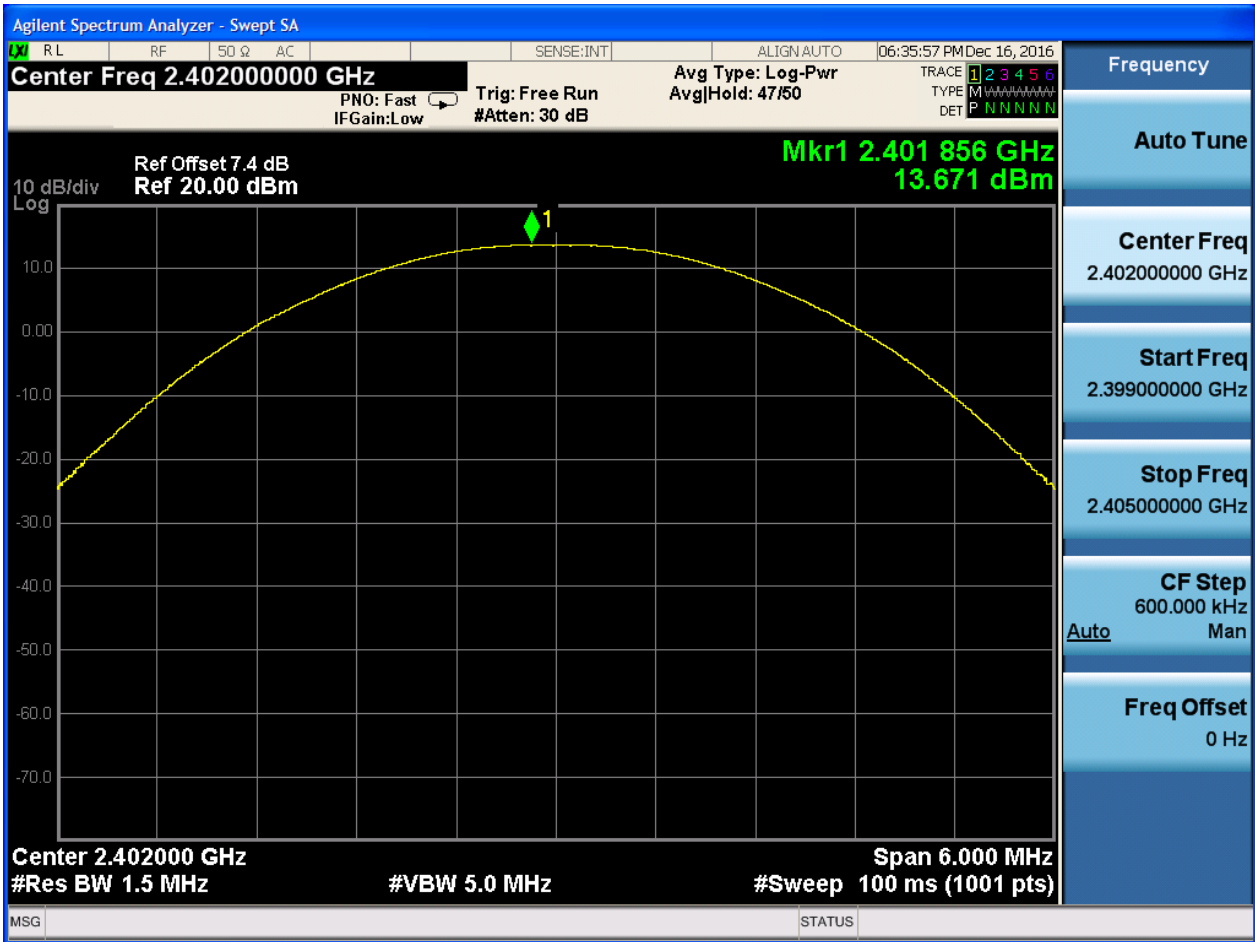


2.6 TM2_2DH5_Ch78



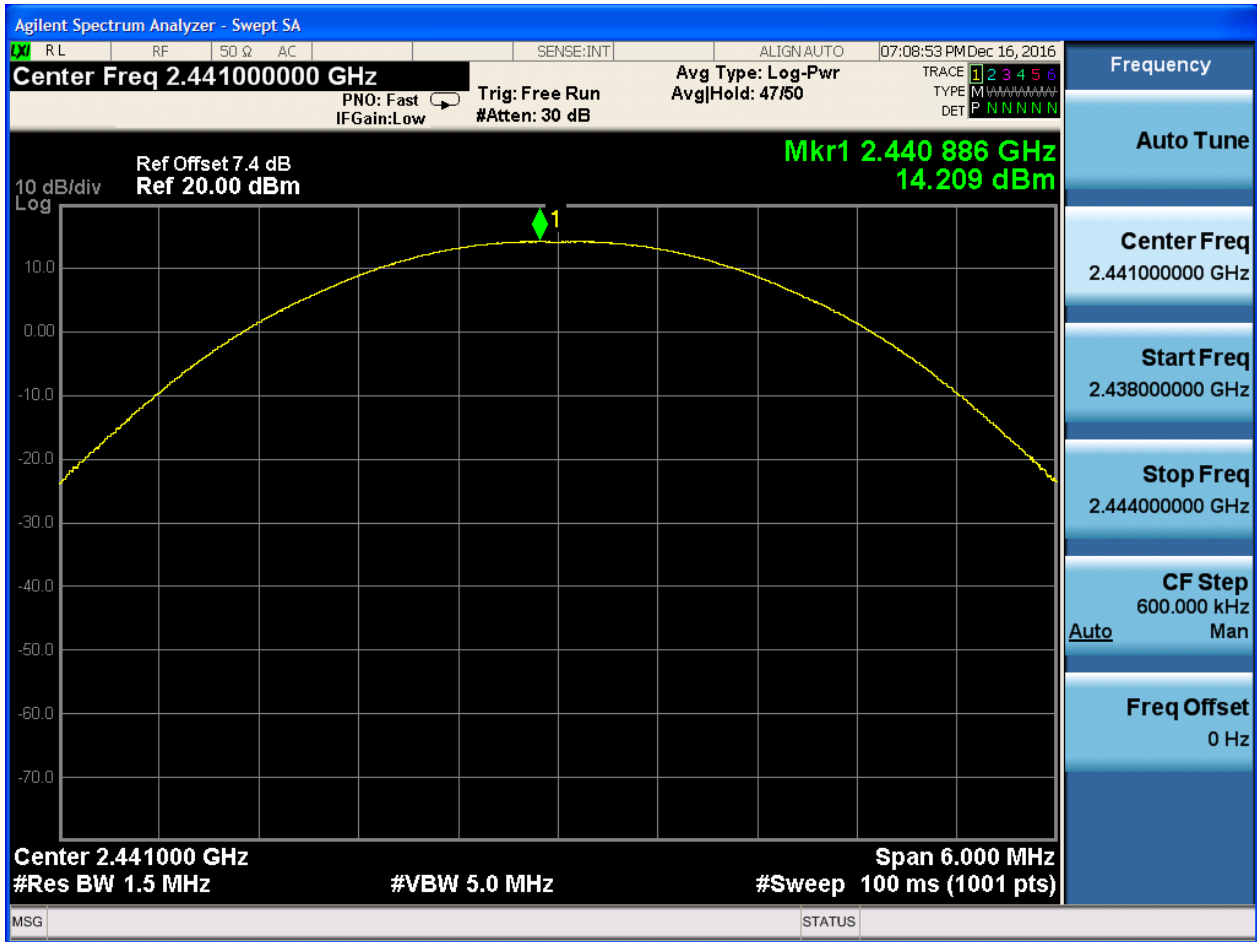


2.7 TM3_3DH5_Ch0



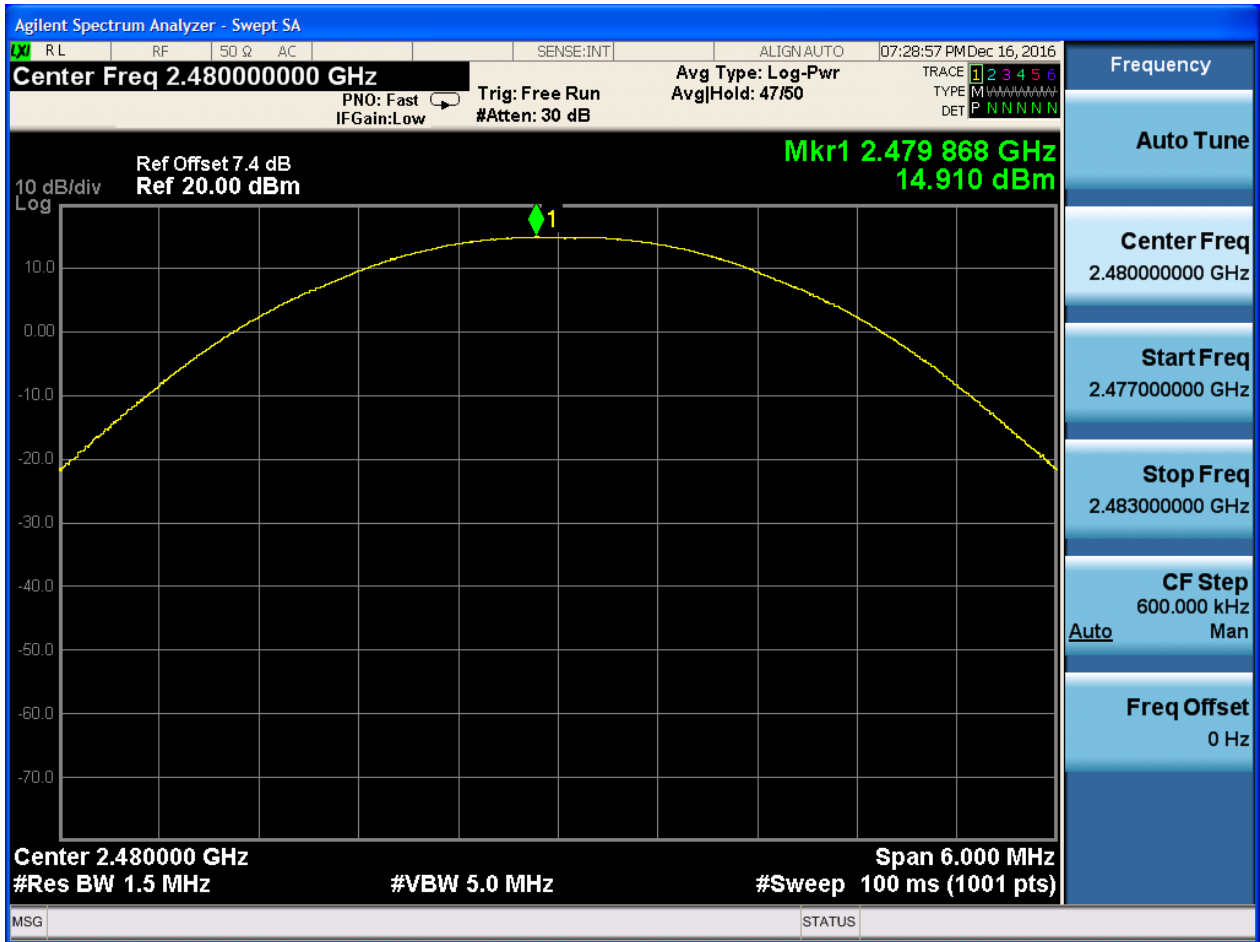


2.8 TM3_3DH5_Ch39





2.9 TM3_3DH5_Ch78





Appendix F: Band edge spurious emission



1 Result Table

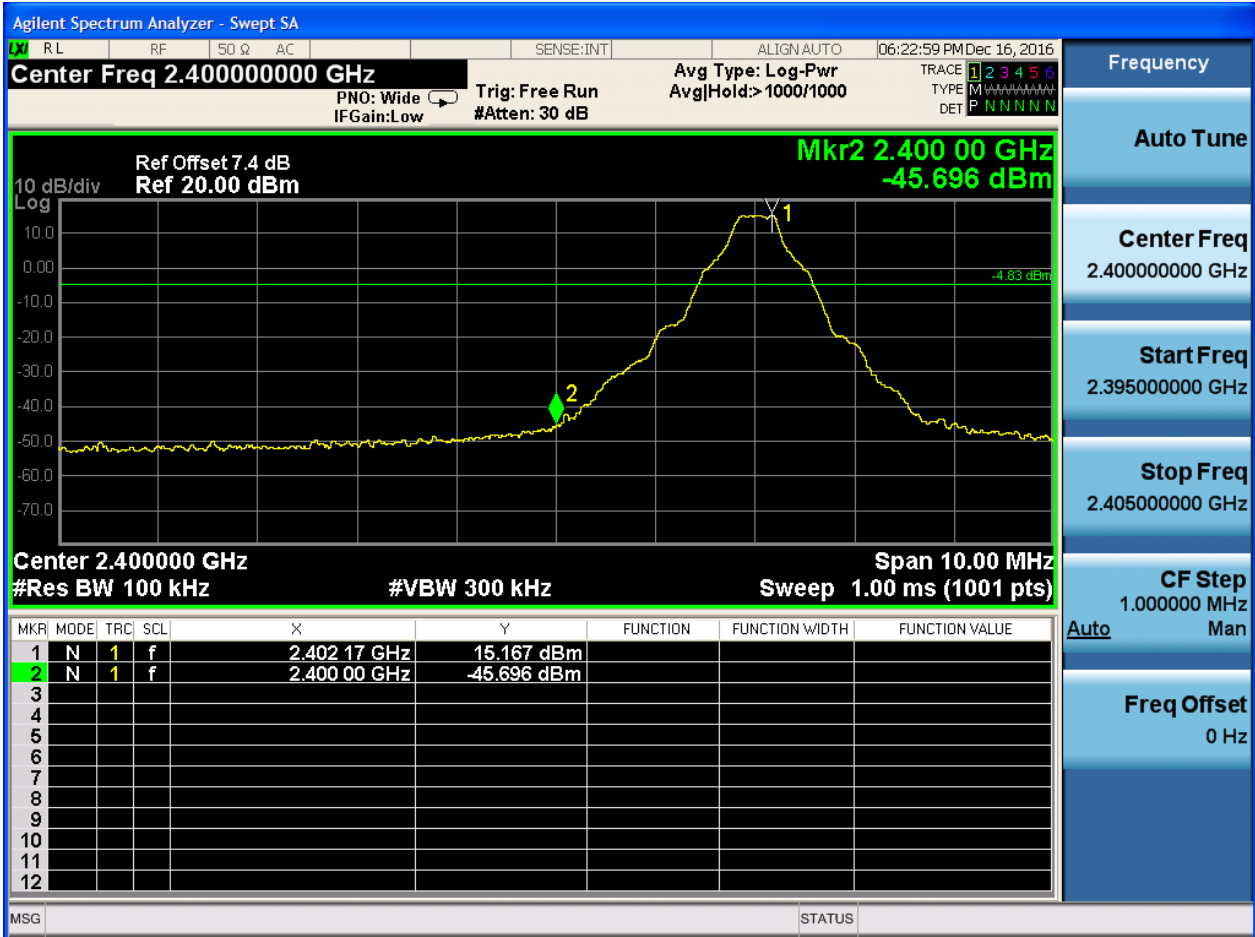
EUT Conf.	Channel No.	Carrier Frequency [MHz]	Max. Spurious Level [dBm]	Frequency Hopping	Carrier Power [dBm]	Limit [dBm]	Result
TM1_DH5 _Ch0	0	2402	-45.696	Off	15.167	-4.833	Pass
	-	-	-48.737	On	14.912	-5.088	Pass
TM1_DH5 _Ch78	78	2480	-49.205	Off	15.812	-4.188	Pass
	-	-	-51.048	On	15.484	-4.516	Pass
TM2_2DH 5_Ch0	0	2402	-46.568	Off	11.235	-8.765	Pass
	-	-	-48.522	On	10.734	-9.266	Pass
TM2_2DH 5_Ch78	78	2480	-49.597	Off	12.849	-7.151	Pass
	-	-	-50.255	On	12.163	-7.837	Pass
TM3_3DH 5_Ch0	0	2402	-46.554	Off	11.399	-8.601	Pass
	-	-	-47.882	On	10.079	-9.921	Pass
TM3_3DH 5_Ch78	78	2480	-48.649	Off	12.703	-7.297	Pass
	-	-	-51.129	On	12.56	-7.44	Pass



2 Test Plot

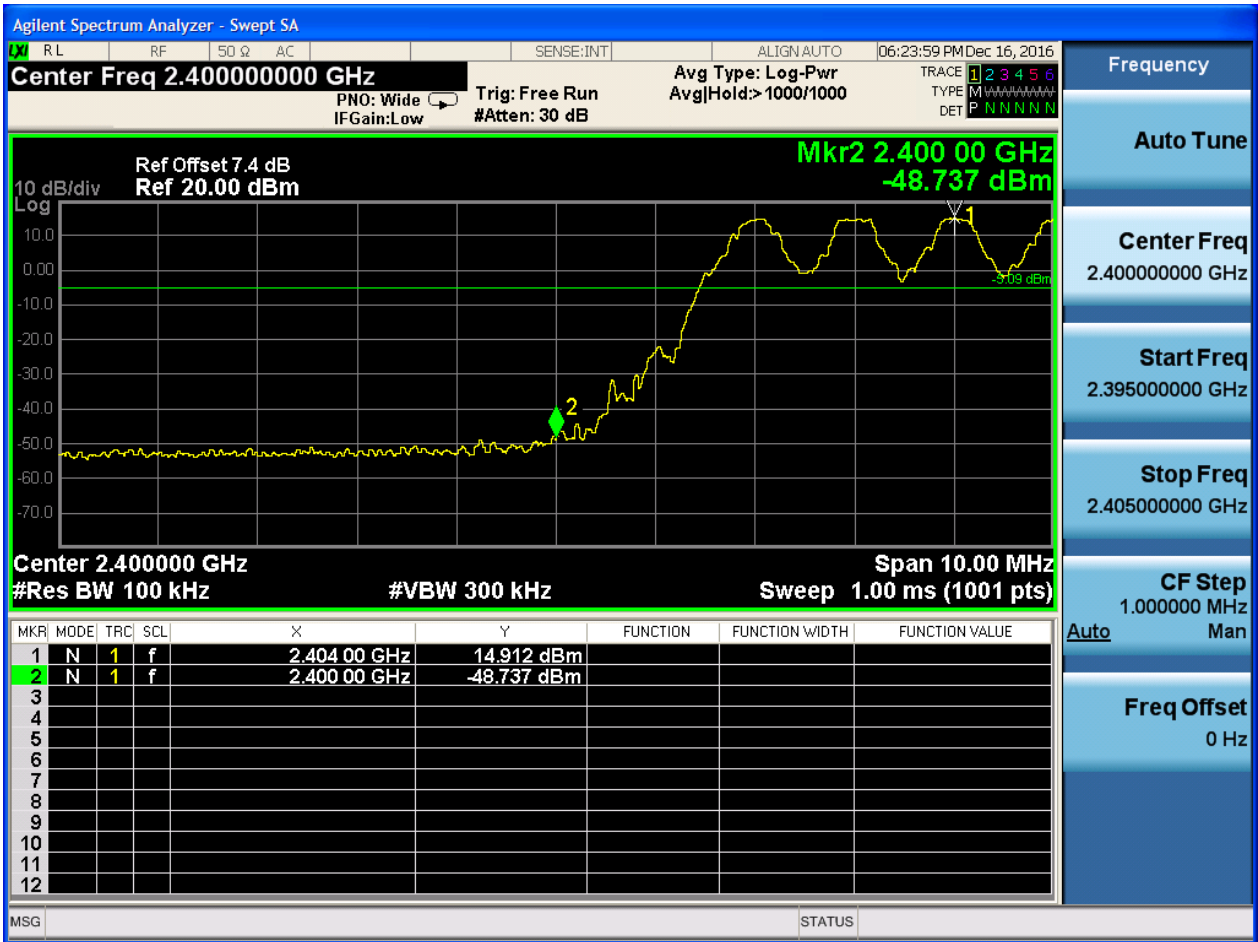
2.1 TM1_DH5_Ch0

No hopping





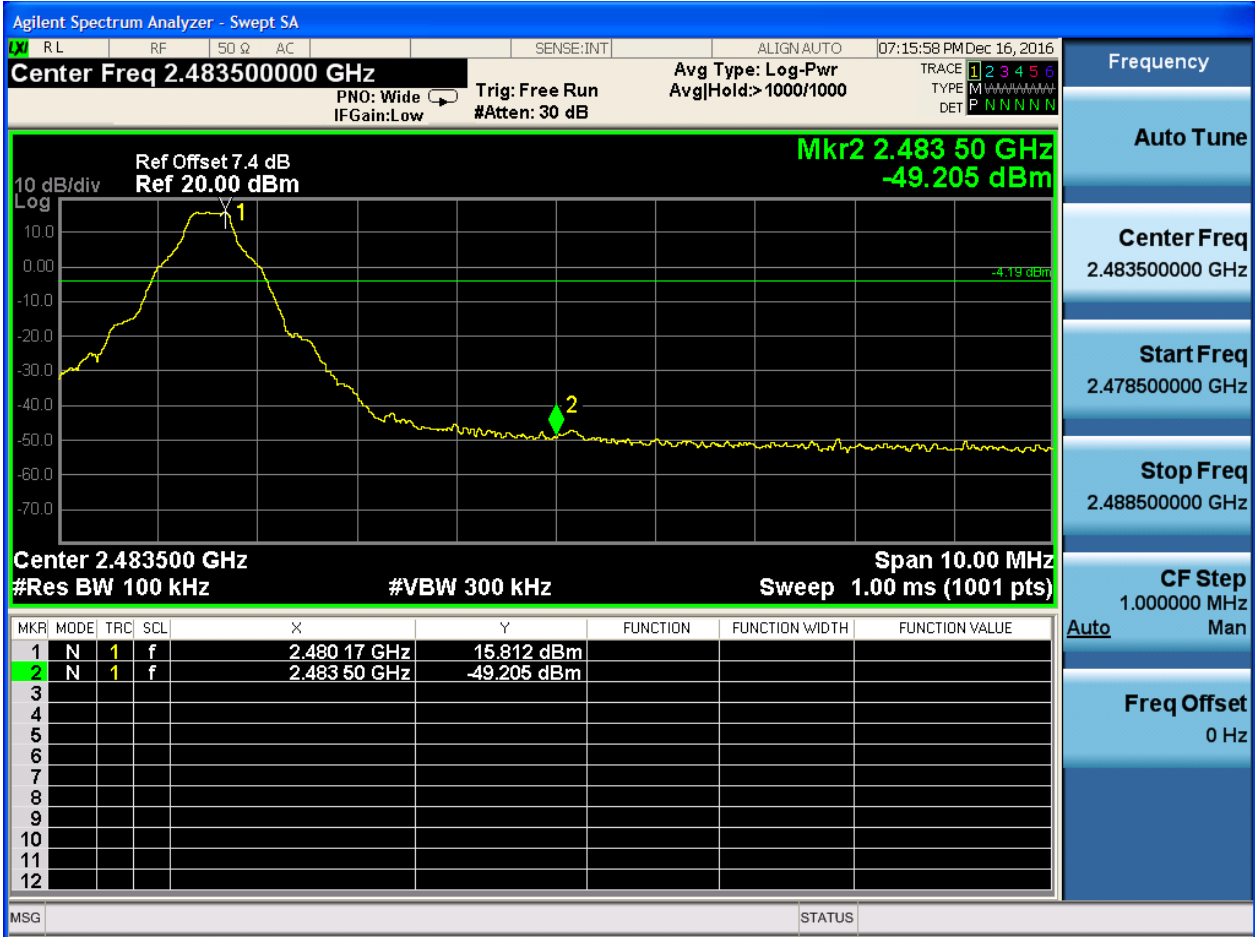
With hopping





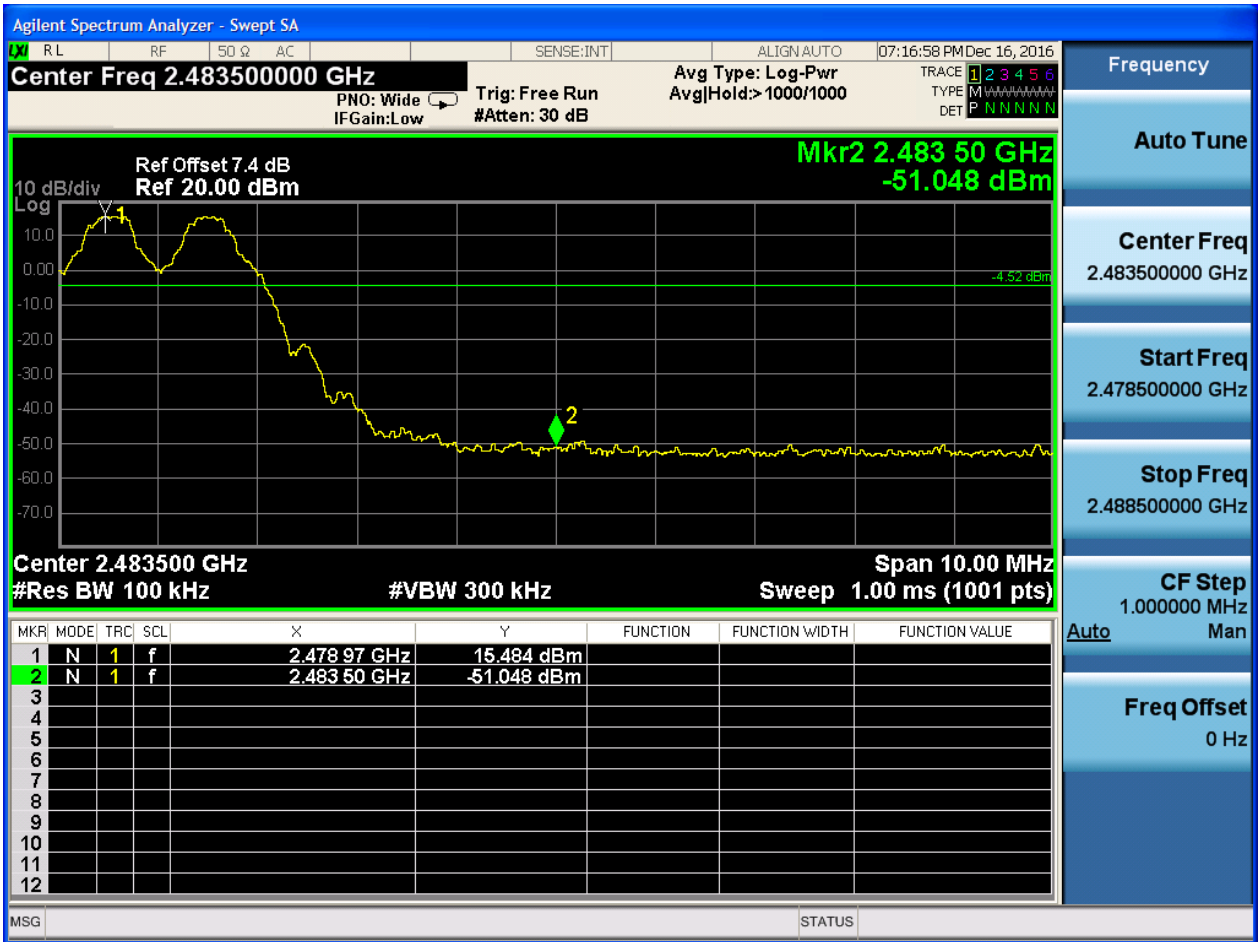
2.2 TM1_DH5_Ch78

No hopping





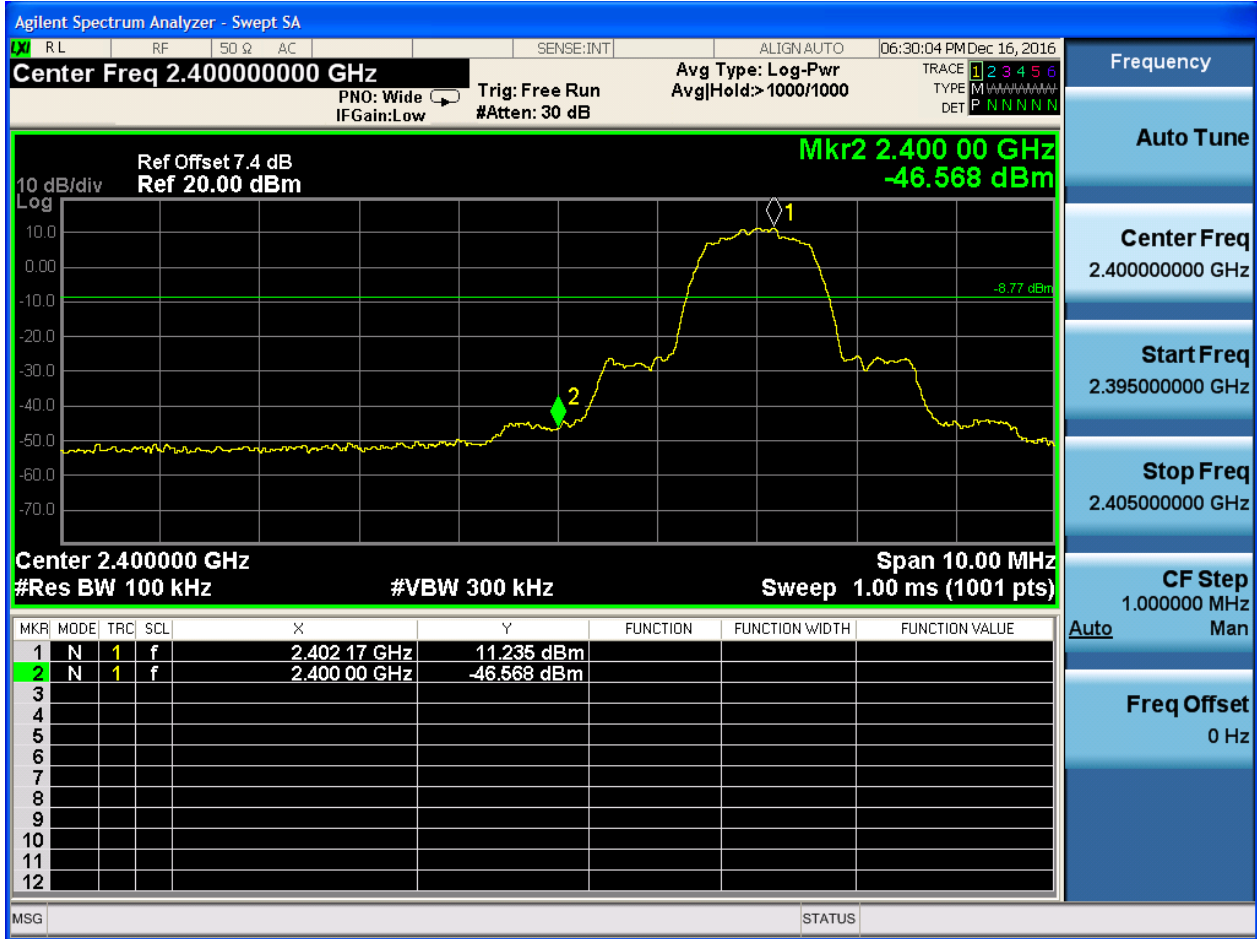
With hopping





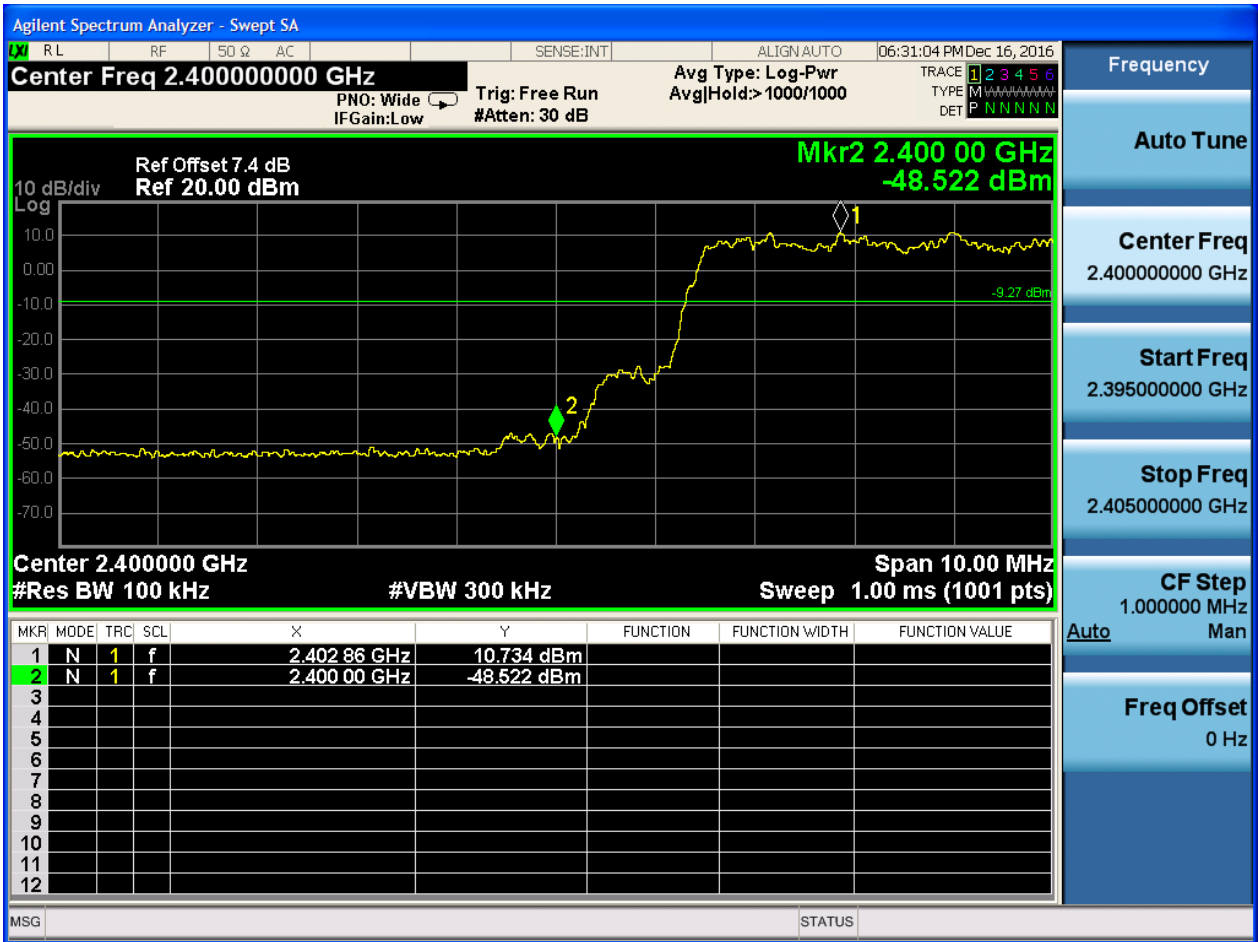
2.3 TM2_2DH5_Ch0

No hopping





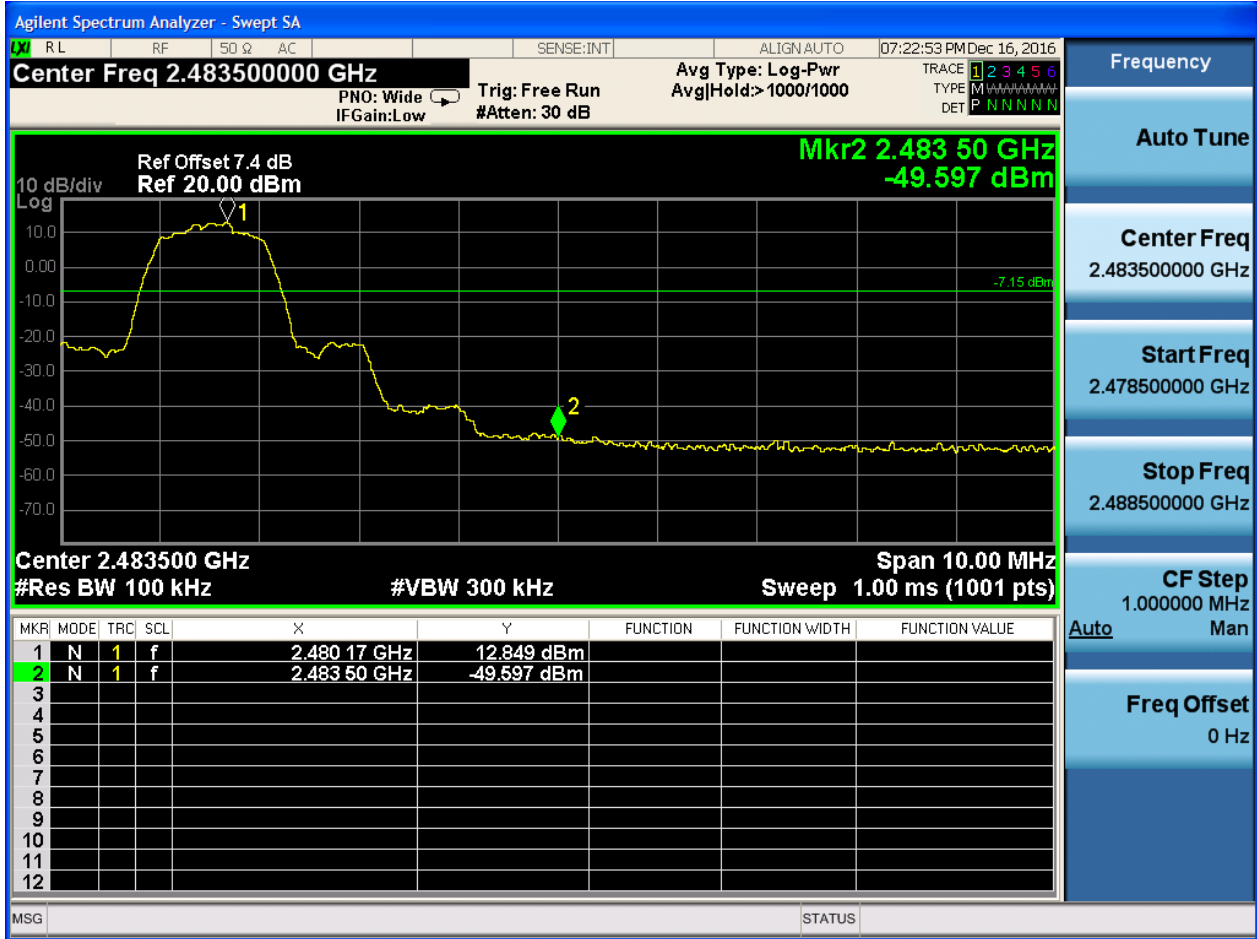
With hopping



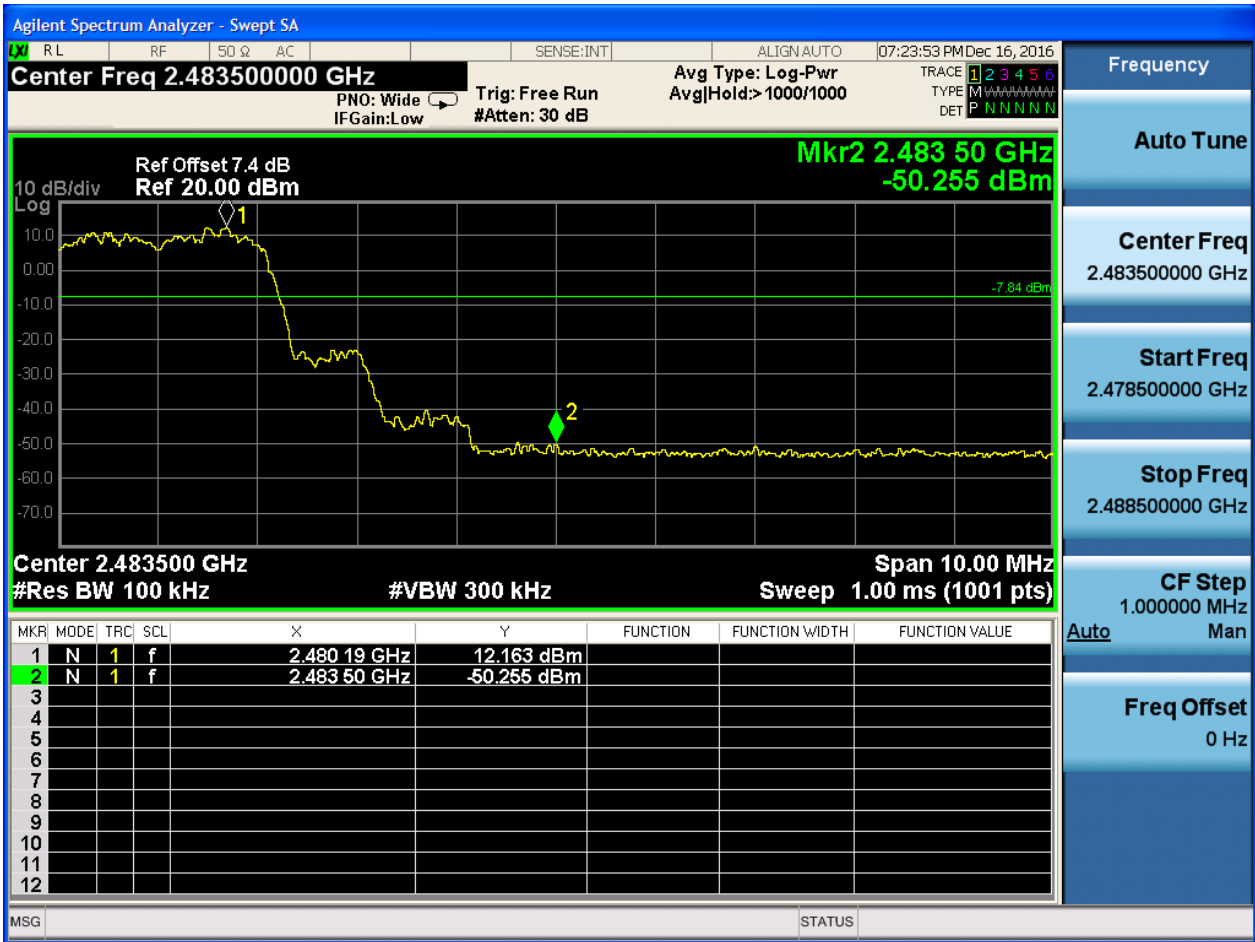


2.4 TM2_2DH5_Ch78

No hopping



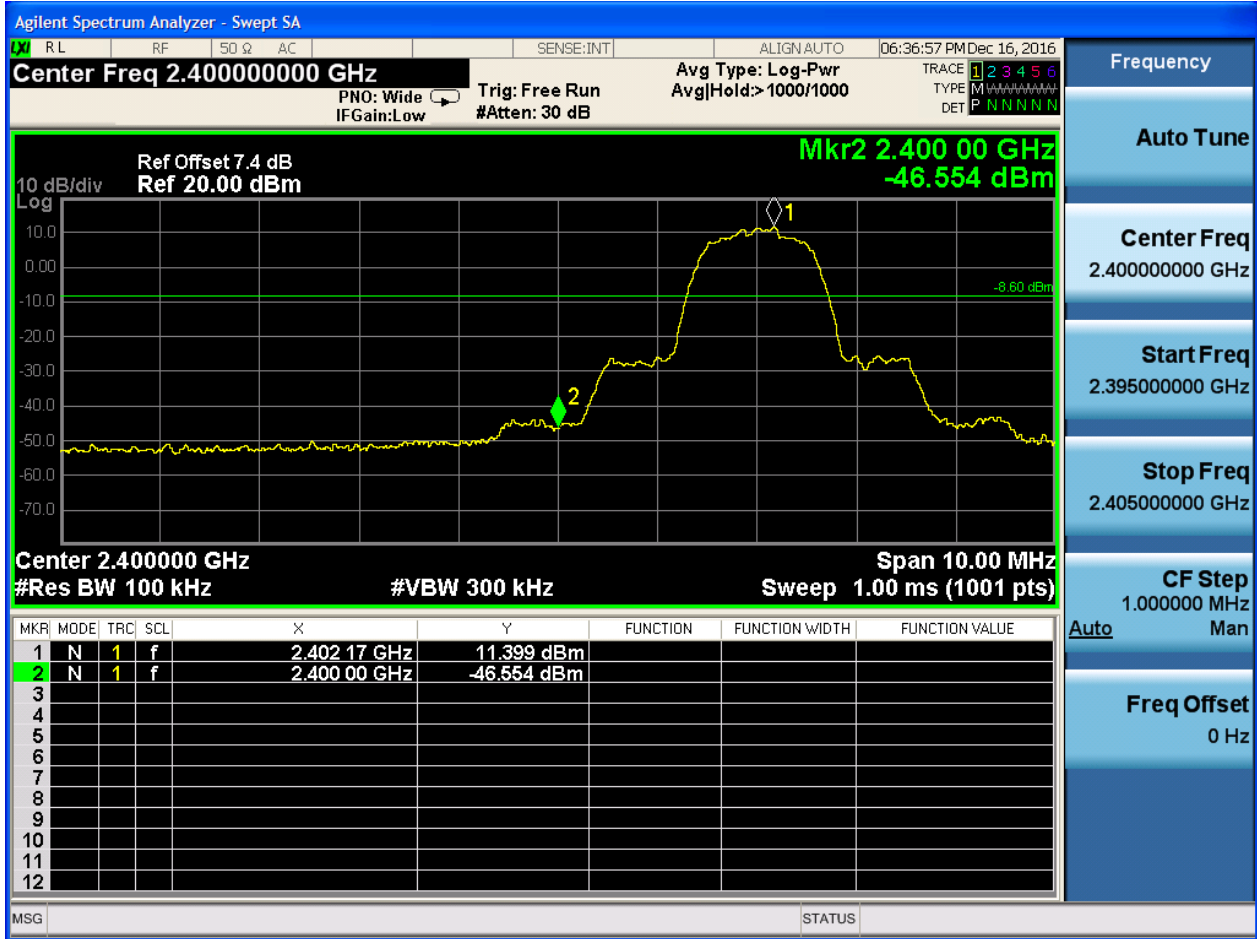
With hopping





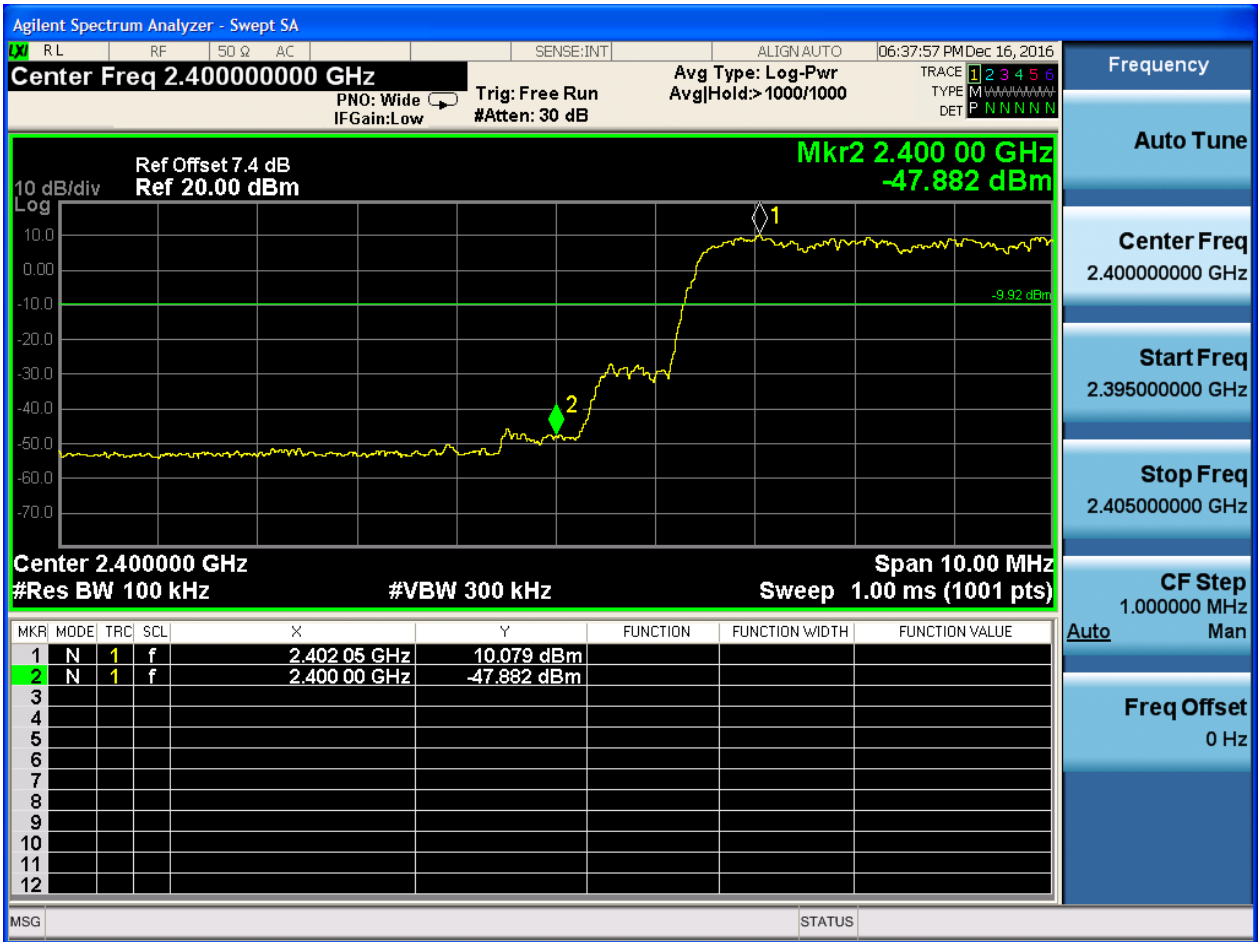
2.5 TM3_3DH5_Ch0

No hopping





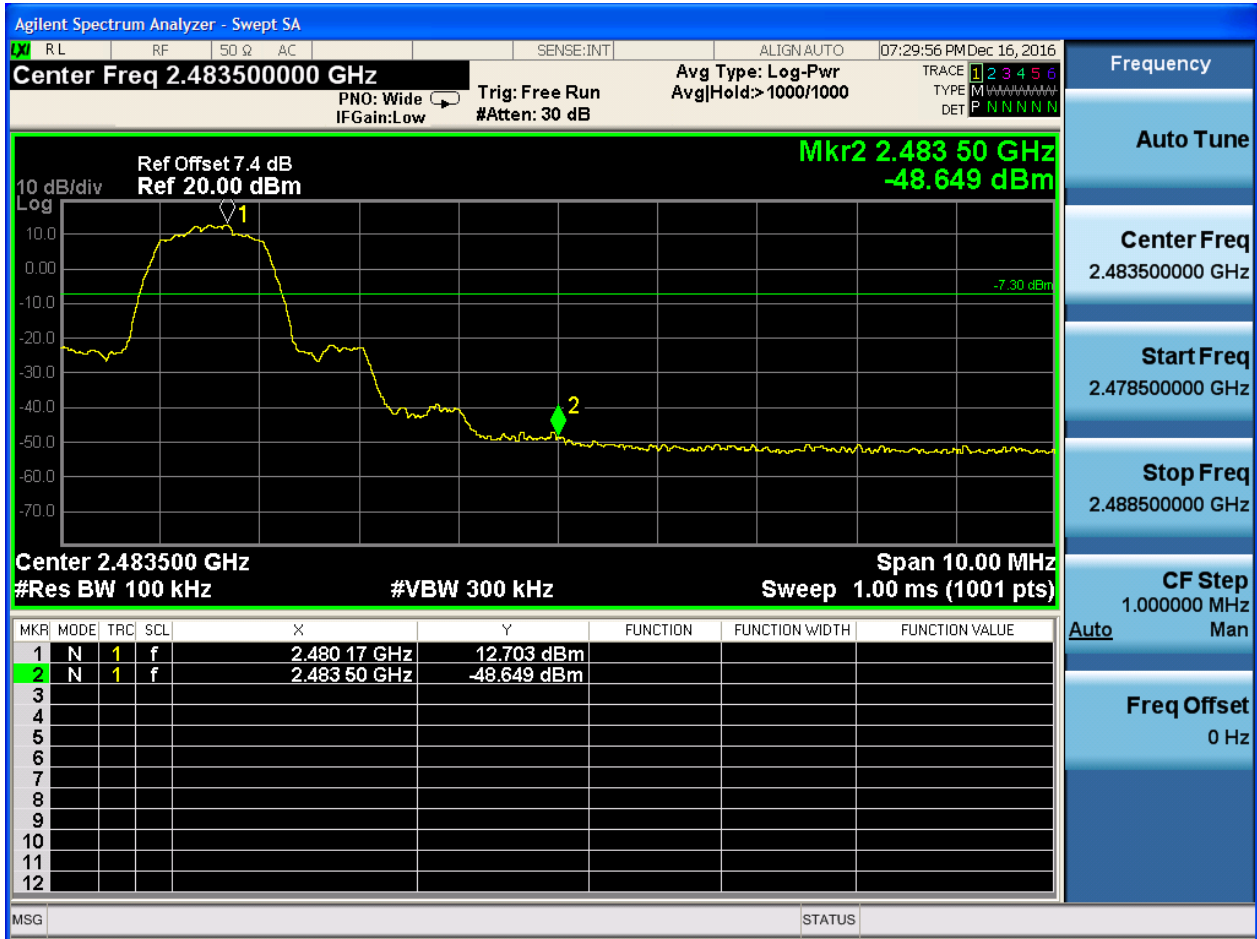
With hopping





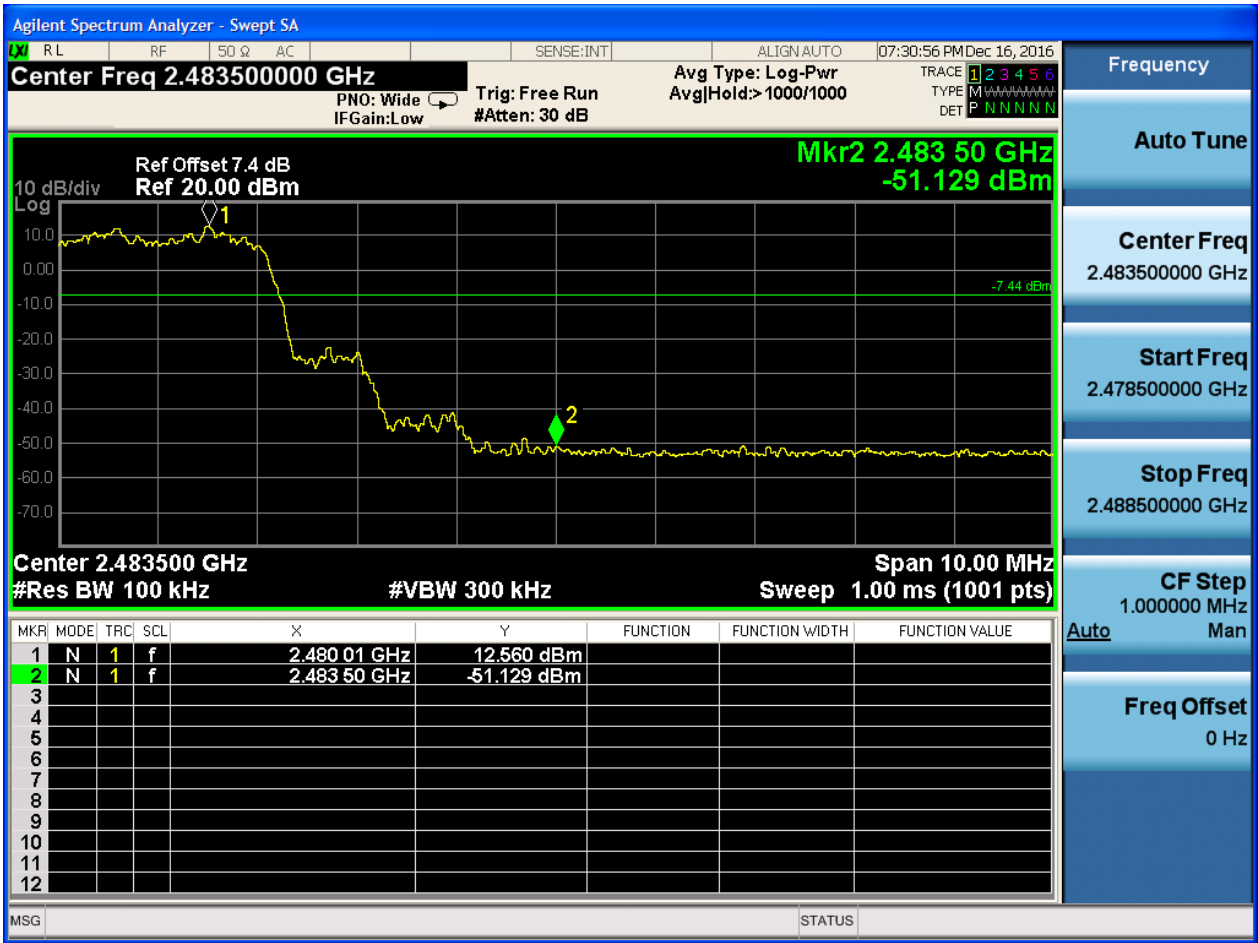
2.6 TM3_3DH5_Ch78

No hopping





With hopping





Appendix G: Conducted RF Spurious Emission



1 Result Table

In this Appendix, the “Pref” refers to the peak power level in any 100 kHz bandwidth within the fundamental emission which is used as the reference level, the “Puw” refers to the maximum emission power in 100 kHz band segments outside of the authorized frequency band.

Considering that the higher ratio of RBW to the span for the frequency ranges below 30 MHz makes the results determination be complicated, a narrower RBW other than 100 kHz is used for these ranges. The measured value should add a RBW correction factor (RBWCF) where $RBWCF [dB] = 10 \times \lg(100 [kHz]/\text{narrower RBW [kHz]})$. As to this Appendix, the narrower RBW is 1 kHz and RBWCF is 20 dB for the frequency 9 kHz to 150 kHz, and the narrower RBW is 10 kHz and RBWCF is 10 dB for the frequency 150 kHz to 30 MHz.

In the result table, the “< Limit” denotes that “The Puw [dBm] is less than Pref [dBm] - 20 [dB], see test plots for detailed”.

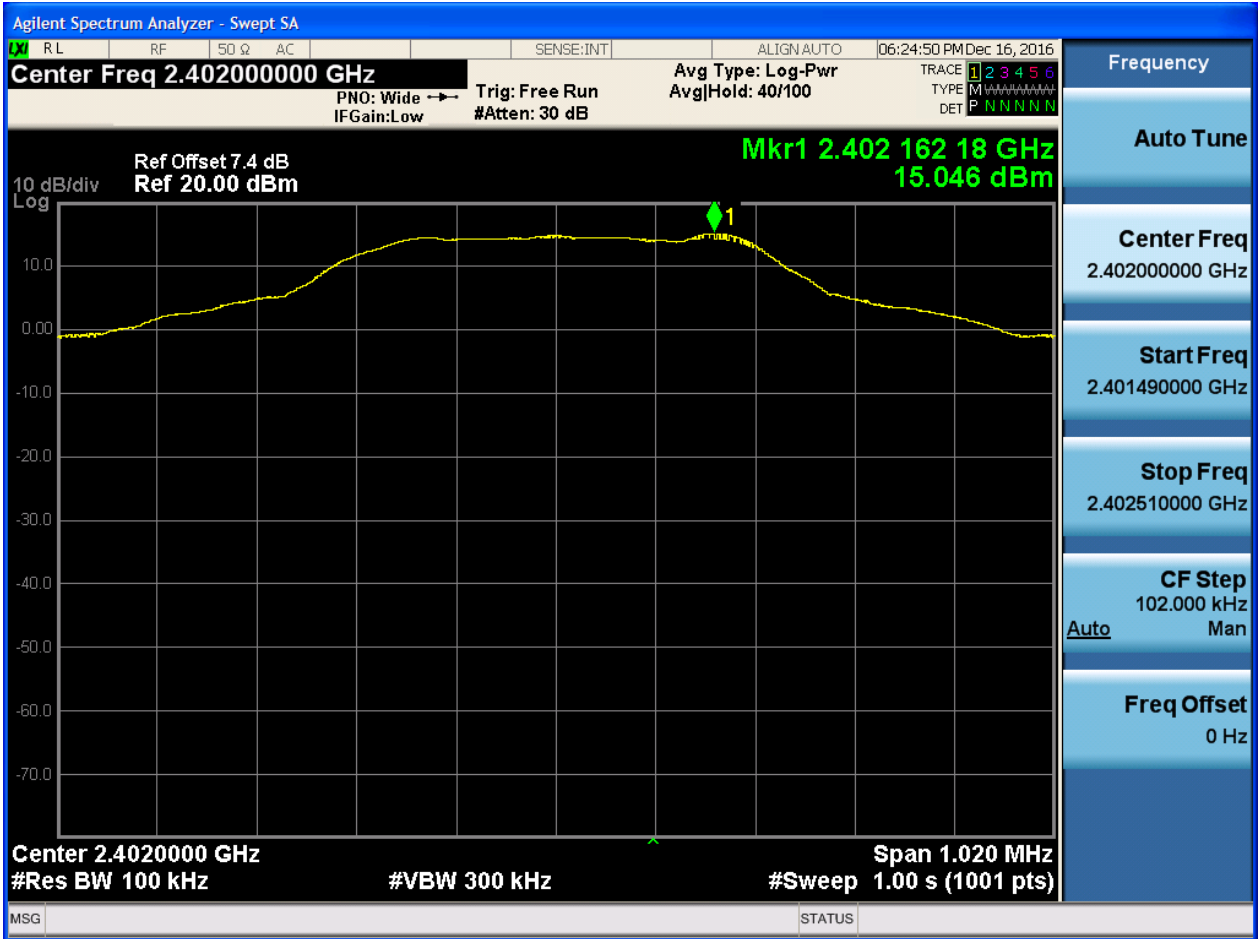
EUT Conf.	Pref [dBm/100 kHz]	Puw [dBm/100 kHz]	Verdict
TM1_DH5_Ch0	15.046	< Limit	Pass
TM1_DH5_Ch39	15.78	< Limit	Pass
TM1_DH5_Ch78	15.714	< Limit	Pass
TM2_2DH5_Ch0	11.206	< Limit	Pass
TM2_2DH5_Ch39	11.979	< Limit	Pass
TM2_2DH5_Ch78	12.816	< Limit	Pass
TM3_3DH5_Ch0	11.35	< Limit	Pass
TM3_3DH5_Ch39	11.928	< Limit	Pass
TM3_3DH5_Ch78	12.633	< Limit	Pass



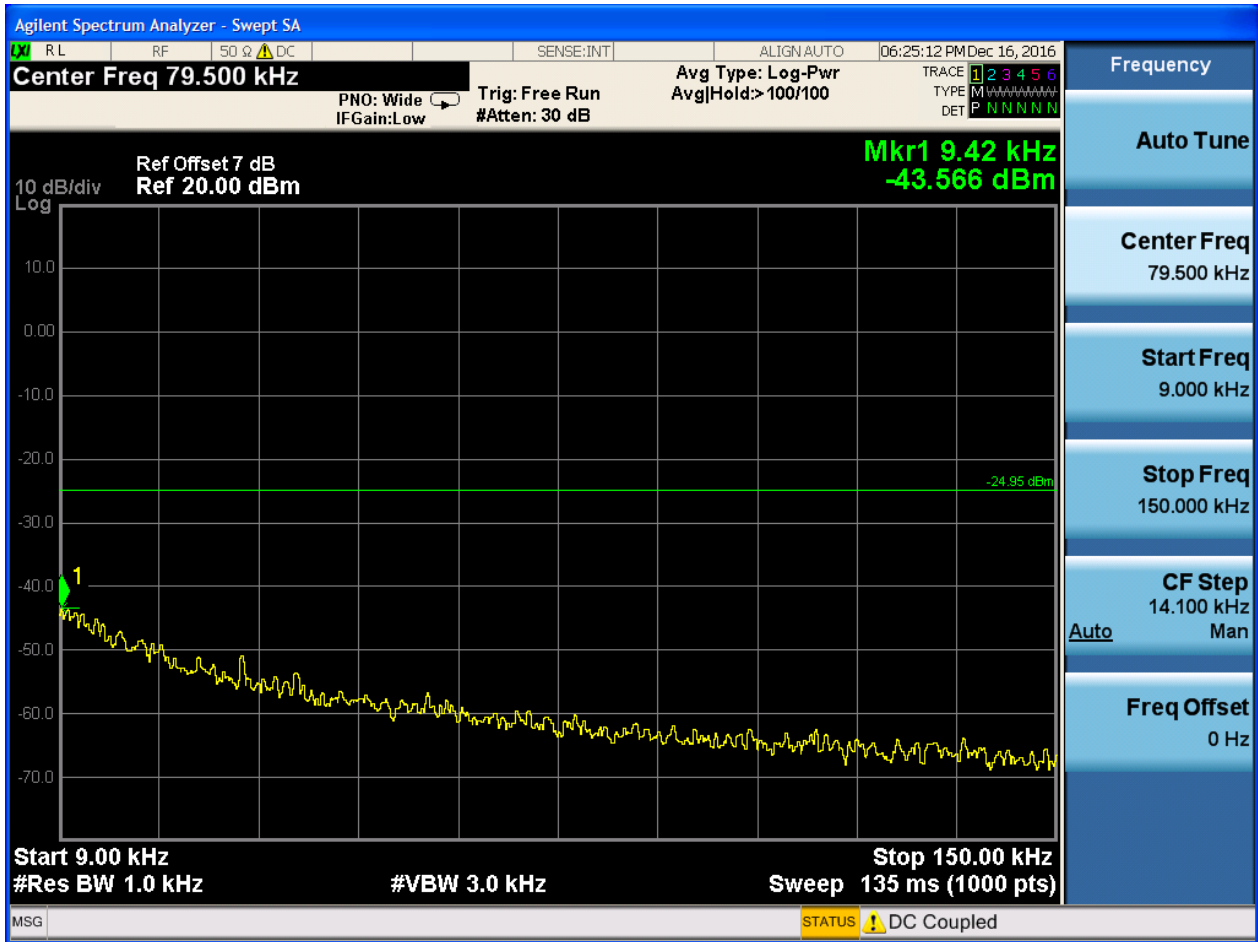
2 Test Plot

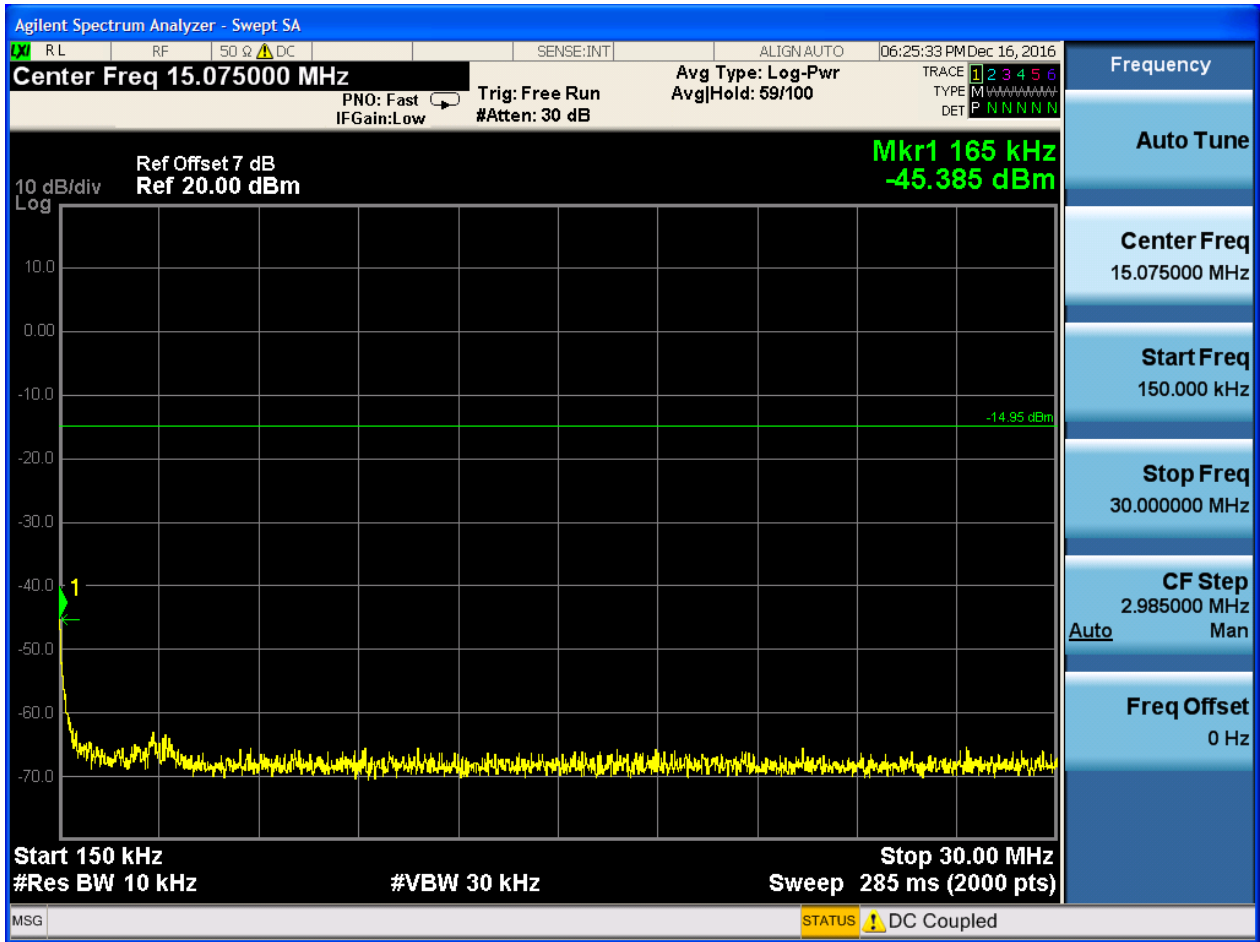
2.1 TM1_DH5_Ch0

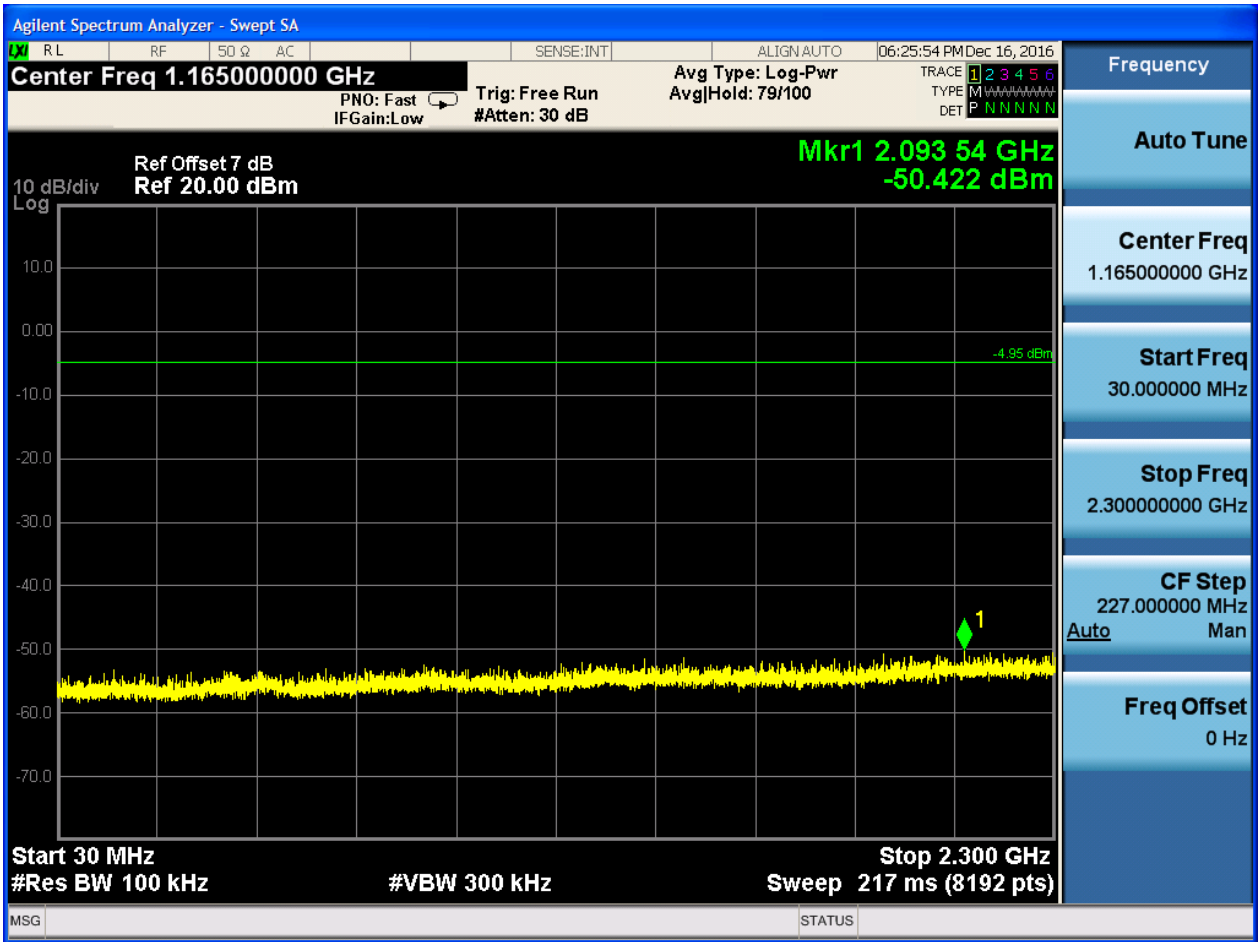
2.1.1 Pref

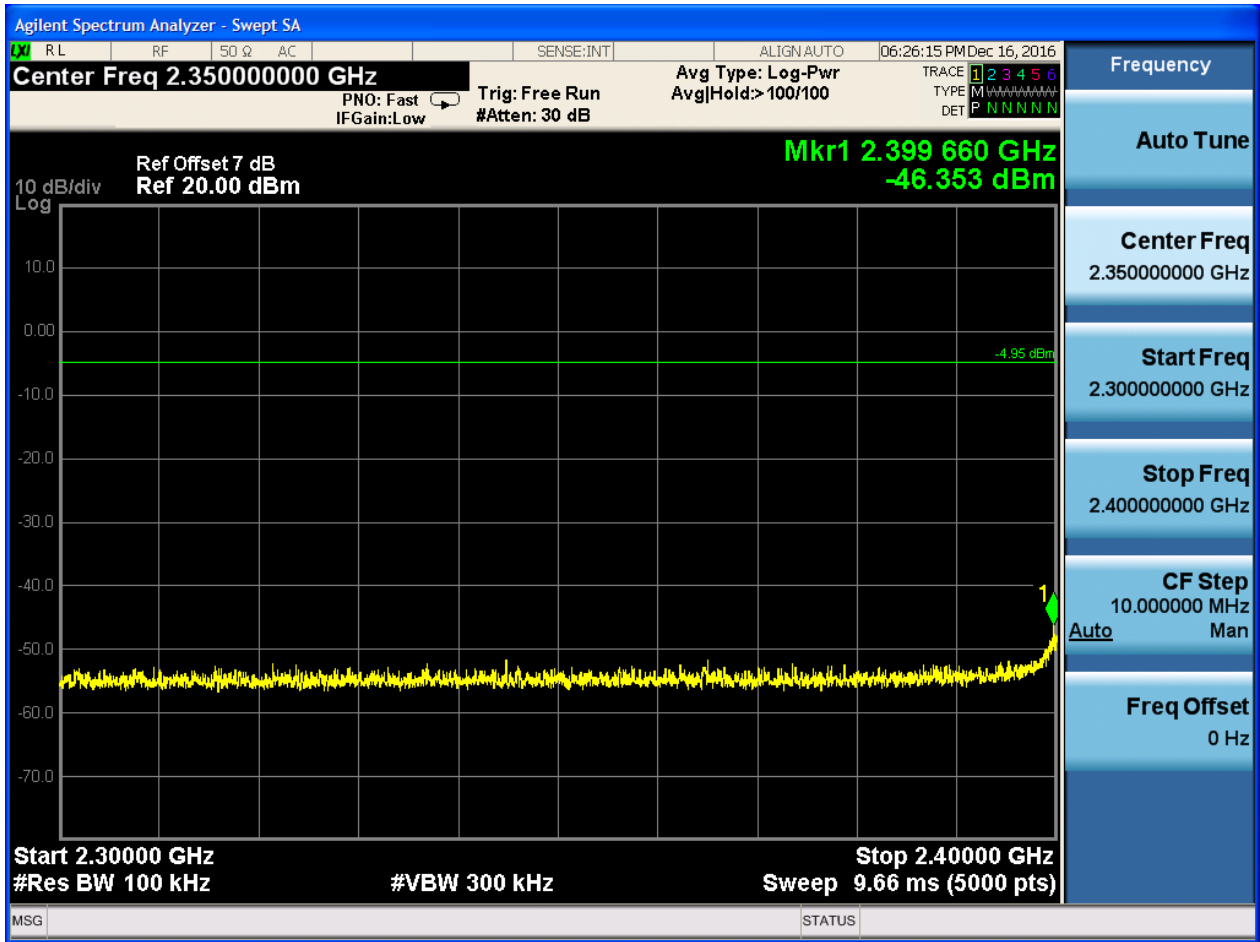


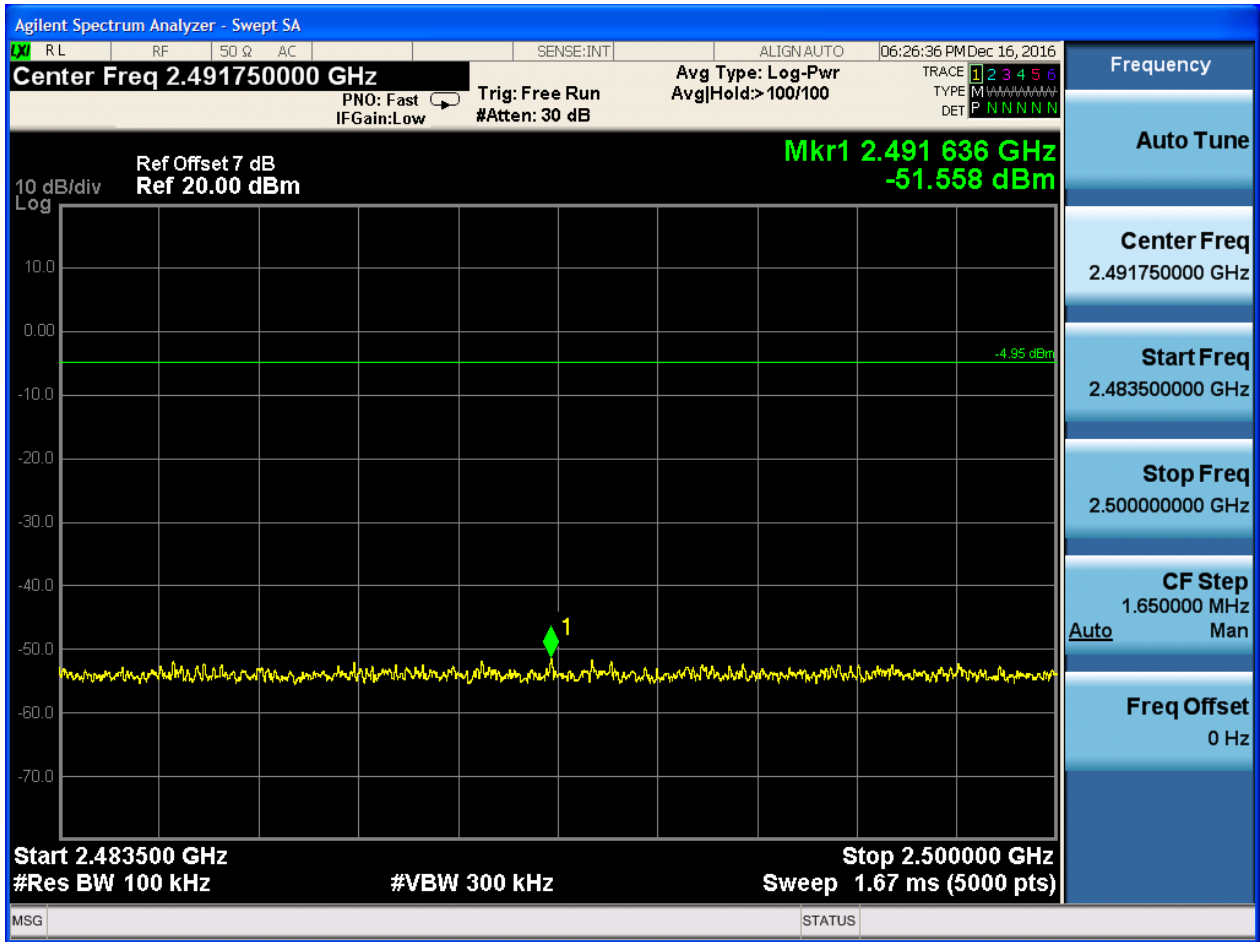
2.1.2 Puw

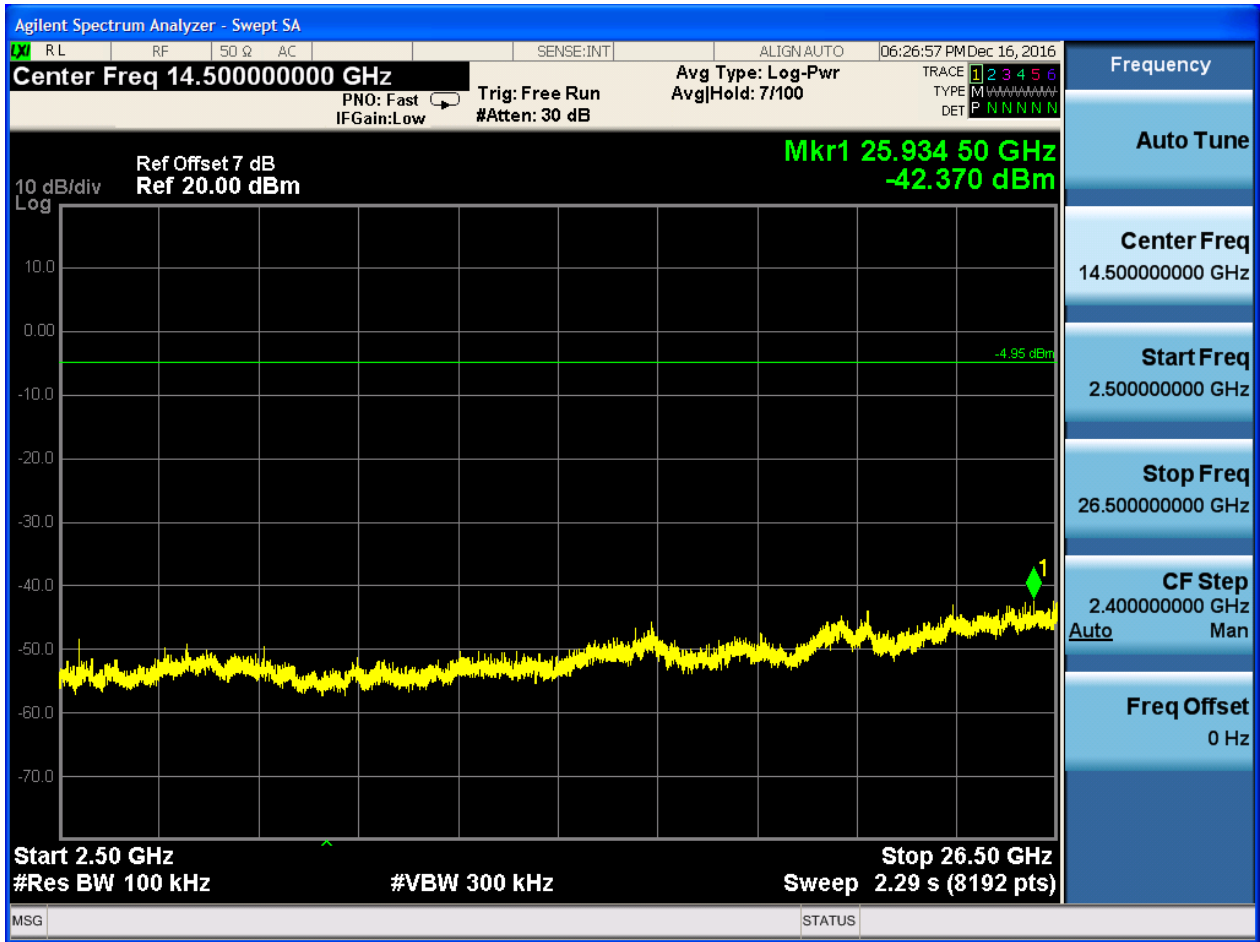














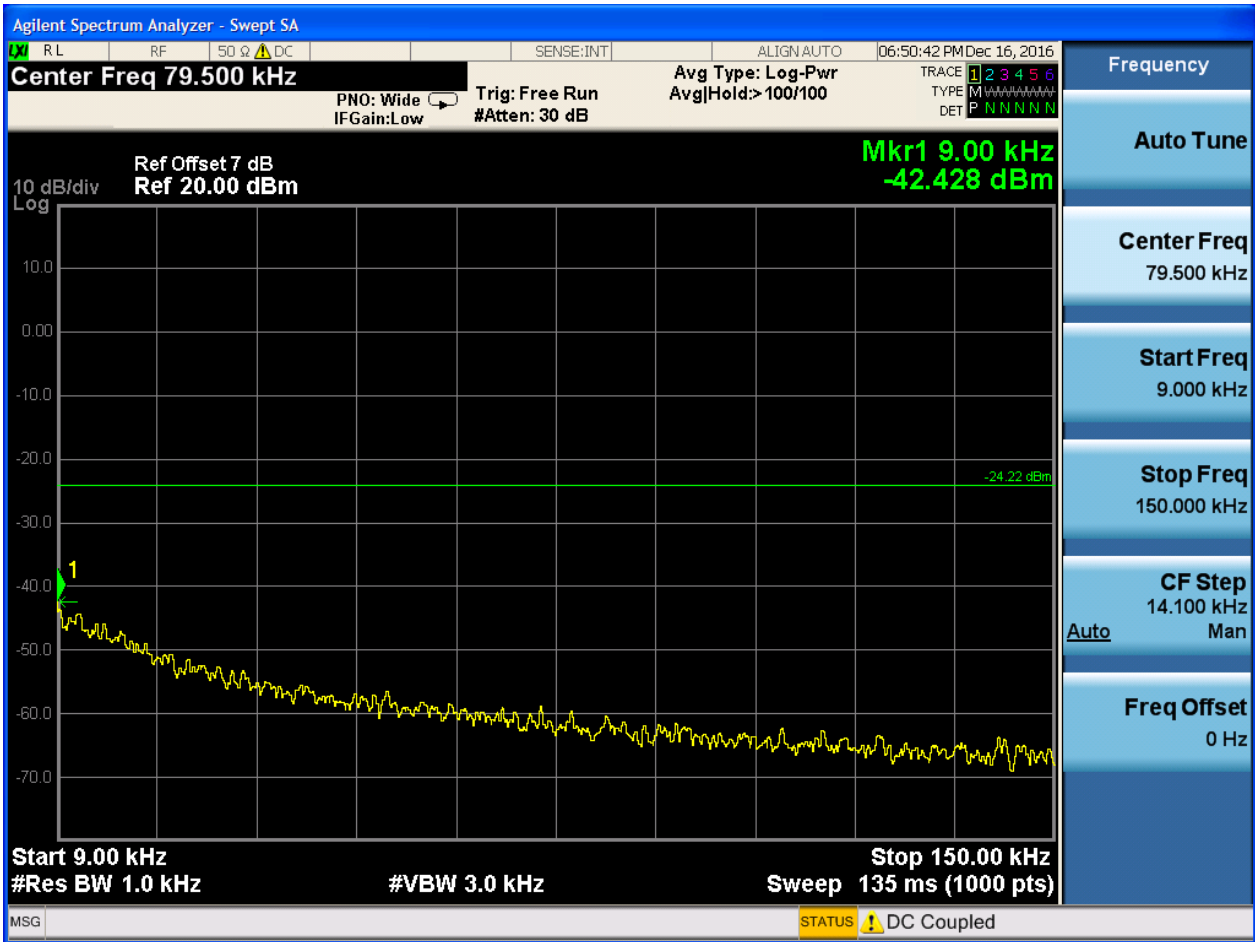
2.2 TM1_DH5_Ch39

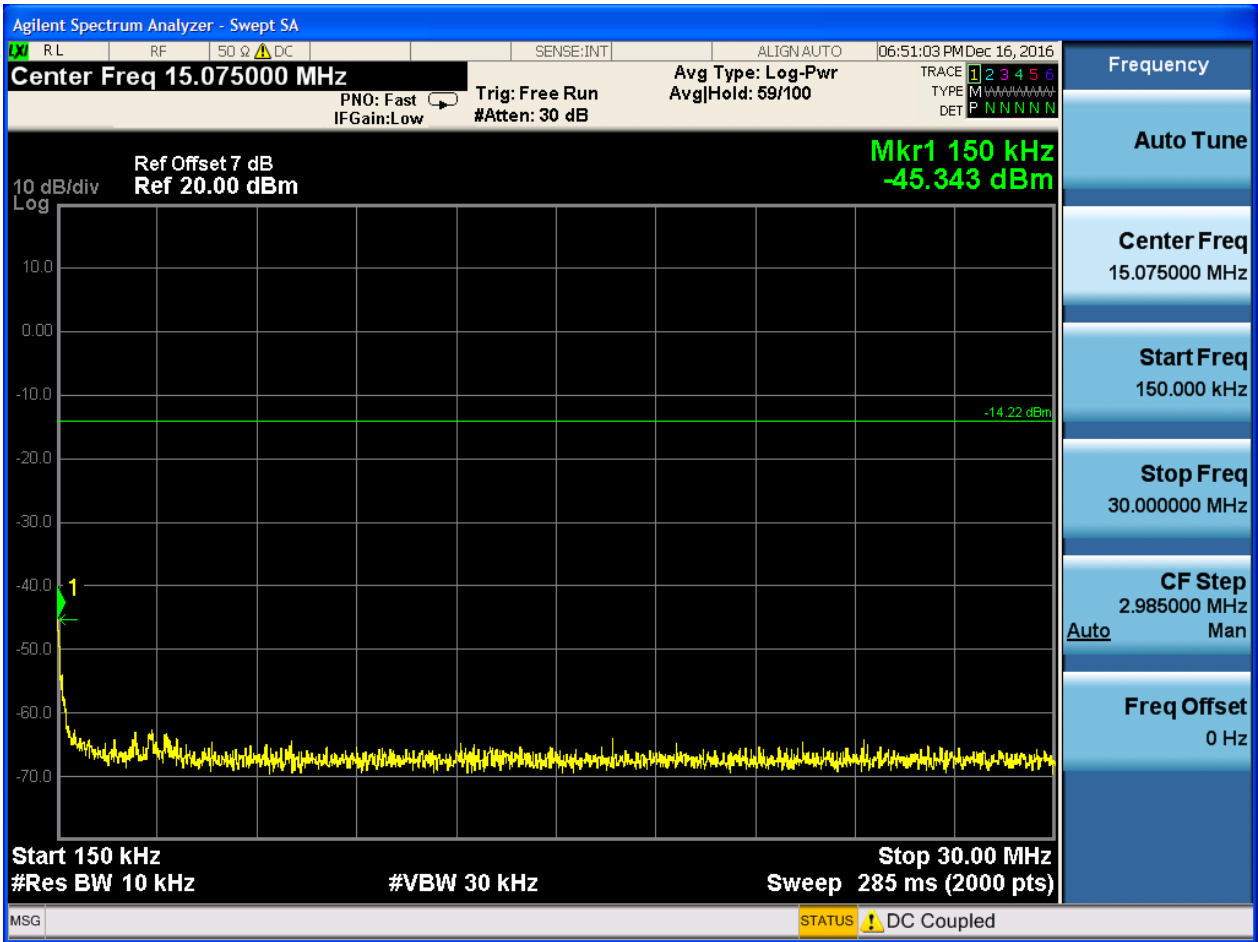
2.2.1 Pref

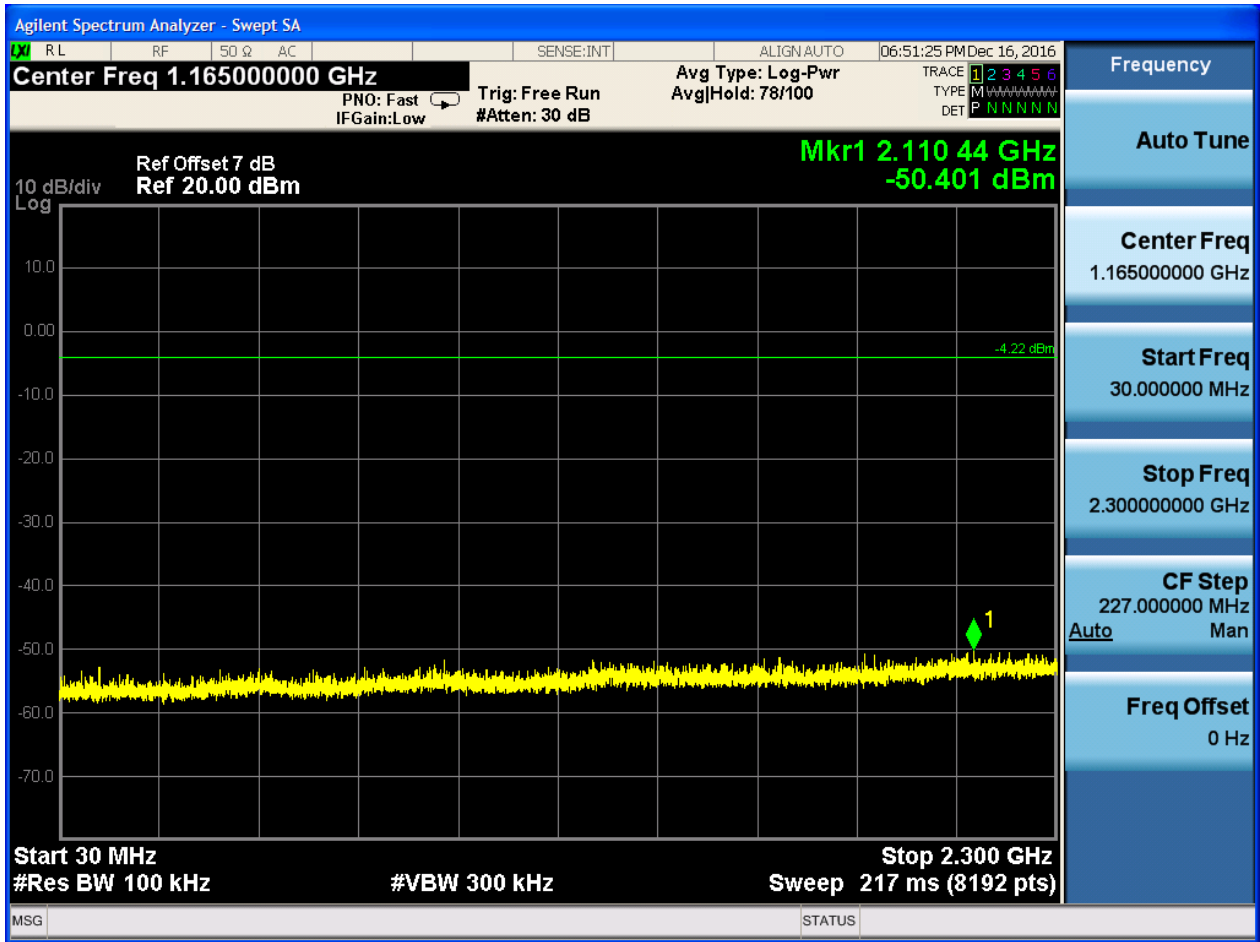


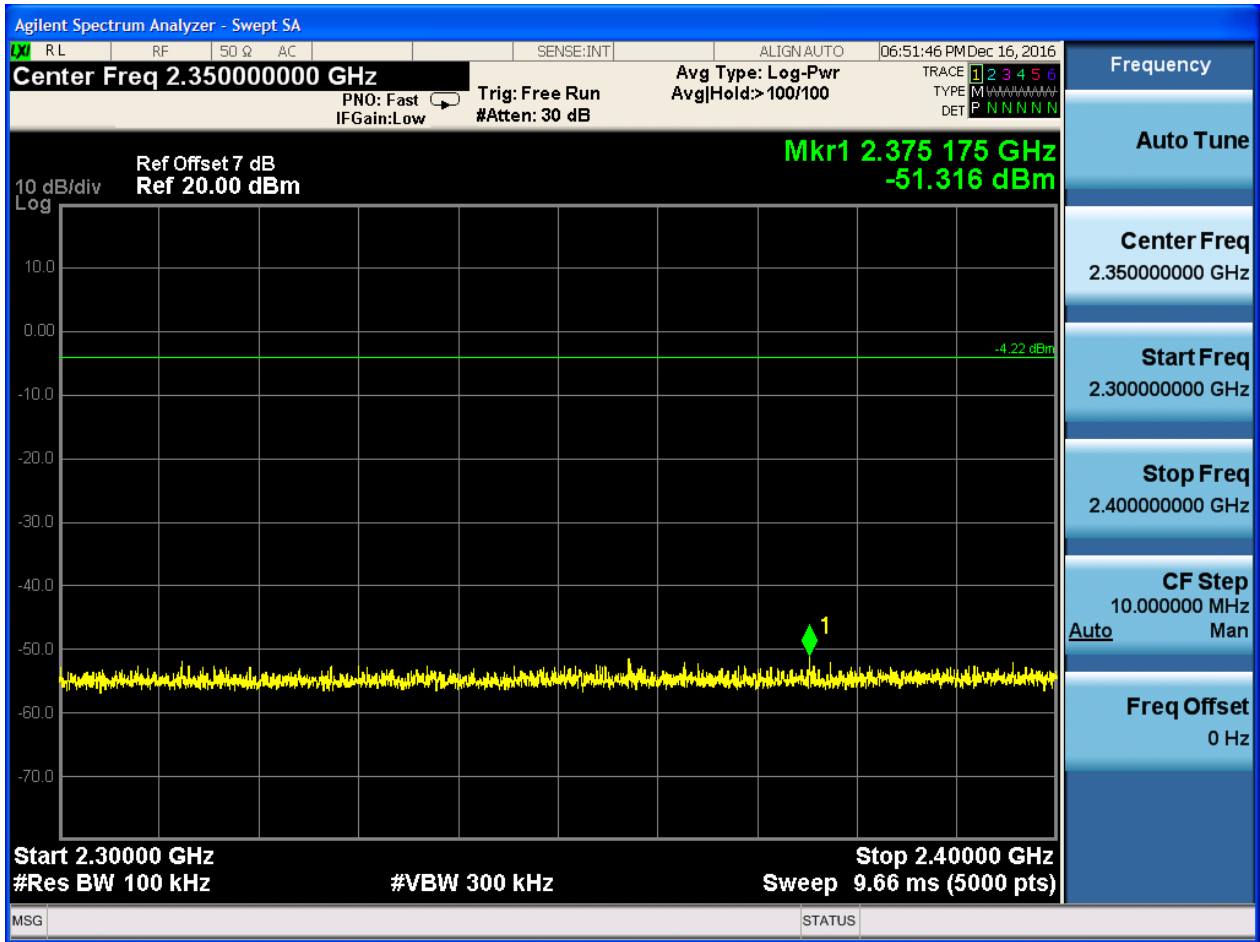


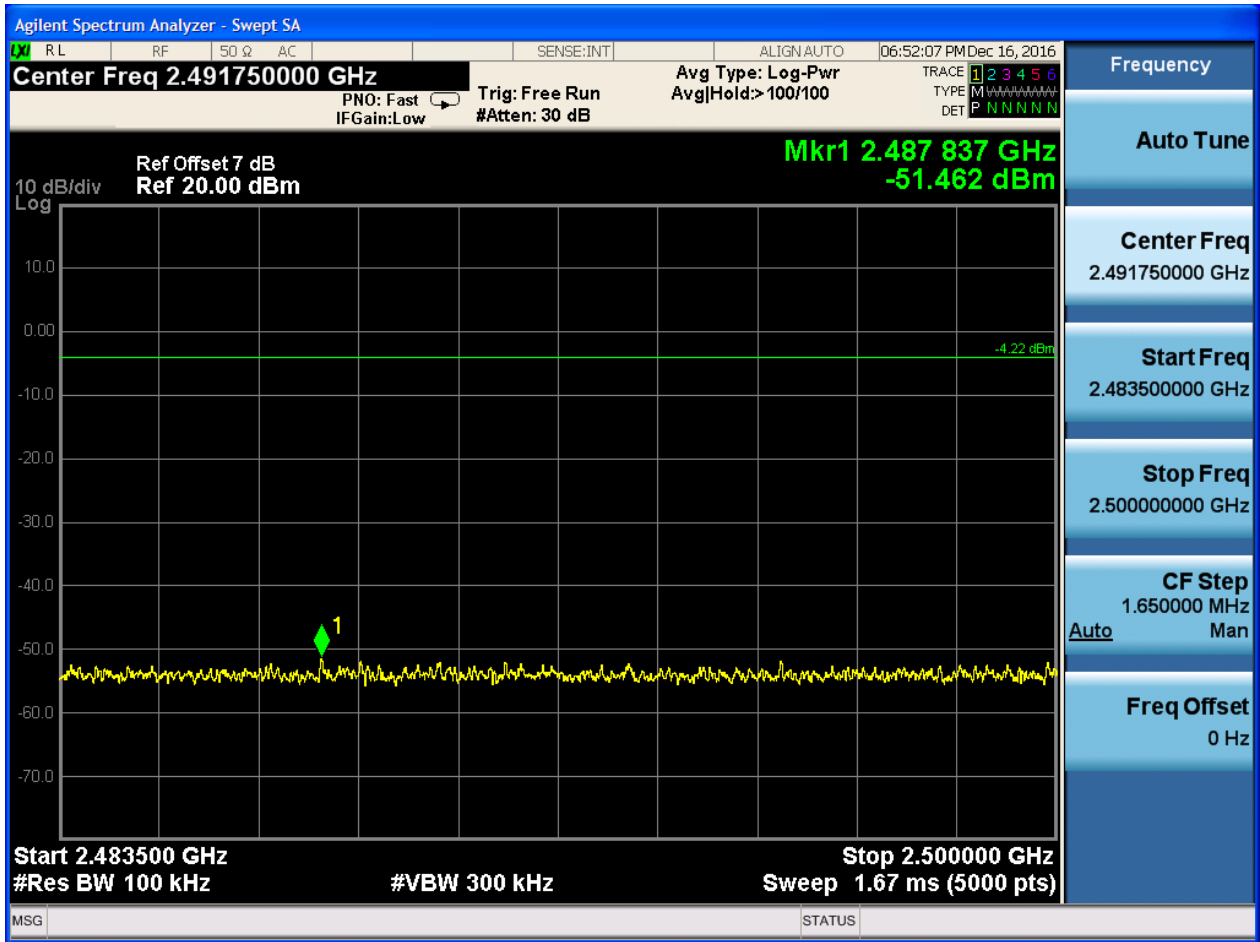
2.2.2 Puw

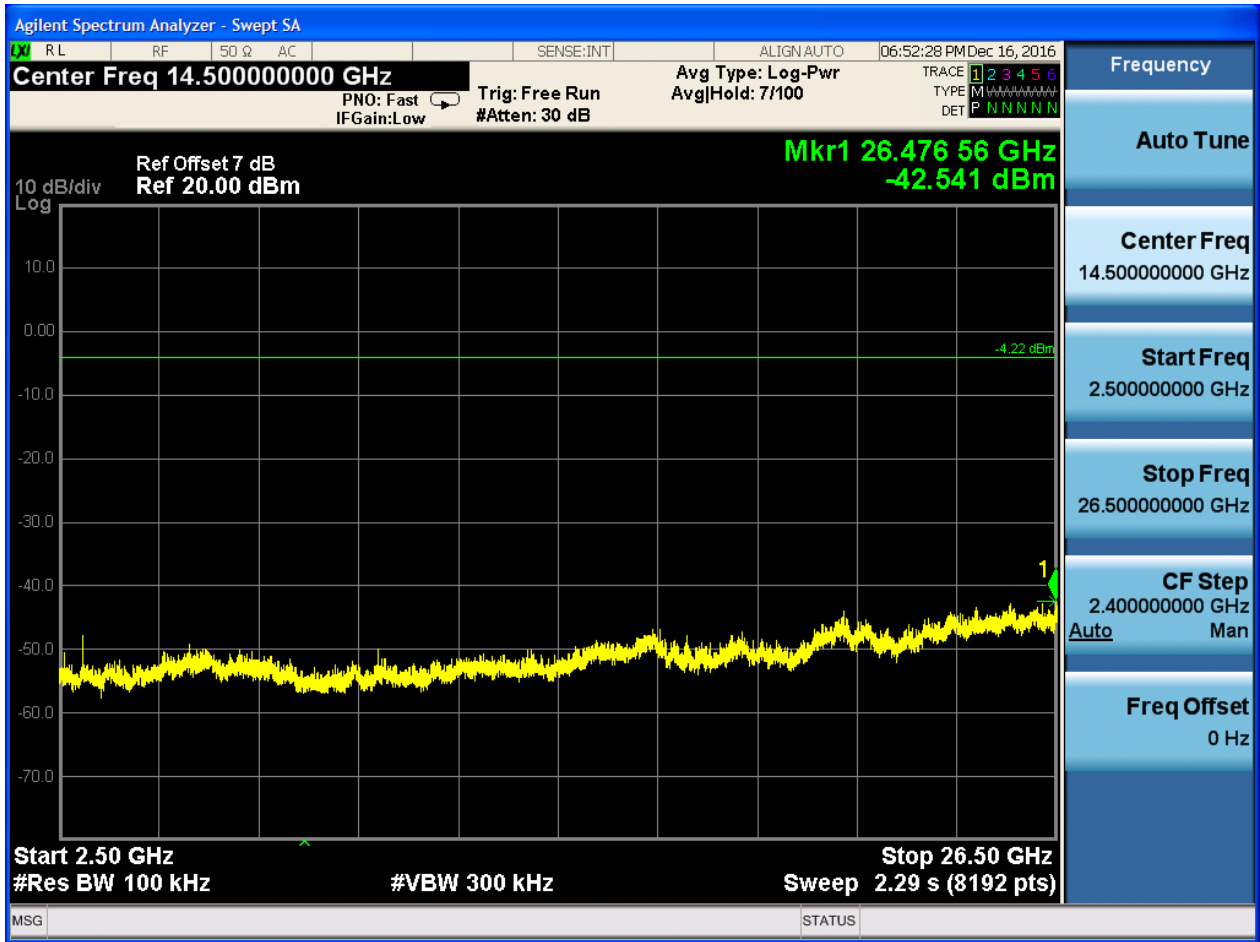








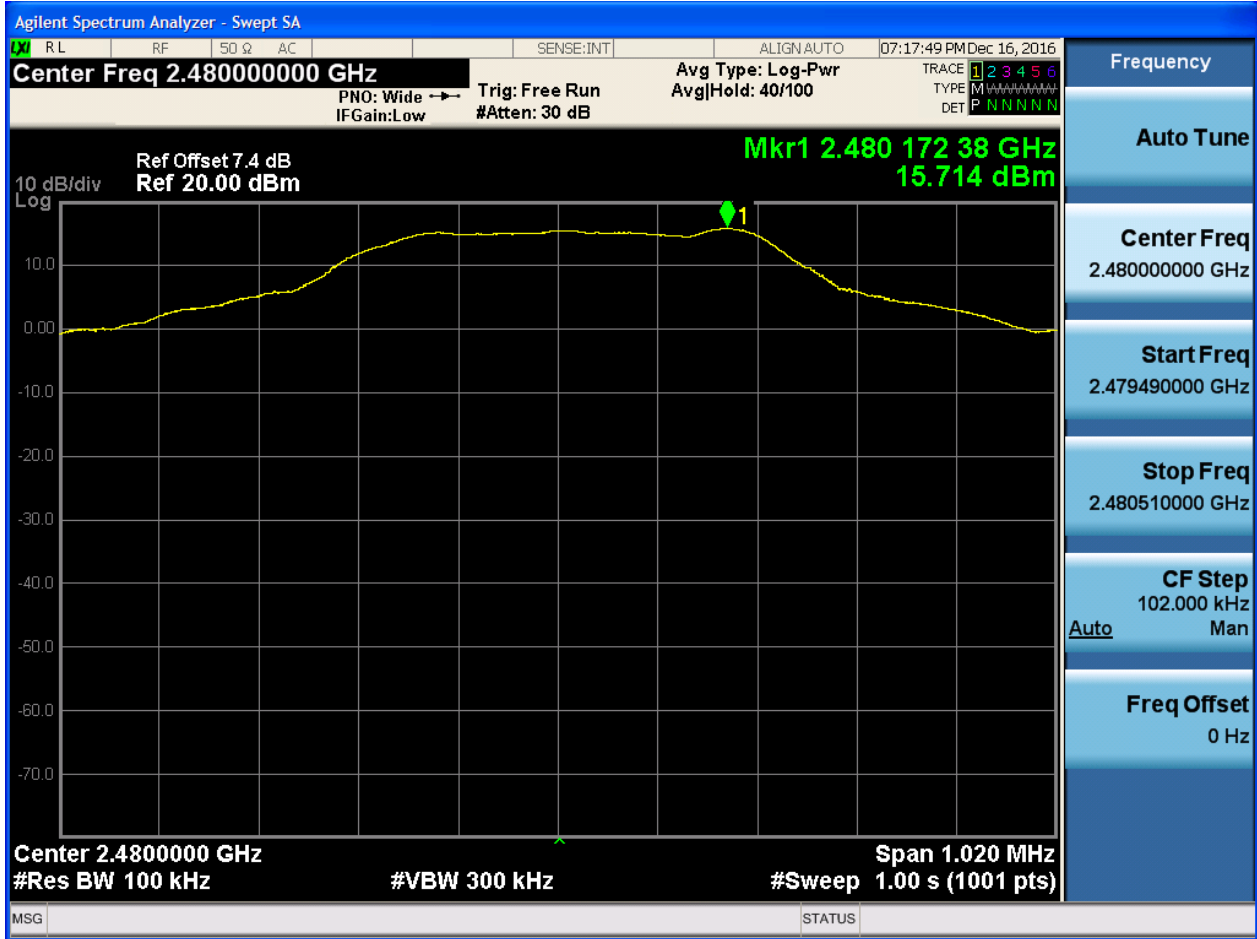






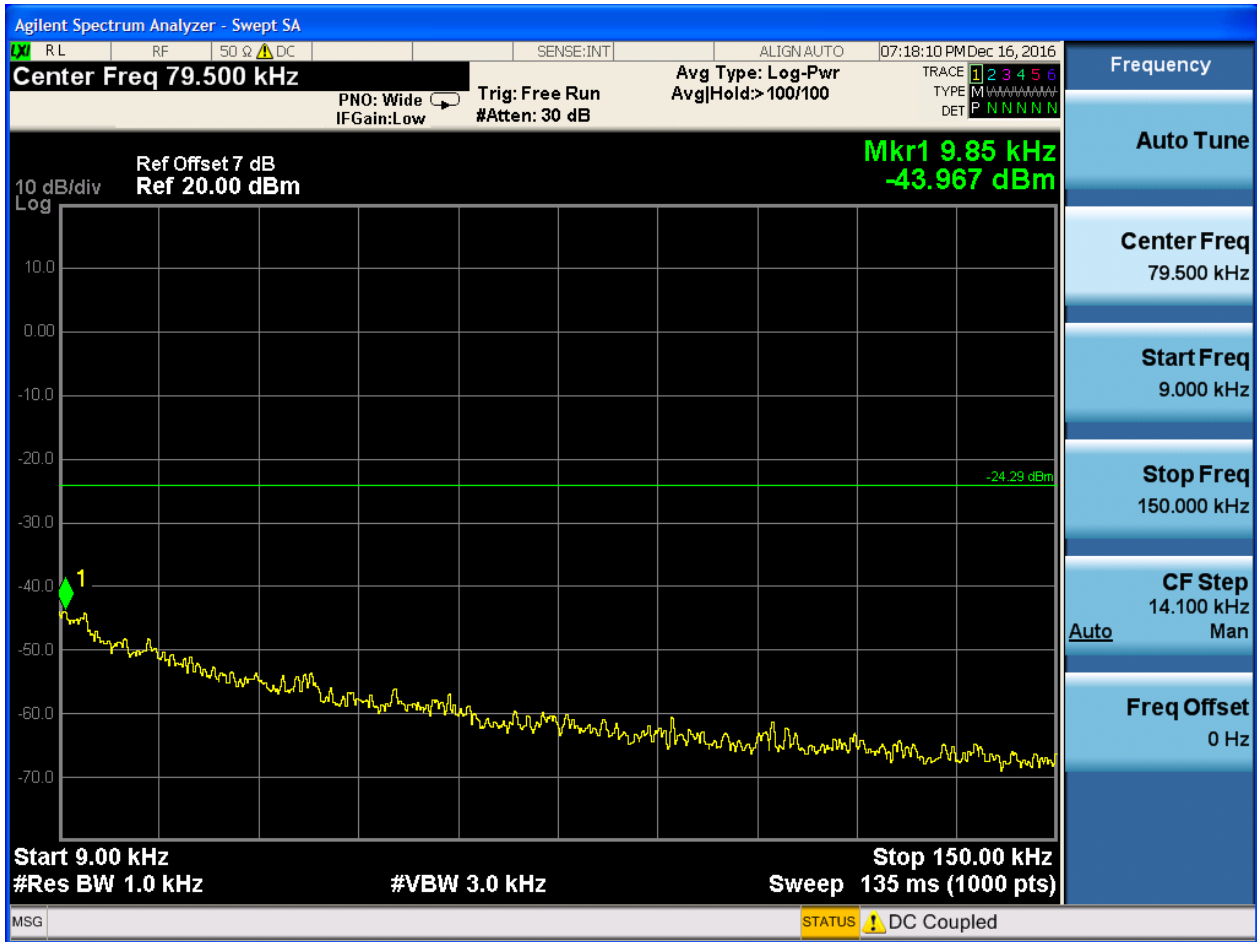
2.3 TM1_DH5_Ch78

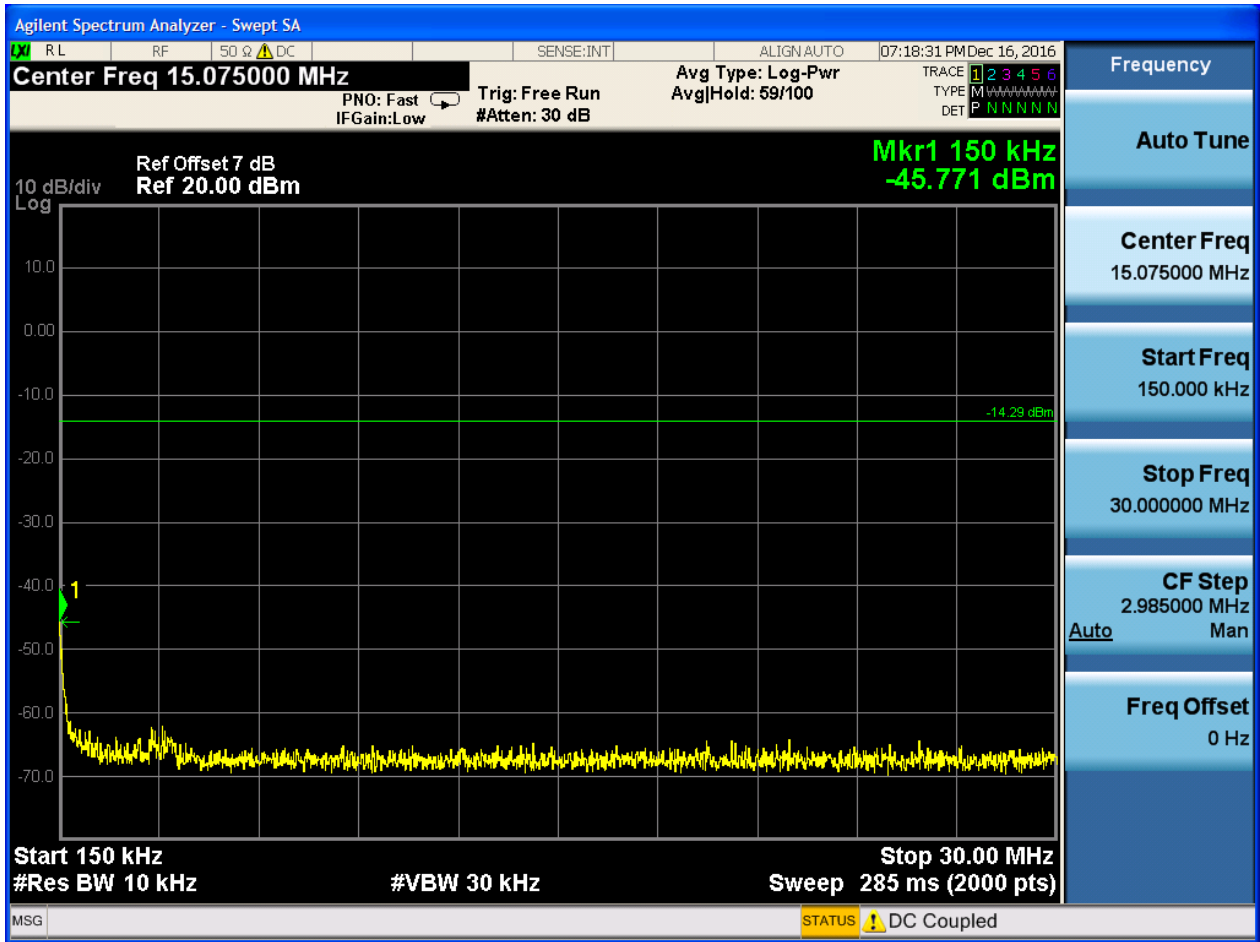
2.3.1 Pref

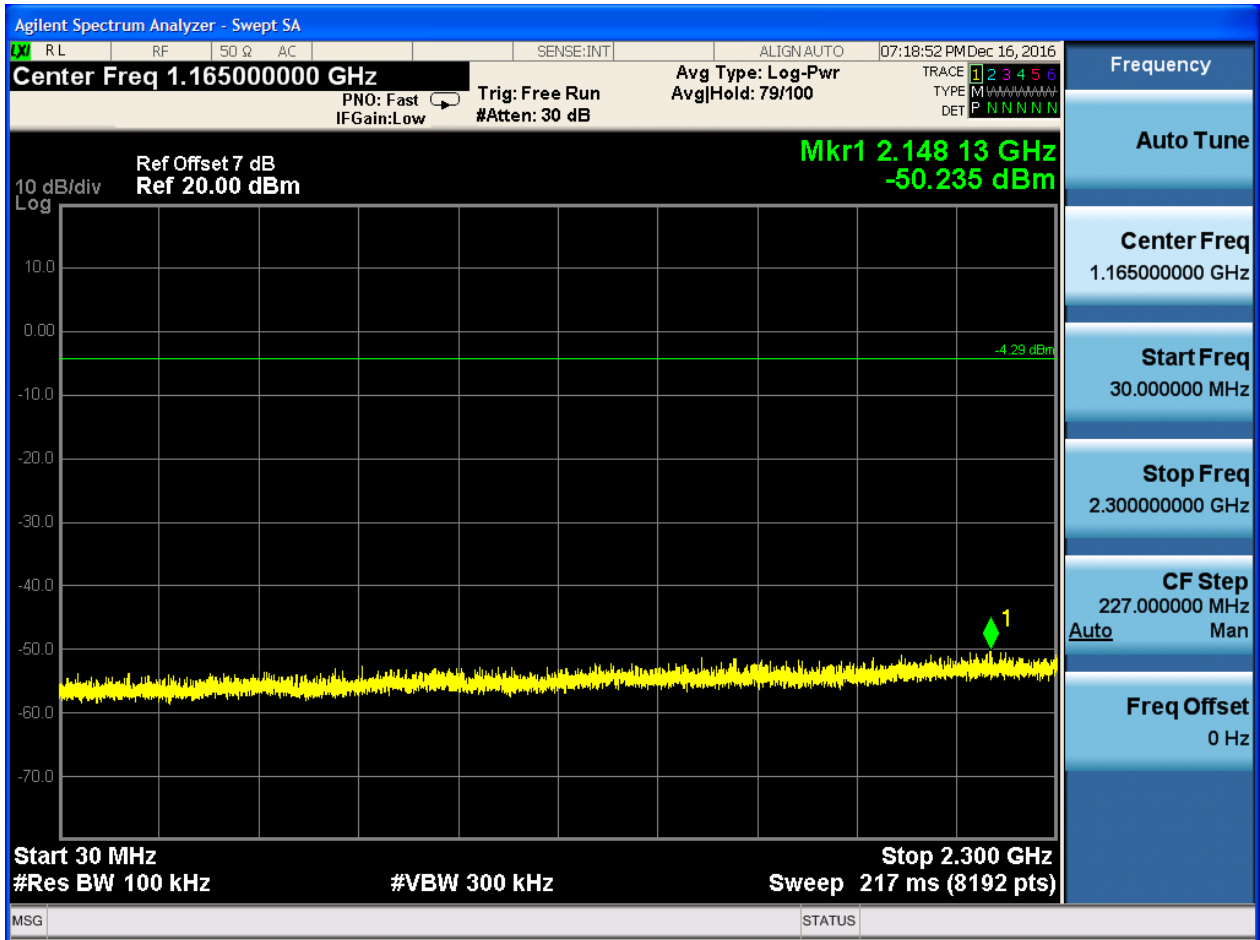


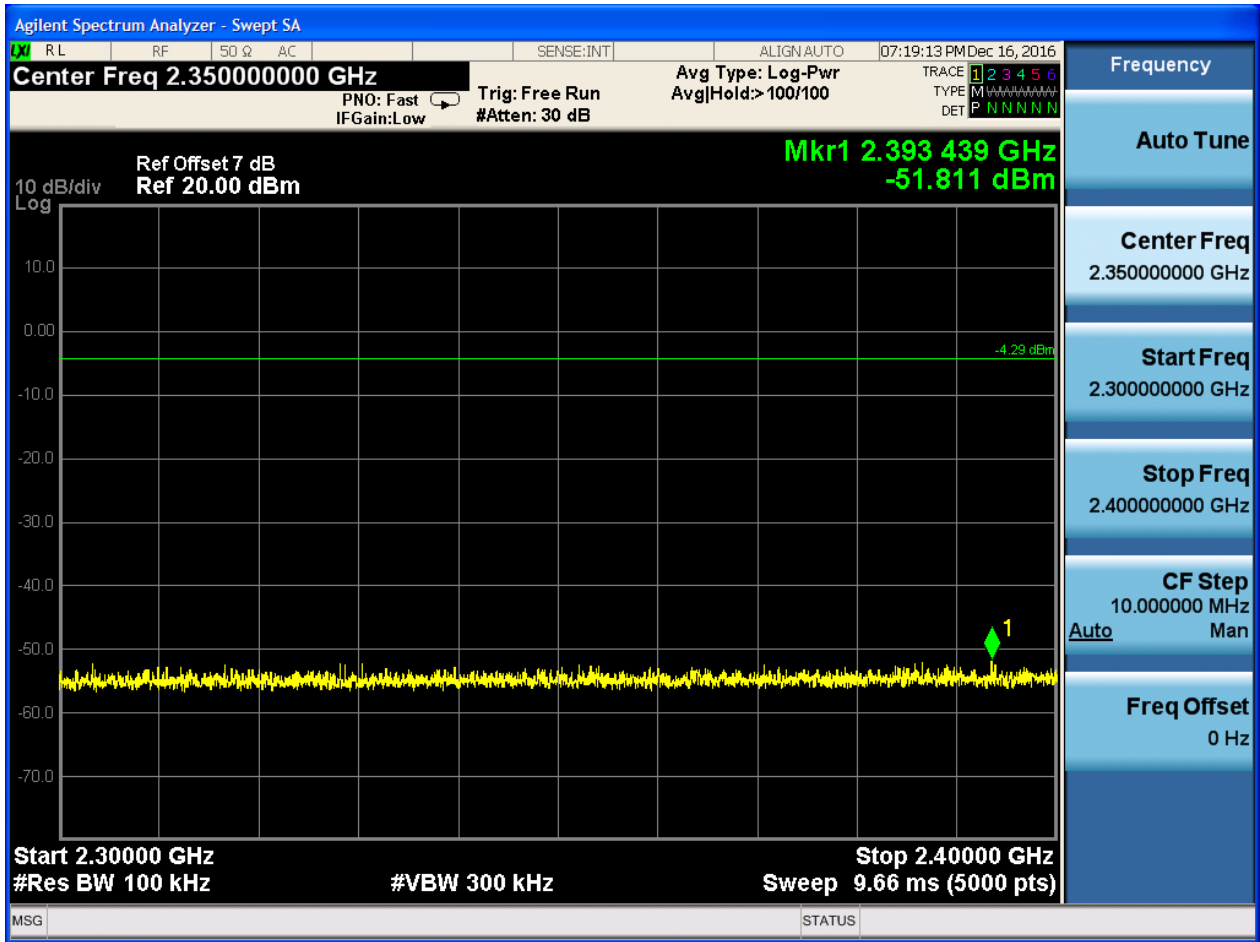


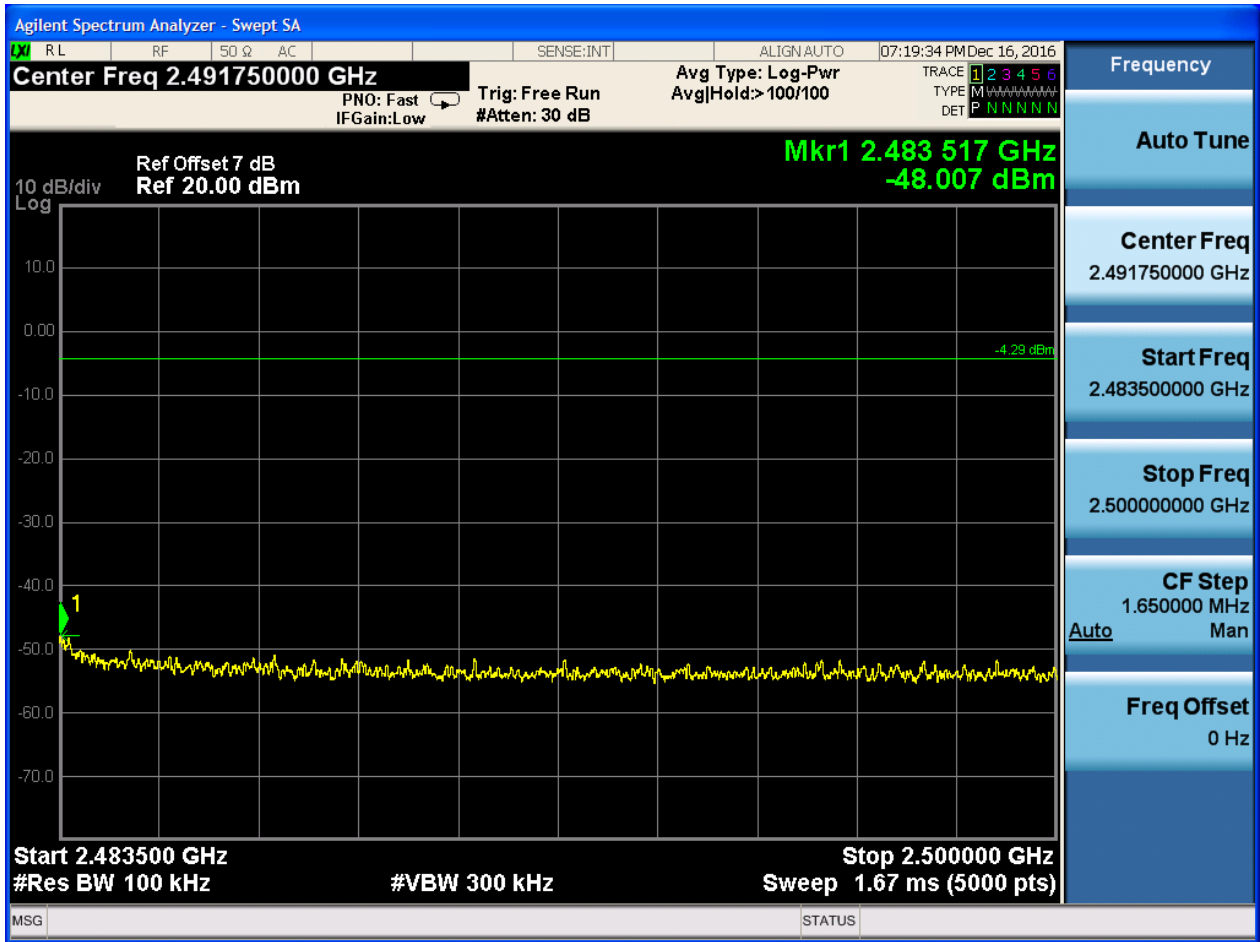
2.3.2 Puw

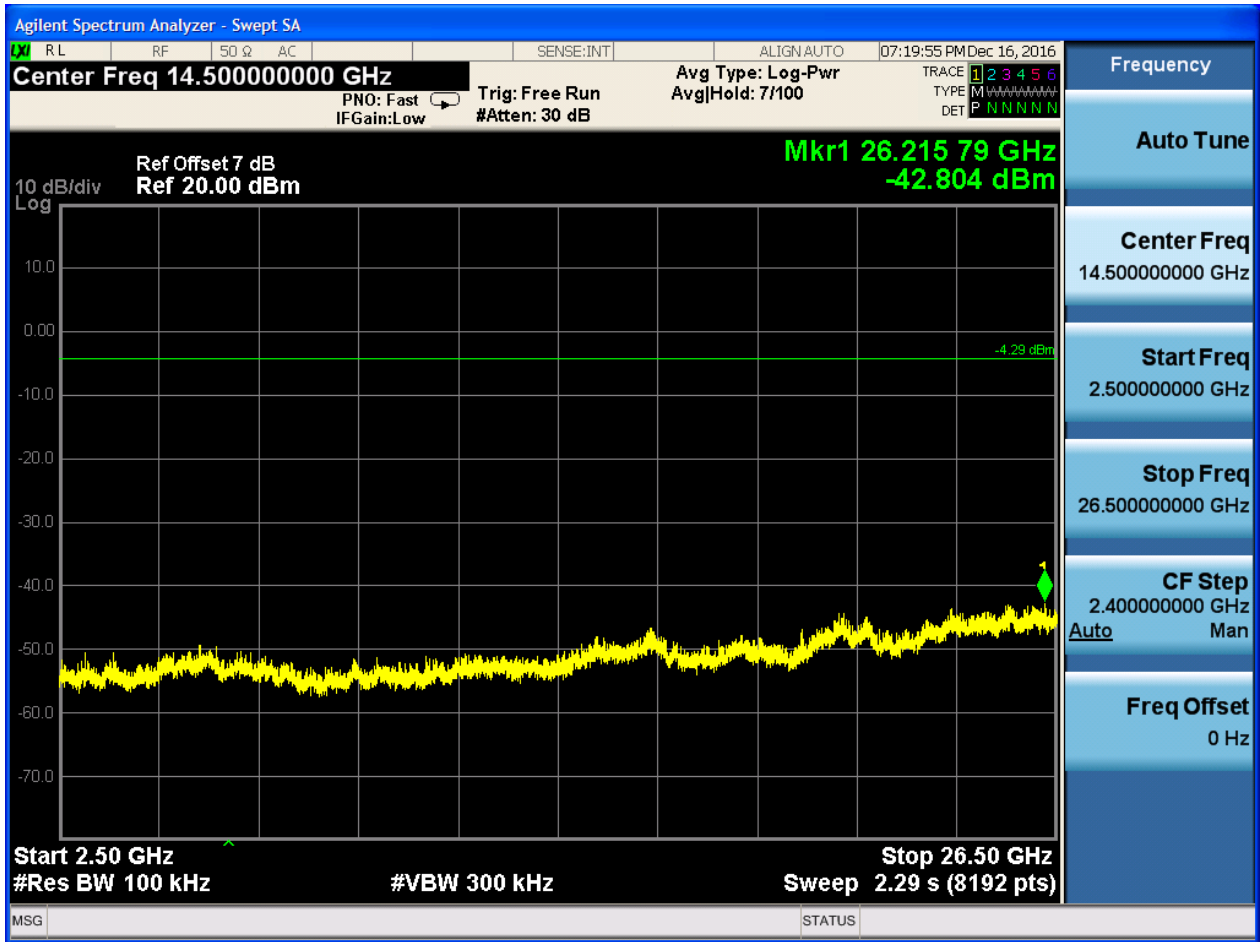














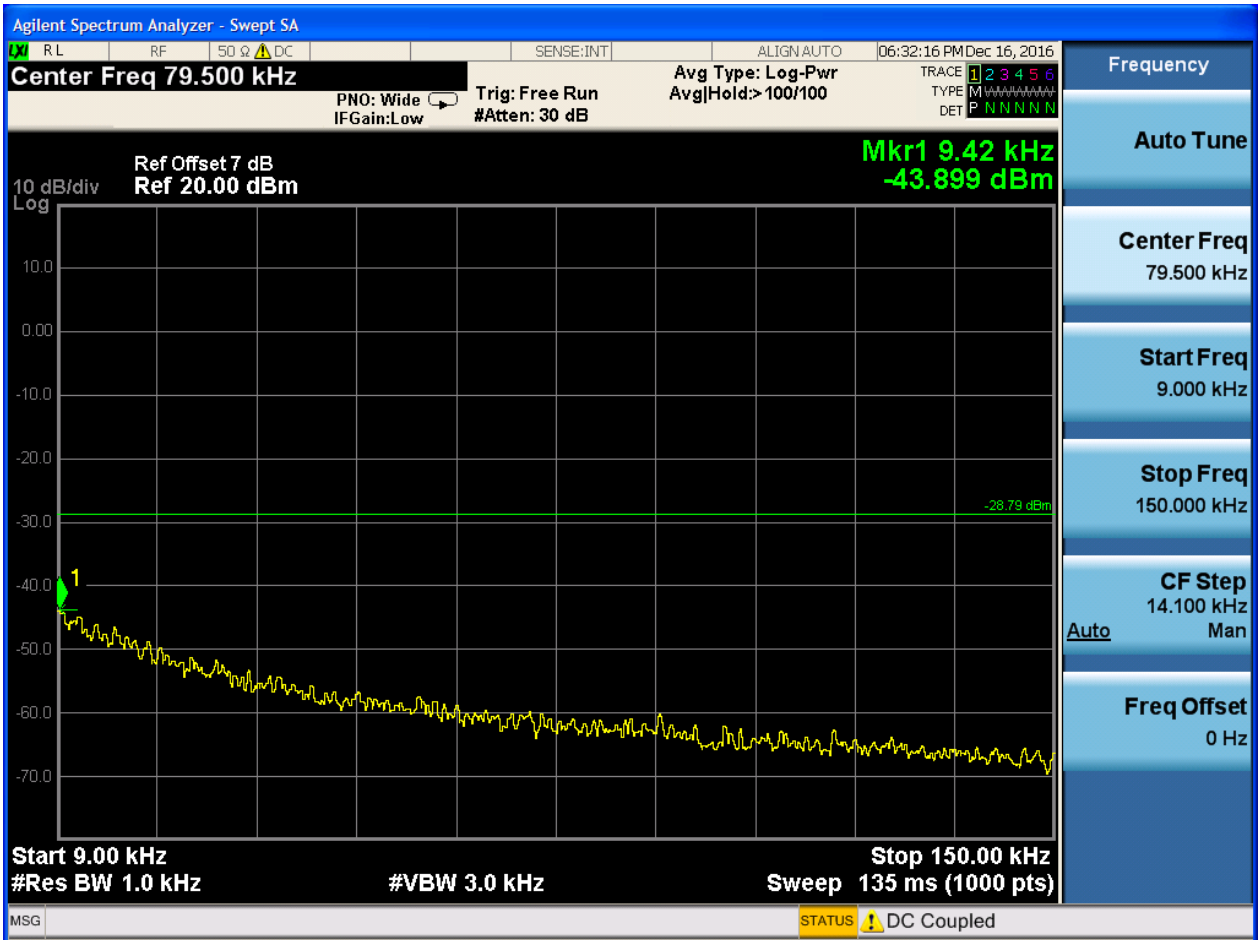
2.4 TM2_2DH5_Ch0

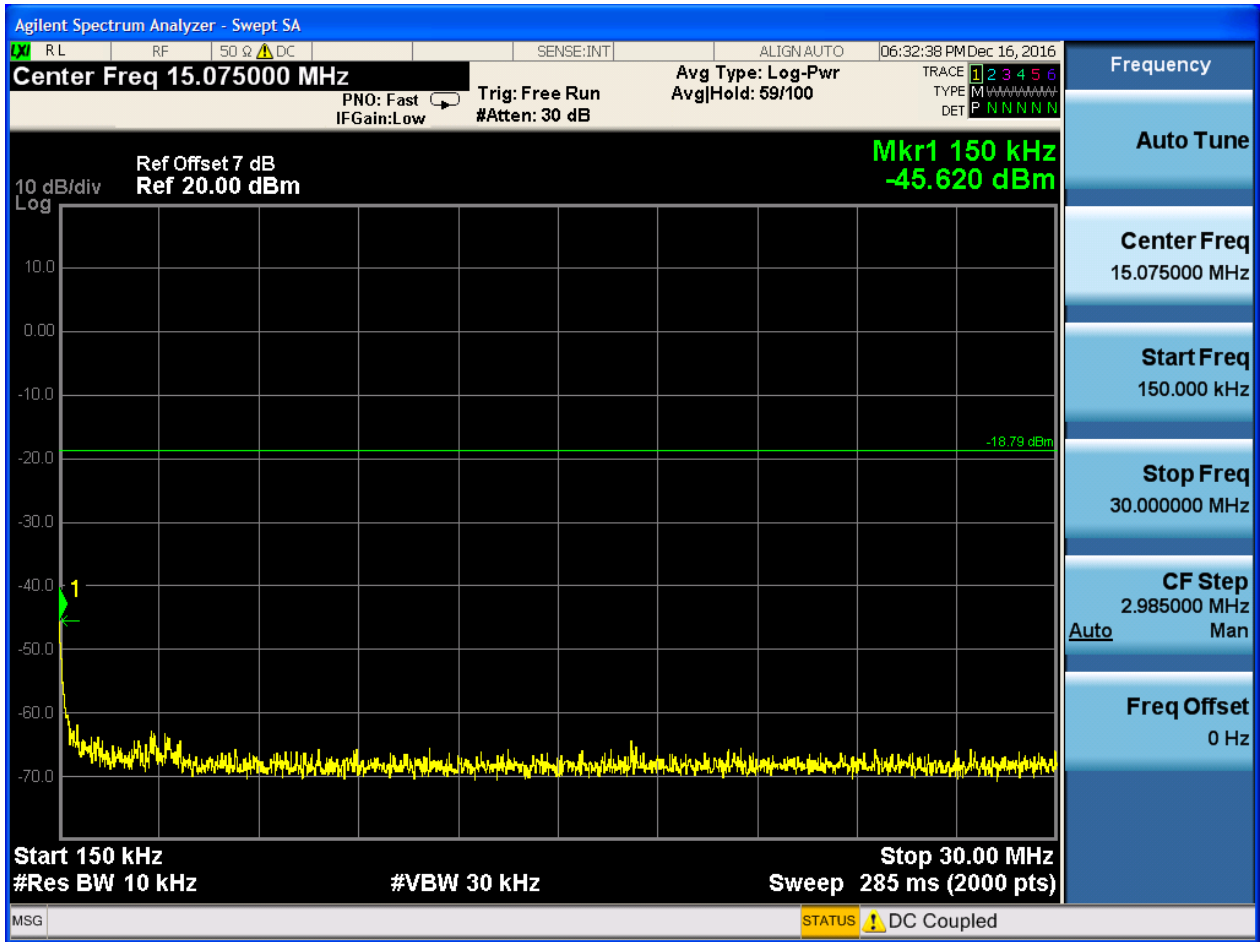
2.4.1 Pref

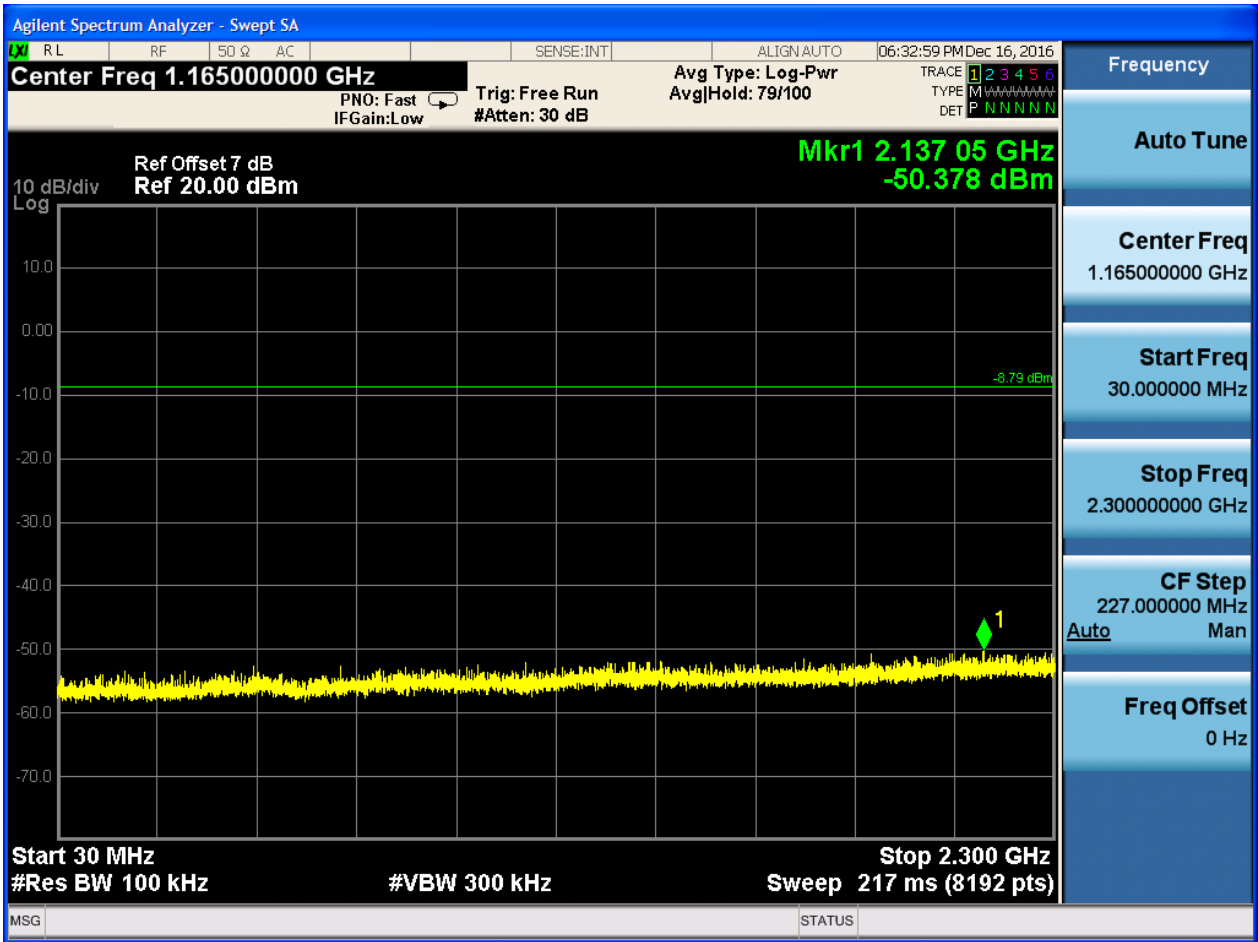


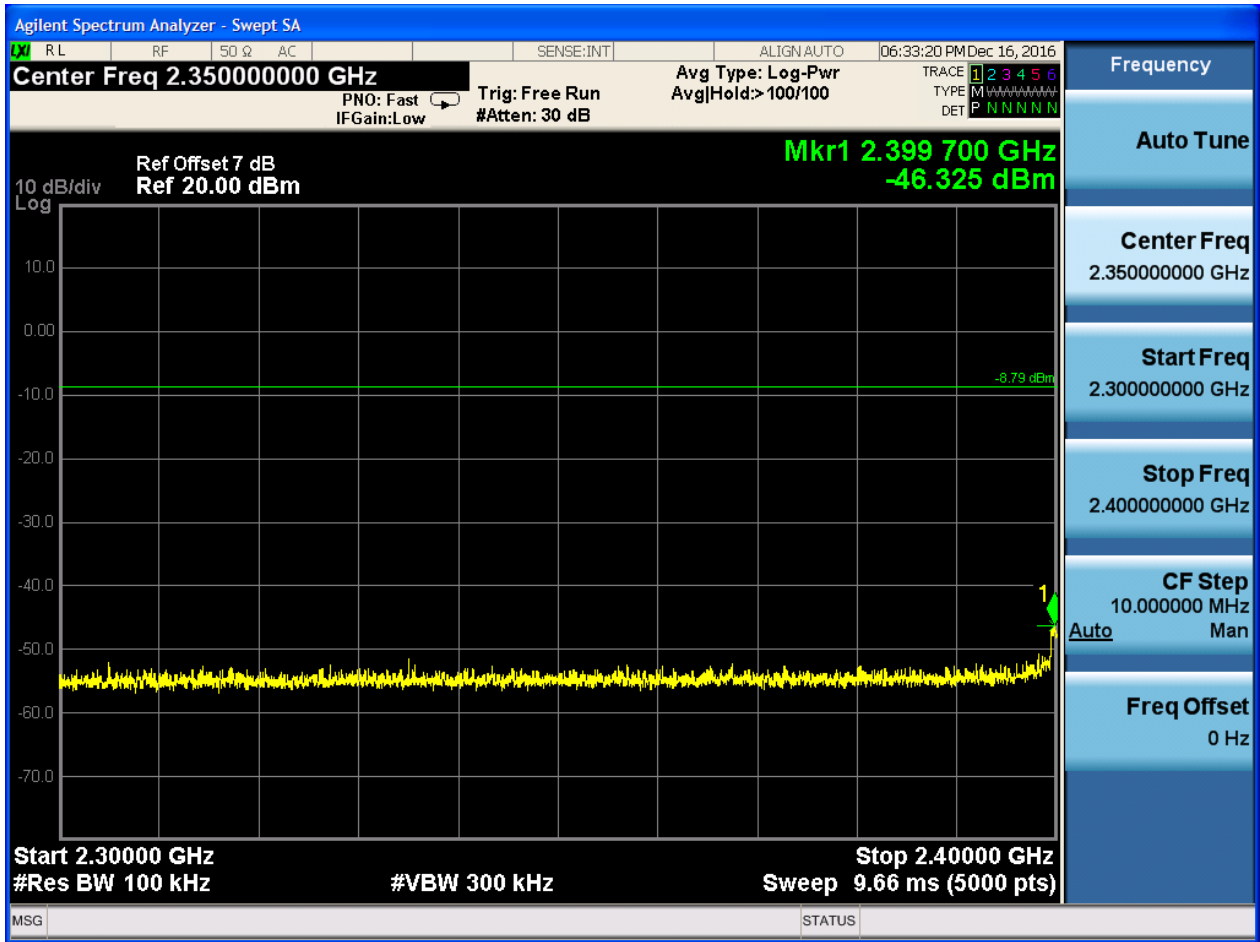


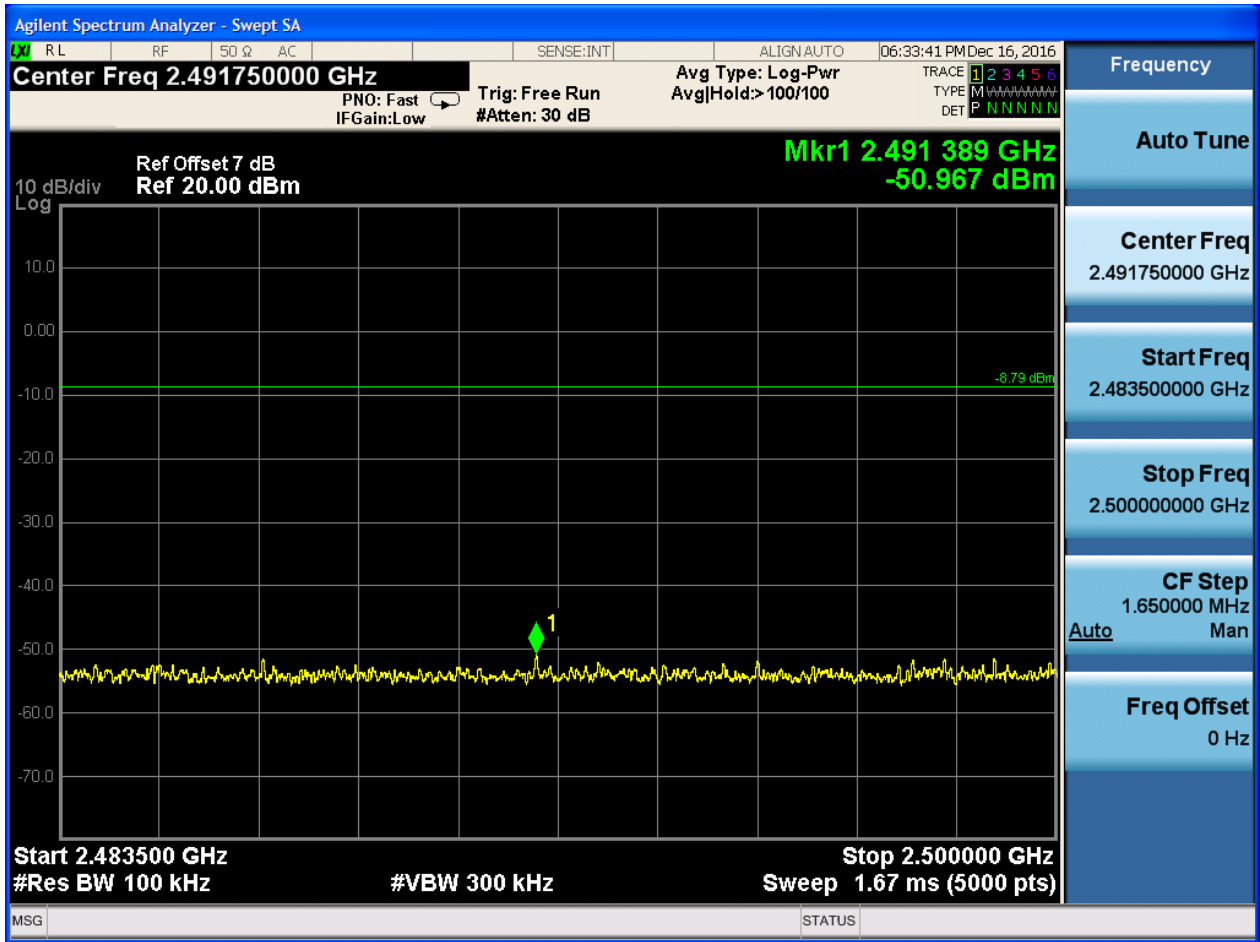
2.4.2 Puw

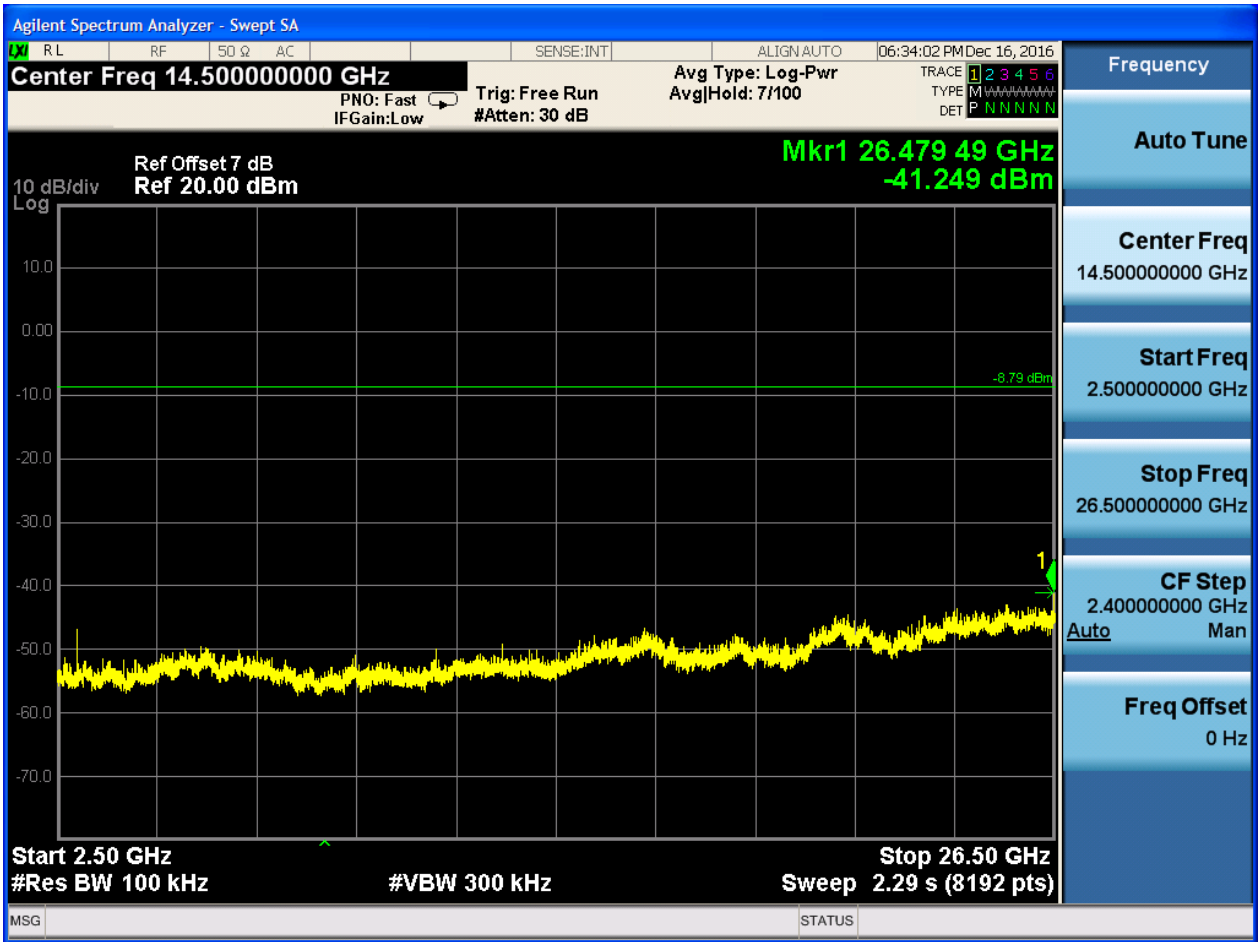








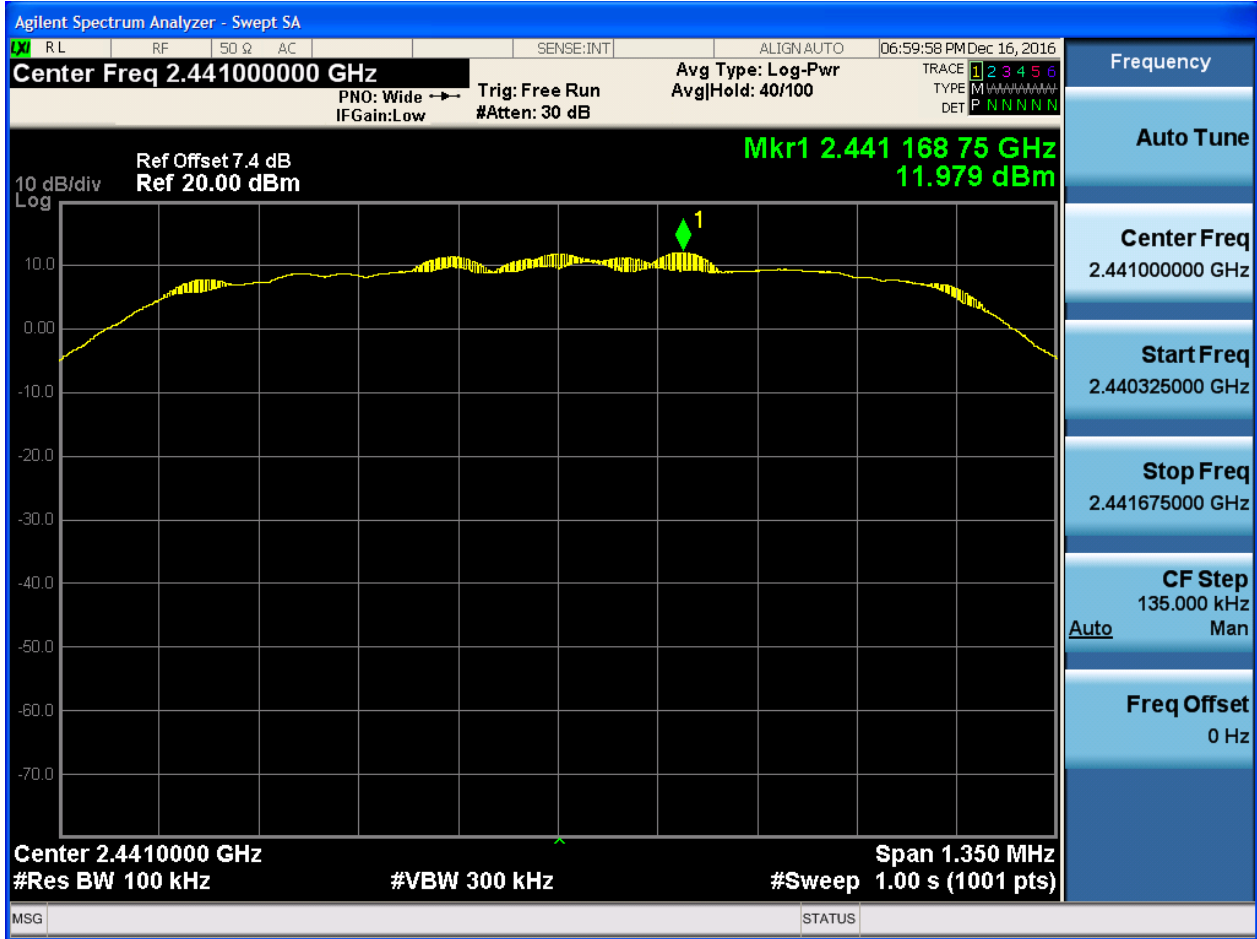






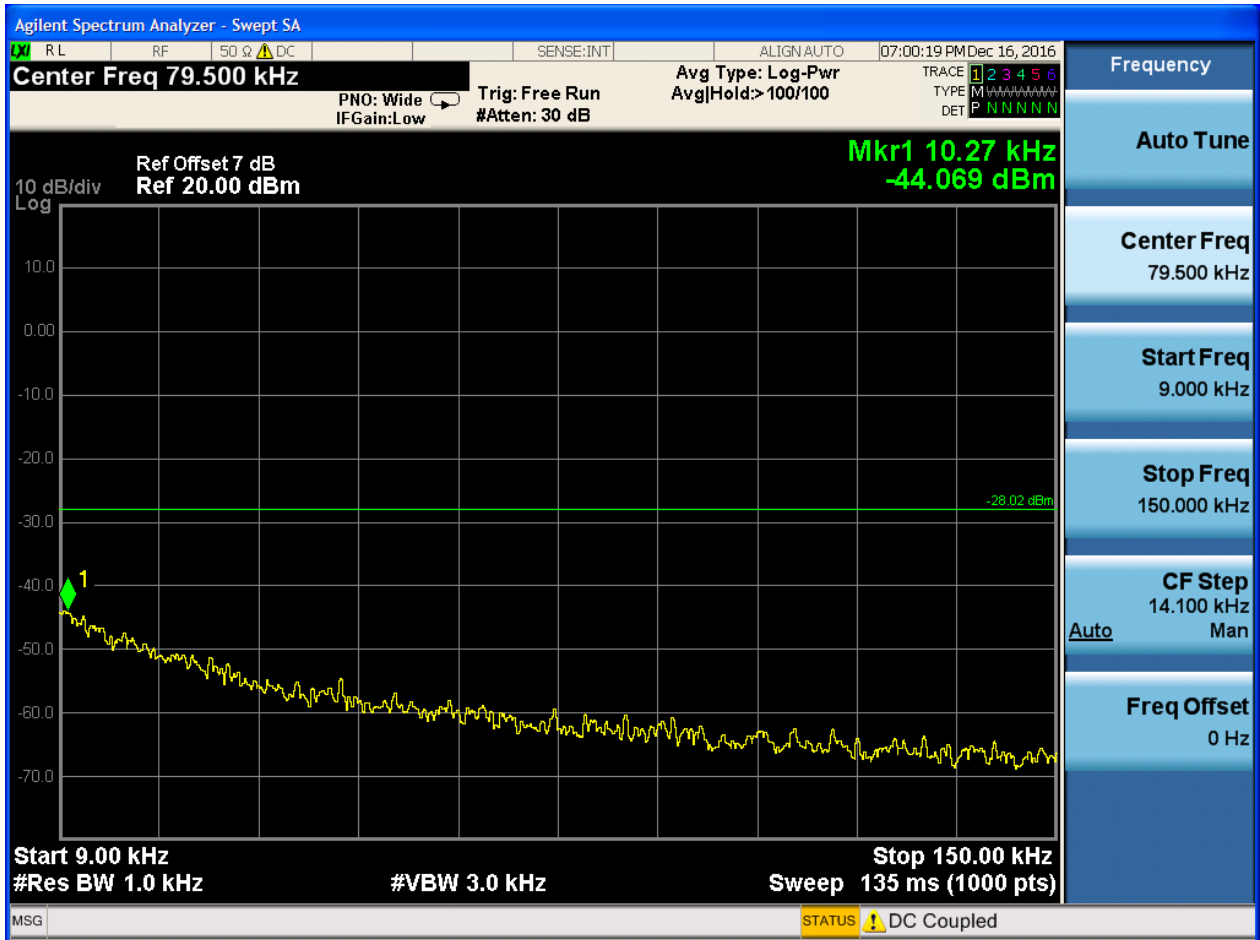
2.5 TM2_2DH5_Ch39

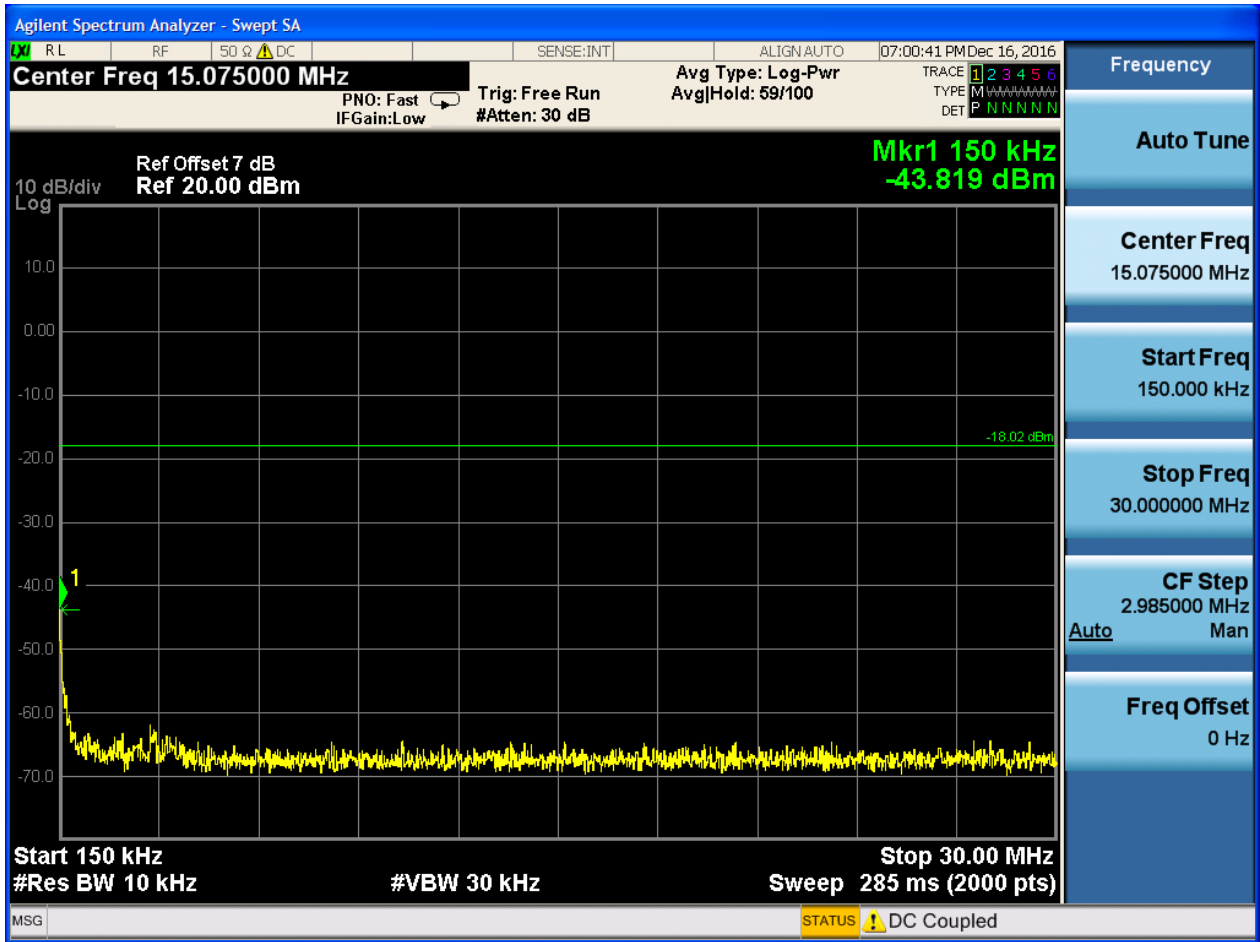
2.5.1 Pref

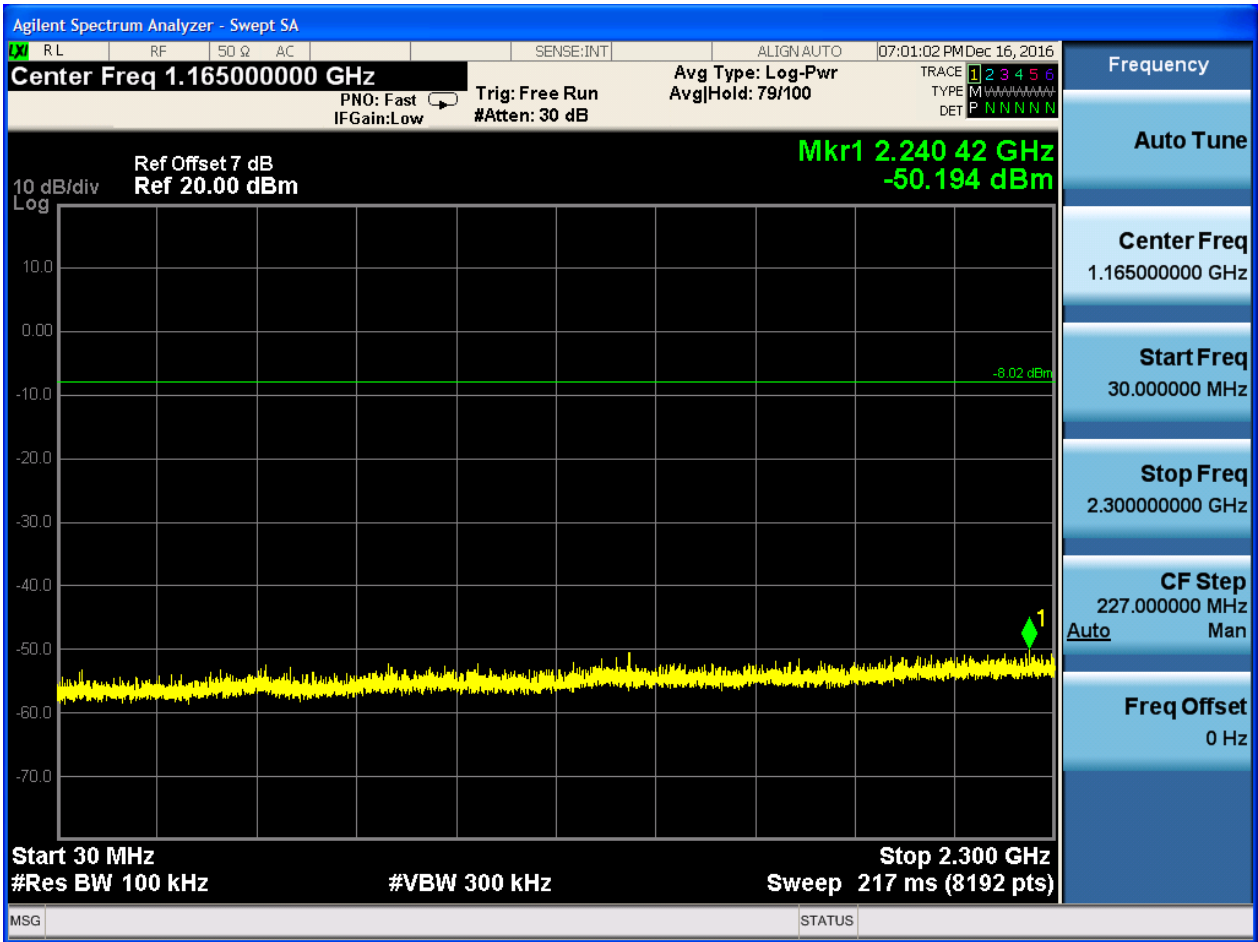


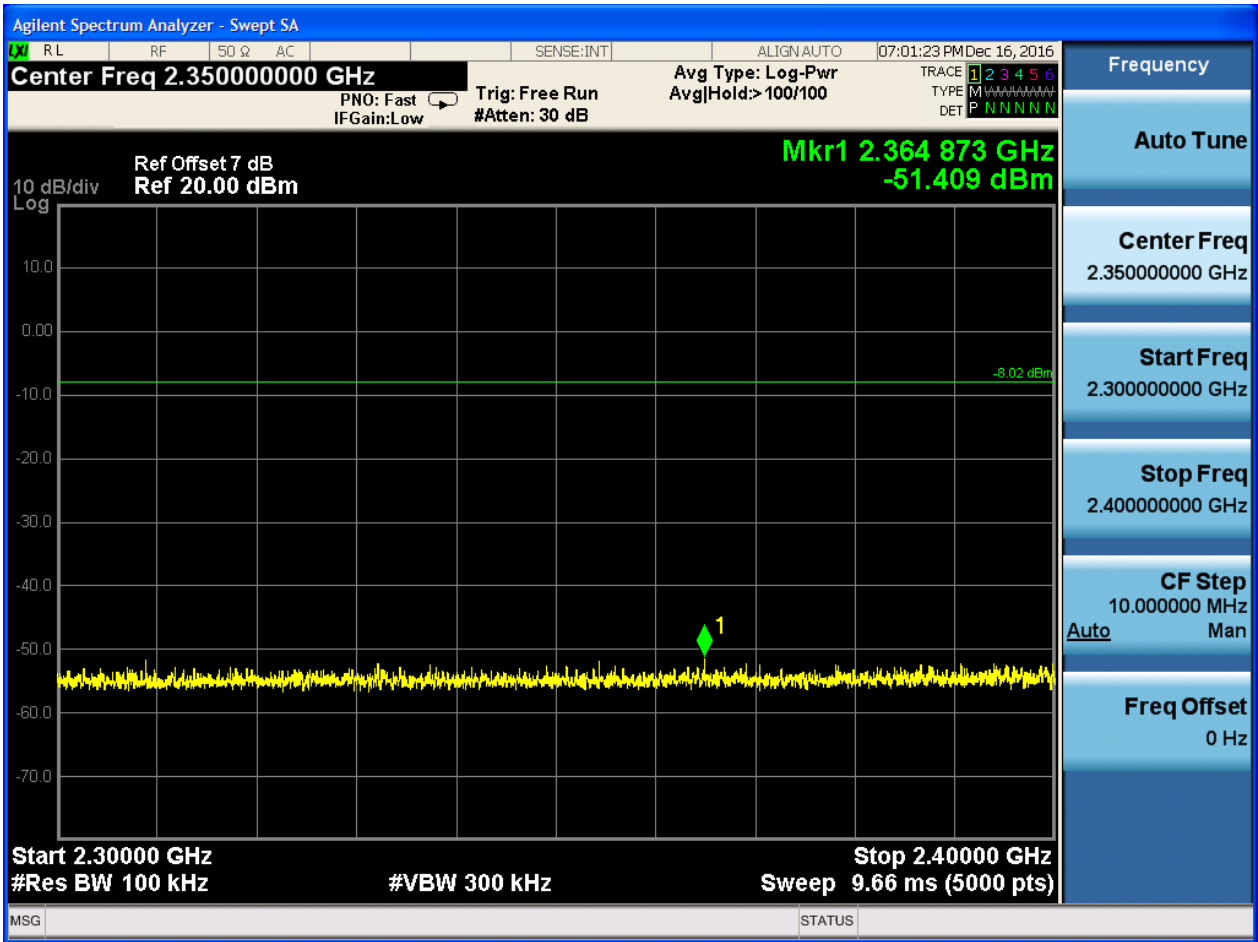


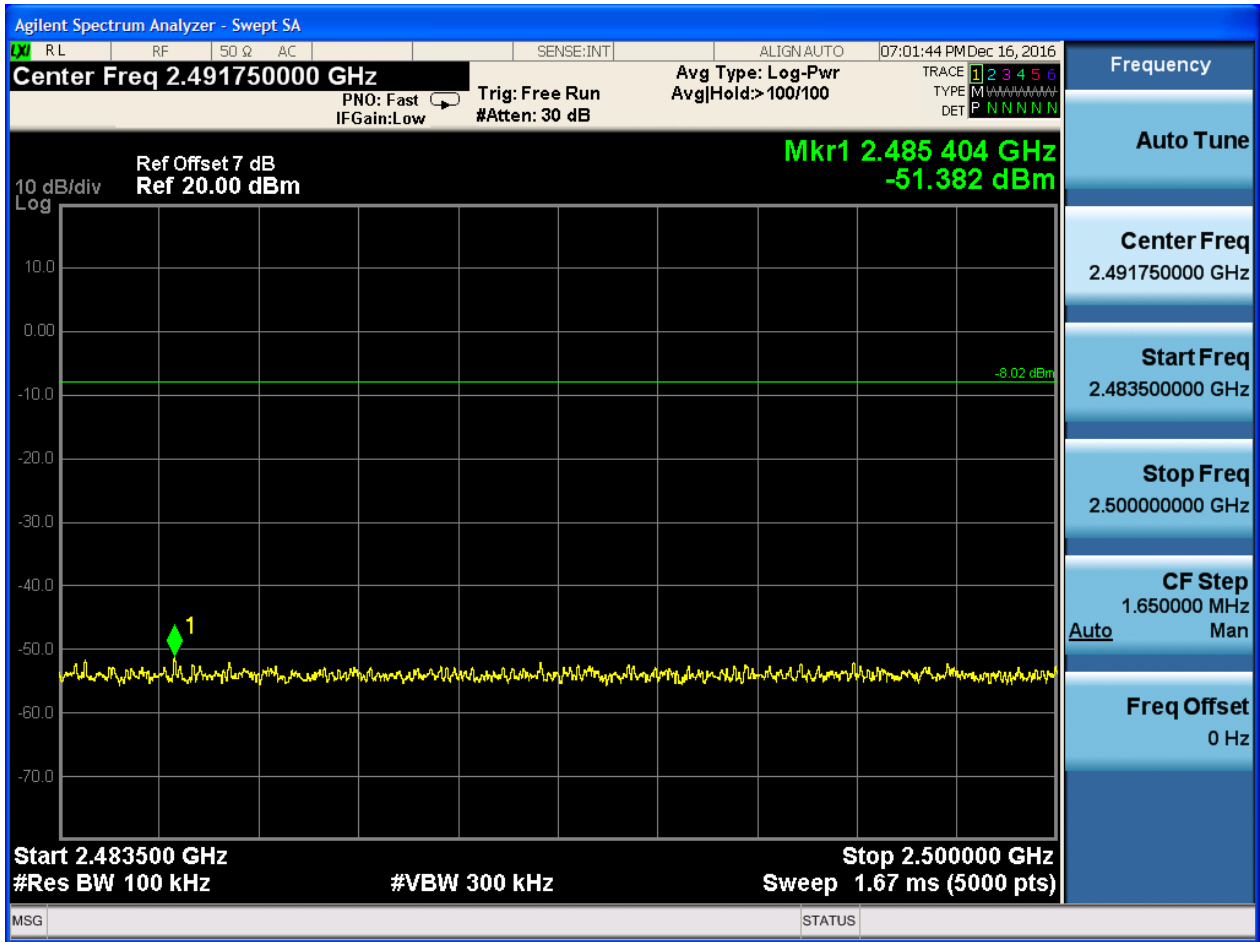
2.5.2 Puw

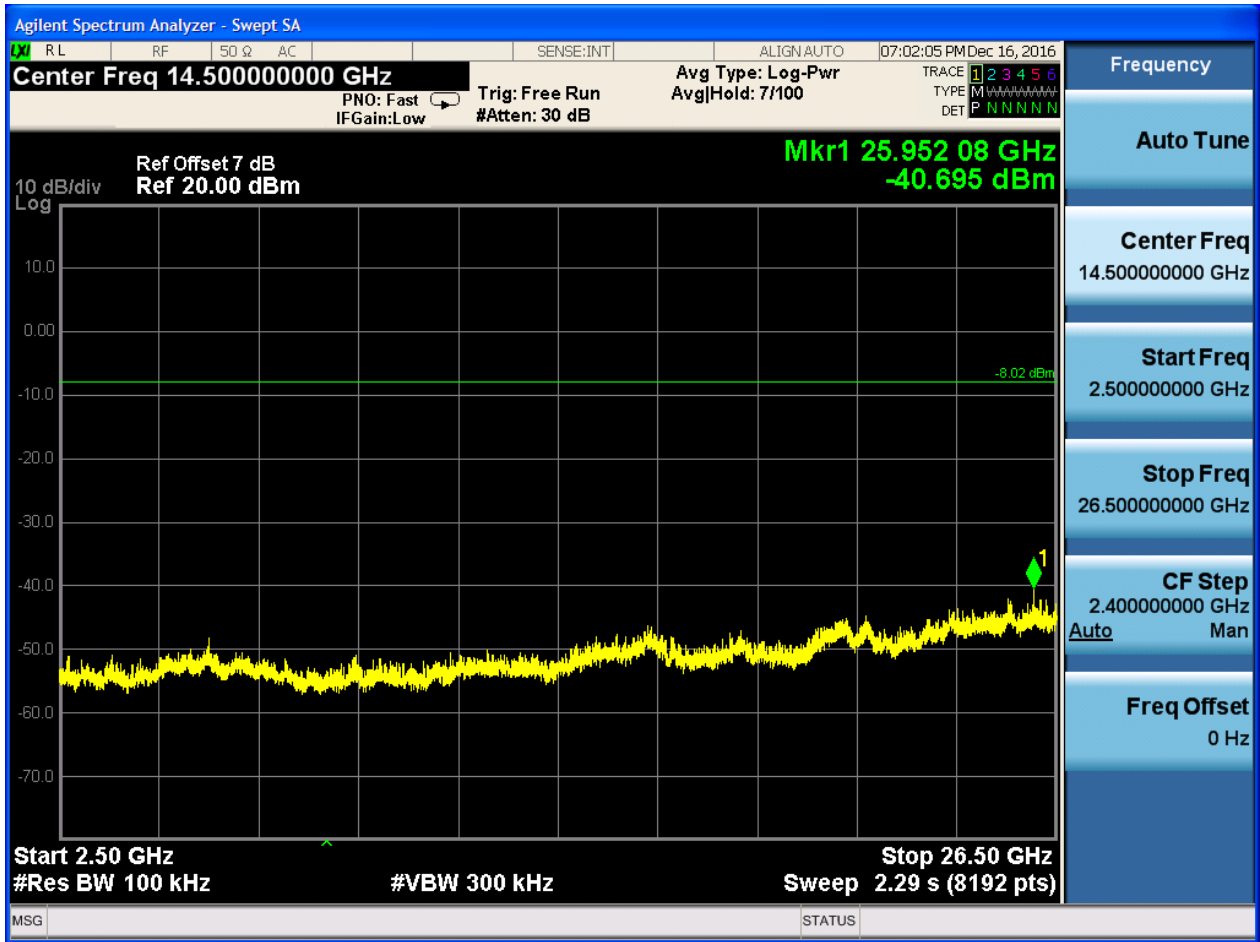














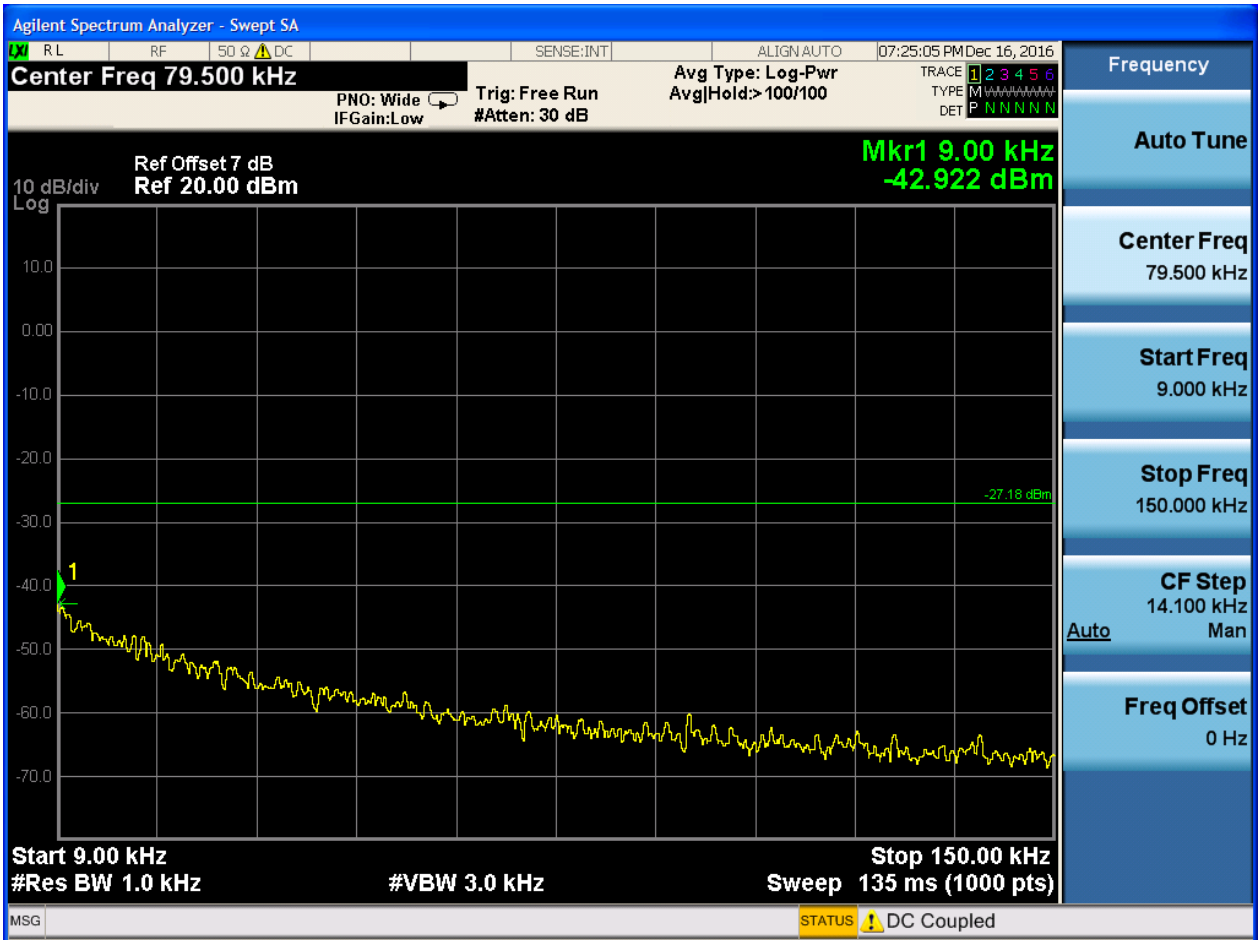
2.6 TM2_2DH5_Ch78

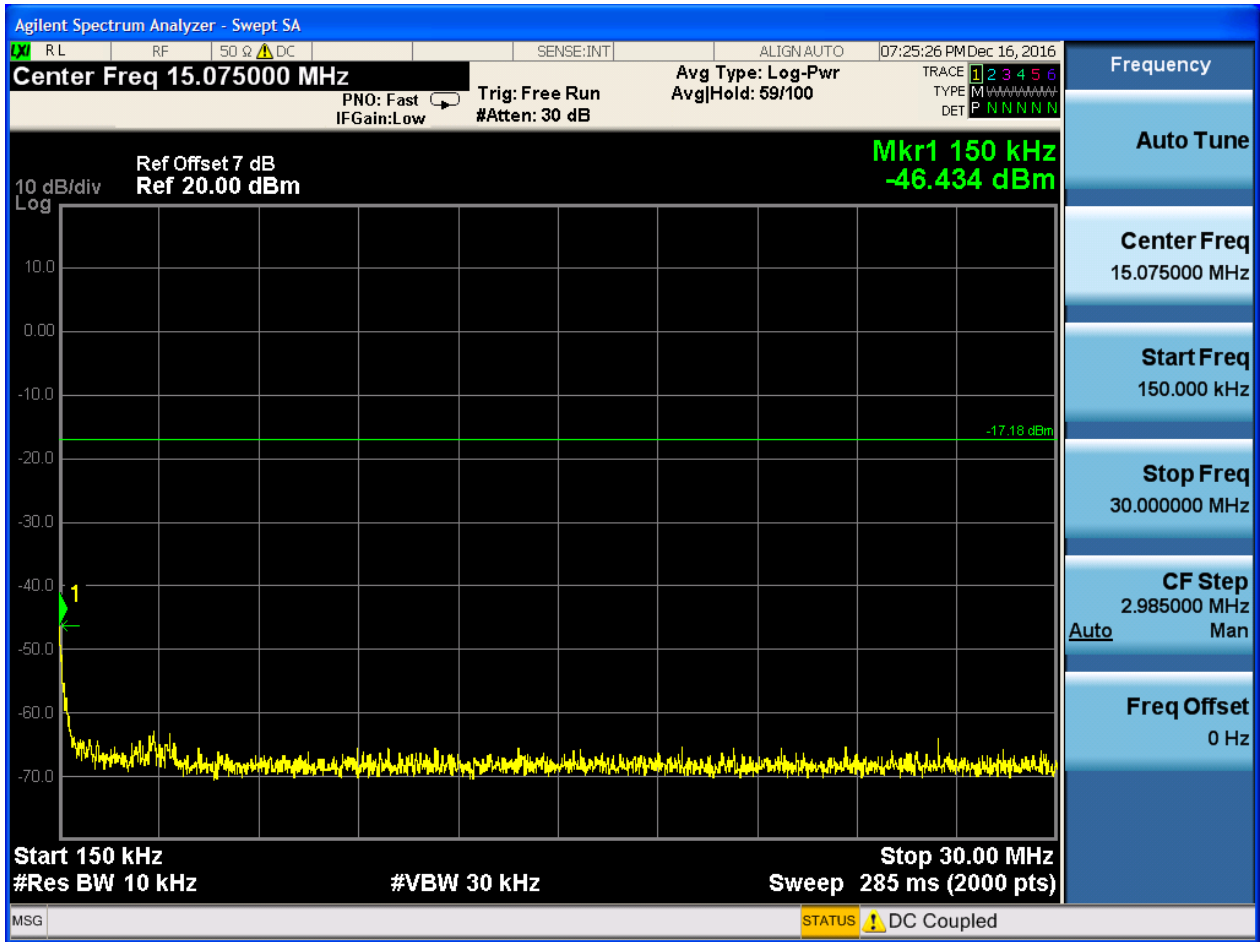
2.6.1 Pref

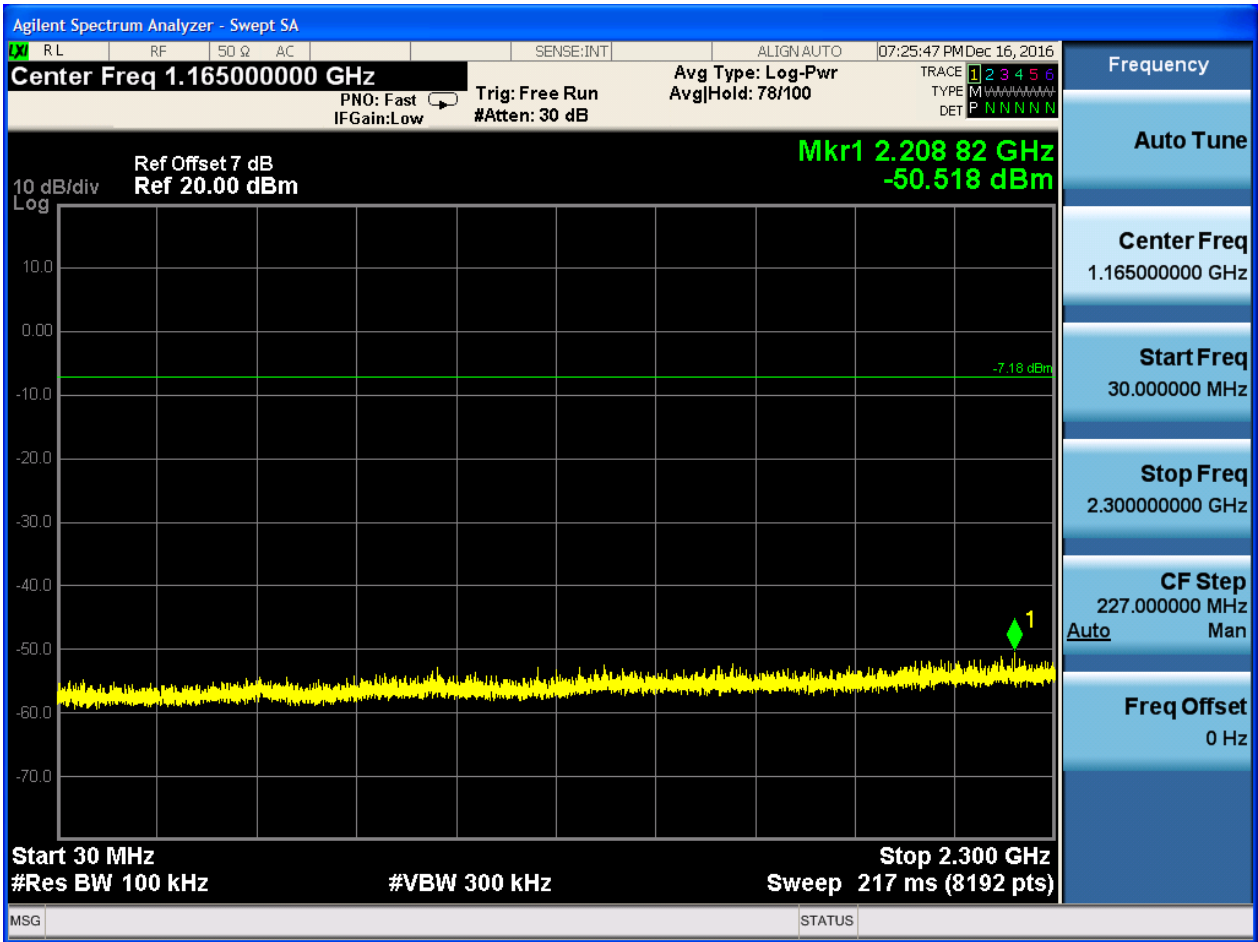


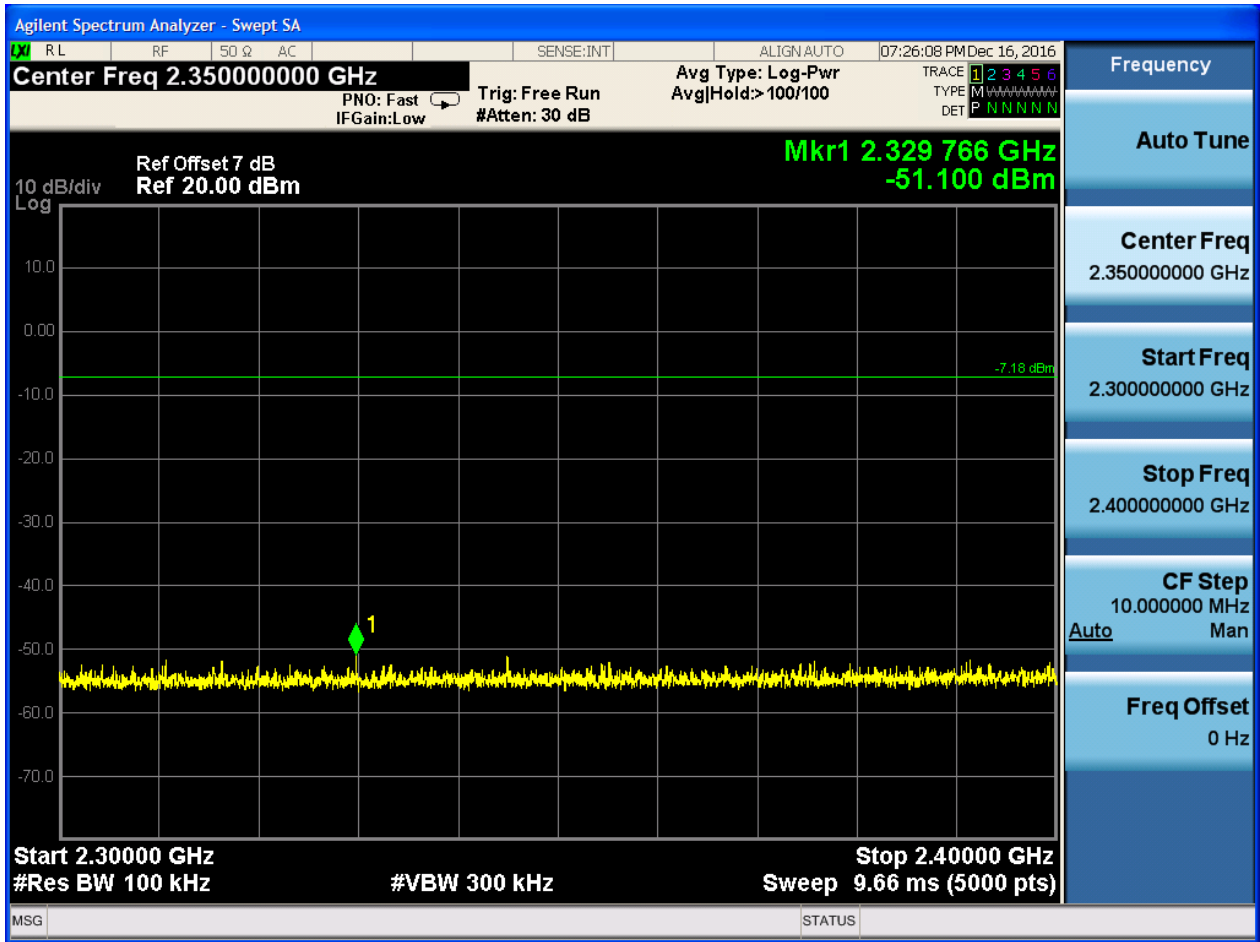


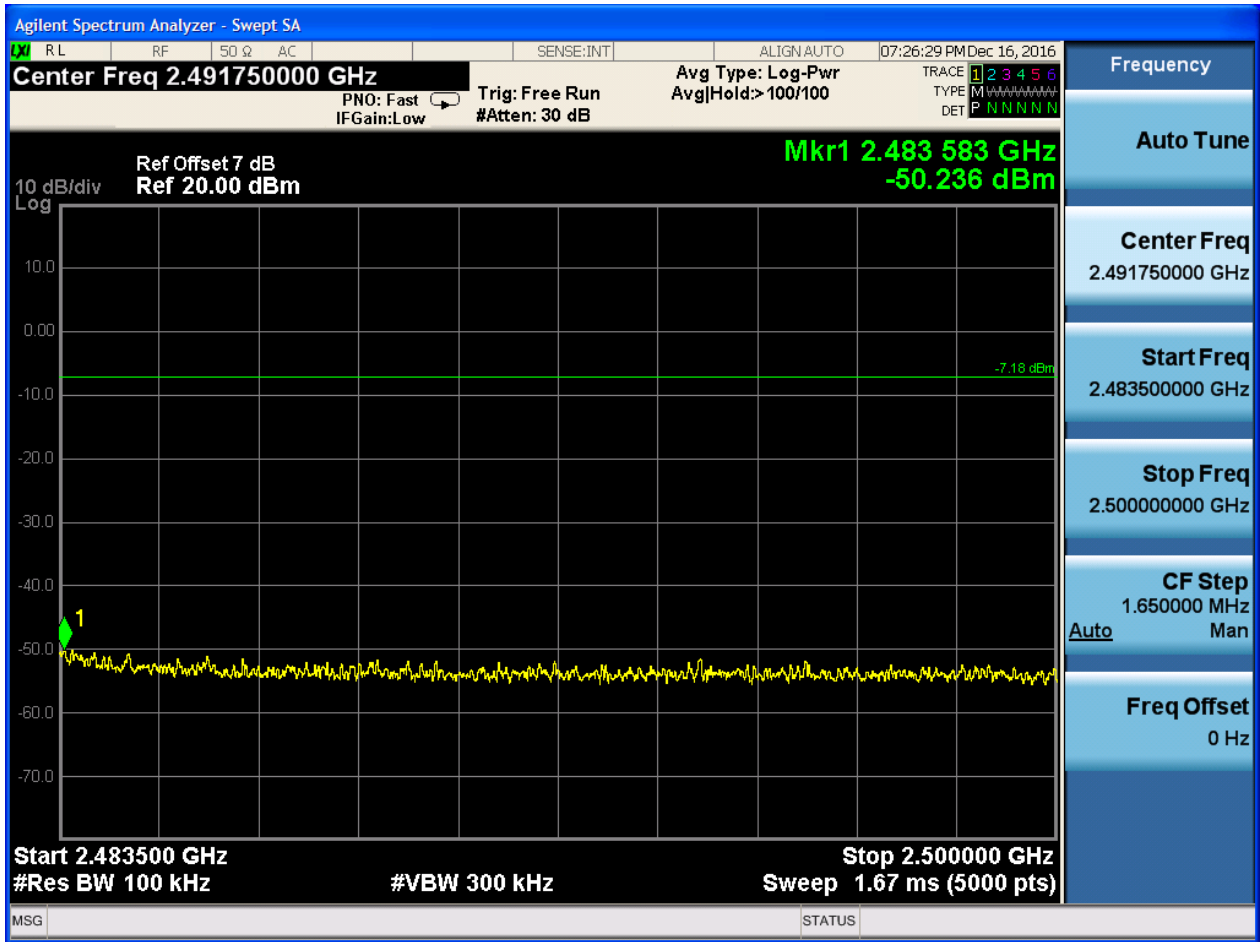
2.6.2 Puw

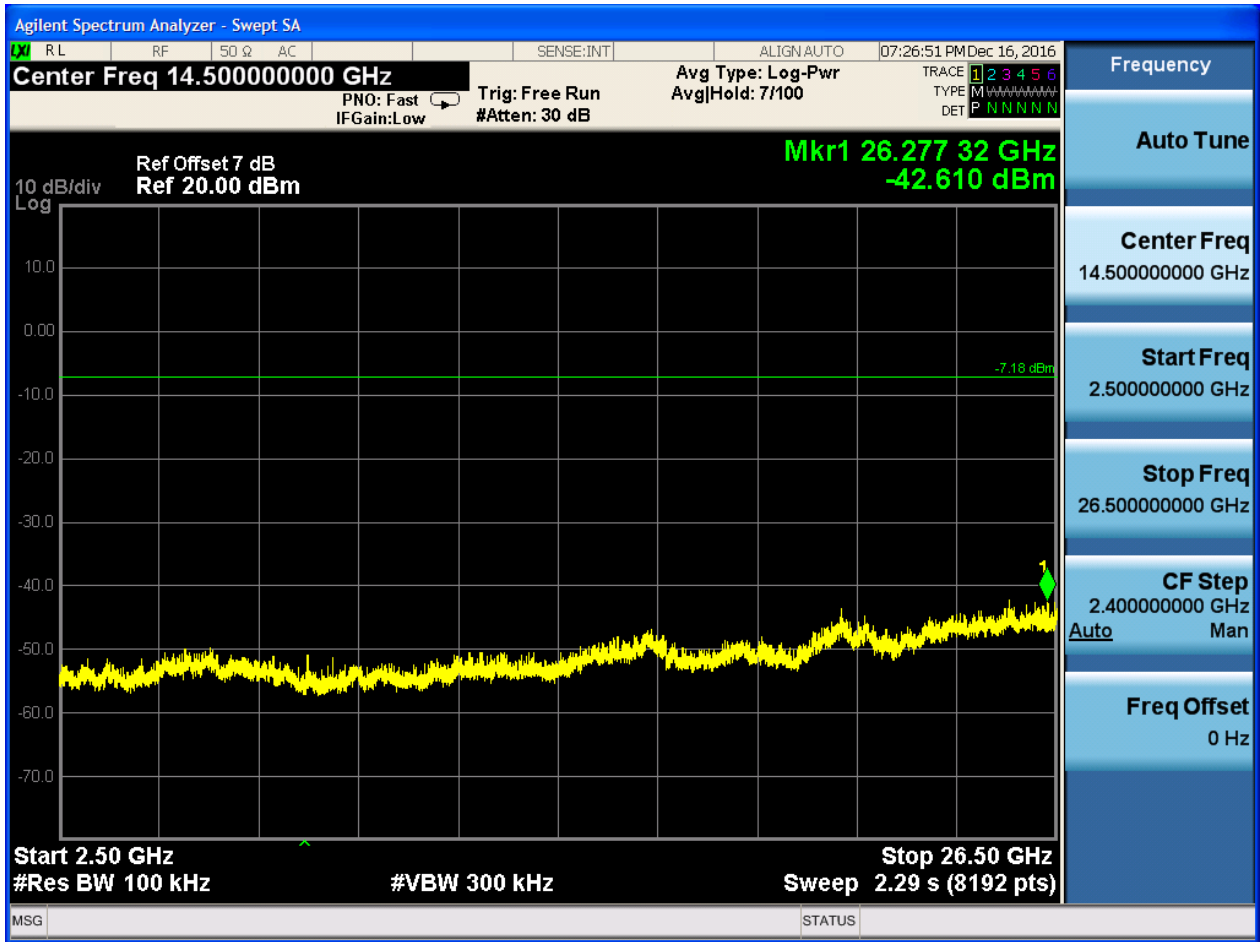














2.7 TM3_3DH5_Ch0

2.7.1 Pref





2.7.2 P_{uw}

