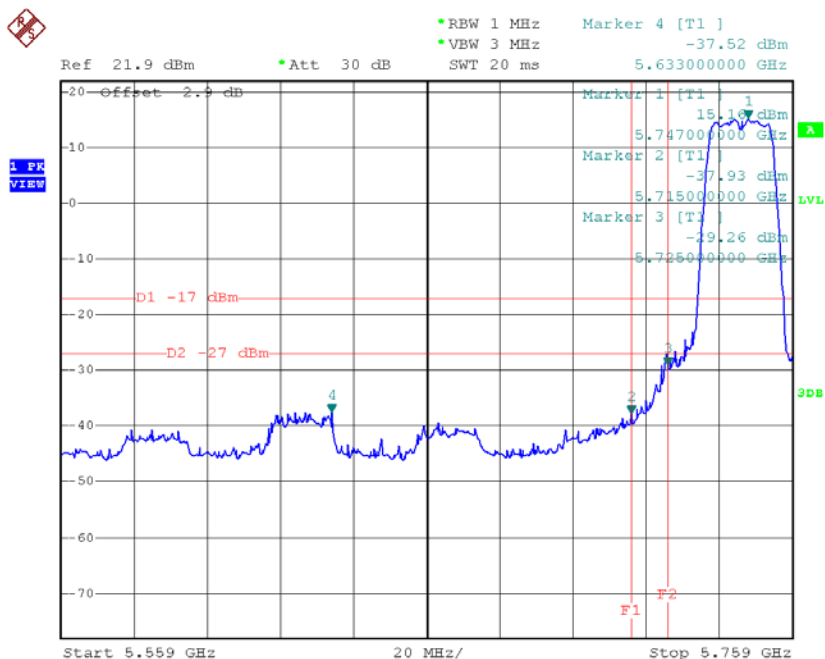


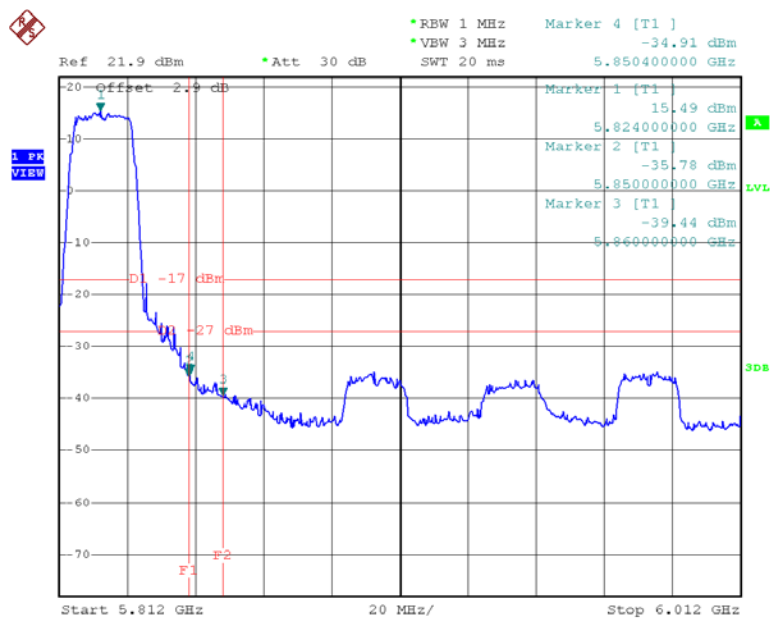
Test Mode: UNII-3/TX N20 Mode_ANT 4

TX HT20 mode CH149



Date: 5.JUL.2015 18:17:51

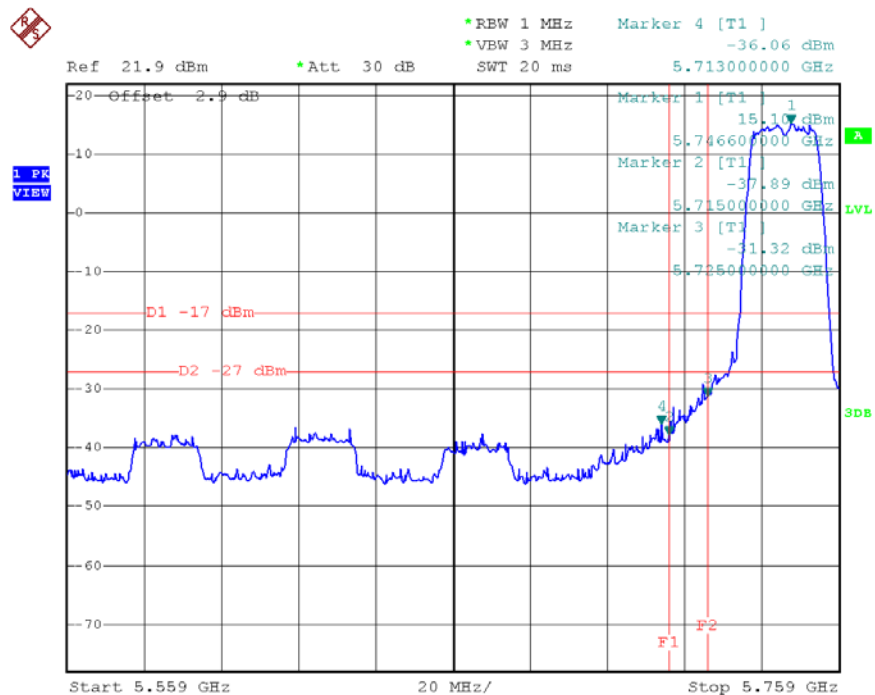
TX HT20 mode CH165



Date: 5.JUL.2015 18:19:45

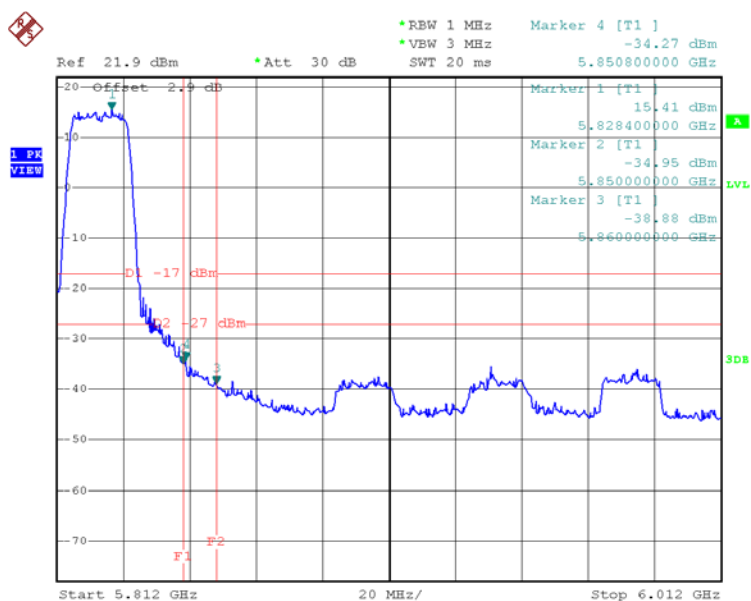
Test Mode: UNII-3/TX N20 Mode_ANT 5

TX HT20 mode CH149



Date: 5.JUL.2015 18:45:58

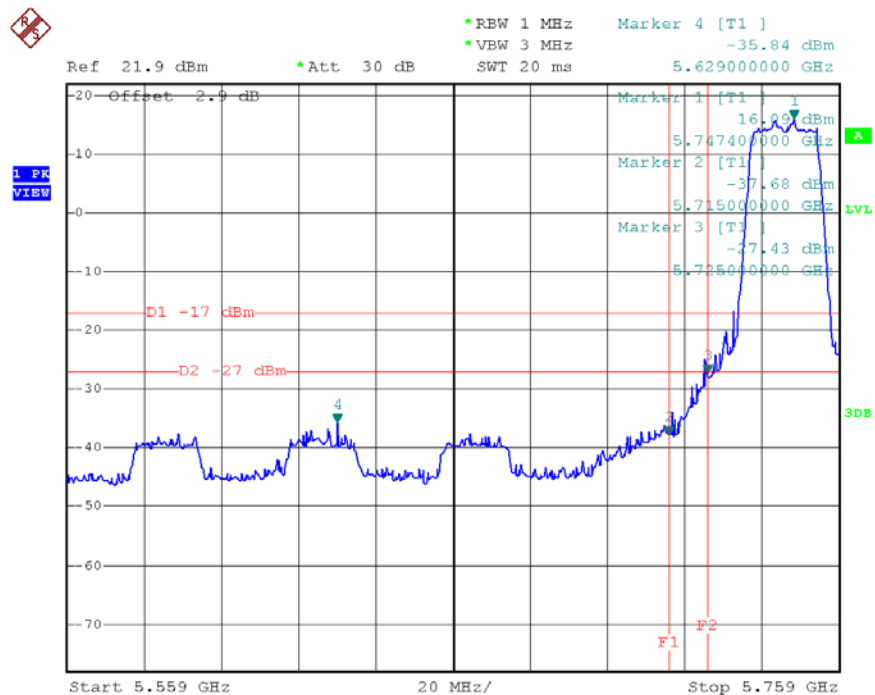
X HT20 mode CH165



Date: 5.JUL.2015 18:47:58

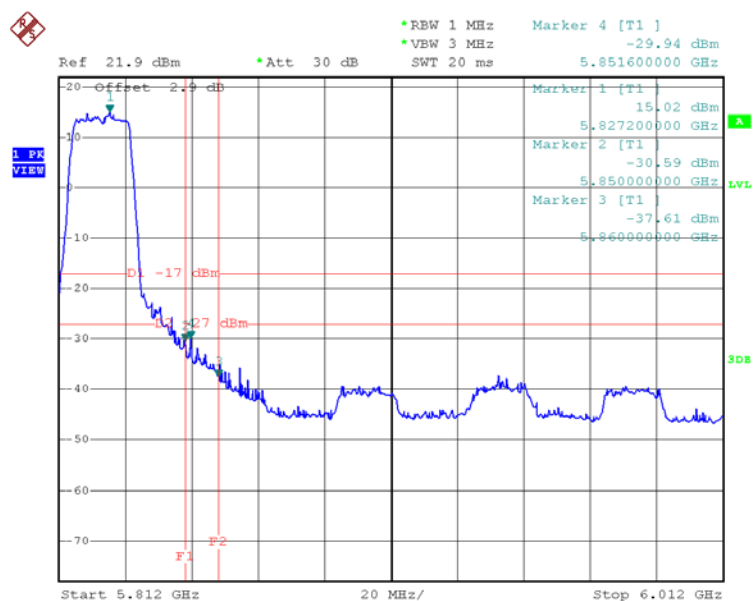
Test Mode: UNII-3/TX N20 Mode_ANT 6

TX HT20 mode CH149



Date: 5.JUL.2015 18:56:46

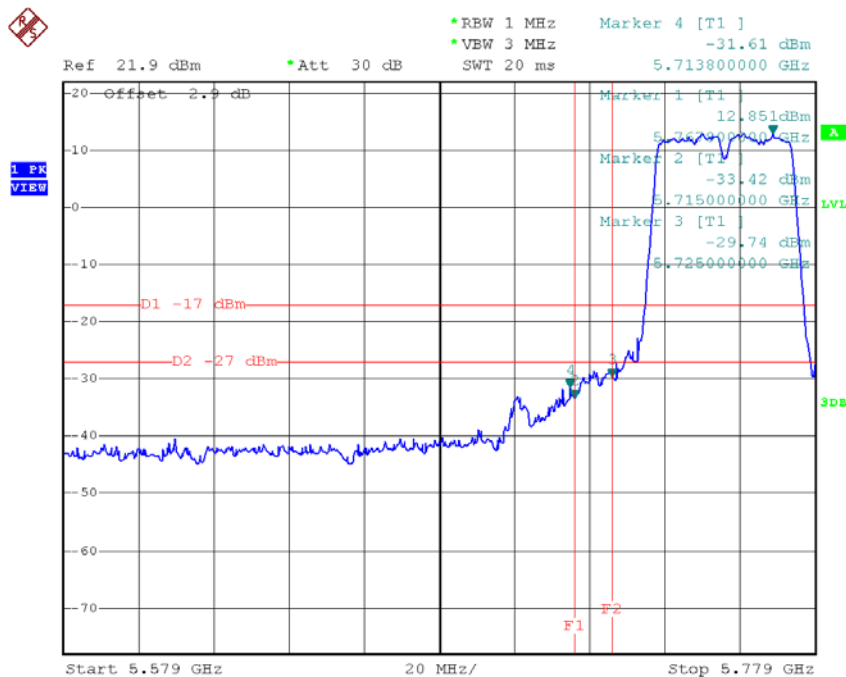
X HT20 mode CH165



Date: 5.JUL.2015 18:59:35

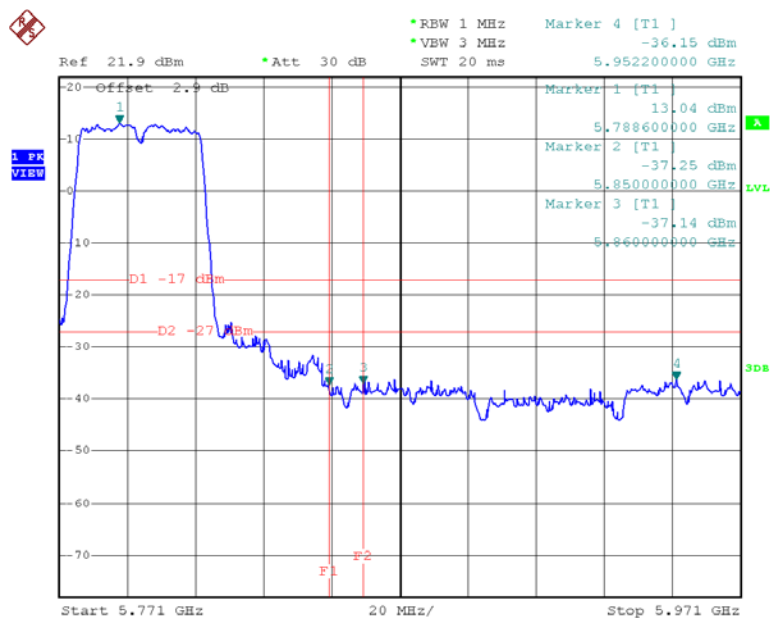
Test Mode: UNII-3/TX N40 Mode_ANT 4

UNII-3/TX HT40 mode CH151



Date: 5.JUL.2015 20:36:01

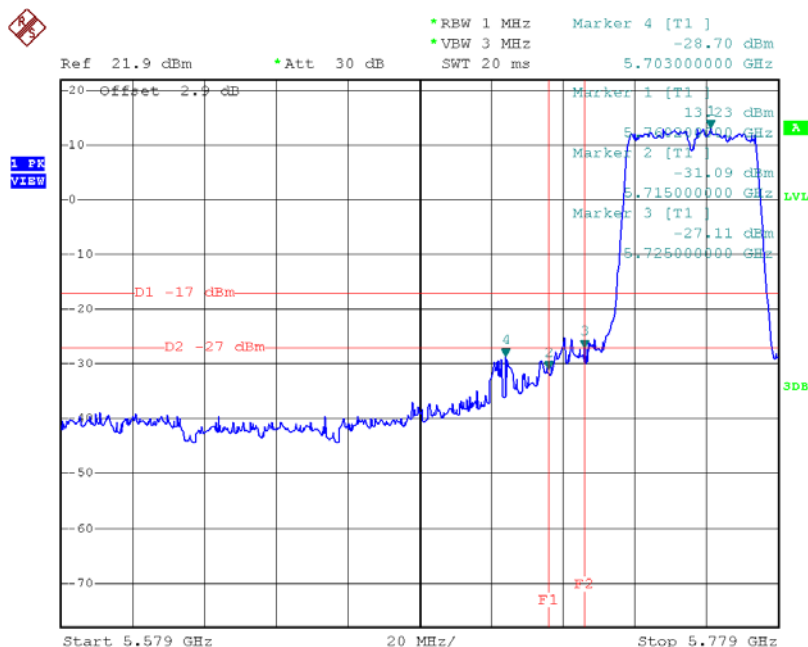
UNII-3/TX HT40 mode CH159



Date: 5.JUL.2015 20:37:04

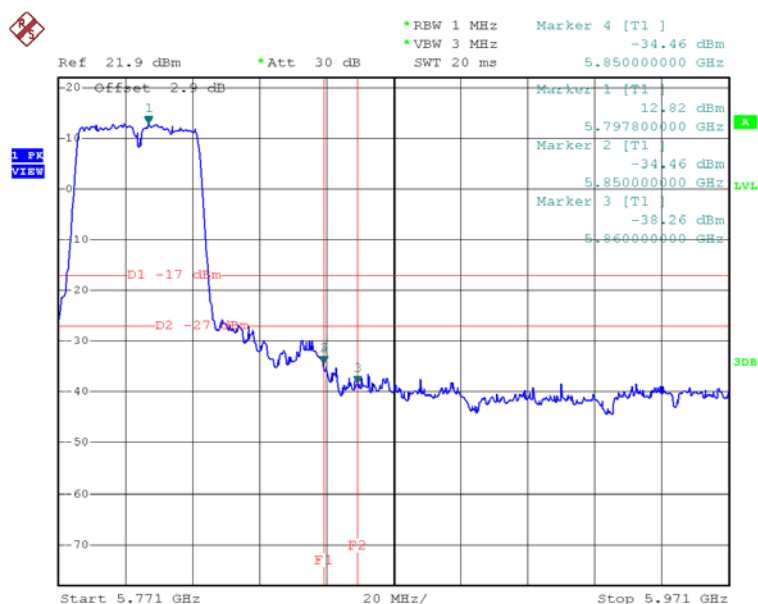
Test Mode: UNII-3/TX N40 Mode_ANT 5

TX HT40 mode CH151



Date: 5.JUL.2015 20:47:36

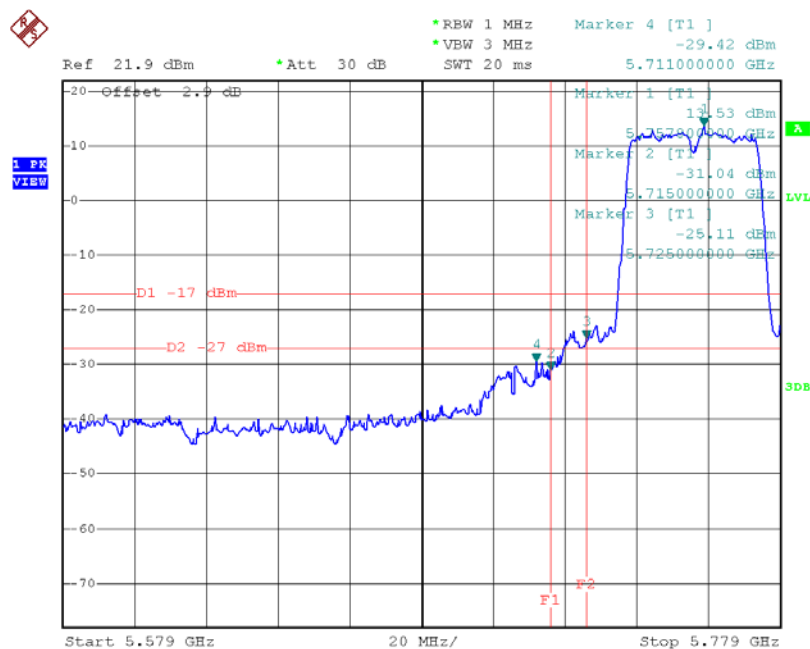
HT40 mode CH159



Date: 5.JUL.2015 20:48:38

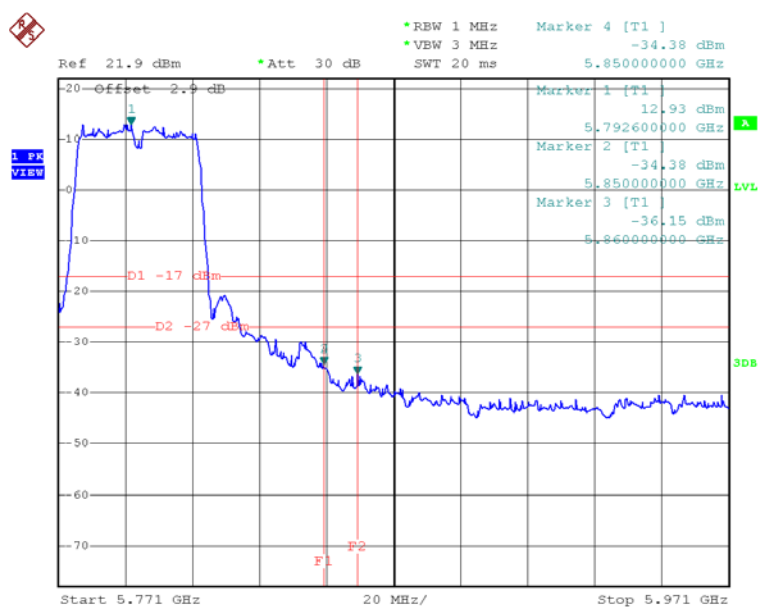
Test Mode: UNII-3/TX N40 Mode_ANT 6

TX HT40 mode CH151



Date: 5.JUL.2015 21:08:39

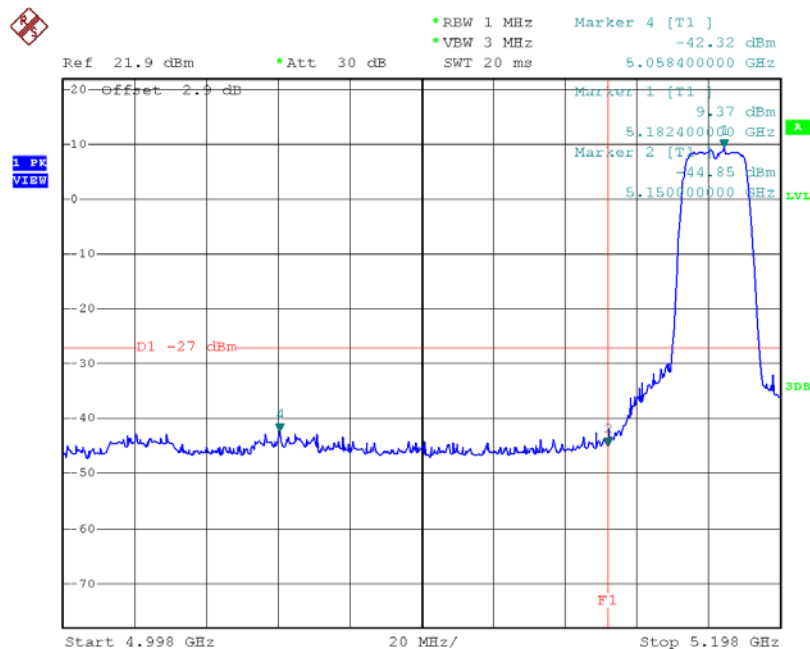
HT40 mode CH159



Date: 5.JUL.2015 20:58:54

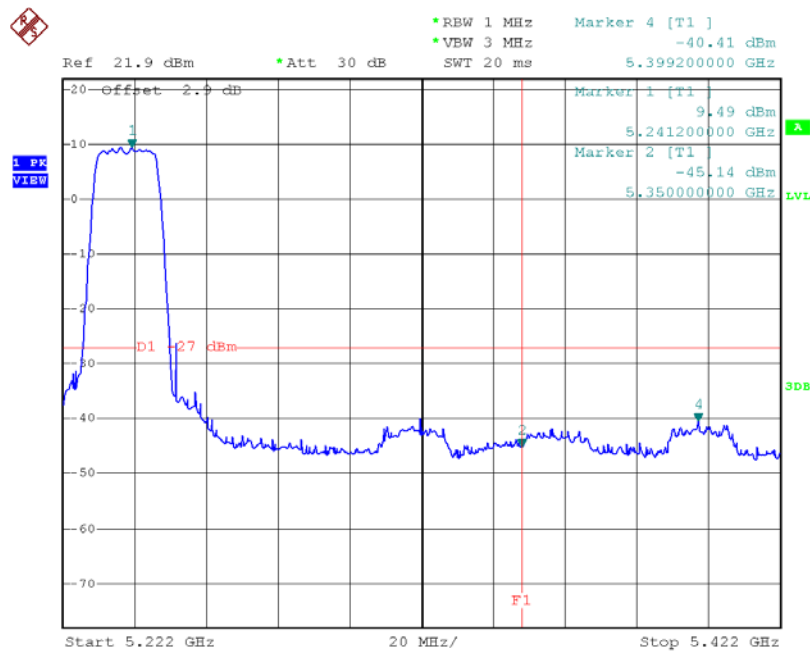
Test Mode: UNII-1/TX AC20 Mode_ANT 4

TX mode CH36



Date: 5.JUL.2015 19:30:24

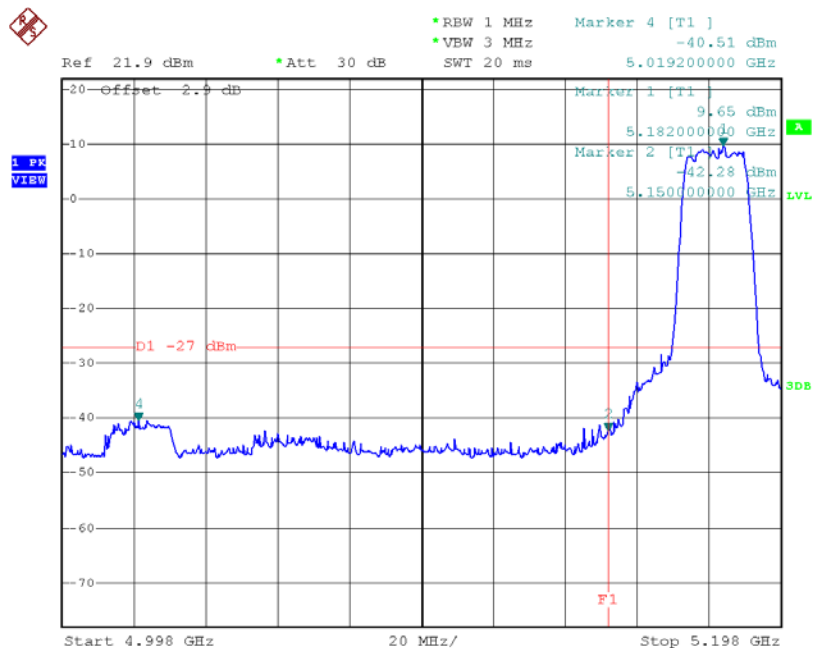
TX mode CH48



Date: 5.JUL.2015 19:32:00

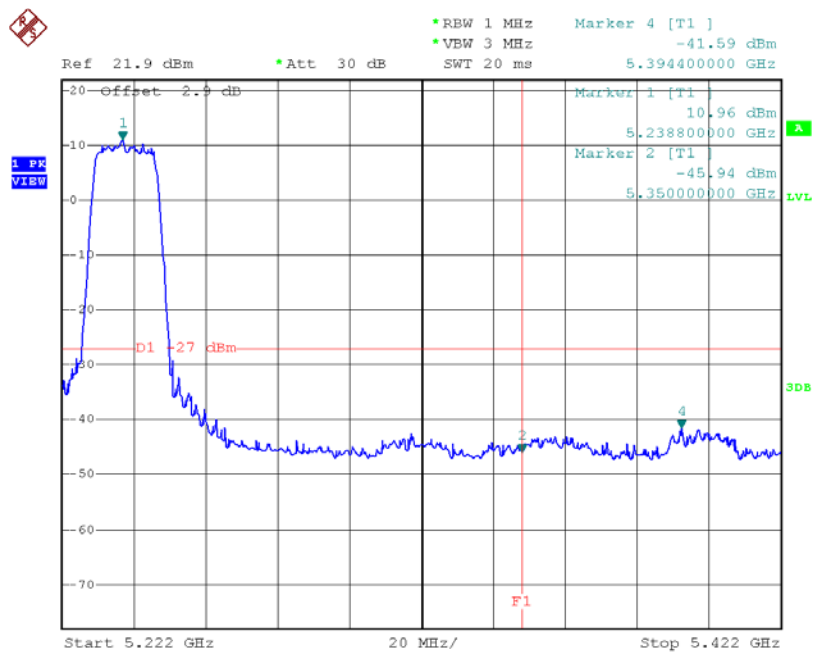
Test Mode: UNII-1/TX AC20 Mode_ANT 5

TX mode CH36



Date: 5.JUL.2015 19:43:33

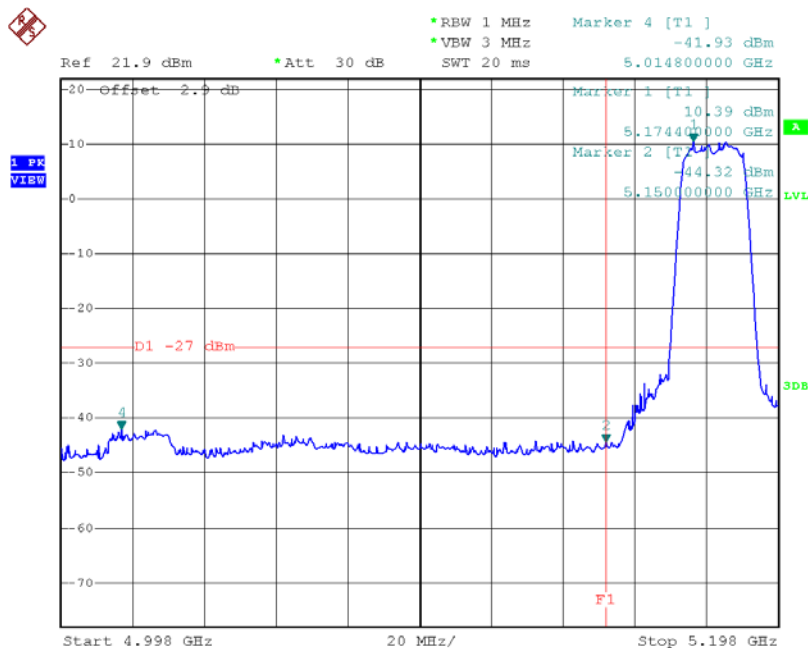
TX mode CH48



Date: 5.JUL.2015 19:45:05

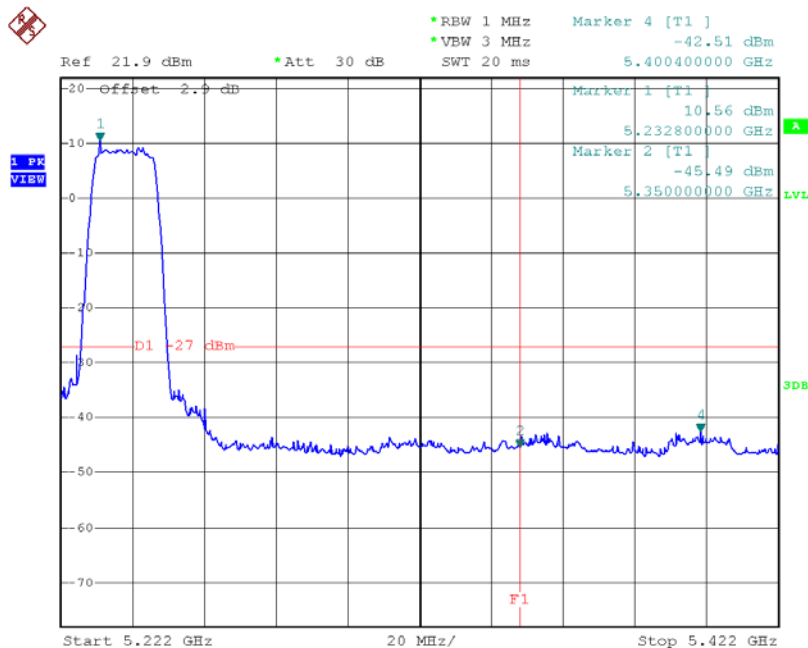
Test Mode: UNII-1/TX AC20 Mode_ANT 6

TX mode CH36



Date: 5.JUL.2015 19:54:57

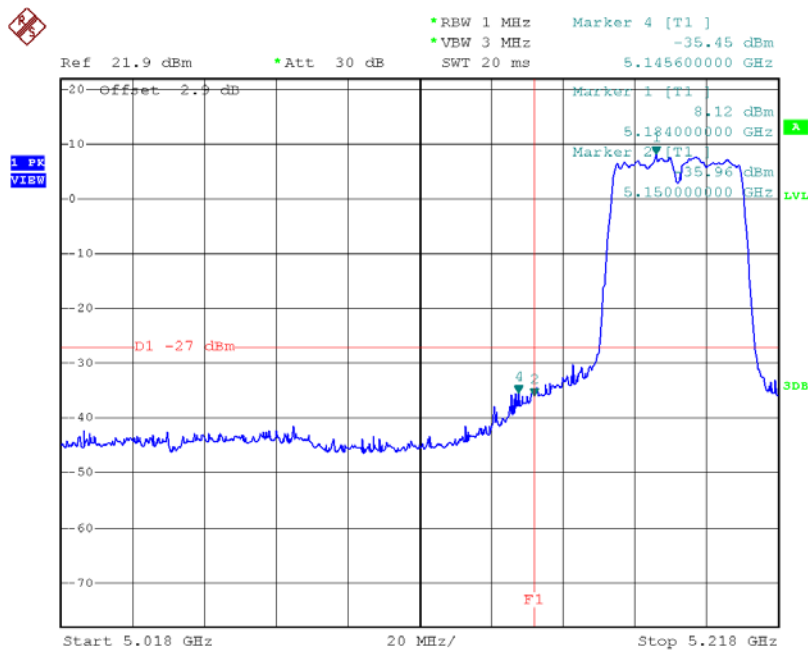
TX mode CH48



Date: 5.JUL.2015 19:56:25

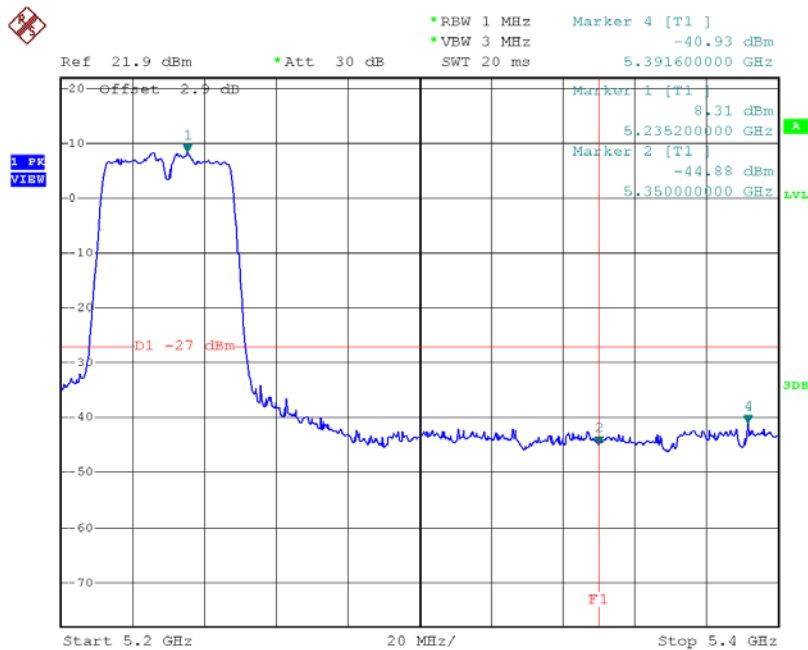
Test Mode: UNII-1/TX AC40 Mode_ANT 4

TX mode CH38



Date: 5.JUL.2015 21:38:43

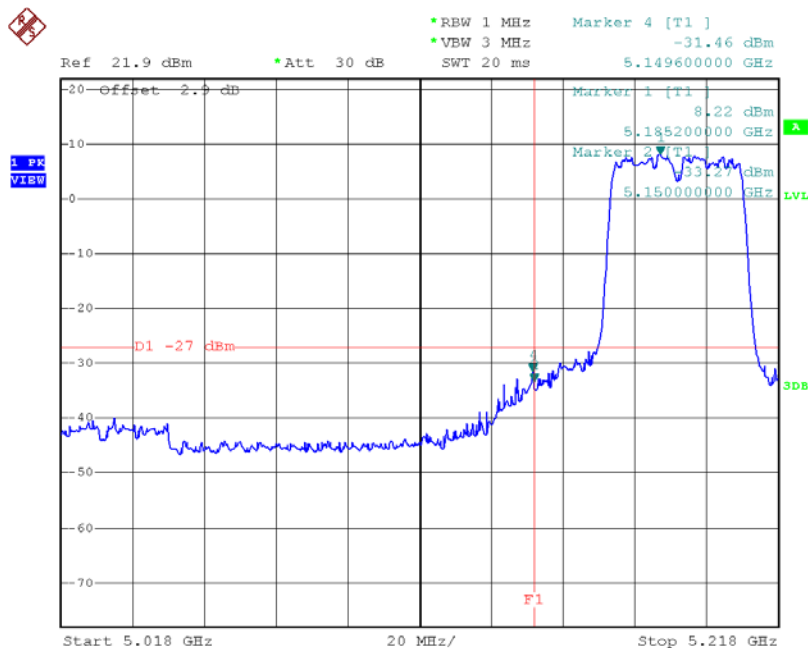
TX mode CH46



Date: 5.JUL.2015 21:39:45

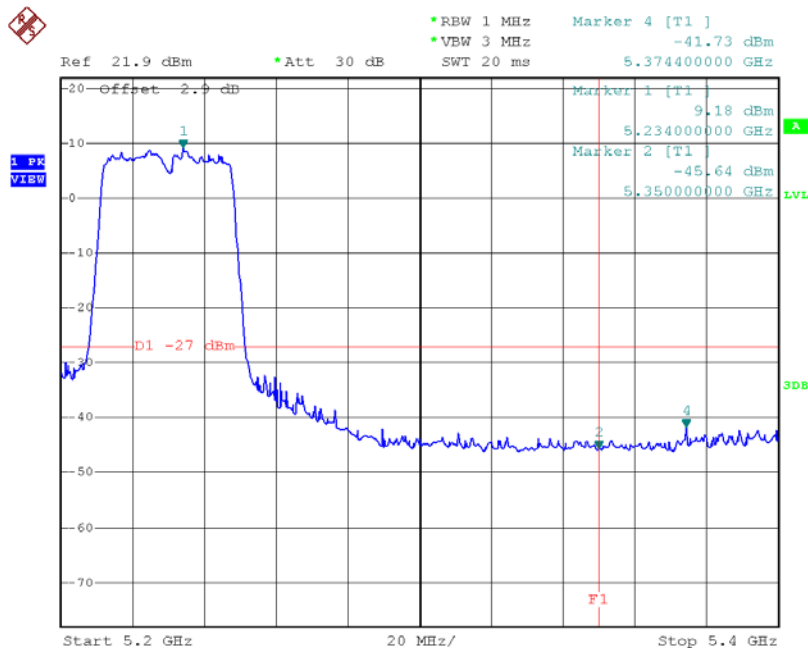
Test Mode: UNII-1/TX AC40 Mode_ANT 5

TX mode CH38



Date: 5.JUL.2015 21:48:17

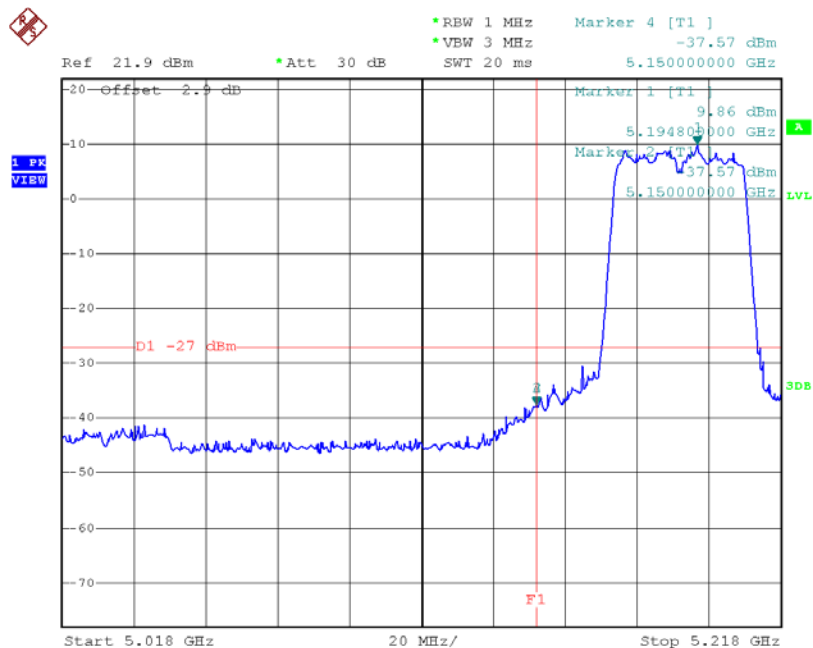
TX mode CH46



Date: 5.JUL.2015 21:49:15

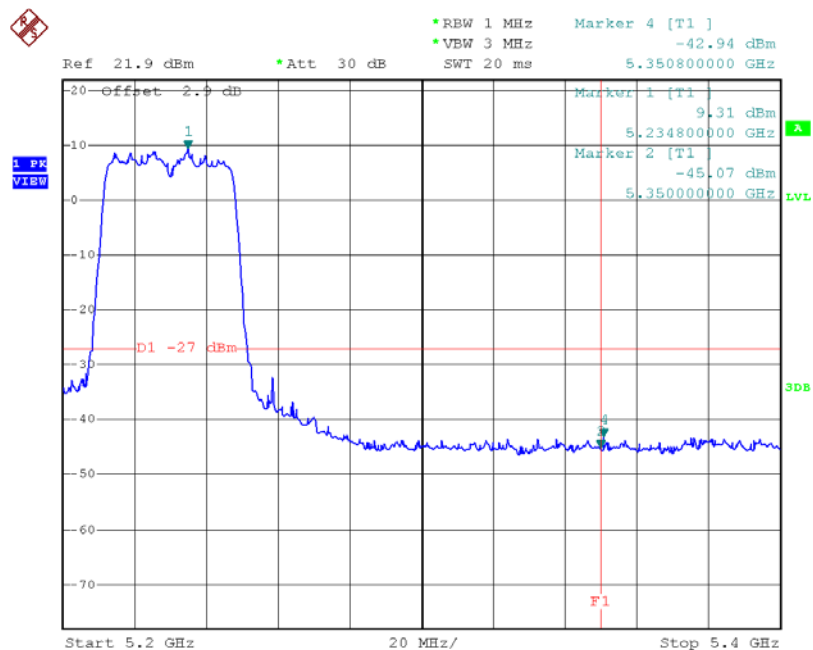
Test Mode: UNII-1/TX AC40 Mode_ANT 6

TX mode CH38



Date: 5.JUL.2015 21:11:08

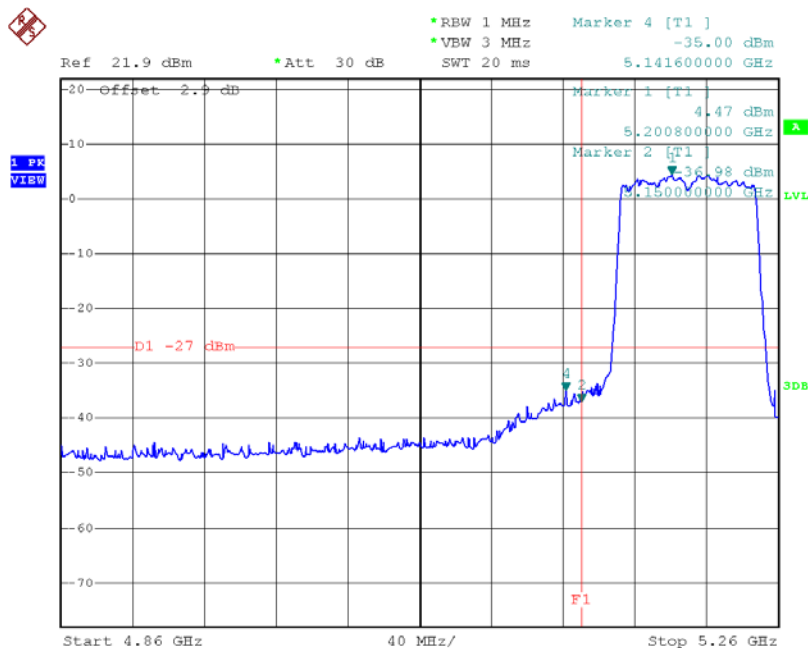
TX mode CH46



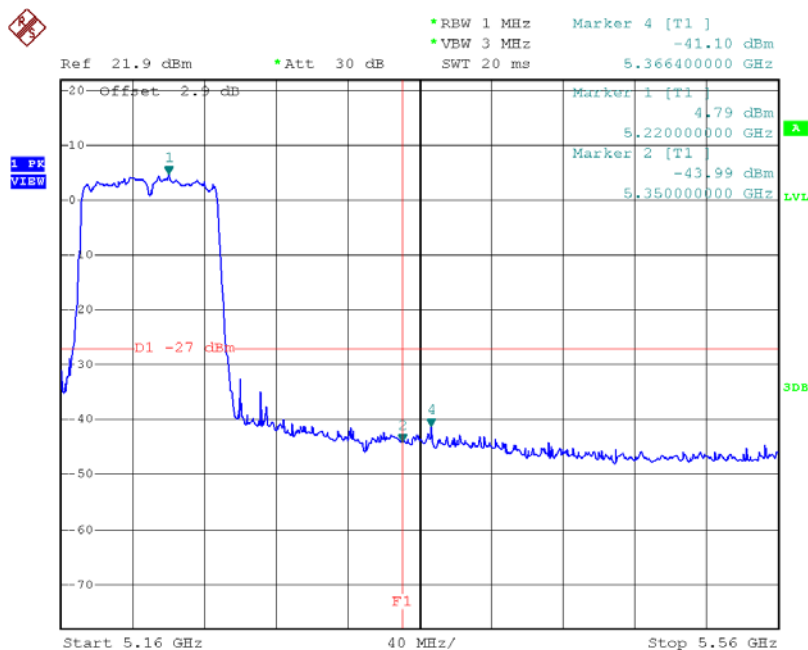
Date: 5.JUL.2015 21:12:51

Test Mode: UNII-1/TX AC80 Mode_ANT 4

TX mode CH42



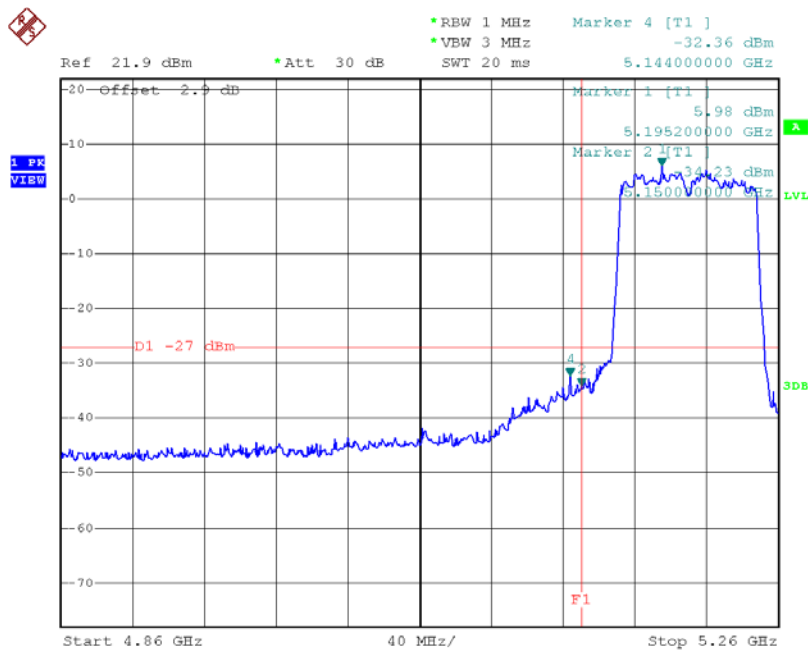
Date: 5.JUL.2015 22:35:46



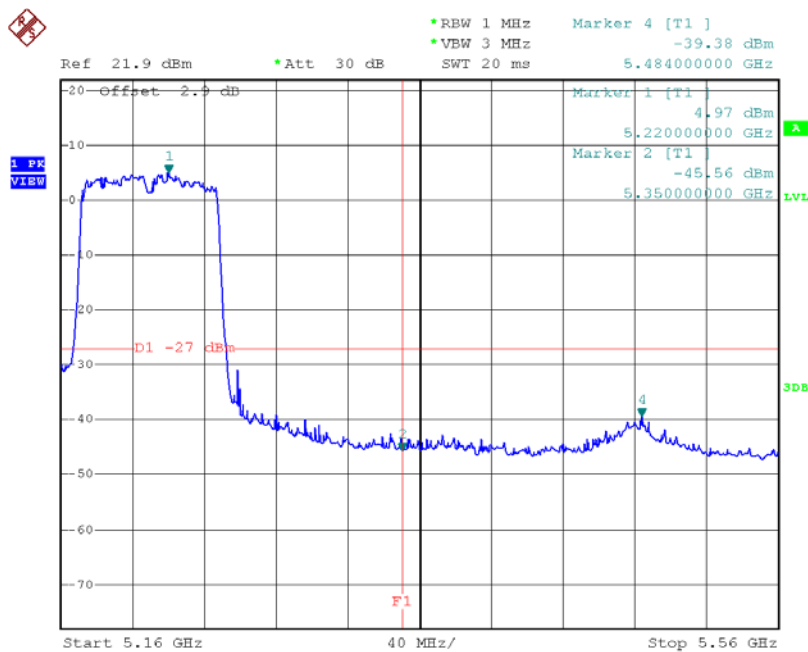
Date: 5.JUL.2015 22:35:54

Test Mode: UNII-1/TX AC80 Mode_ANT 5

TX mode CH42



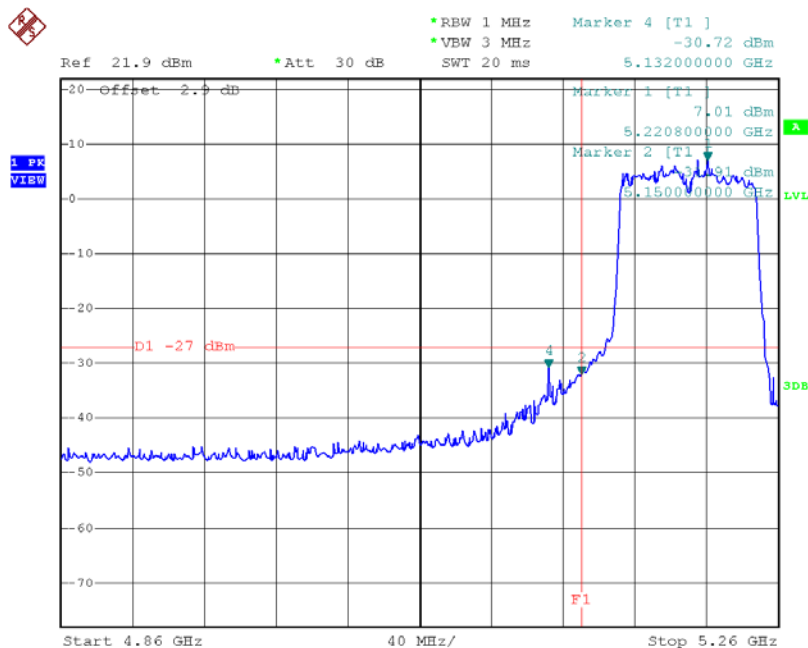
Date: 5.JUL.2015 22:32:09



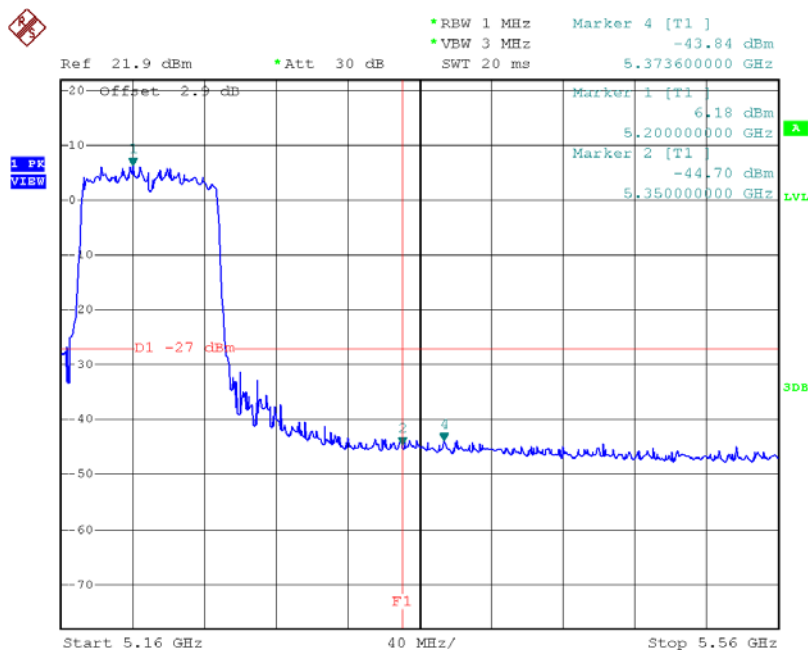
Date: 5.JUL.2015 22:32:17

Test Mode: UNII-1/TX AC80 Mode_ANT 6

TX mode CH42



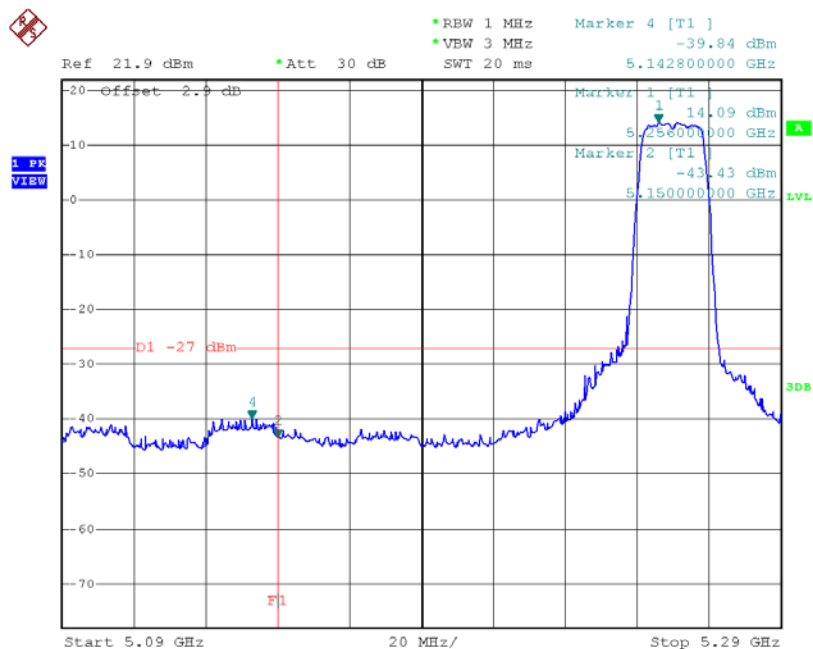
Date: 5.JUL.2015 22:12:52



Date: 5.JUL.2015 22:13:00

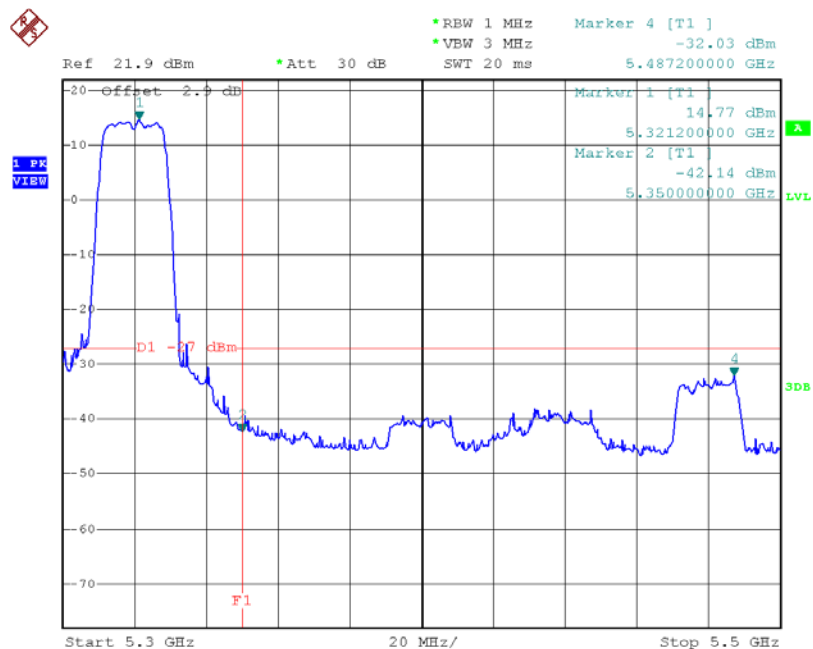
Test Mode: UNII-2A/TX AC20 Mode_ANT 4

TX mode CH52



Date: 5.JUL.2015 19:32:50

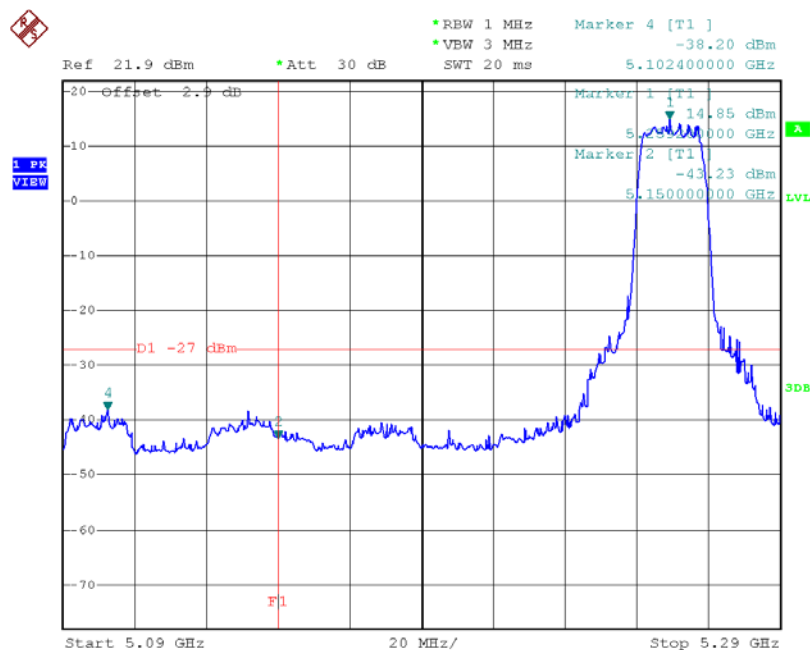
TX mode CH64



Date: 5.JUL.2015 19:34:30

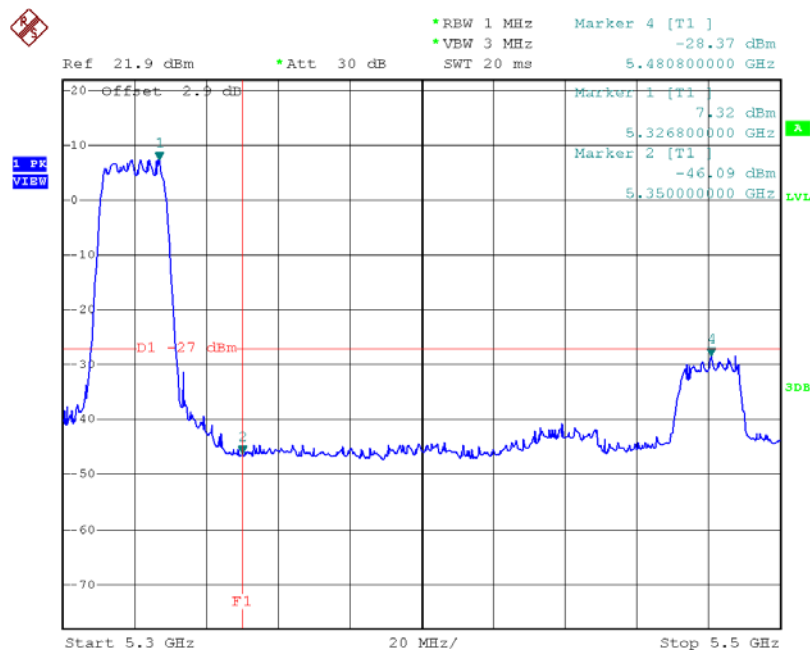
Test Mode:	UNII-2A/TX AC20 Mode_ANT 5
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TX mode CH52



Date: 5.JUL.2015 19:45:55

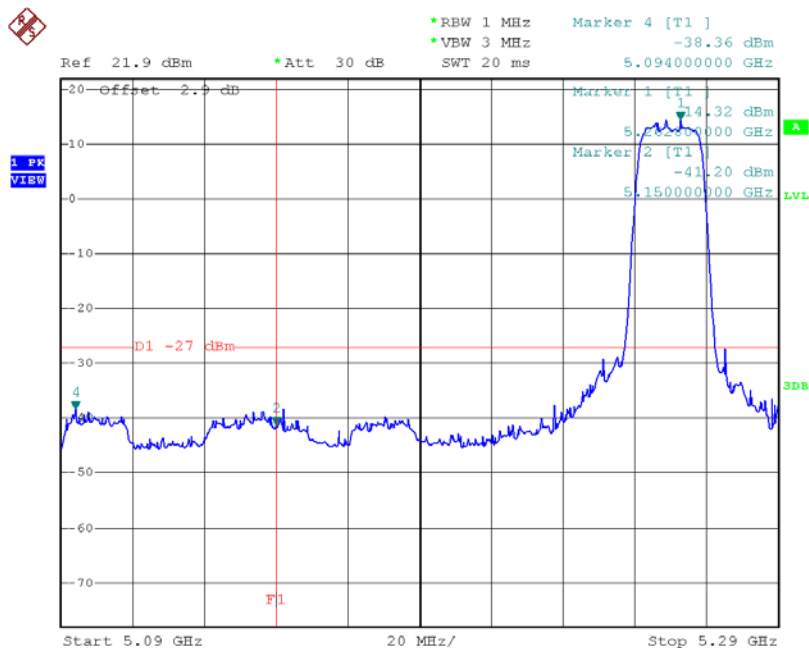
TX mode CH64



Date: 5.JUL.2015 20:08:15

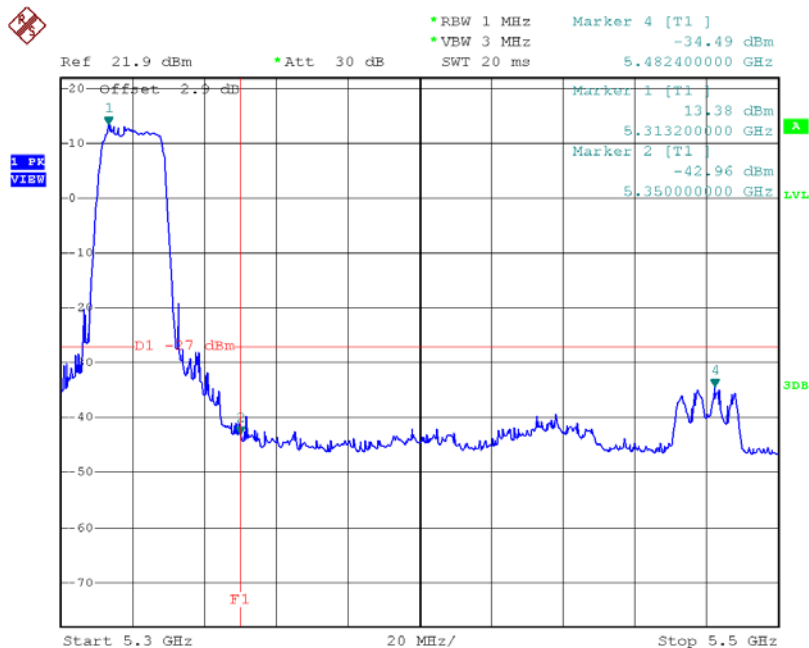
Test Mode:	UNII-2A/TX AC20 Mode_ANT 6
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TX mode CH52



Date: 5.JUL.2015 19:57:13

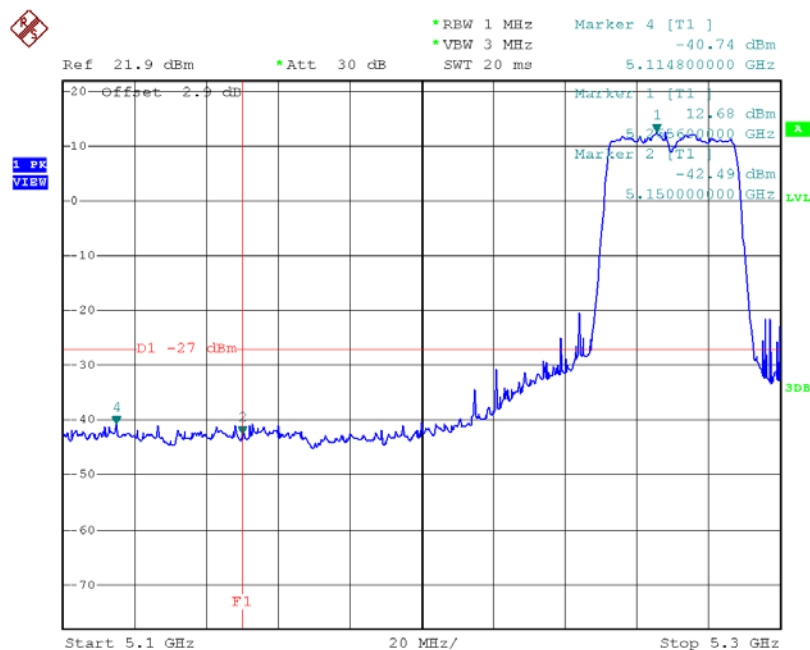
TX mode CH64



Date: 5.JUL.2015 19:58:42

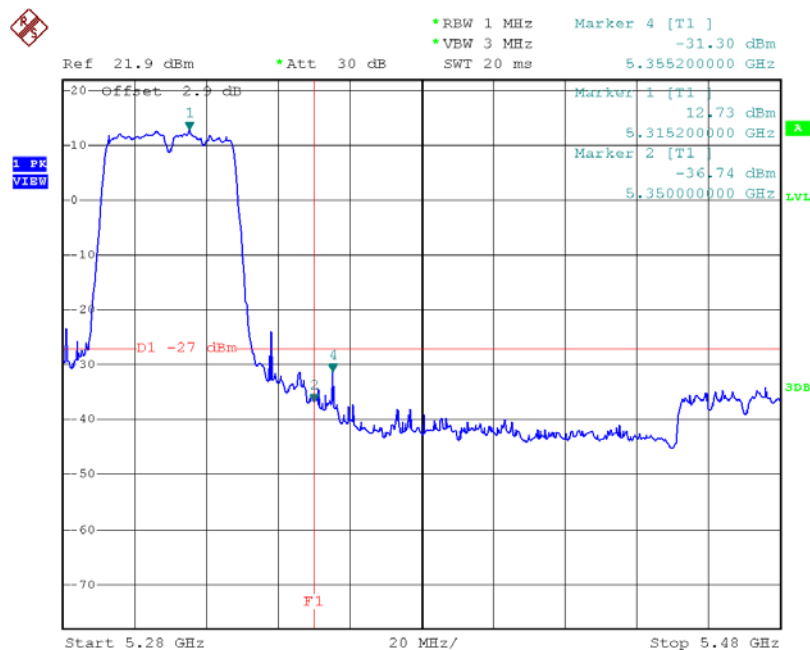
Test Mode:	UNII-2A/TX AC40 Mode_ANT 4
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TX mode CH54



Date: 5.JUL.2015 21:40:42

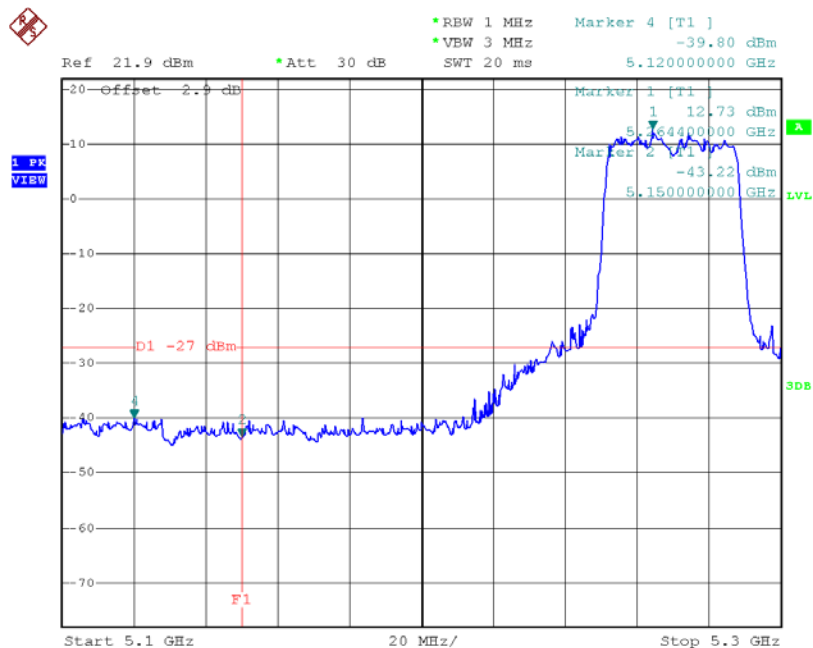
TX mode CH62



Date: 5.JUL.2015 21:41:40

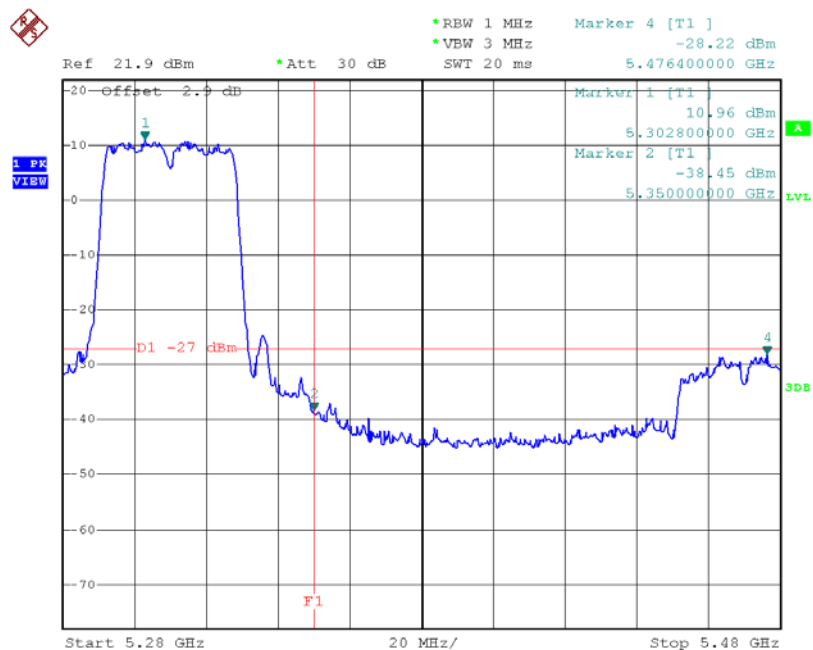
Test Mode:	UNII-2A/TX AC40 Mode_ANT 5
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TX mode CH54



Date: 5.JUL.2015 21:50:18

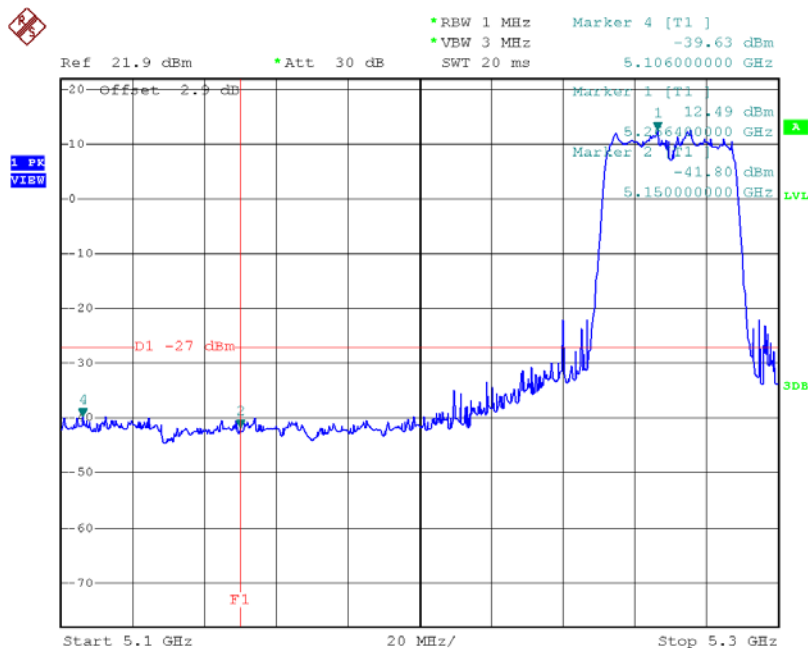
TX mode CH62



Date: 16.AUG.2015 14:29:16

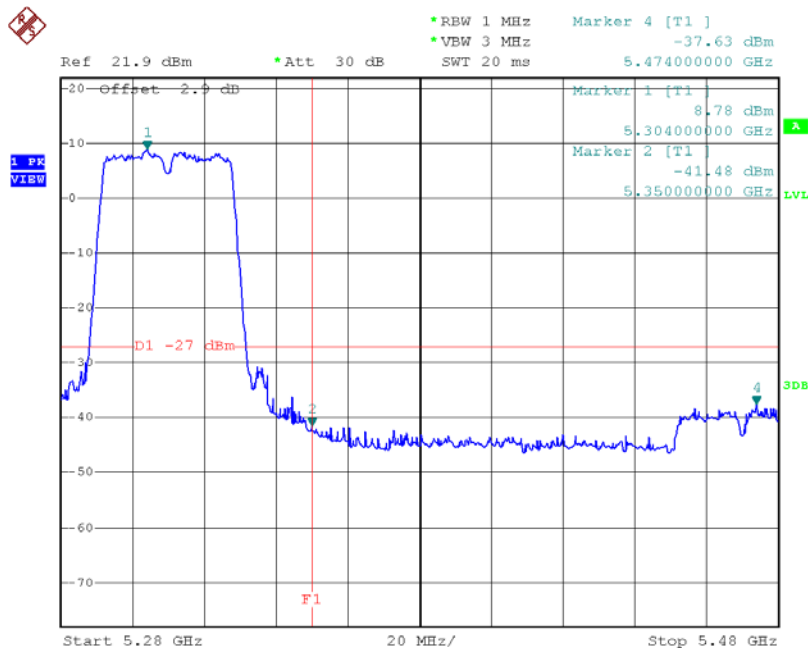
Test Mode: UNII-2A/TX AC40 Mode_ANT 6

TX mode CH54



Date: 5.JUL.2015 21:13:48

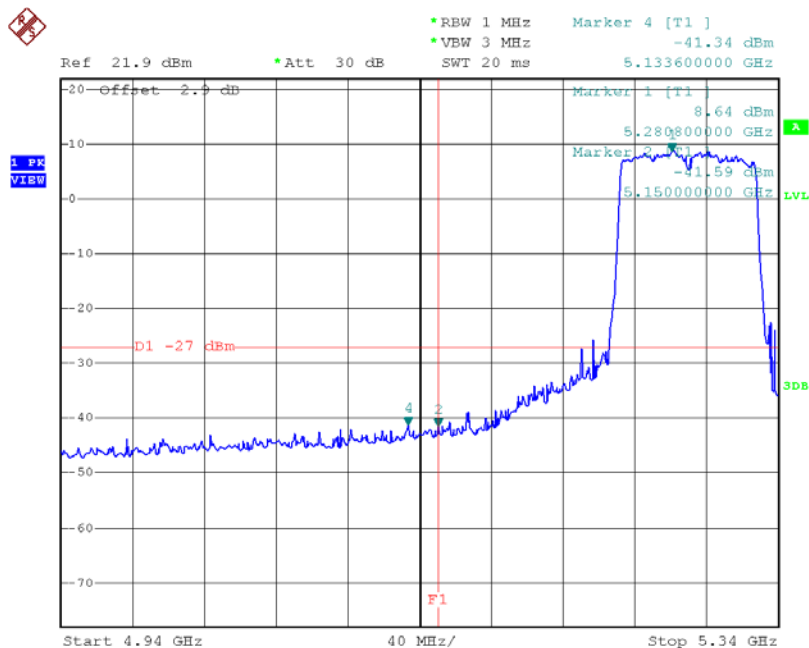
TX mode CH62



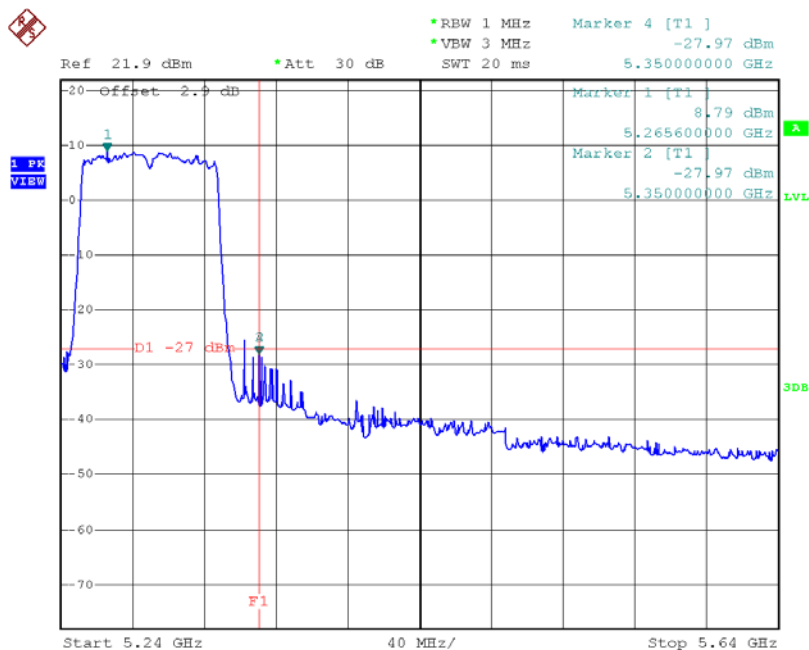
Date: 16.AUG.2015 14:26:48

Test Mode: UNII-2A/TX AC80 Mode_ANT 4

TX mode CH58



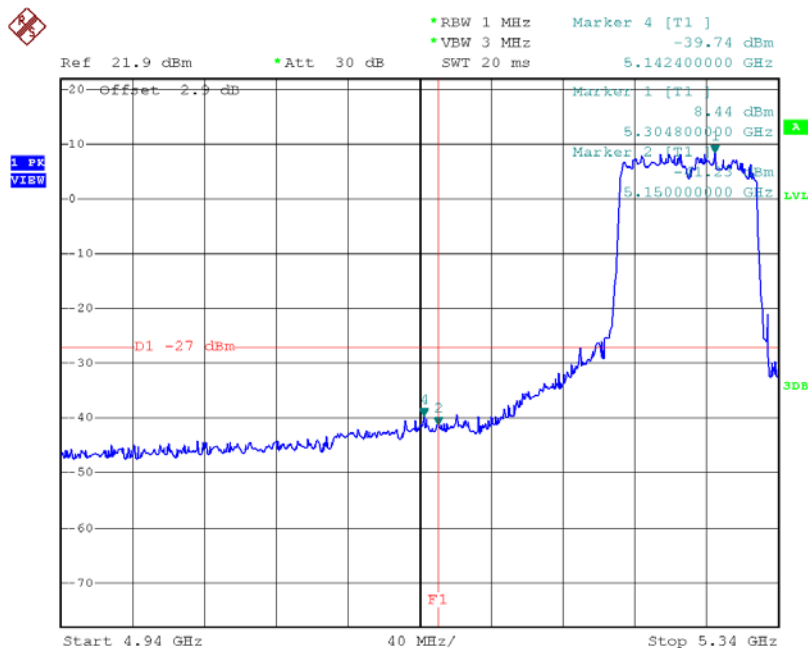
Date: 5.JUL.2015 22:56:25



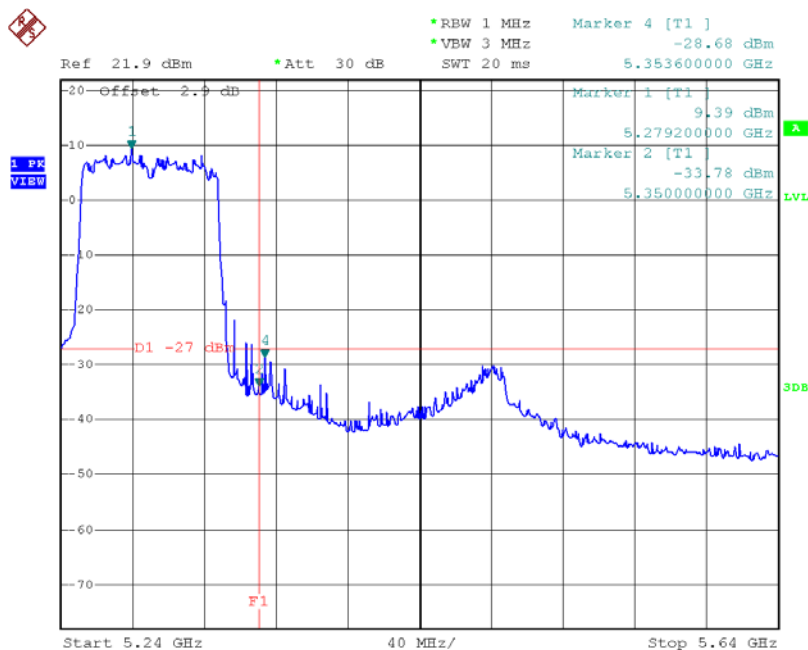
Date: 5.JUL.2015 22:56:34

Test Mode: UNII-2A/TX AC80 Mode_ANT 5

TX mode CH58



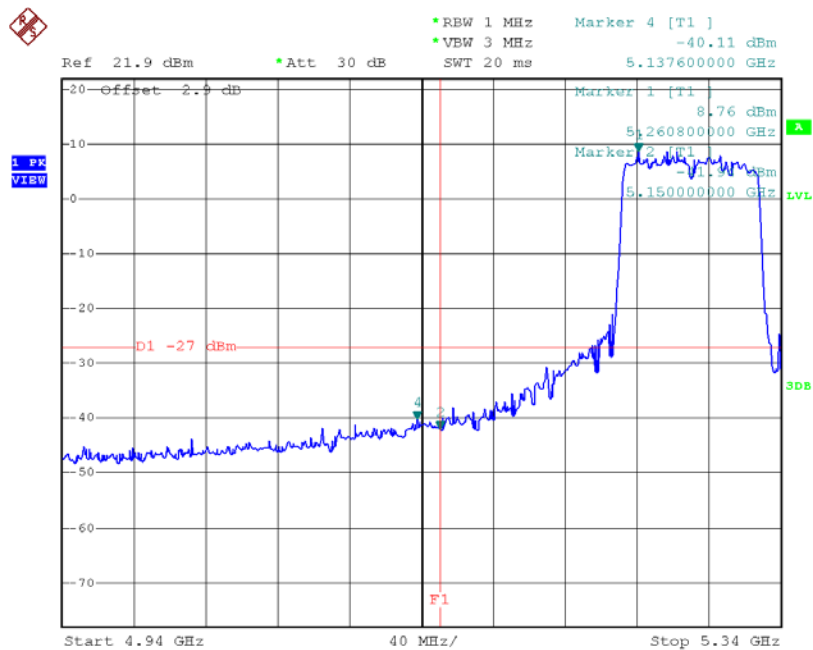
Date: 5.JUL.2015 22:57:14



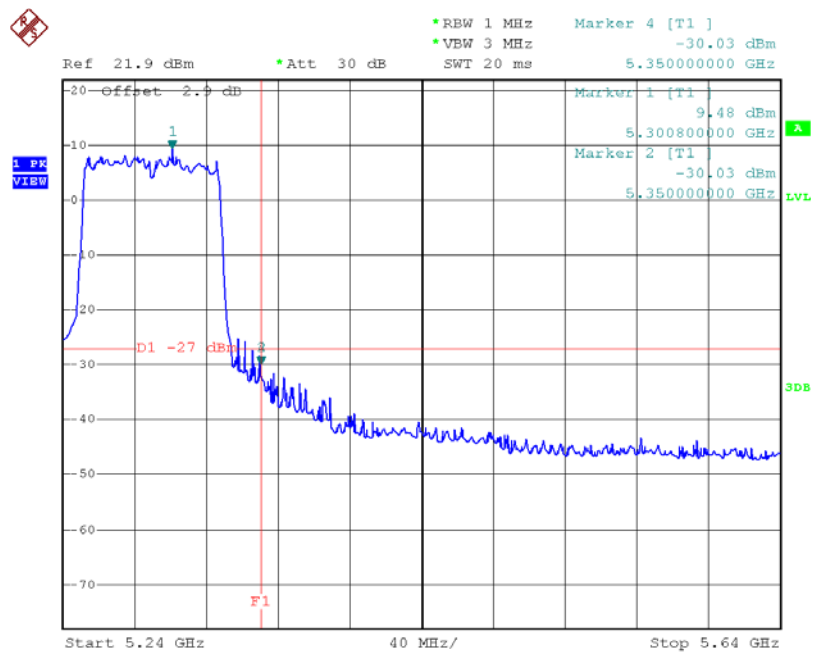
Date: 5.JUL.2015 22:57:22

Test Mode: UNII-2A/TX AC80 Mode_ANT 6

TX mode CH58



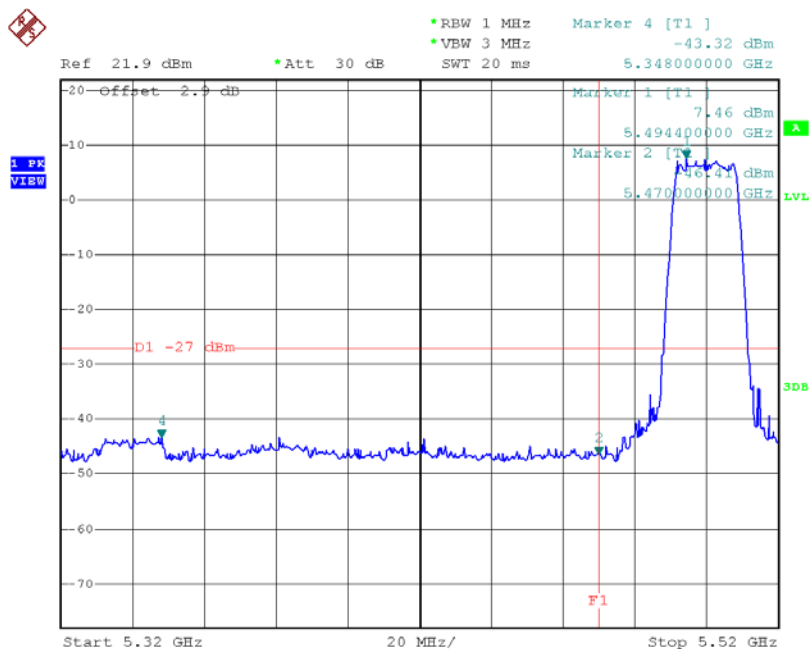
Date: 5.JUL.2015 22:57:43



Date: 5.JUL.2015 22:57:51

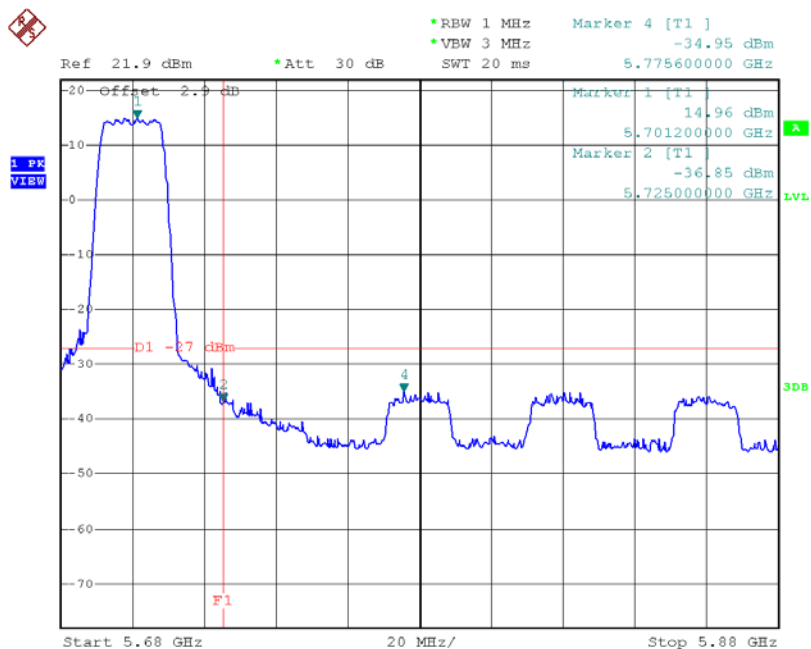
Test Mode: UNII-2C/TX AC20 Mode_ANT 4

TX mode CH100



Date: 16.AUG.2015 14:07:40

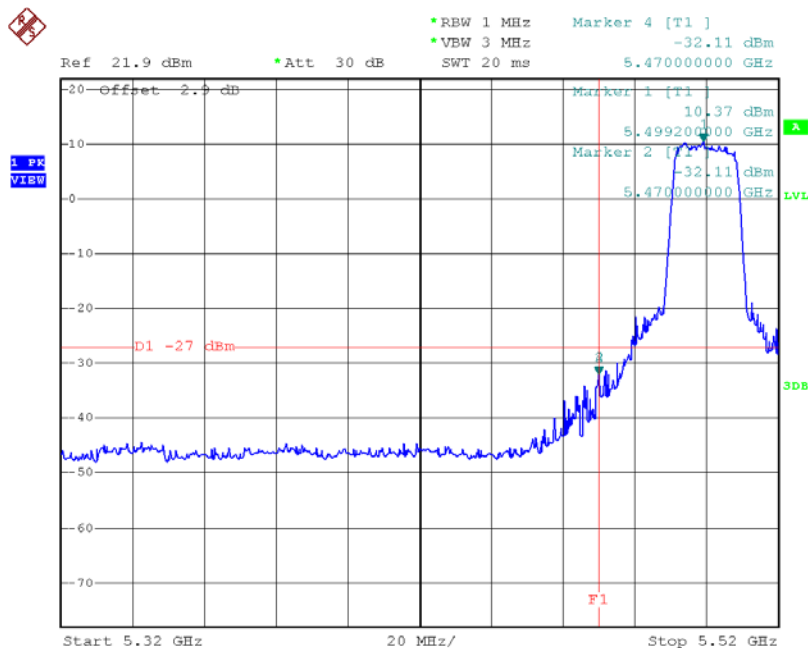
TX mode CH140



Date: 5.JUL.2015 19:36:47

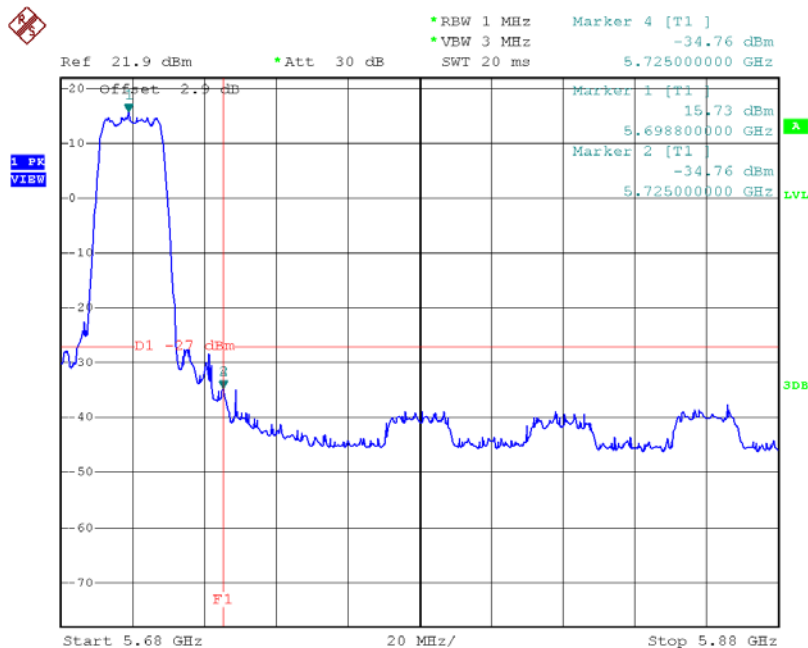
Test Mode:	UNII-2C/TX AC20 Mode_ANT 5
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TX mode CH100



Date: 16.AUG.2015 14:08:40

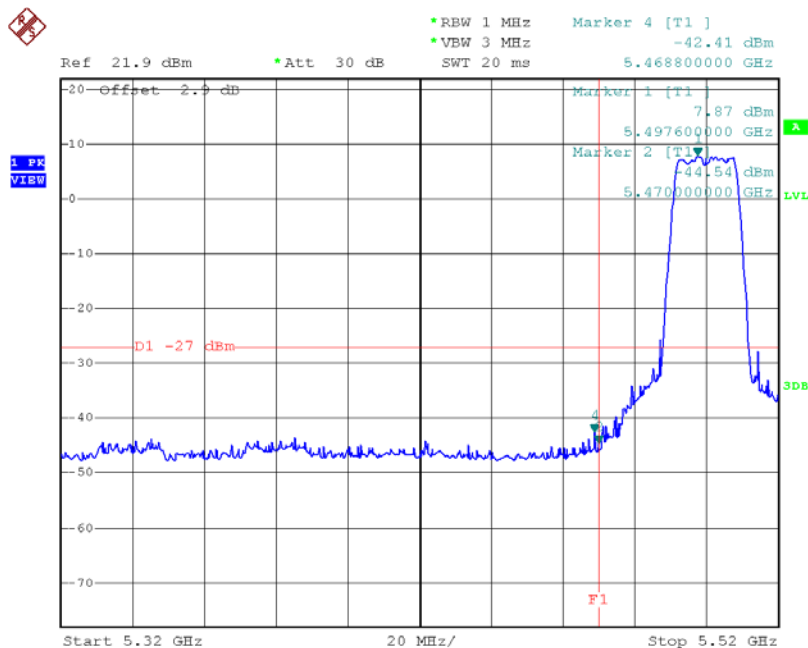
TX mode CH140



Date: 5.JUL.2015 19:50:35

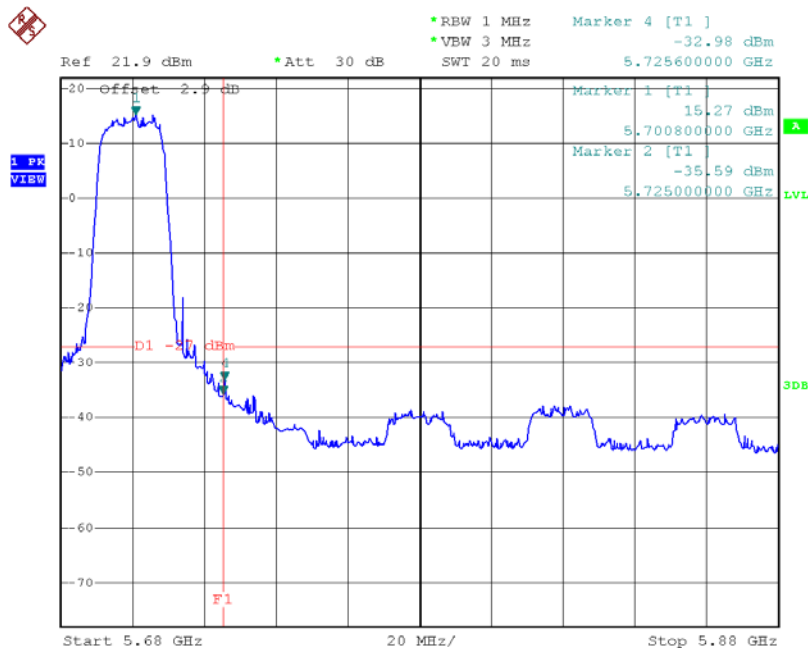
Test Mode: UNII-2C/TX AC20 Mode_ANT 6

TX mode CH100



Date: 16.AUG.2015 14:09:46

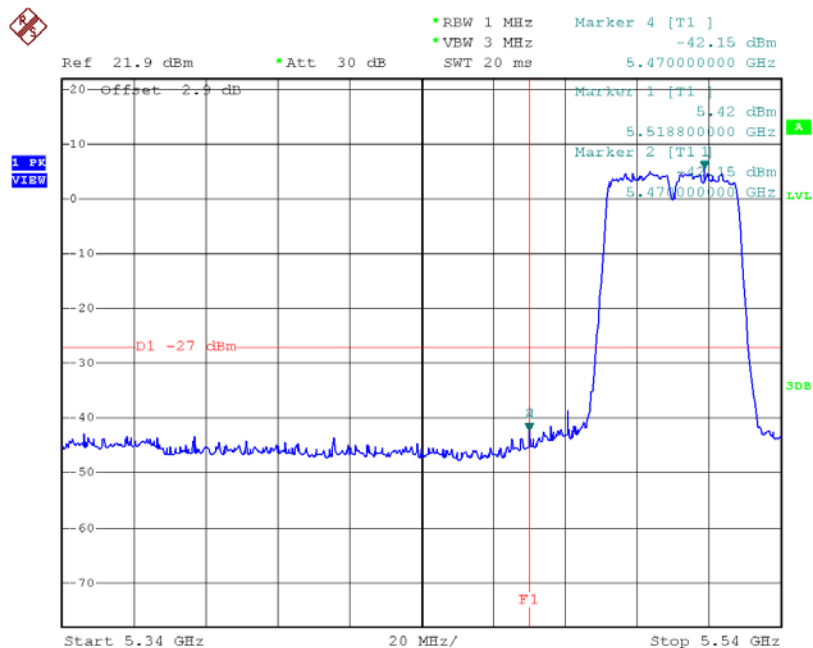
TX mode CH140



Date: 5.JUL.2015 20:00:59

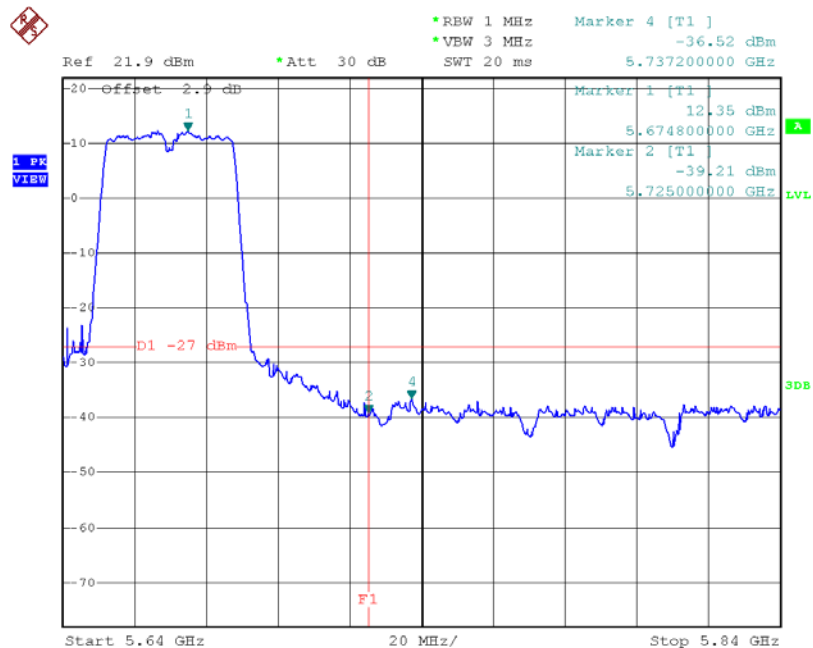
Test Mode: UNII-2C/TX AC40 Mode_ANT 4

TX mode CH102



Date: 16.AUG.2015 14:32:22

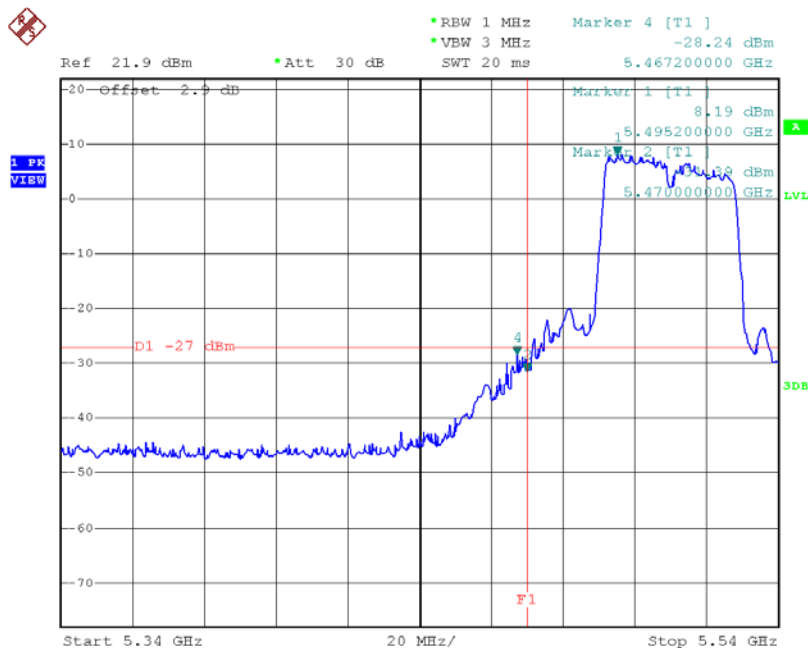
TX mode CH134



Date: 5.JUL.2015 21:44:28

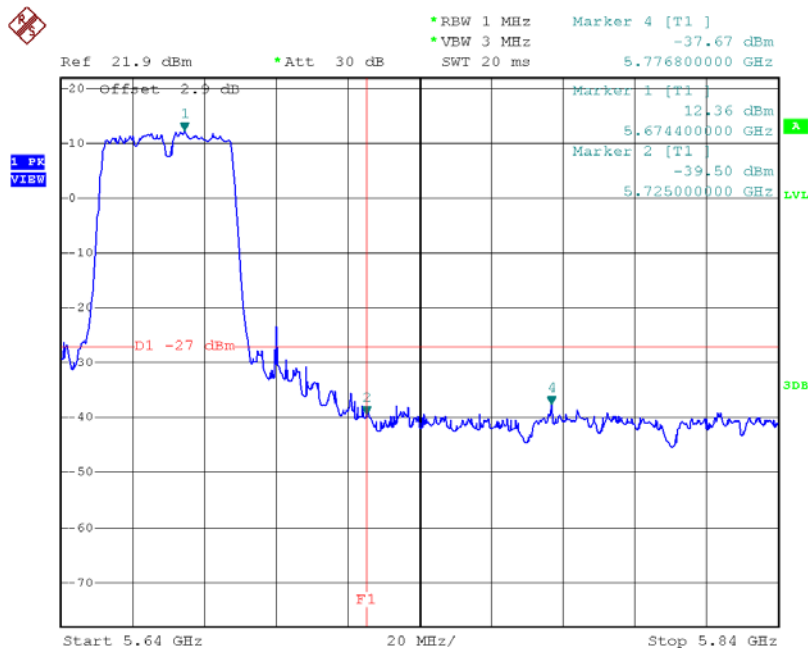
Test Mode:	UNII-2C/TX AC40 Mode_ANT 5
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TX mode CH102



Date: 16.AUG.2015 14:31:18

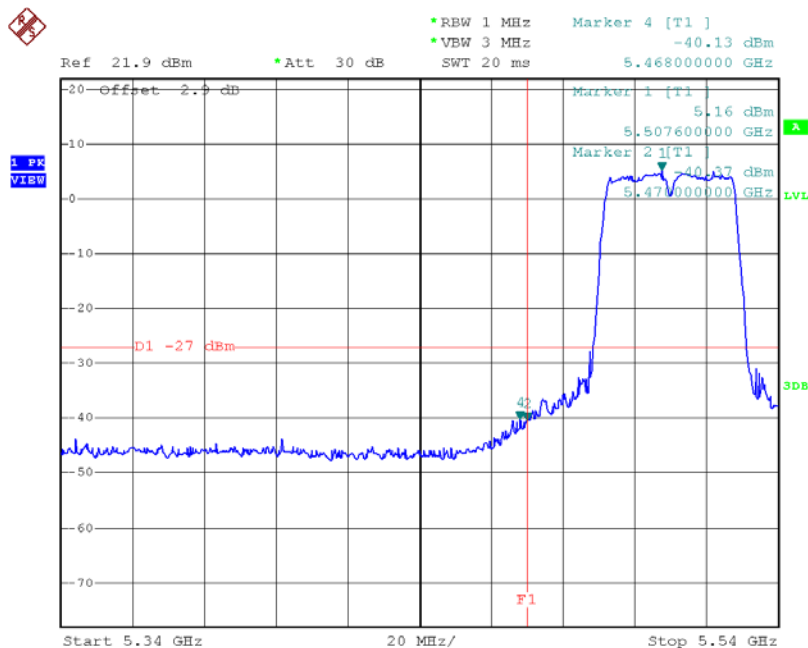
TX mode CH134



Date: 5.JUL.2015 21:54:40

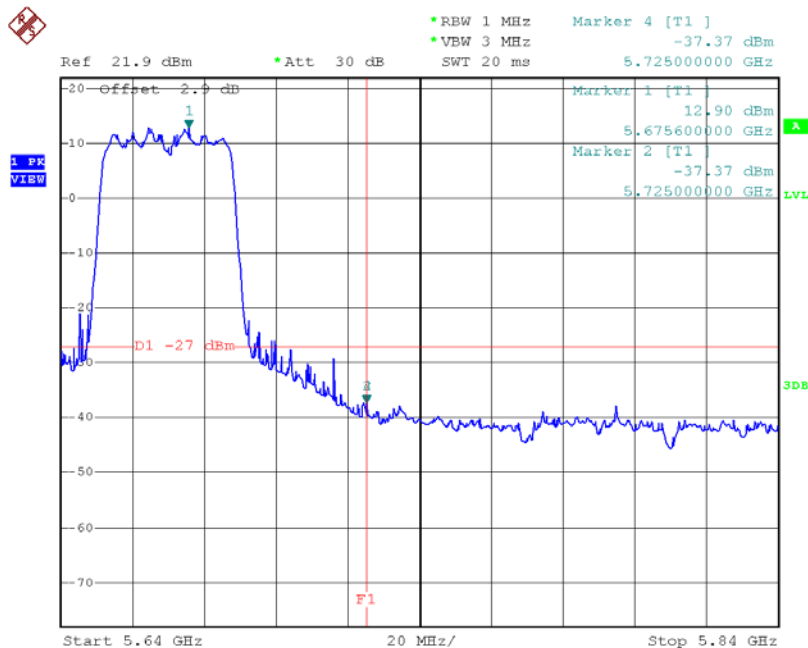
Test Mode:	UNII-2C/TX AC40 Mode_ANT 6
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TX mode CH102



Date: 16.AUG.2015 14:33:40

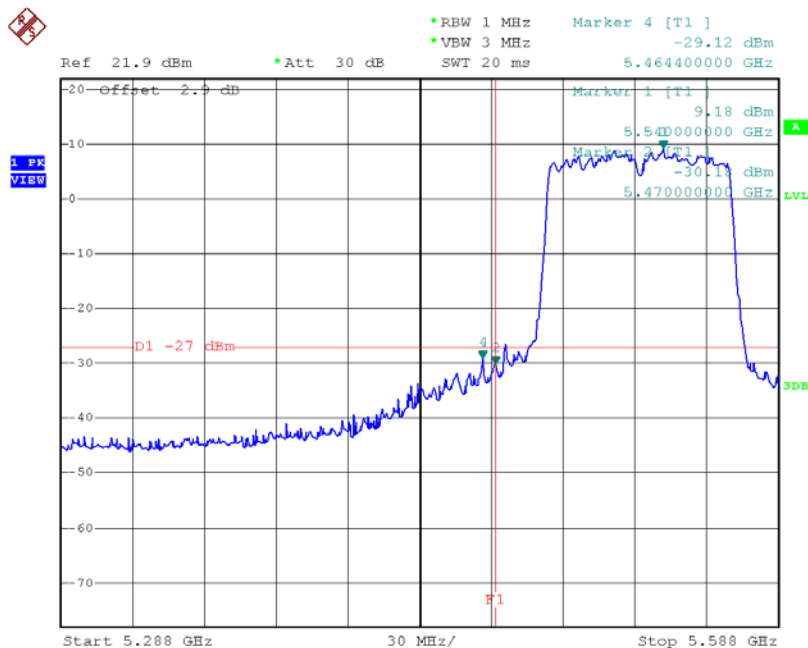
TX mode CH134



Date: 5.JUL.2015 21:18:29

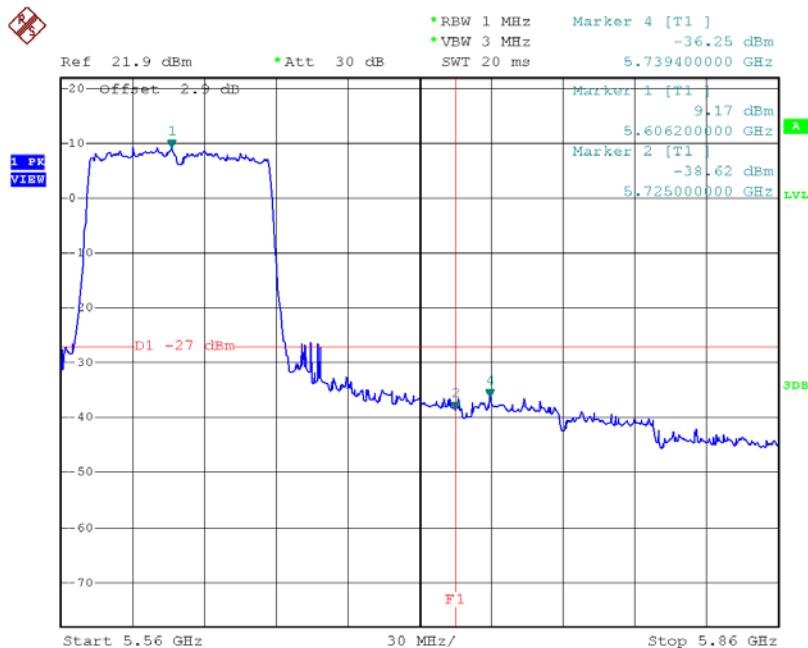
Test Mode: UNII-2C/TX AC80 Mode_ANT 4

TX mode CH106



Date: 5.JUL.2015 22:38:13

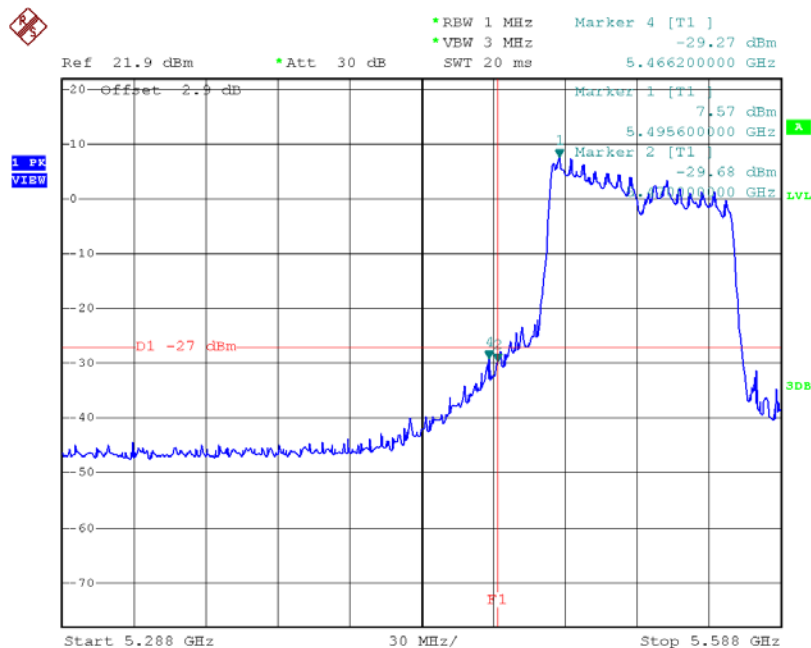
TX mode CH122



Date: 5.JUL.2015 22:39:20

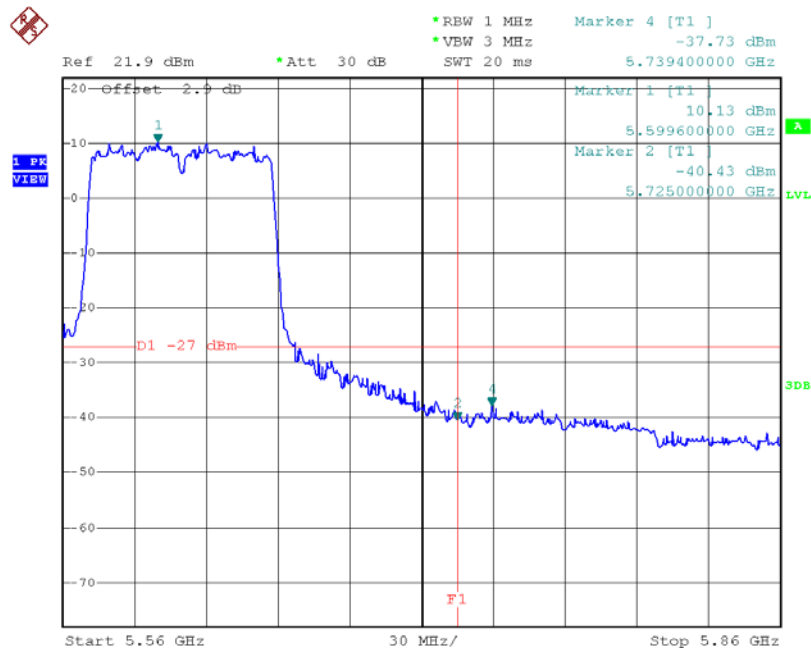
Test Mode:	UNII-2C/TX AC80 Mode_ANT 5
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TX mode CH106



Date: 5.JUL.2015 22:46:53

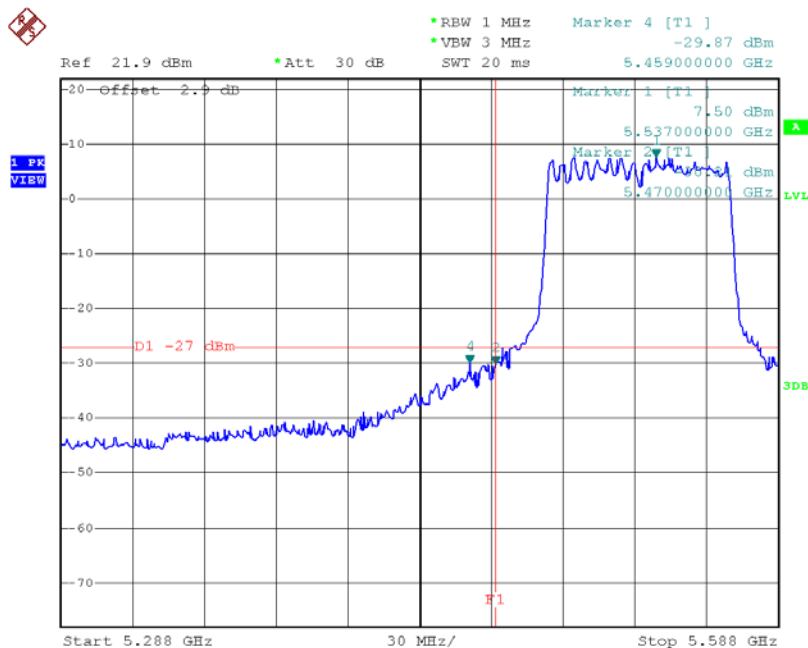
TX mode CH122



Date: 5.JUL.2015 22:28:35

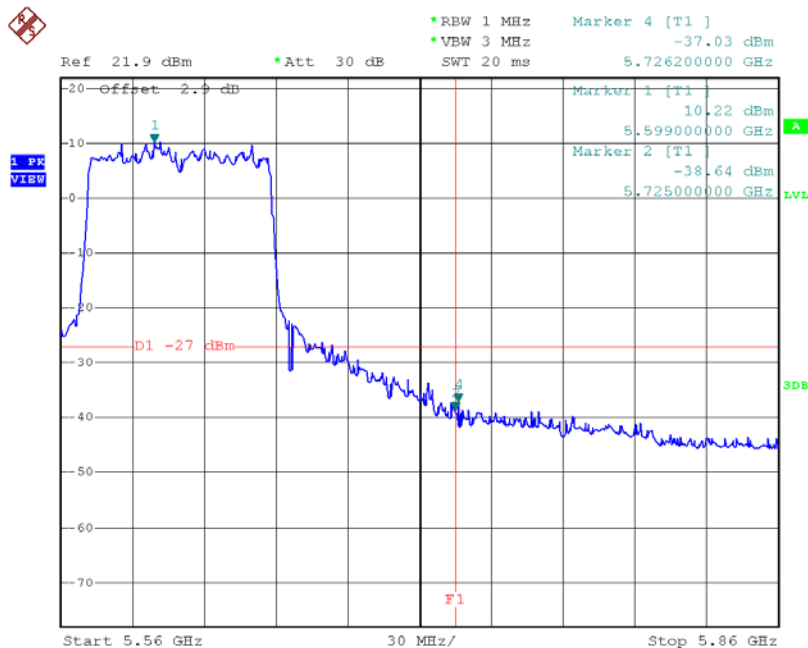
Test Mode:	UNII-2C/TX AC80 Mode_ANT 6
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TX mode CH106



Date: 5.JUL.2015 22:16:47

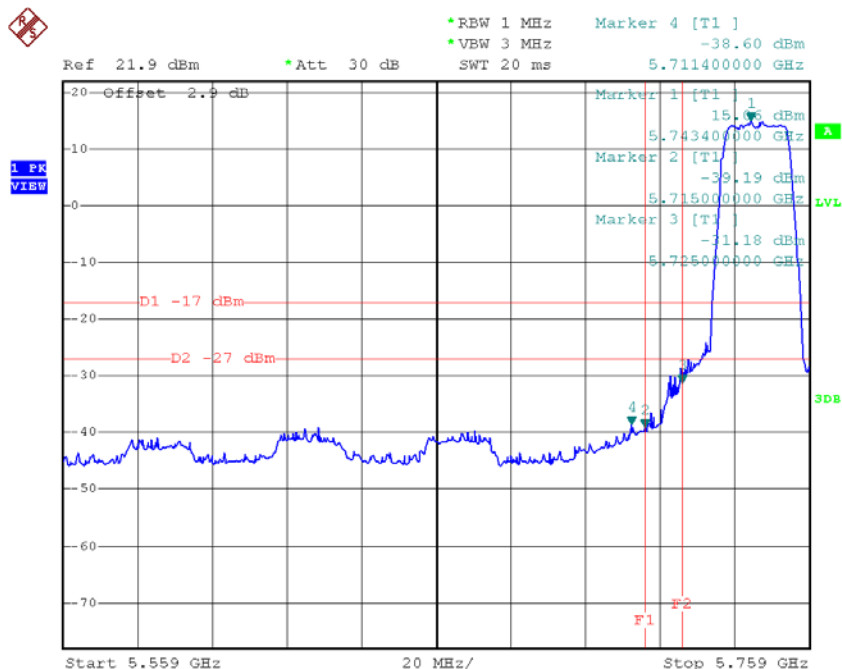
TX mode CH122



Date: 5.JUL.2015 22:18:24

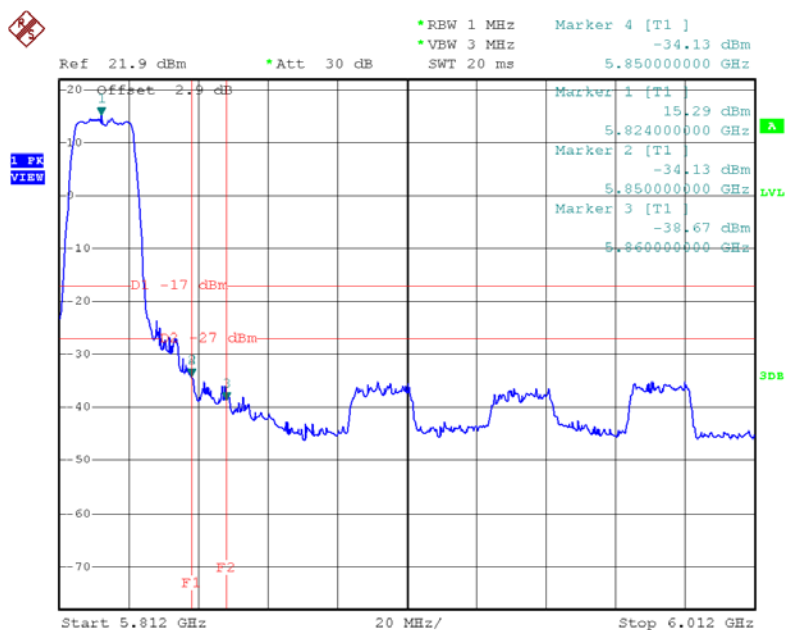
Test Mode: UNII-3/TX AC20 Mode_ANT 4

TX AC HT20 mode CH149



Date: 5.JUL.2015 19:37:41

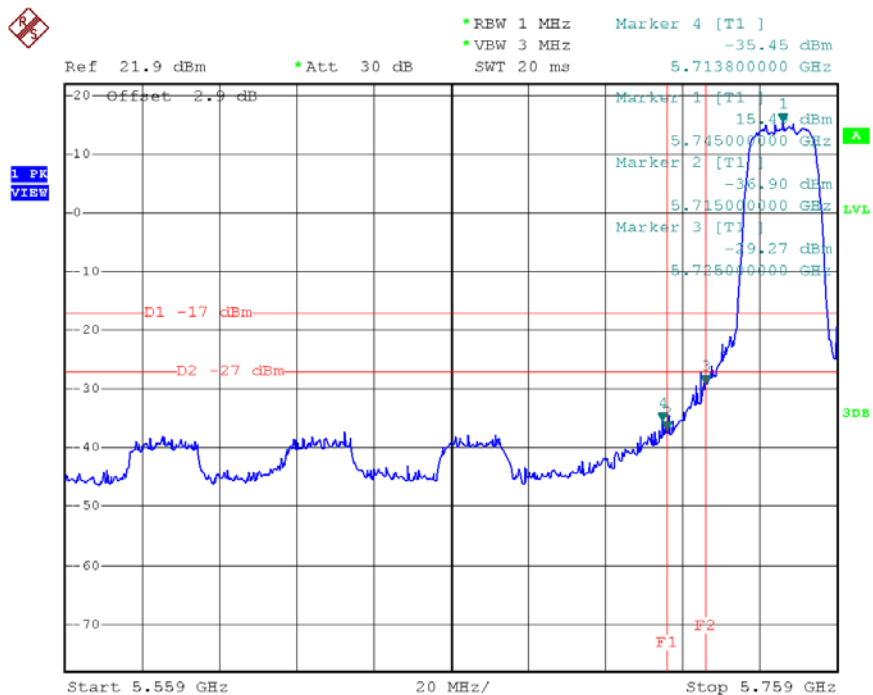
TX AC HT20 mode CH165



Date: 5.JUL.2015 19:39:16

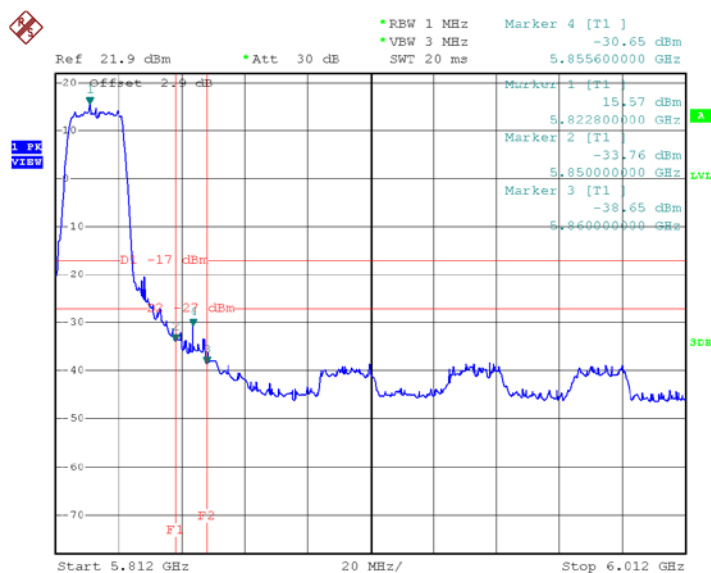
Test Mode: UNII-3/TX AC20 Mode_ANT 6

TX AC HT20 mode CH149



Date: 5.JUL.2015 20:01:50

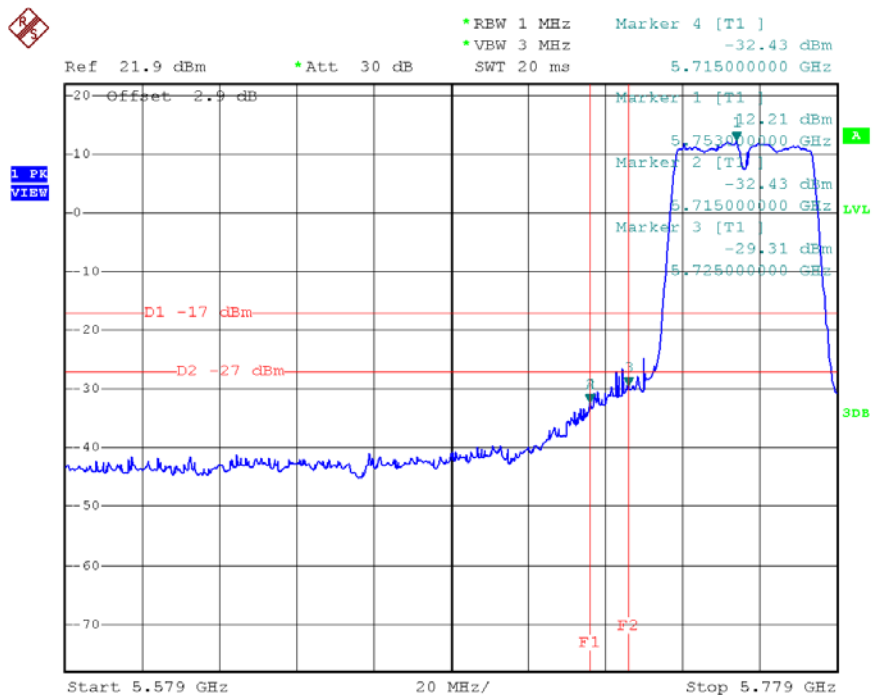
TX AC HT20 mode CH165



Date: 5.JUL.2015 20:03:25

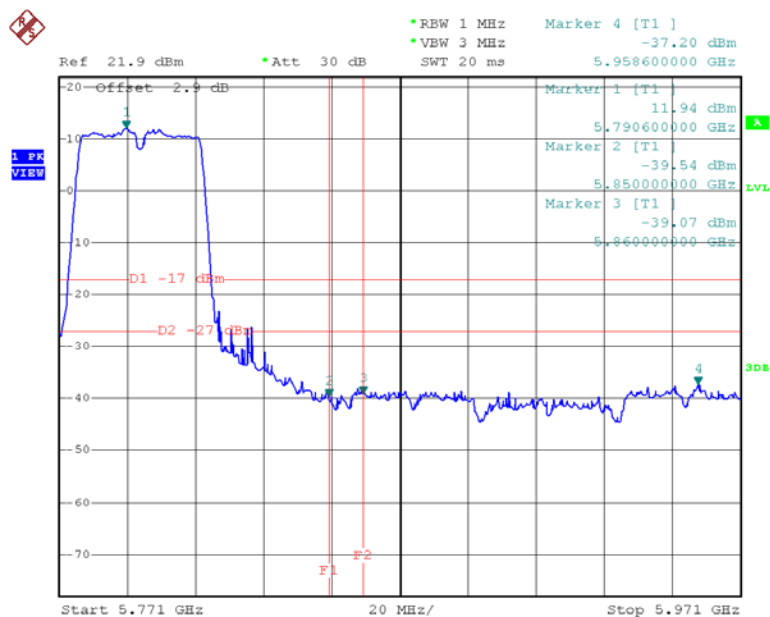
Test Mode: UNII-3/TX AC40 Mode_ANT 4

TX AC HT40 mode CH151



Date: 5.JUL.2015 21:45:30

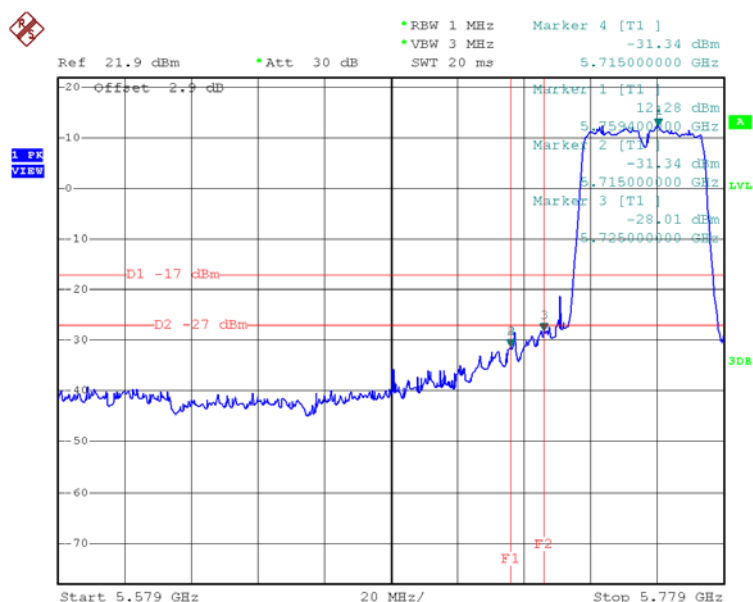
TX AC HT40 mode CH159



Date: 5.JUL.2015 21:46:34

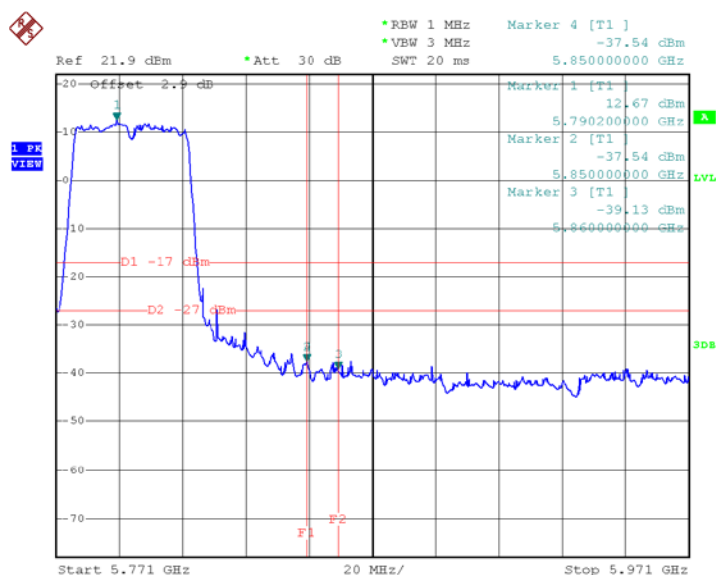
Test Mode: UNII-3/TX AC40 Mode_ANT 5

TX AC HT40 mode CH151



Date: 5.JUL.2015 21:55:40

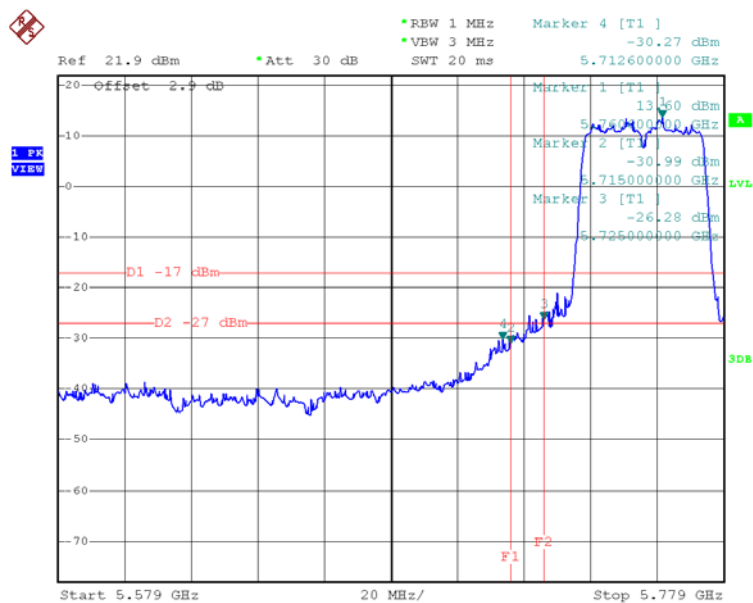
TX AC HT40 mode CH159



Date: 5.JUL.2015 21:56:42

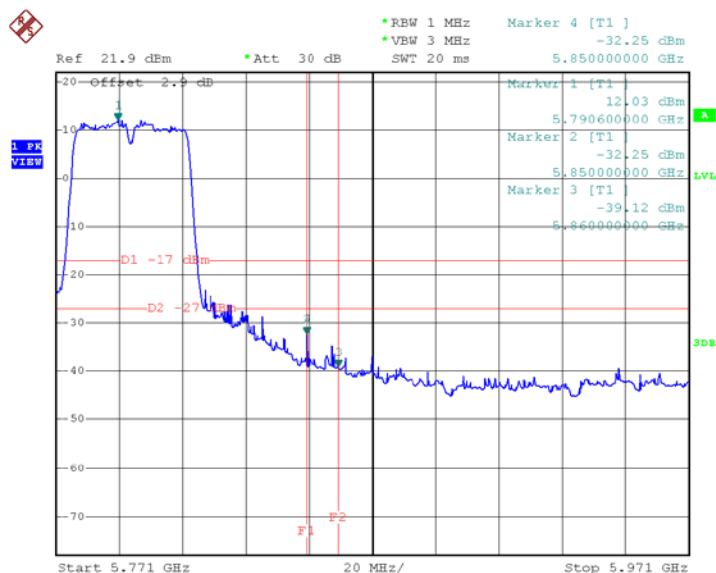
Test Mode: UNII-3/TX AC40 Mode_ANT 6

TX AC HT40 mode CH151



Date: 5.JUL.2015 21:22:05

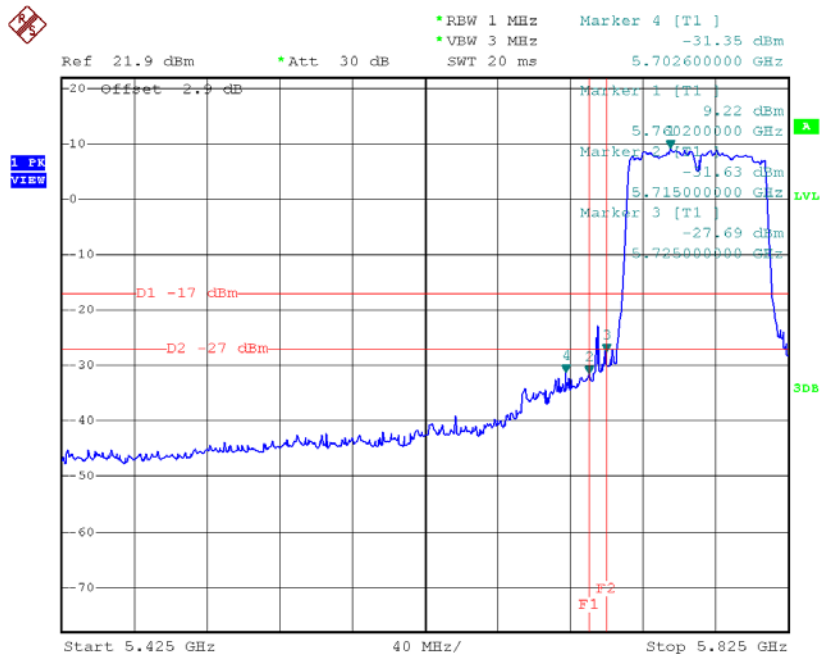
TX AC HT40 mode CH159



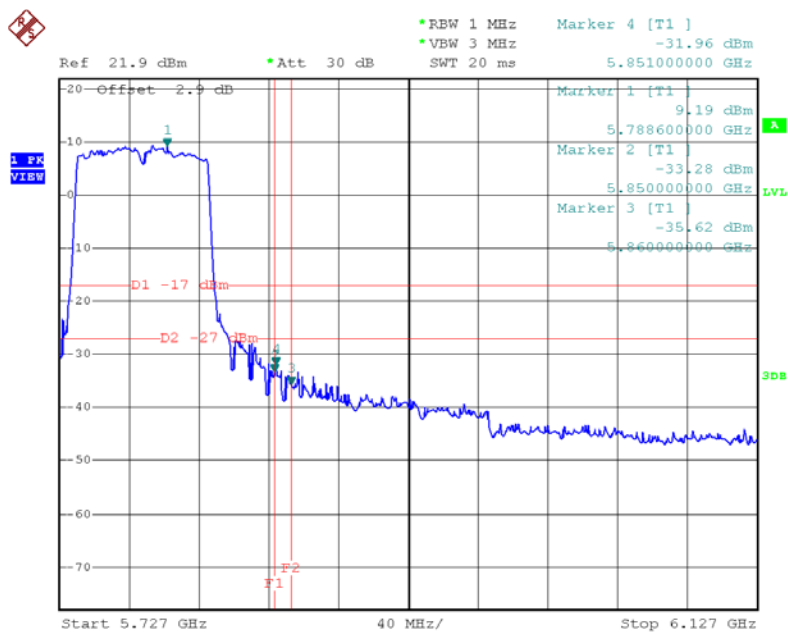
Date: 5.JUL.2015 21:23:21

Test Mode: UNII-3/TX AC80 Mode_ANT 4

TX AC HT80 mode CH155



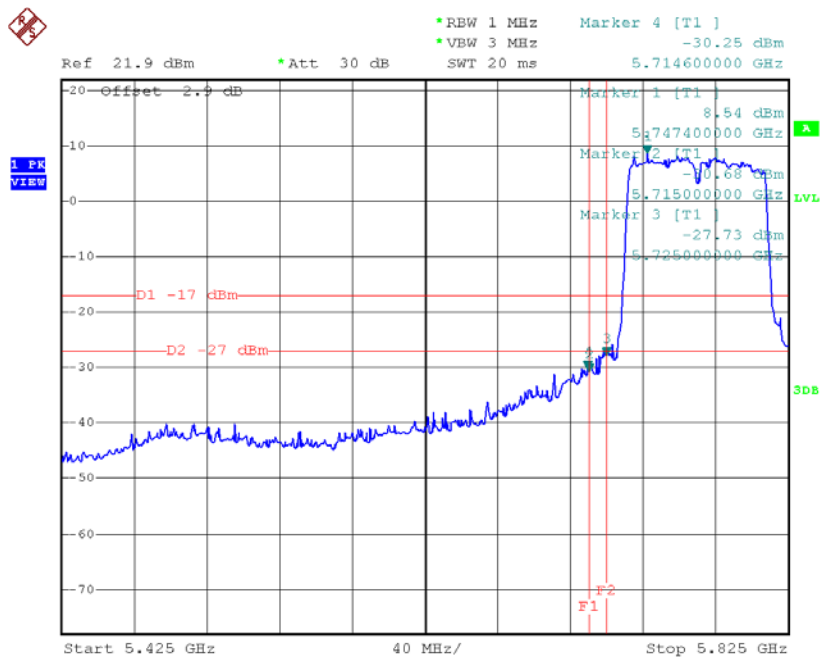
Date: 5.JUL.2015 22:40:28



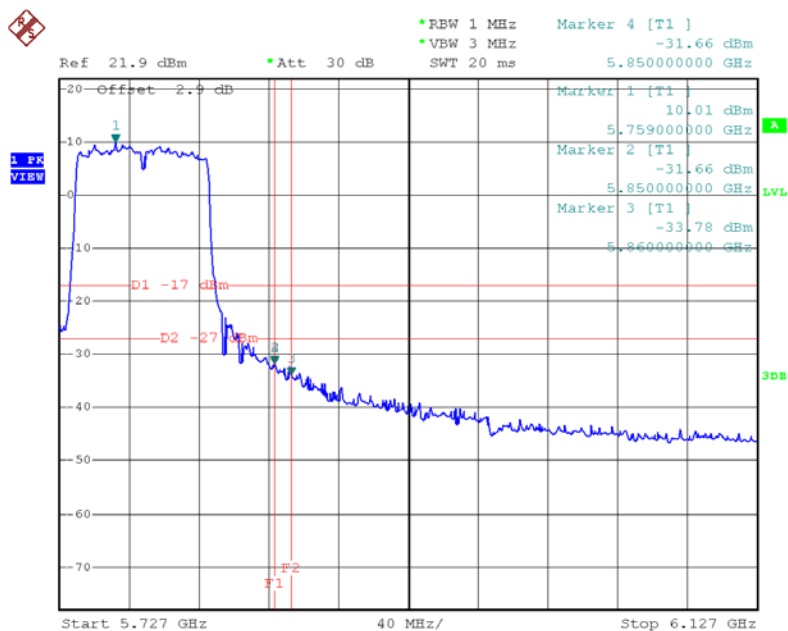
Date: 5.JUL.2015 22:40:37

Test Mode: UNII-3/TX AC80 Mode_ANT 5

TX AC HT80 mode CH155



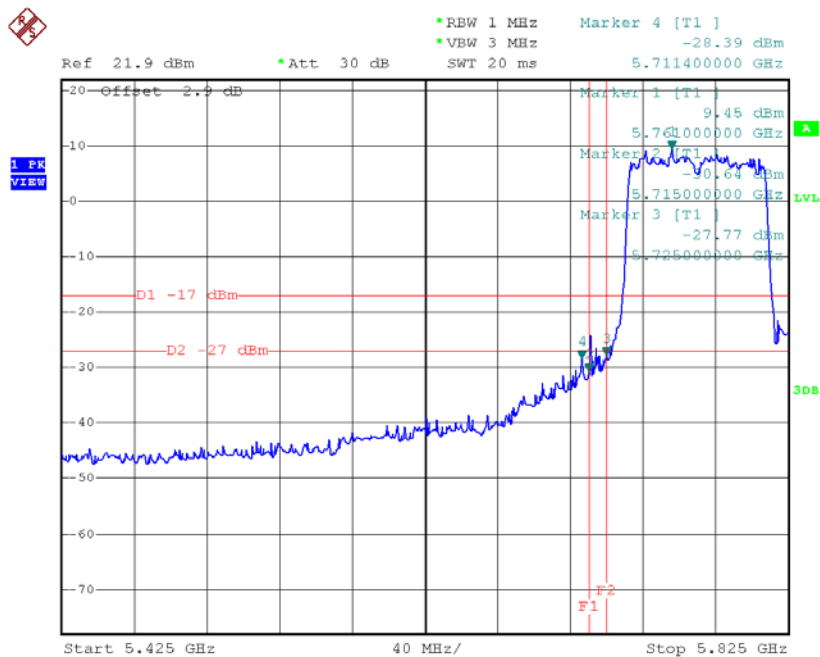
Date: 5.JUL.2015 22:51:33



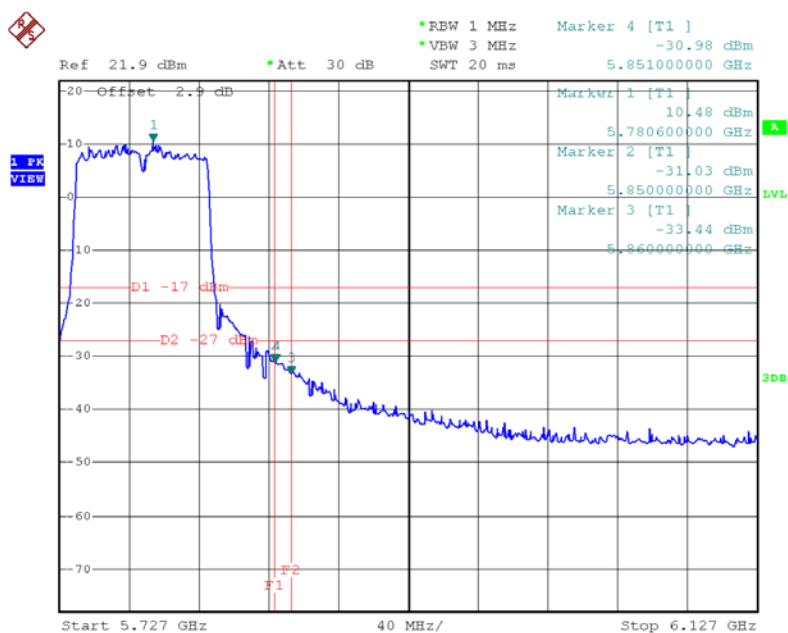
Date: 5.JUL.2015 22:30:12

Test Mode: UNII-3/TX AC80 Mode_ANT 6

TX AC HT80 mode CH155



Date: 5.JUL.2015 22:51:05

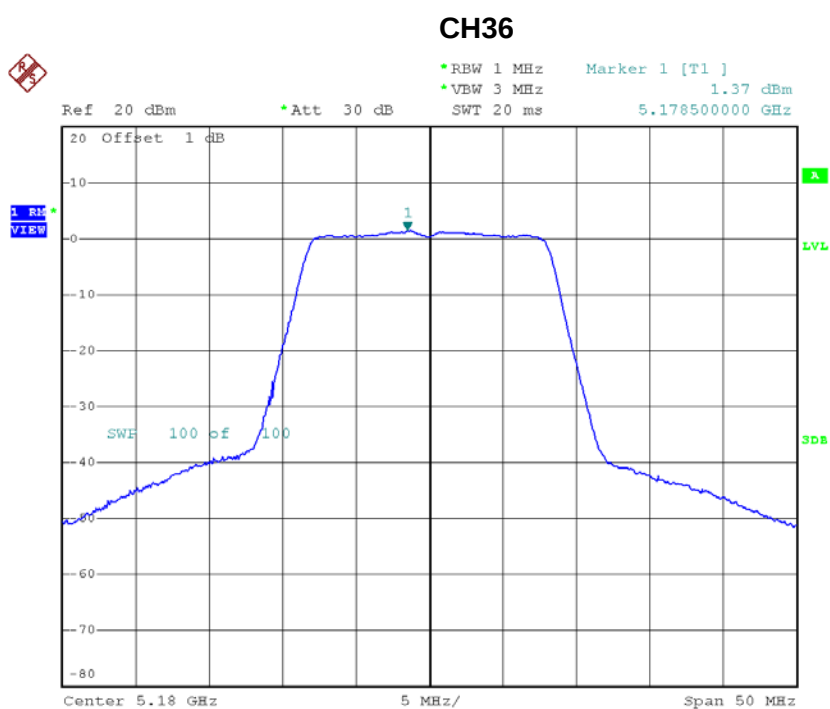


Date: 5.JUL.2015 22:20:04

ATTACHMENT H - POWER SPECTRAL DENSITY

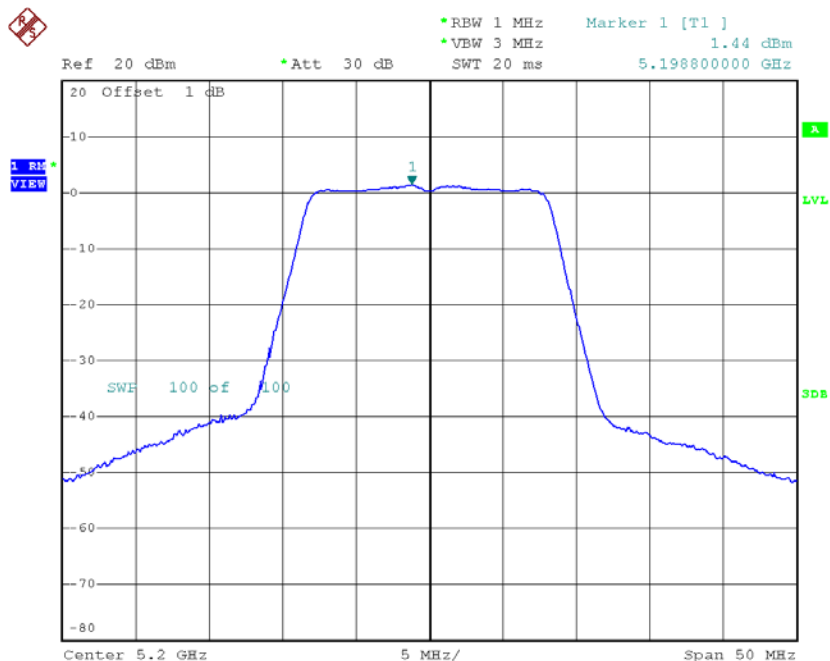
Test Mode: UNII-1/ TX A Mode_CH36/CH40/CH48

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	1.37	0.12	1.49	17.00
CH40	5200	1.44	0.12	1.56	17.00
CH48	5240	1.65	0.12	1.77	17.00



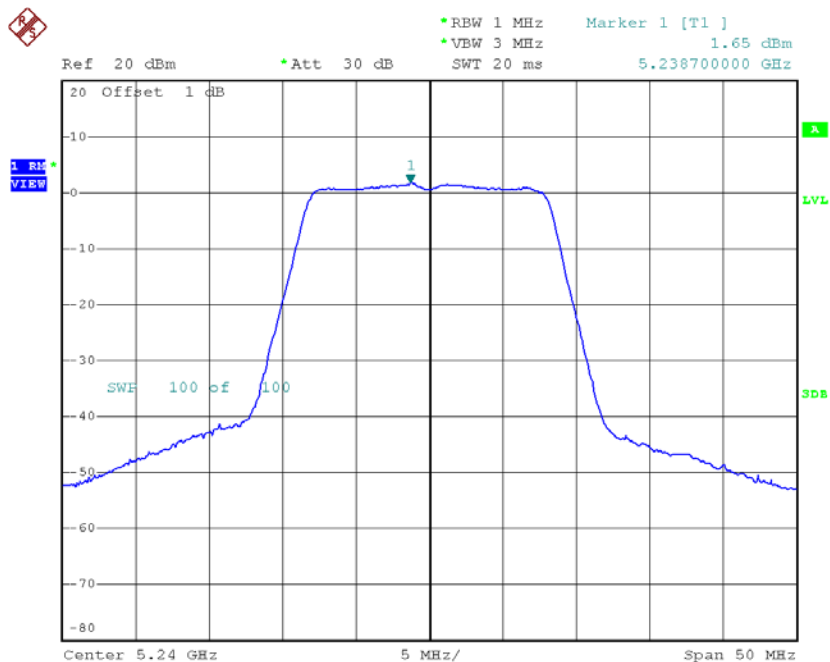
Date: 5.JUL.2015 17:52:02

CH40



Date: 5.JUL.2015 17:54:10

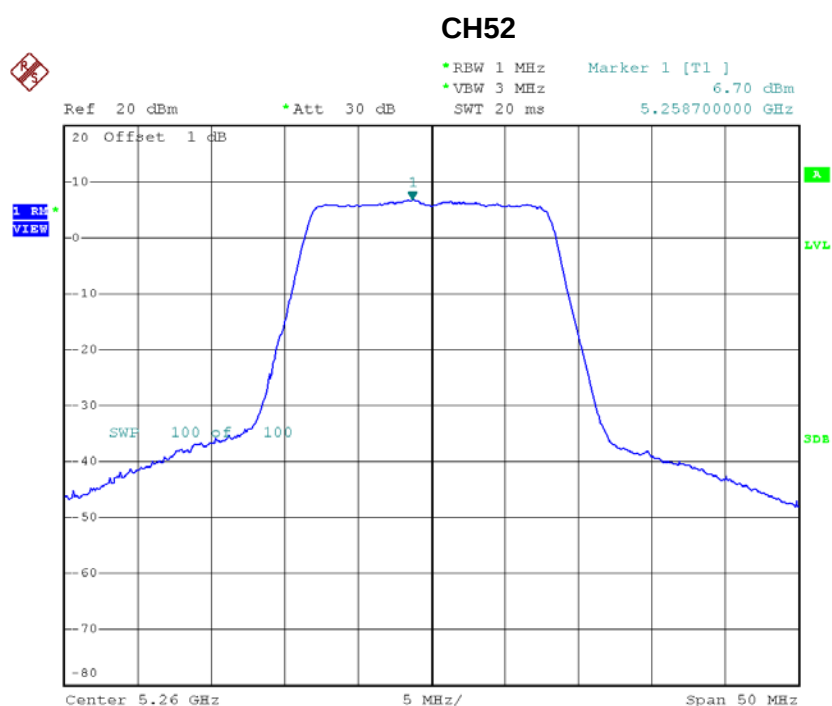
CH48



Date: 5.JUL.2015 17:55:07

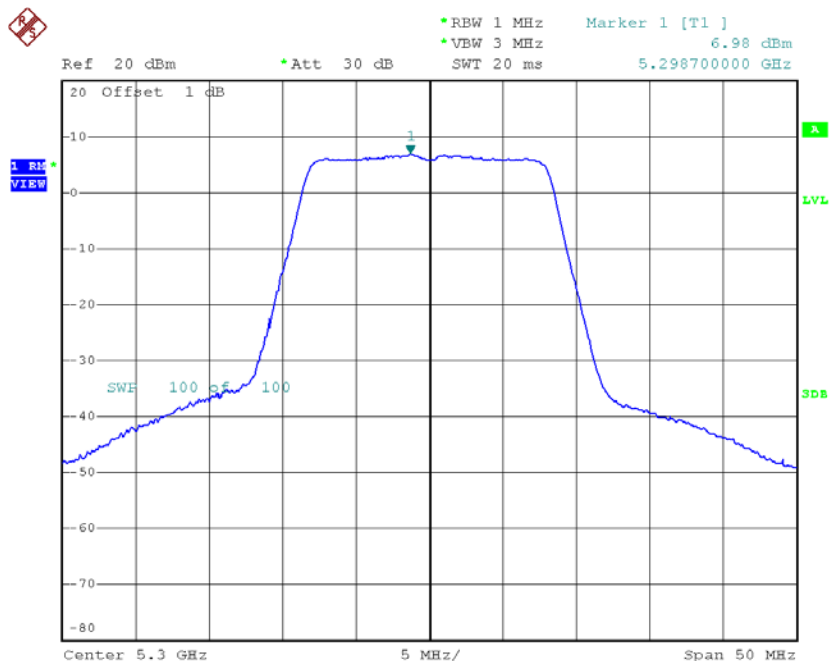
Test Mode: UNII-2A/ TX A Mode_CH52/CH60/CH64

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH52	5260	6.70	0.12	6.82	11.00
CH60	5300	6.98	0.12	7.10	11.00
CH64	5320	6.85	0.12	6.97	11.00



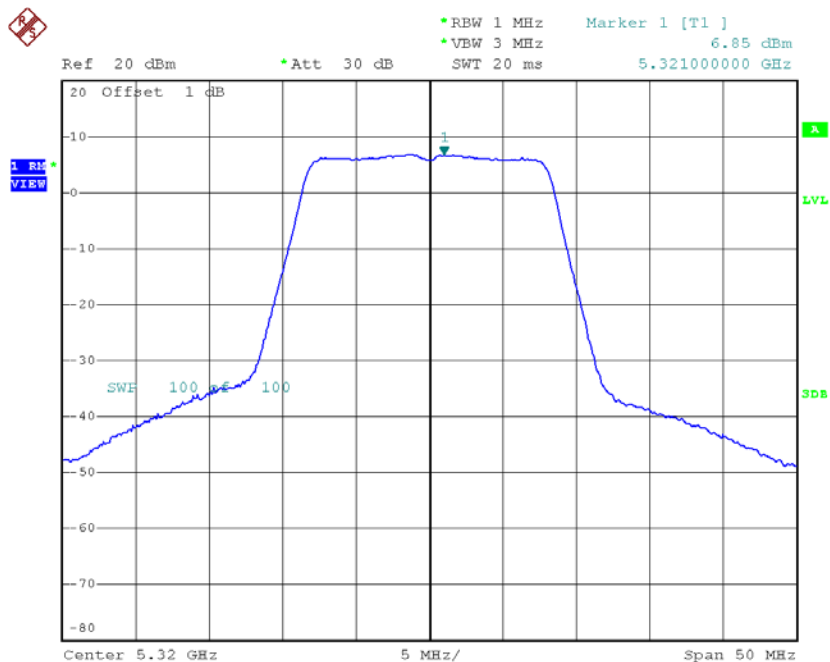
Date: 5.JUL.2015 17:56:23

CH60



Date: 5.JUL.2015 17:58:03

CH64

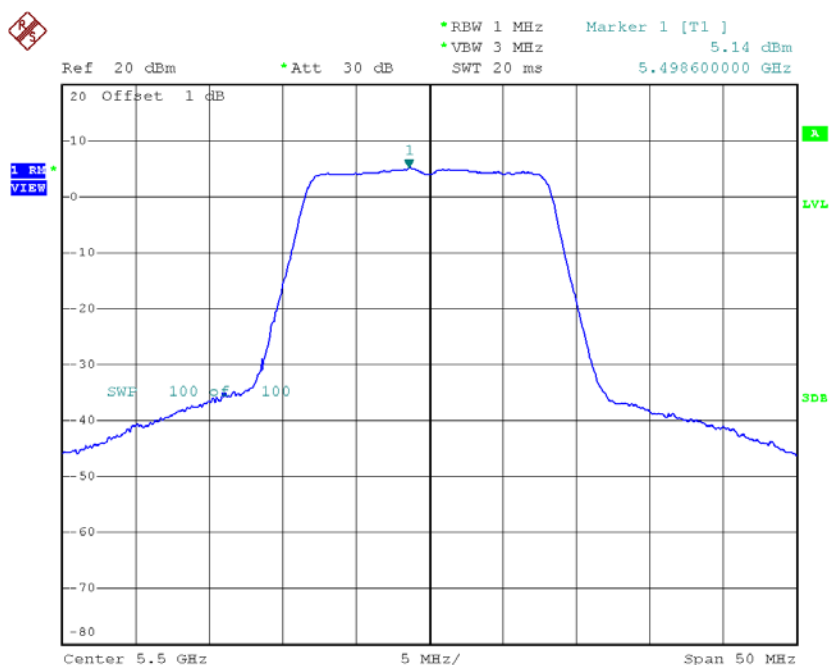


Date: 5.JUL.2015 17:58:56

Test Mode: UNII-2C/ TX A Mode_CH100/CH116/CH140

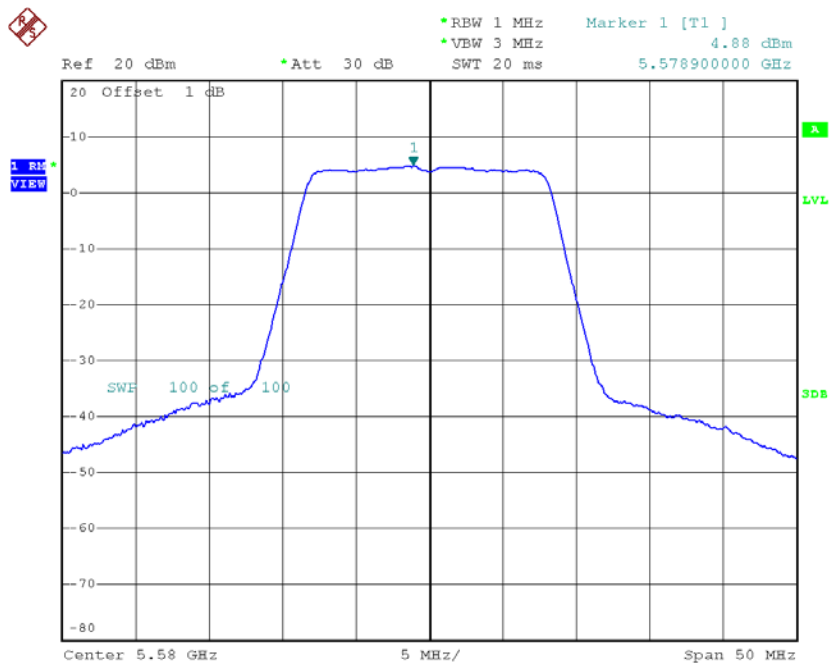
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH100	5500	5.14	0.12	5.26	11.00
CH116	5580	4.88	0.12	5.00	11.00
CH140	5700	5.16	0.12	5.28	11.00

CH100



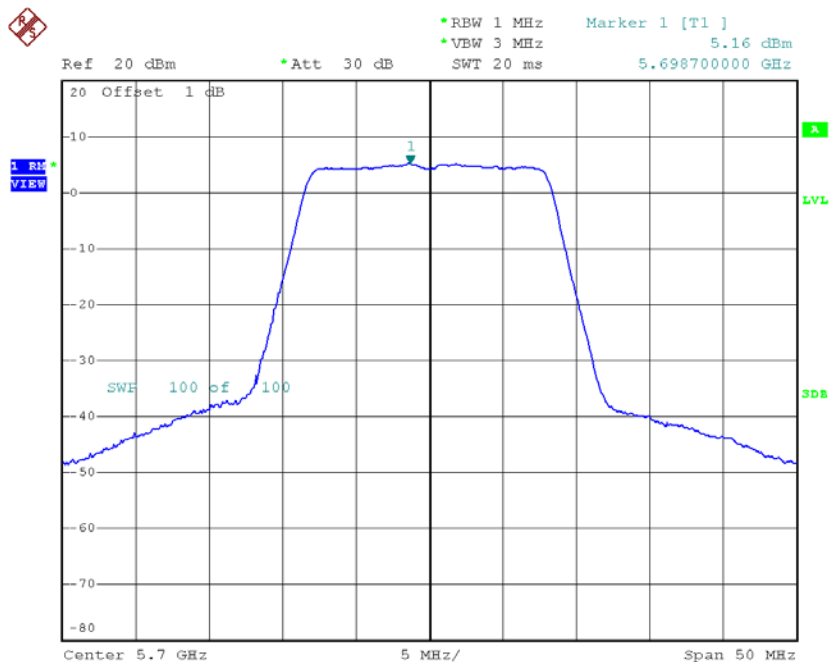
Date: 5.JUL.2015 18:00:31

CH116



Date: 5.JUL.2015 18:02:05

CH140

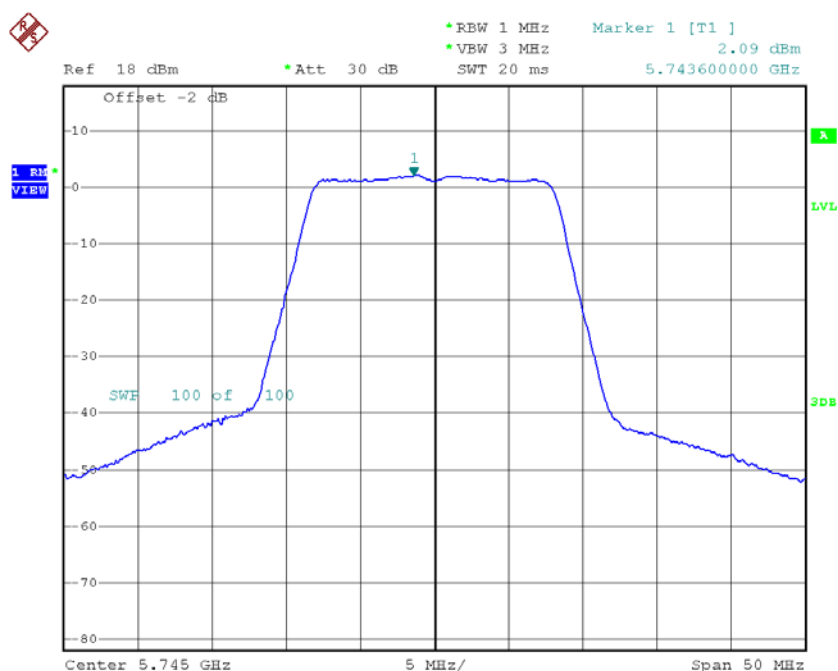


Date: 5.JUL.2015 18:02:56

Test Mode: UNII-3/TX A Mode_CH149/CH157/CH165

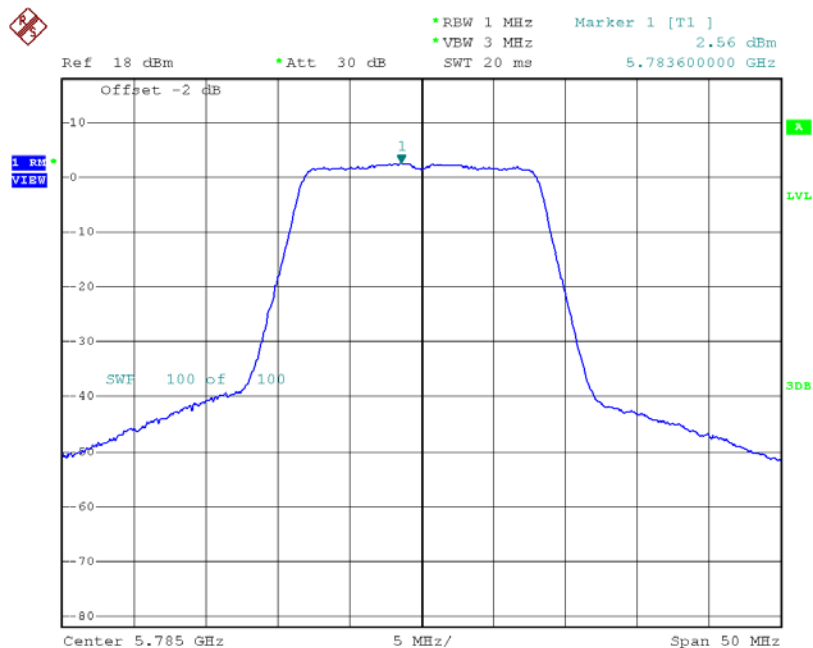
Channel	Frequency (MHz)	Power Density (dBm/500kHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/500kHz)	Limit (dBm/500kHz)
CH149	5745	2.09	0.12	2.21	30.00
CH157	5785	2.56	0.12	2.68	30.00
CH165	5825	2.77	0.12	2.89	30.00

TX CH149



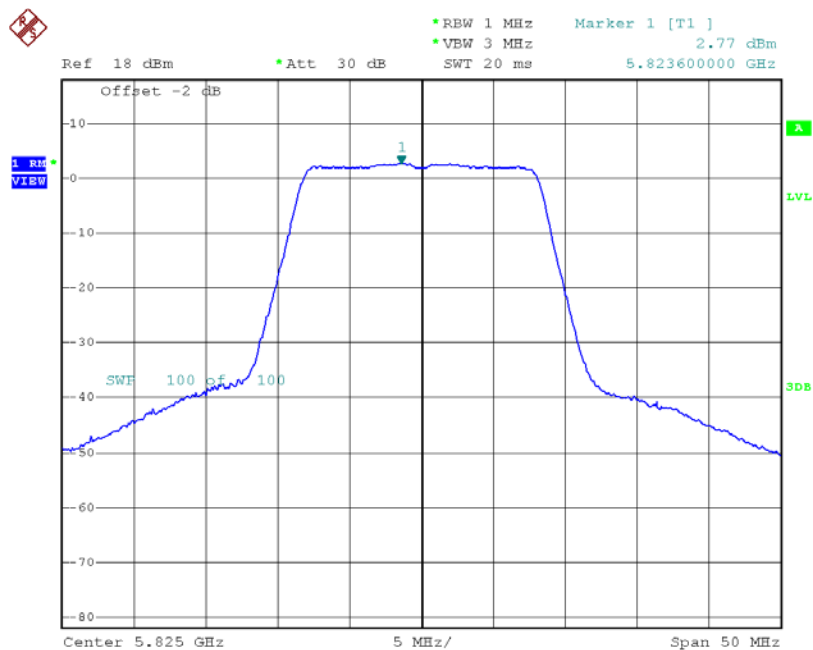
Date: 5.JUL.2015 18:03:51

TX CH157



Date: 5.JUL.2015 18:05:44

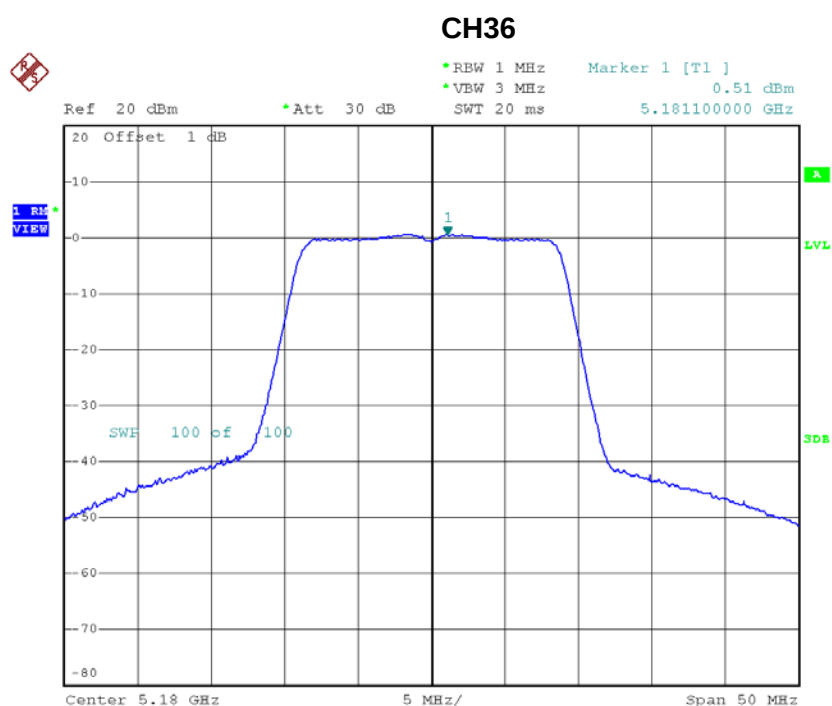
TX CH165



Date: 5.JUL.2015 18:06:42

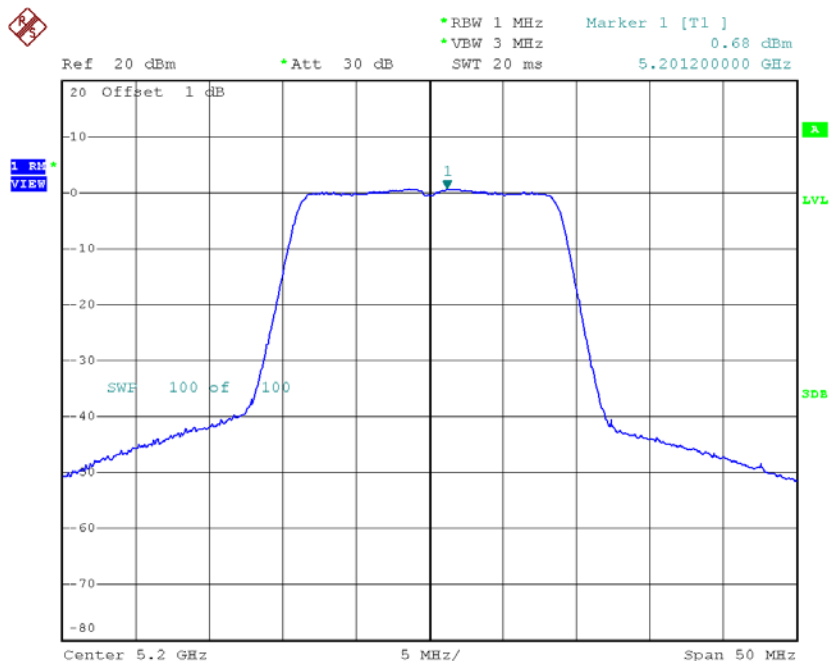
Test Mode: UNII-1/TX N20 Mode_CH36/CH40/CH48_ANT 4

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	0.51	0.47	0.98	17.00
CH40	5200	0.68	0.47	1.15	17.00
CH48	5240	0.46	0.47	0.93	17.00



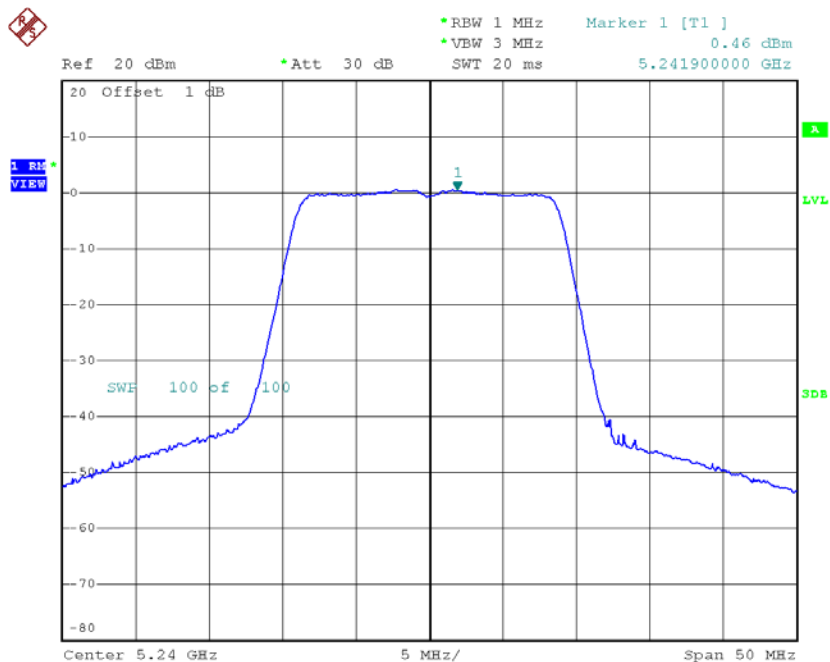
Date: 5.JUL.2015 18:09:02

CH40



Date: 5.JUL.2015 18:10:04

CH48

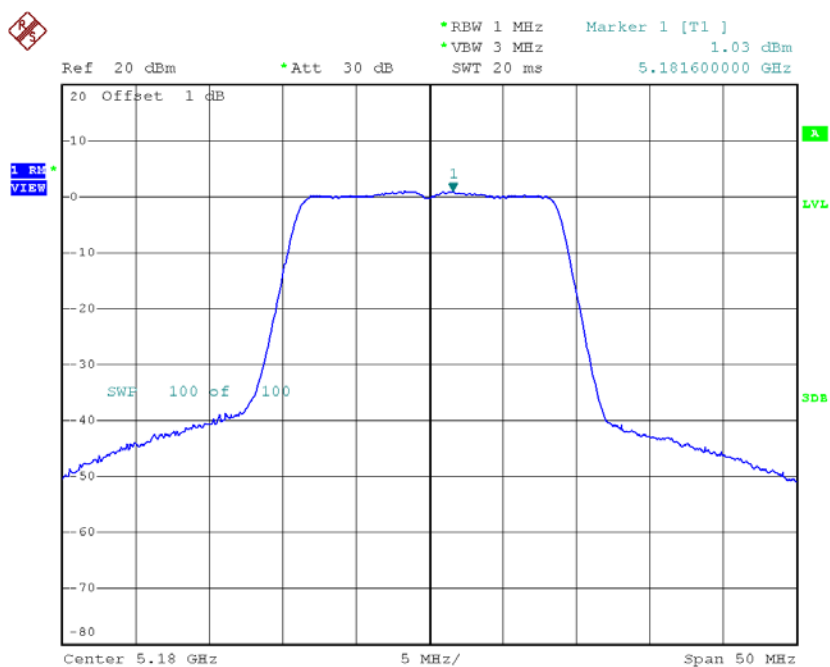


Date: 5.JUL.2015 18:10:46

Test Mode: UNII-1/TX N20 Mode_CH36/CH40/CH48_ANT 5

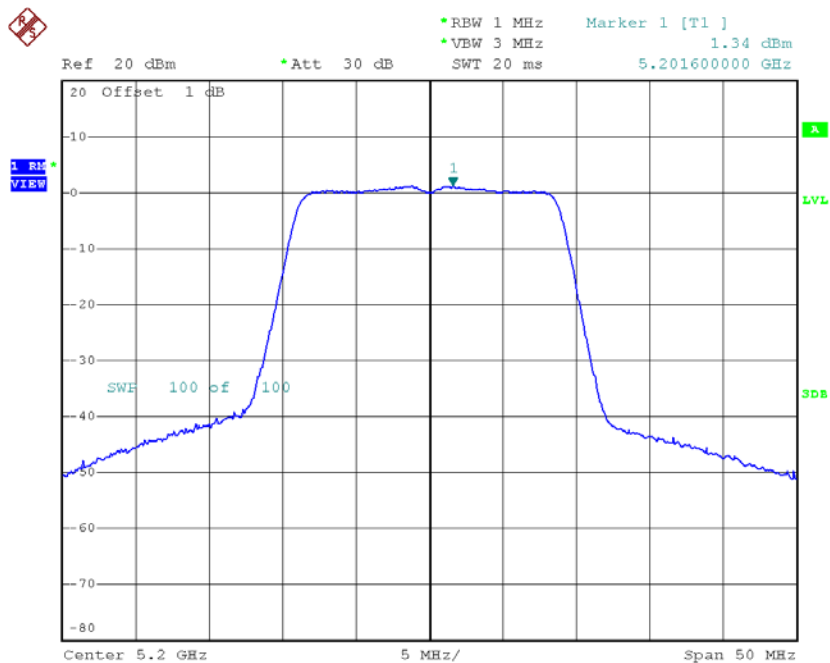
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	1.03	0.47	1.50	17.00
CH40	5200	1.34	0.47	1.81	17.00
CH48	5240	0.57	0.47	1.04	17.00

CH36



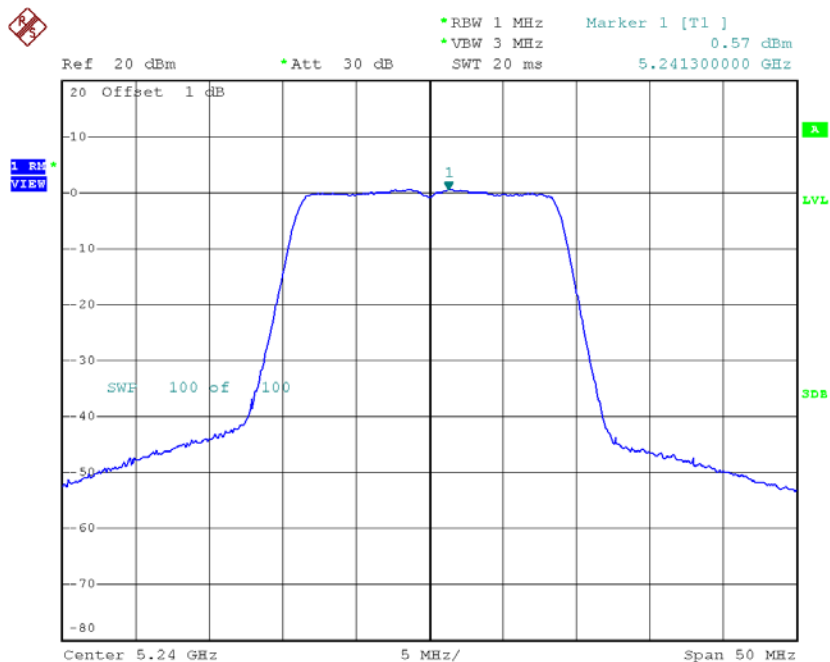
Date: 5.JUL.2015 18:20:52

CH40



Date: 5.JUL.2015 18:22:03

CH48

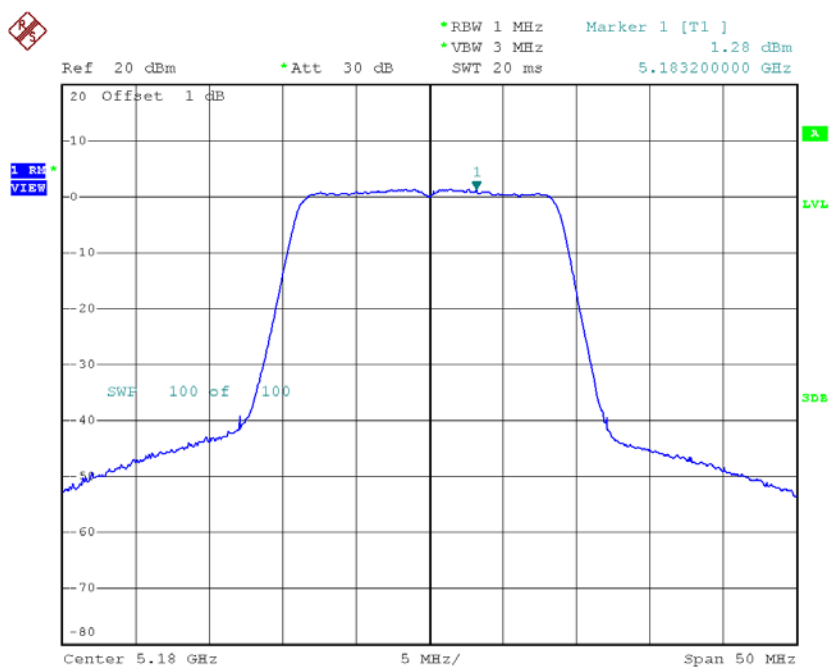


Date: 5.JUL.2015 18:22:58

Test Mode: UNII-1/TX N20 Mode_CH36/CH40/CH48_ANT 6

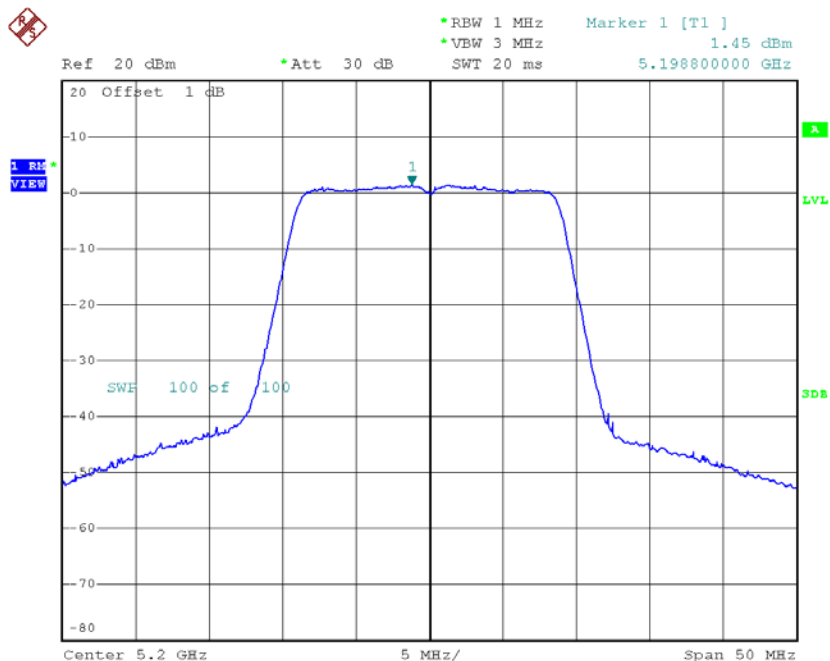
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	1.28	0.47	1.75	17.00
CH40	5200	1.45	0.47	1.92	17.00
CH48	5240	0.13	0.47	0.60	17.00

CH36



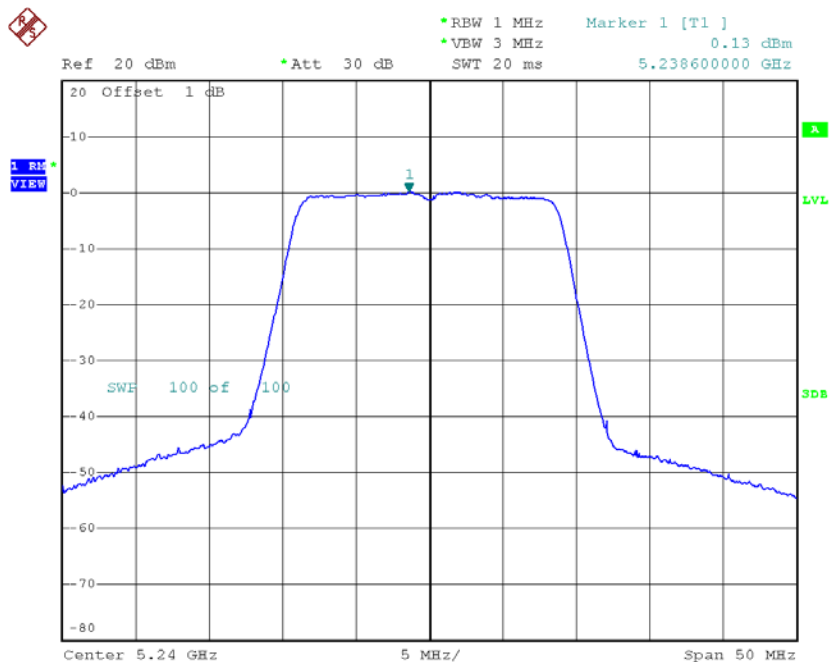
Date: 5.JUL.2015 18:49:40

CH40



Date: 5.JUL.2015 18:50:32

CH48



Date: 5.JUL.2015 18:51:13

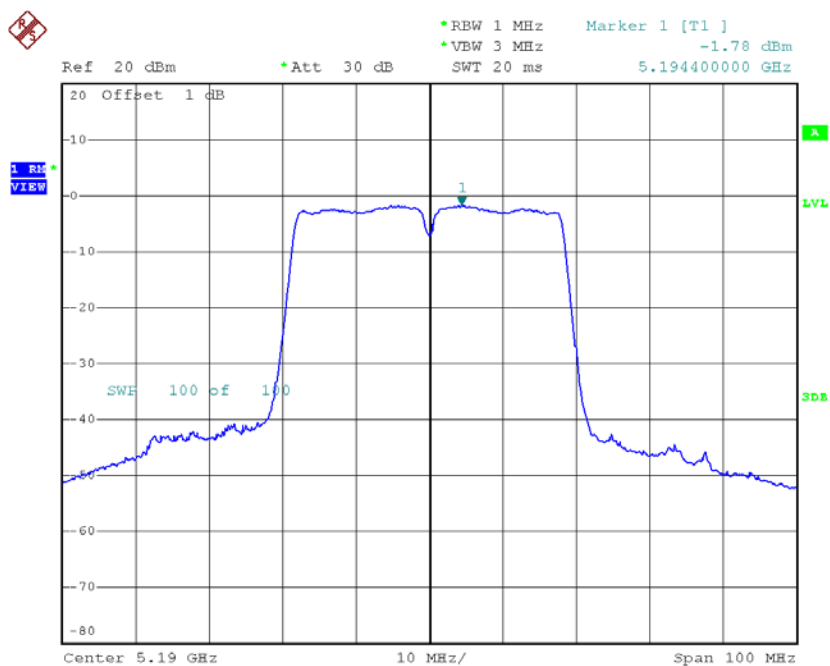
Test Mode: UNII-1/TX N20 Mode_CH36/CH40/CH48_Total

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Limit (dBm/MHz)
CH36	5180	6.19	17.00
CH40	5200	6.41	17.00
CH48	5240	5.63	17.00

Test Mode: UNII-1/TX N40 Mode_CH38/CH46_ANT 4

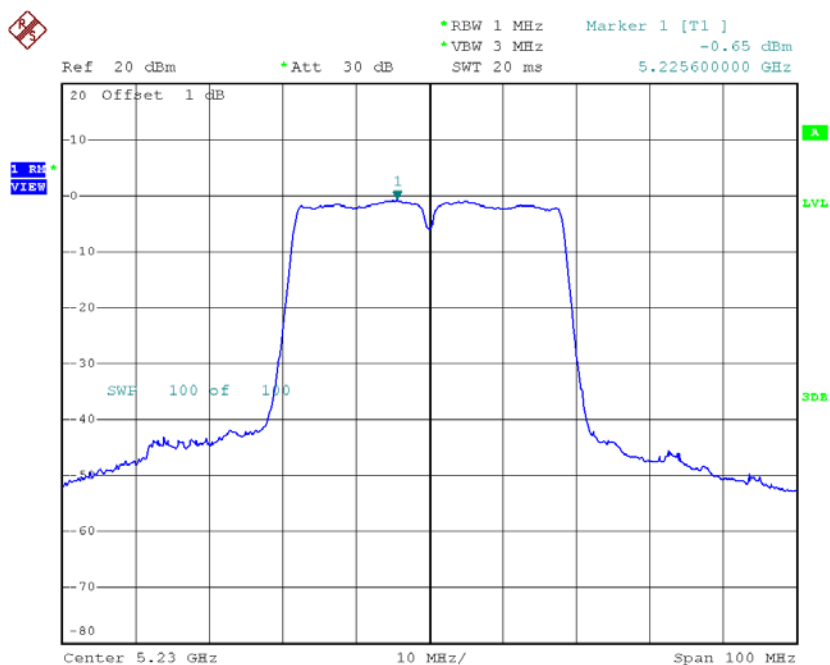
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH38	5190	-1.78	0.83	-0.95	17.00
CH46	5230	-0.65	0.83	0.18	17.00

CH38



Date: 5.JUL.2015 20:28:21

CH46

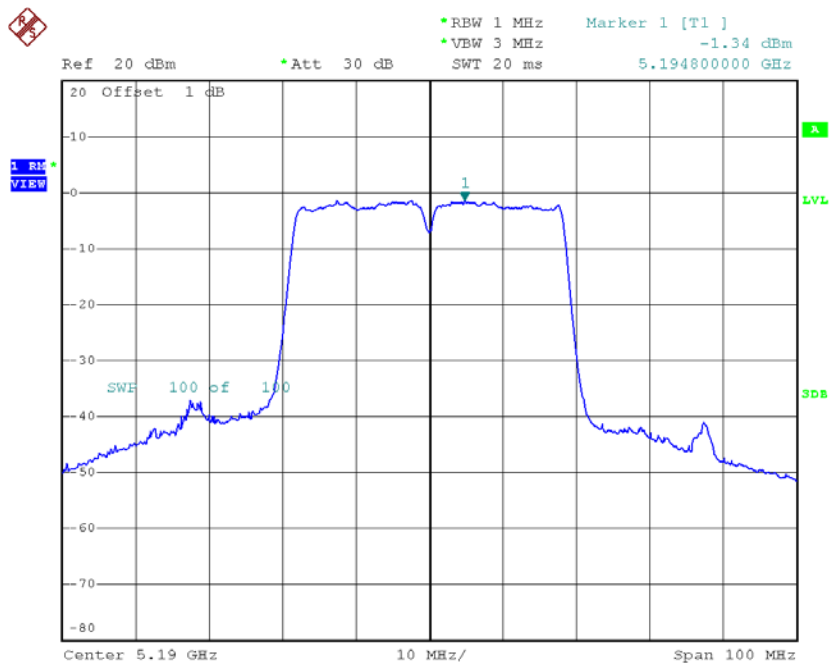


Date: 5.JUL.2015 20:29:59

Test Mode: UNII-1/TX N40 Mode_CH38/CH46_ANT 5

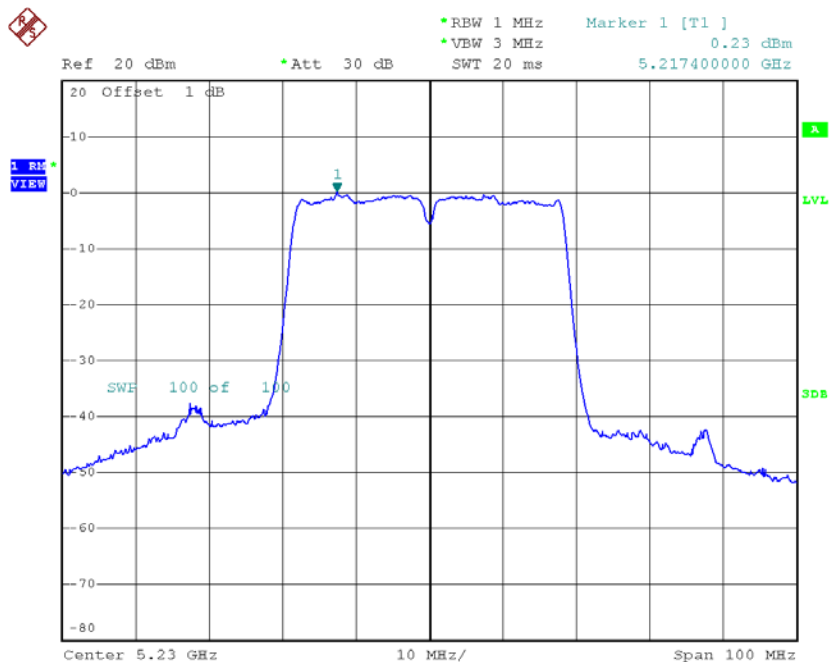
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH38	5190	-1.34	0.83	-0.51	17.00
CH46	5230	0.23	0.83	1.06	17.00

CH38



Date: 5.JUL.2015 20:38:41

CH46

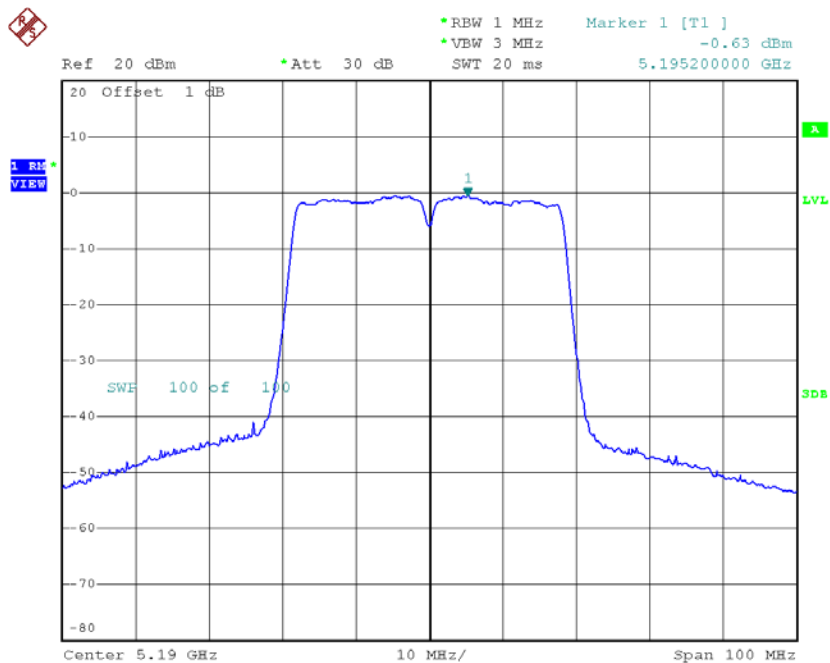


Date: 5.JUL.2015 20:40:46

Test Mode: UNII-1/TX N40 Mode_CH38/CH46_ANT 6

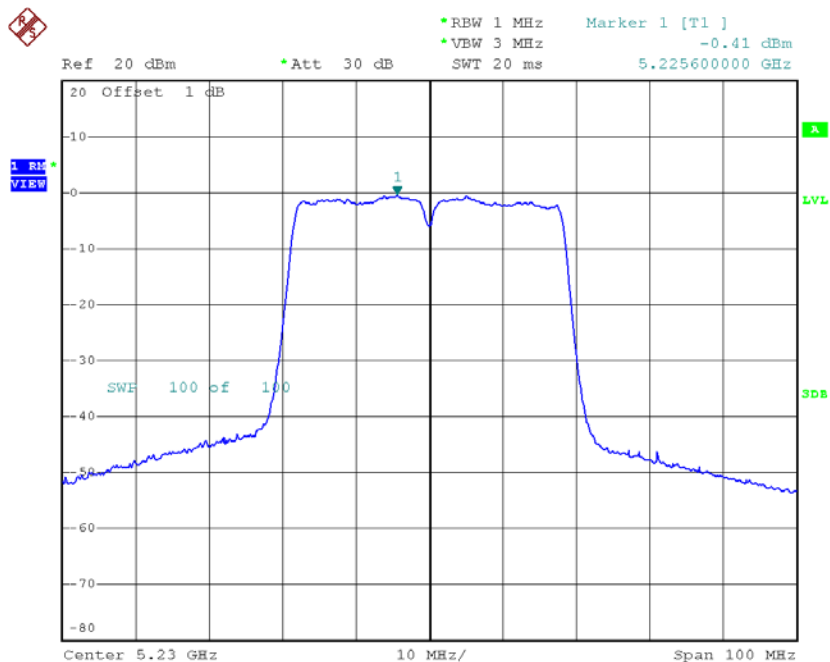
Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH38	5190	-0.63	0.83	0.20	17.00
CH46	5230	-0.41	0.83	0.42	17.00

CH38



Date: 5.JUL.2015 20:50:04

CH46



Date: 5.JUL.2015 20:51:03

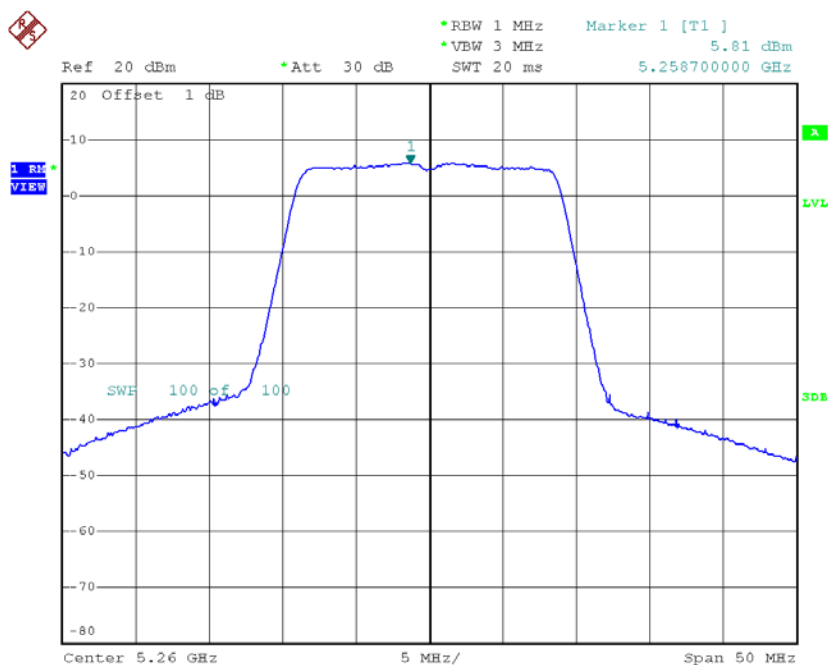
Test Mode: UNII-1/TX N40 Mode_CH38/CH46_Total

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Limit (dBm/MHz)
CH38	5190	4.38	17.00
CH46	5230	5.34	17.00

Test Mode: UNII-2A/TX N20 Mode_CH52/CH60/CH64_ANT 4

Channel	Frequency (MHz)	Power Density (dBm/MHz)	Duty Factor (dBm/MHz)	Power Density + Duty Factor (dBm/MHz)	Limit (dBm/MHz)
CH52	5260	5.81	0.47	6.28	11.00
CH60	5300	6.13	0.47	6.60	11.00
CH64	5320	-1.28	0.47	-0.81	11.00

CH52



Date: 5.JUL.2015 19:06:36