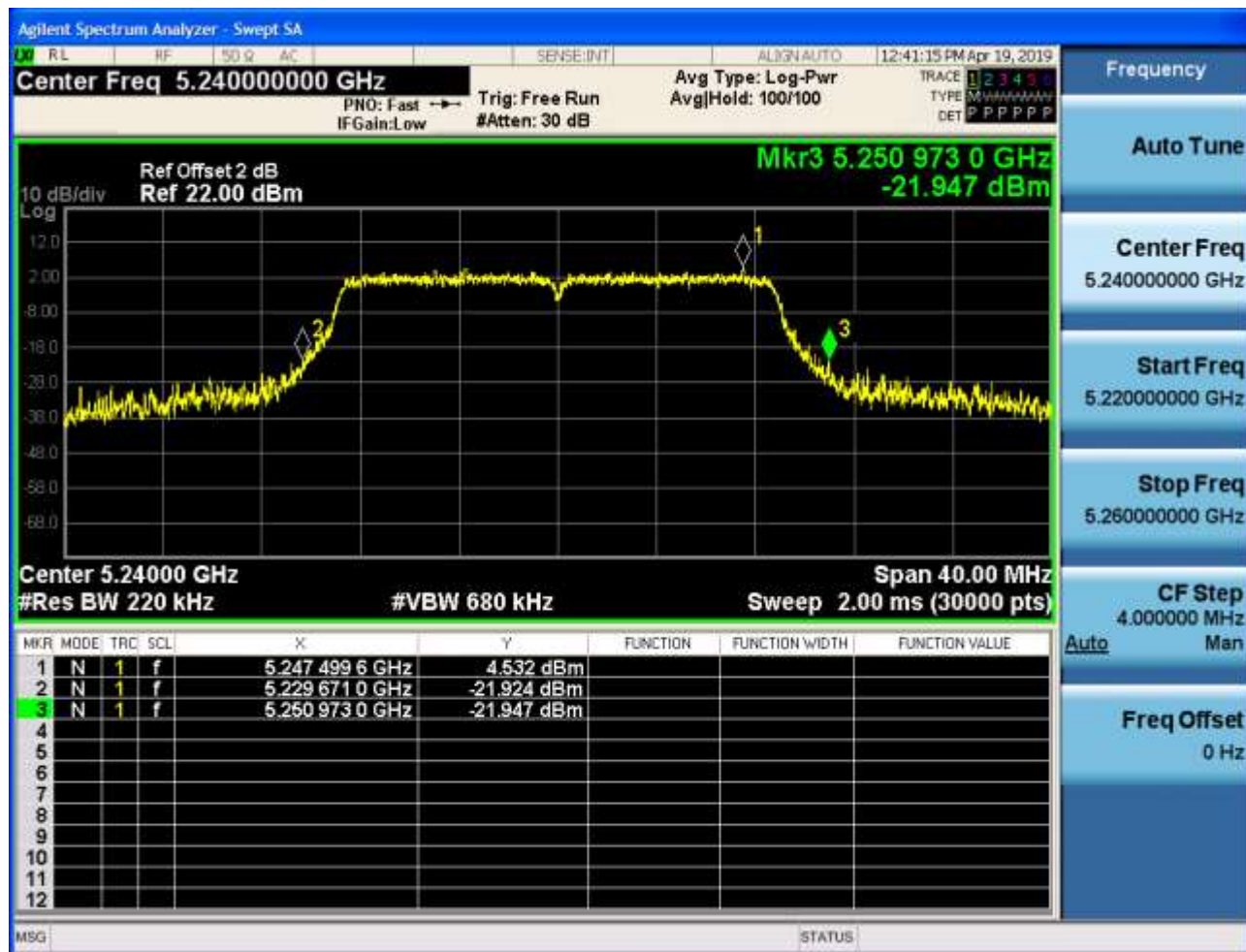


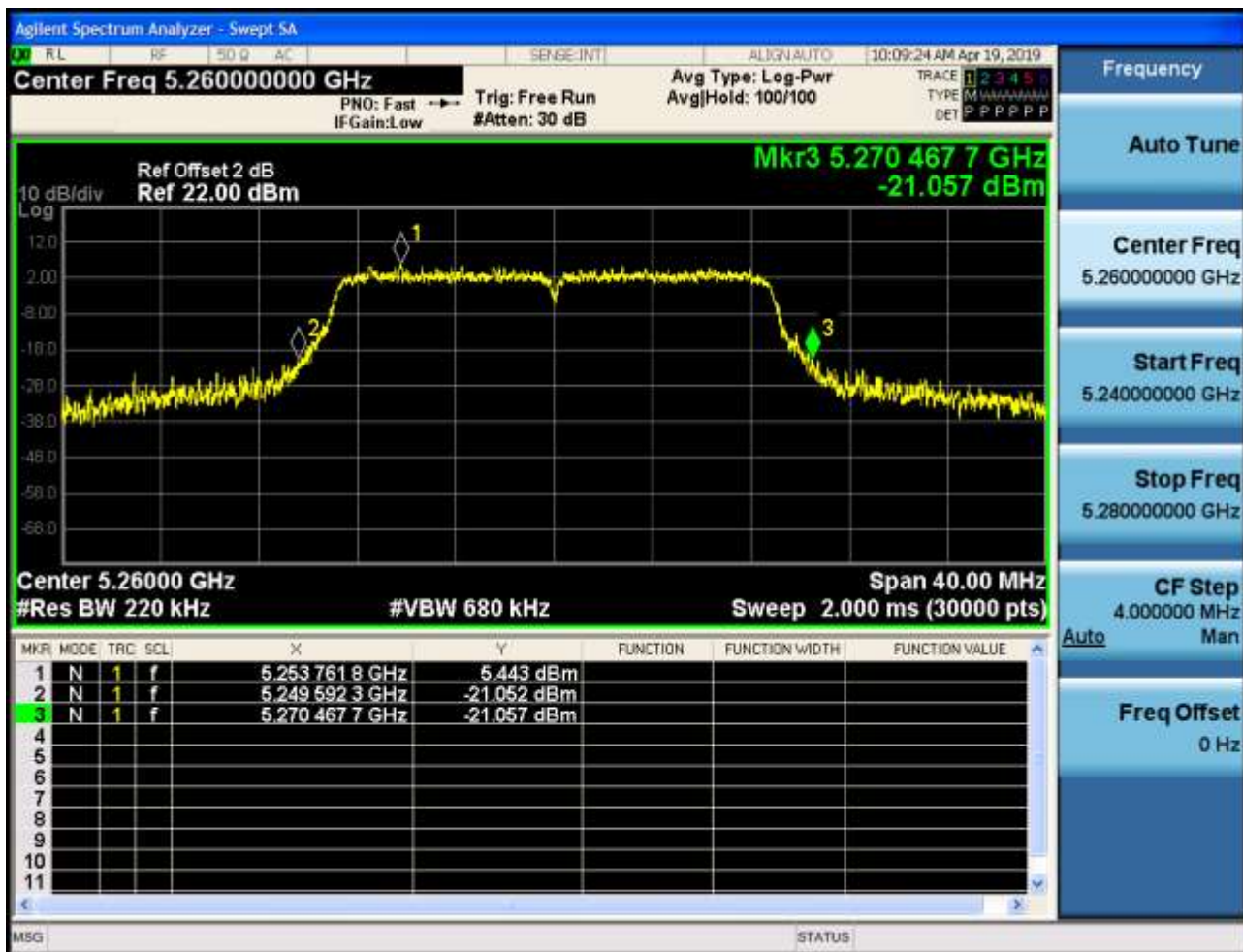


1.90 11AC20SISO_Ant2_5240





1.91 11AC20SISO_Ant1_5260





1.92 11AC20SISO_Ant2_5260





1.93 11AC20SISO_Ant1_5280



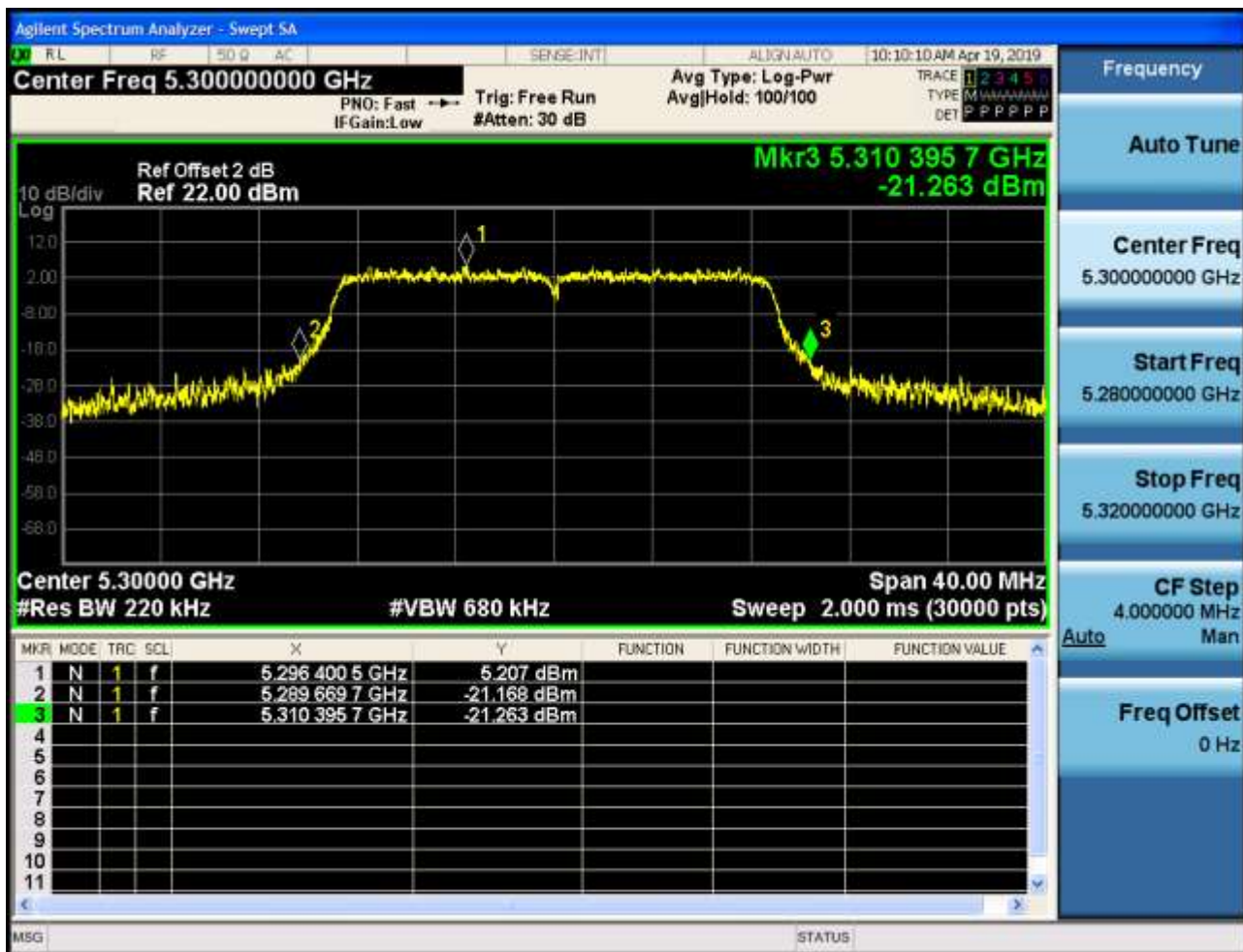


1.94 11AC20SISO_Ant2_5280





1.95 11AC20SISO_Ant1_5300



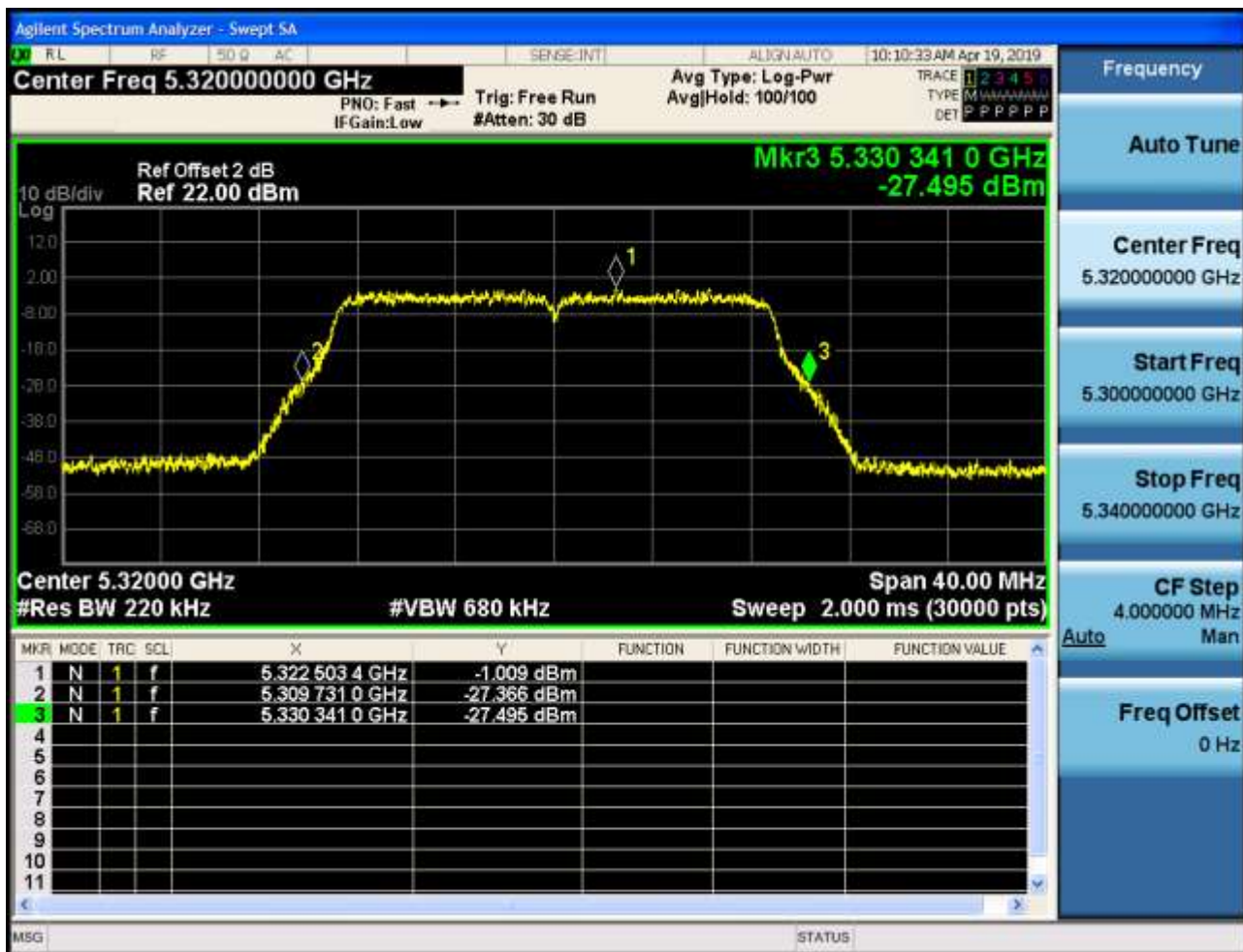


1.96 11AC20SISO_Ant2_5300





1.97 11AC20SISO_Ant1_5320





1.98 11AC20SISO_Ant2_5320





1.99 11AC20SISO_Ant1_5500





1.100 11AC20SISO_Ant2_5500



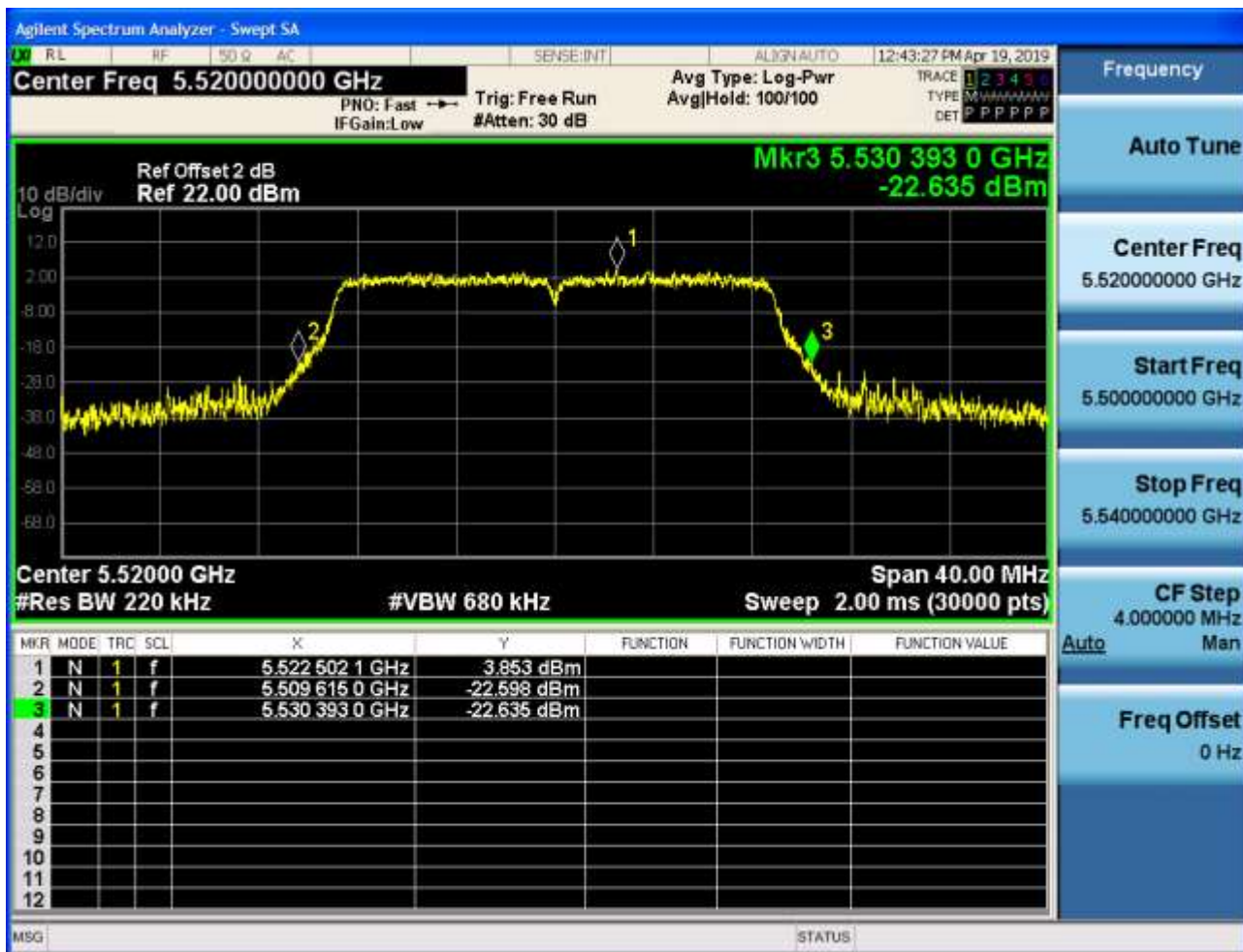


1.101 11AC20SISO_Ant1_5520



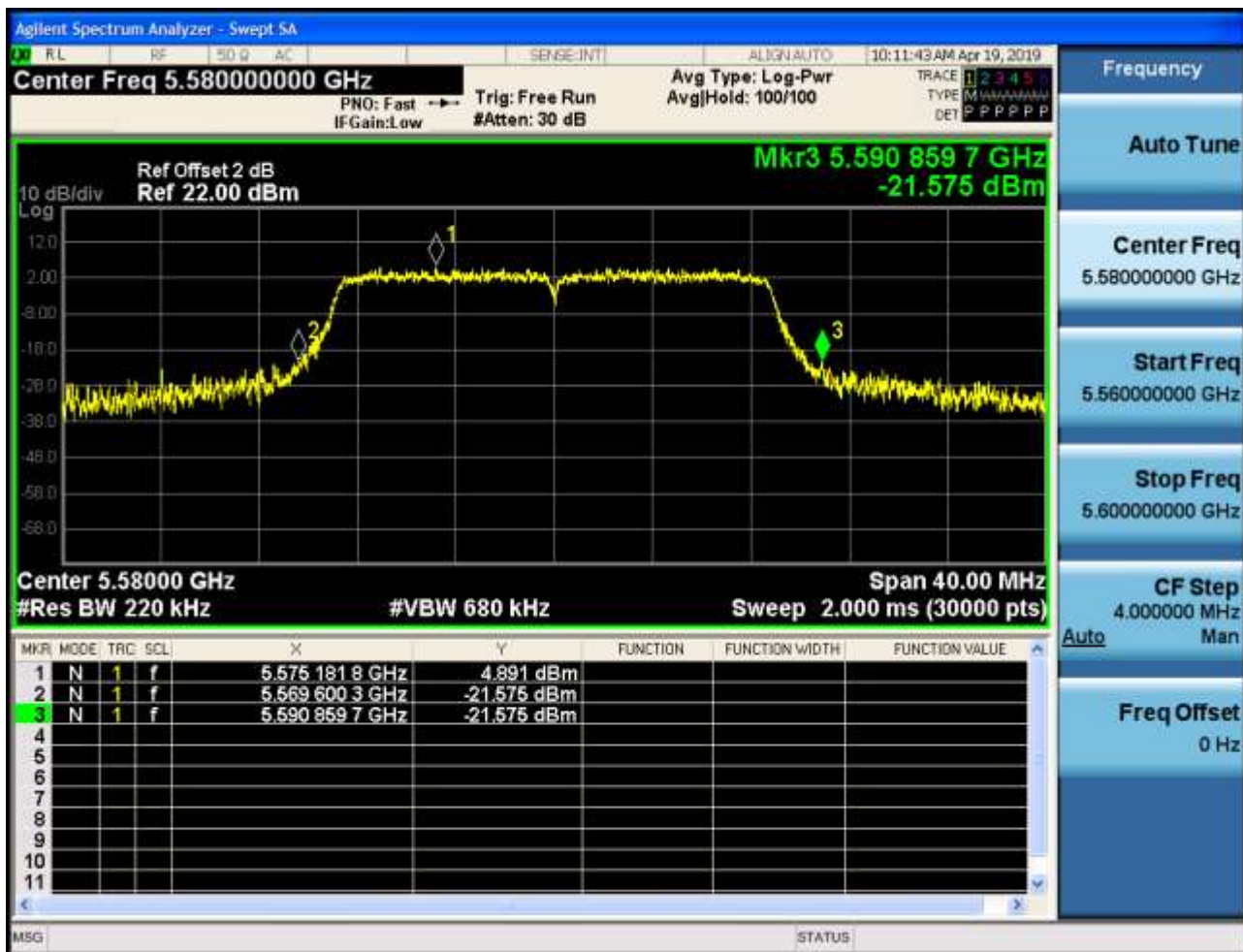


1.102 11AC20SISO_Ant2_5520



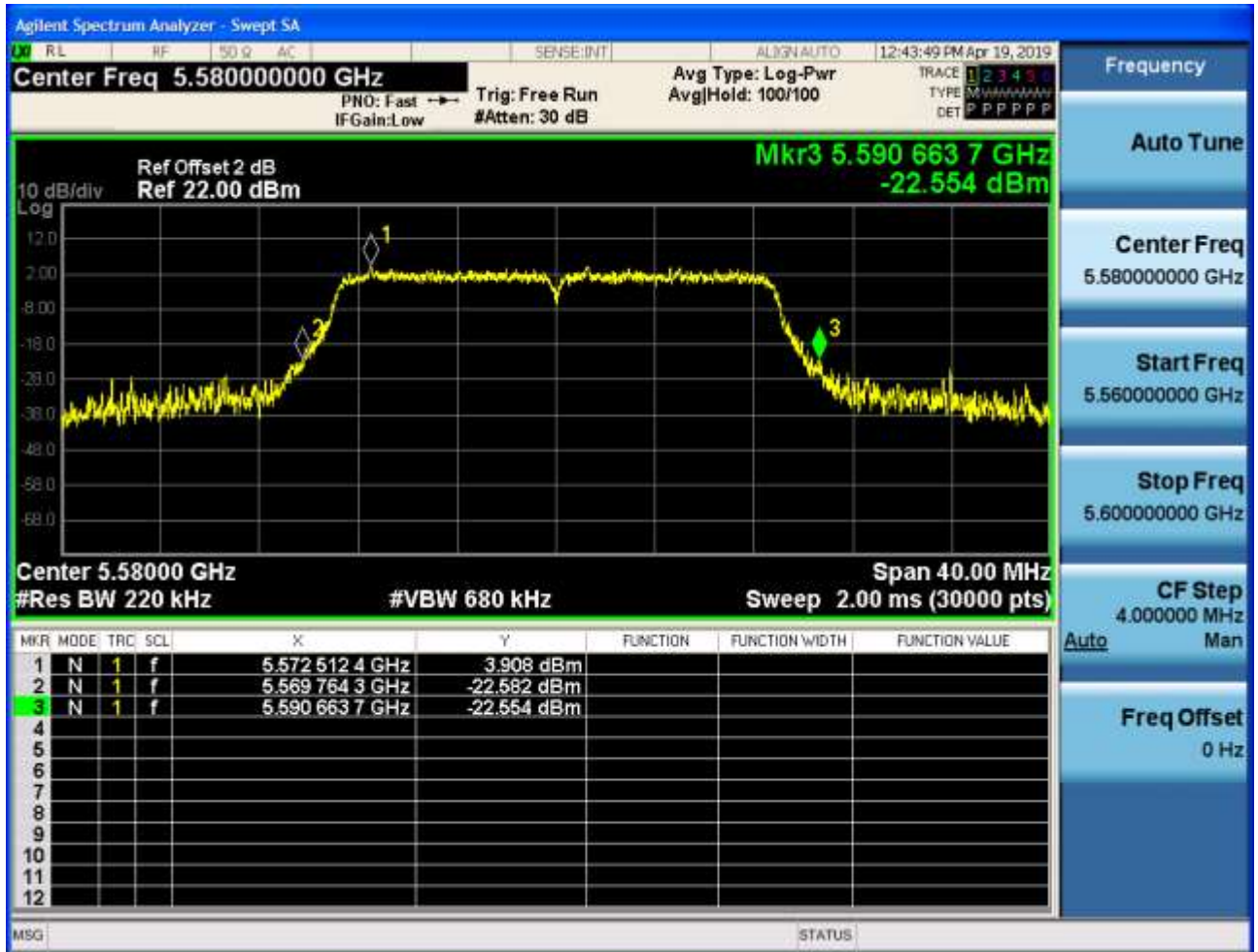


1.103 11AC20SISO_Ant1_5580



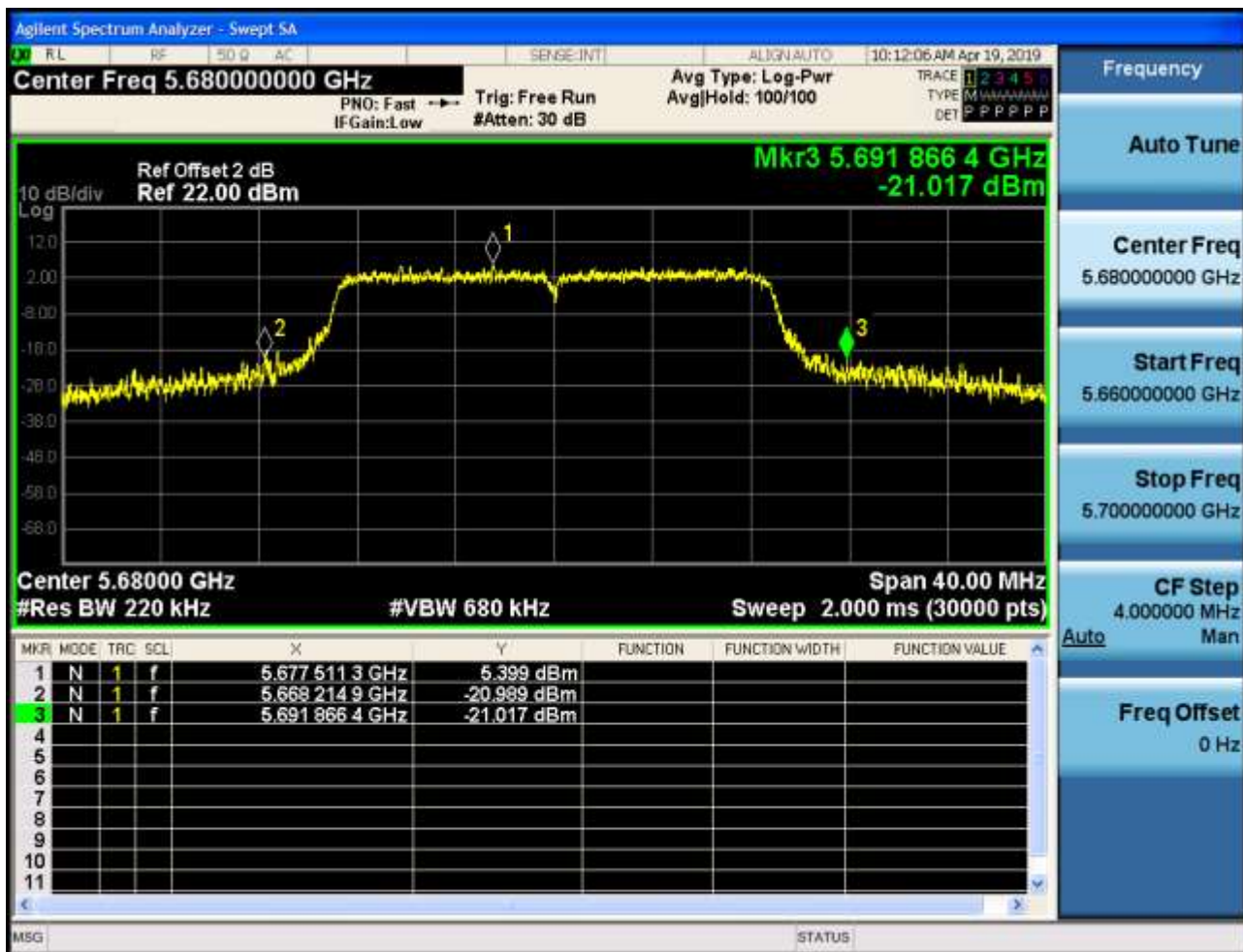


1.104 11AC20SISO_Ant2_5580



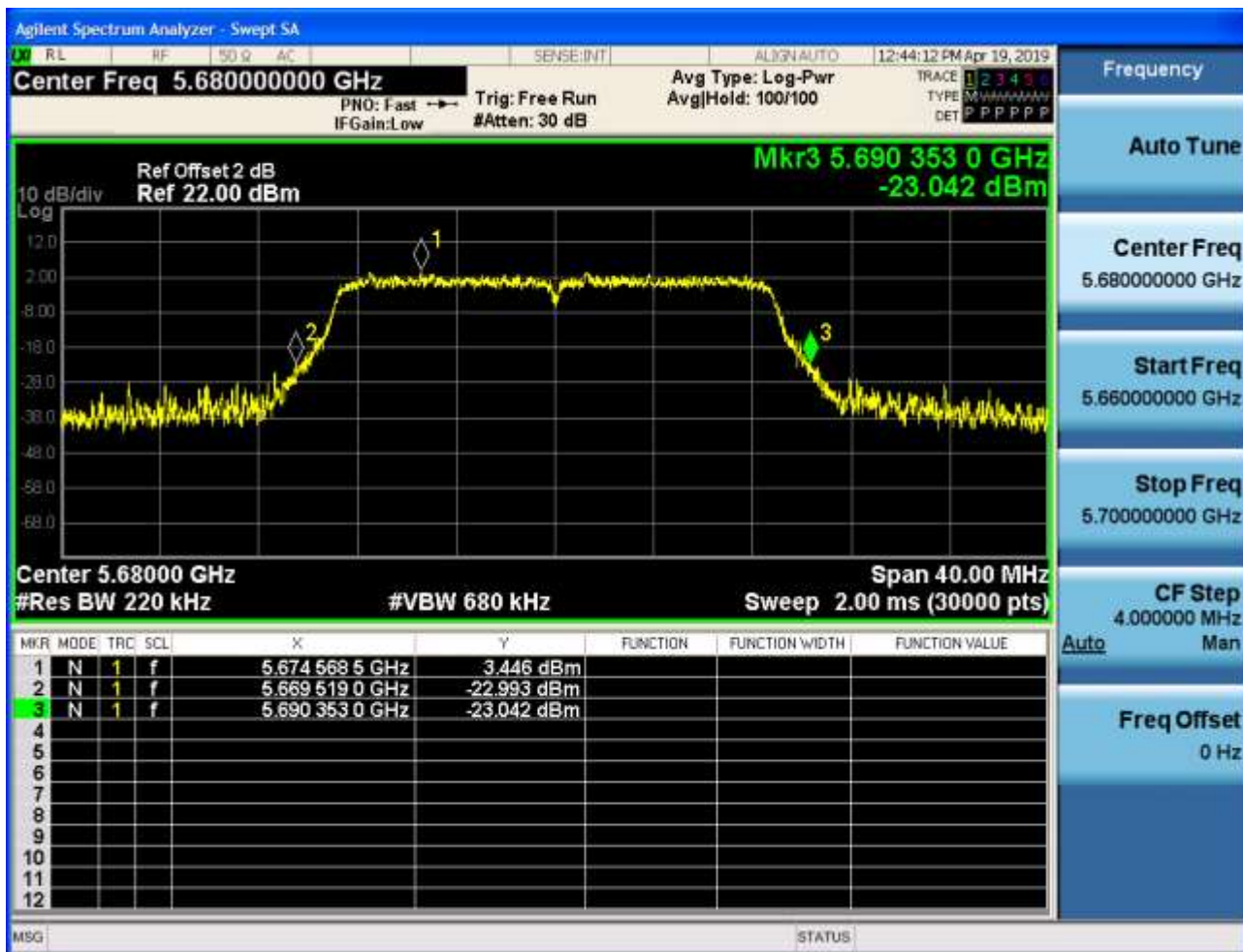


1.105 11AC20SISO_Ant1_5680





1.106 11AC20SISO_Ant2_5680



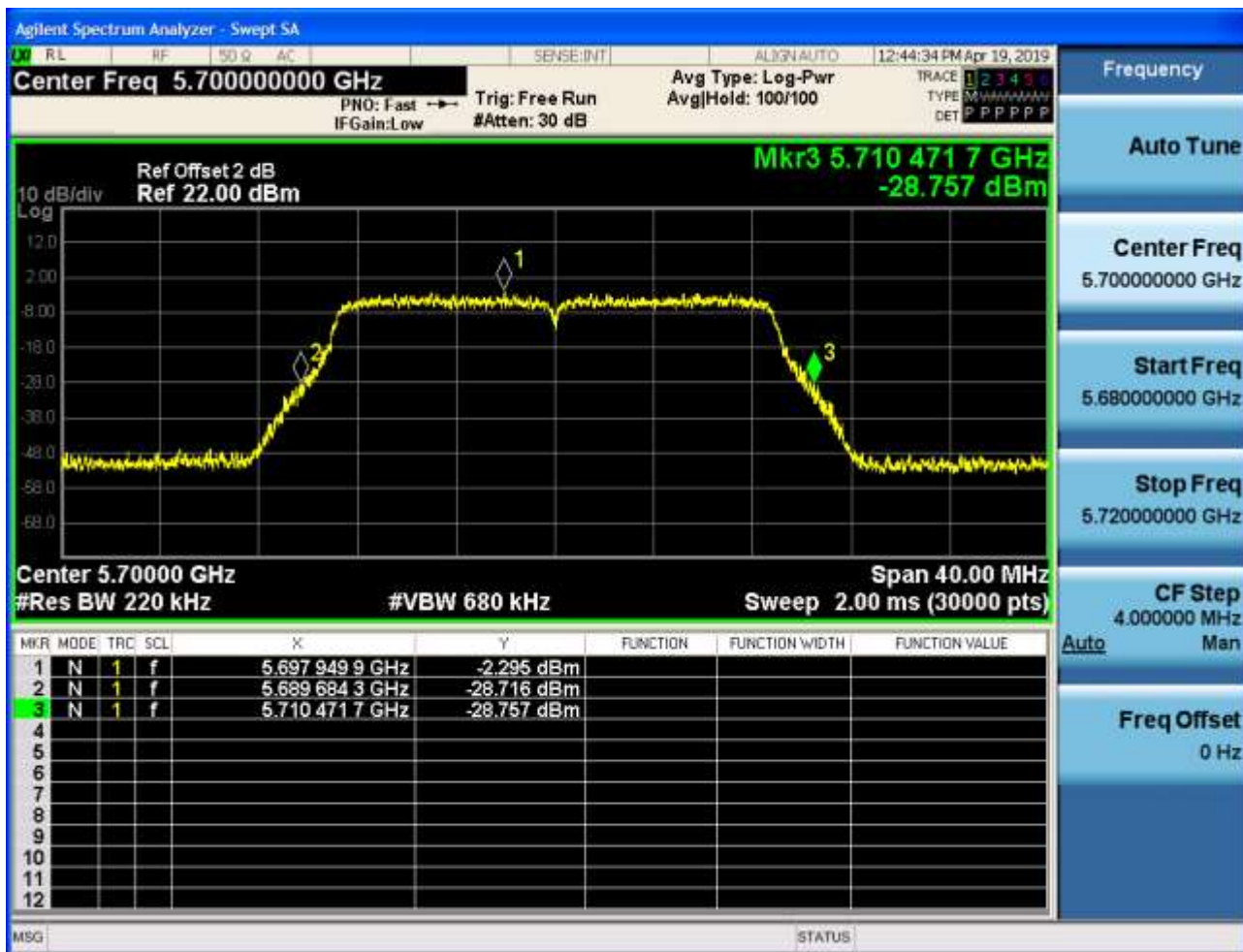


1.107 11AC20SISO_Ant1_5700



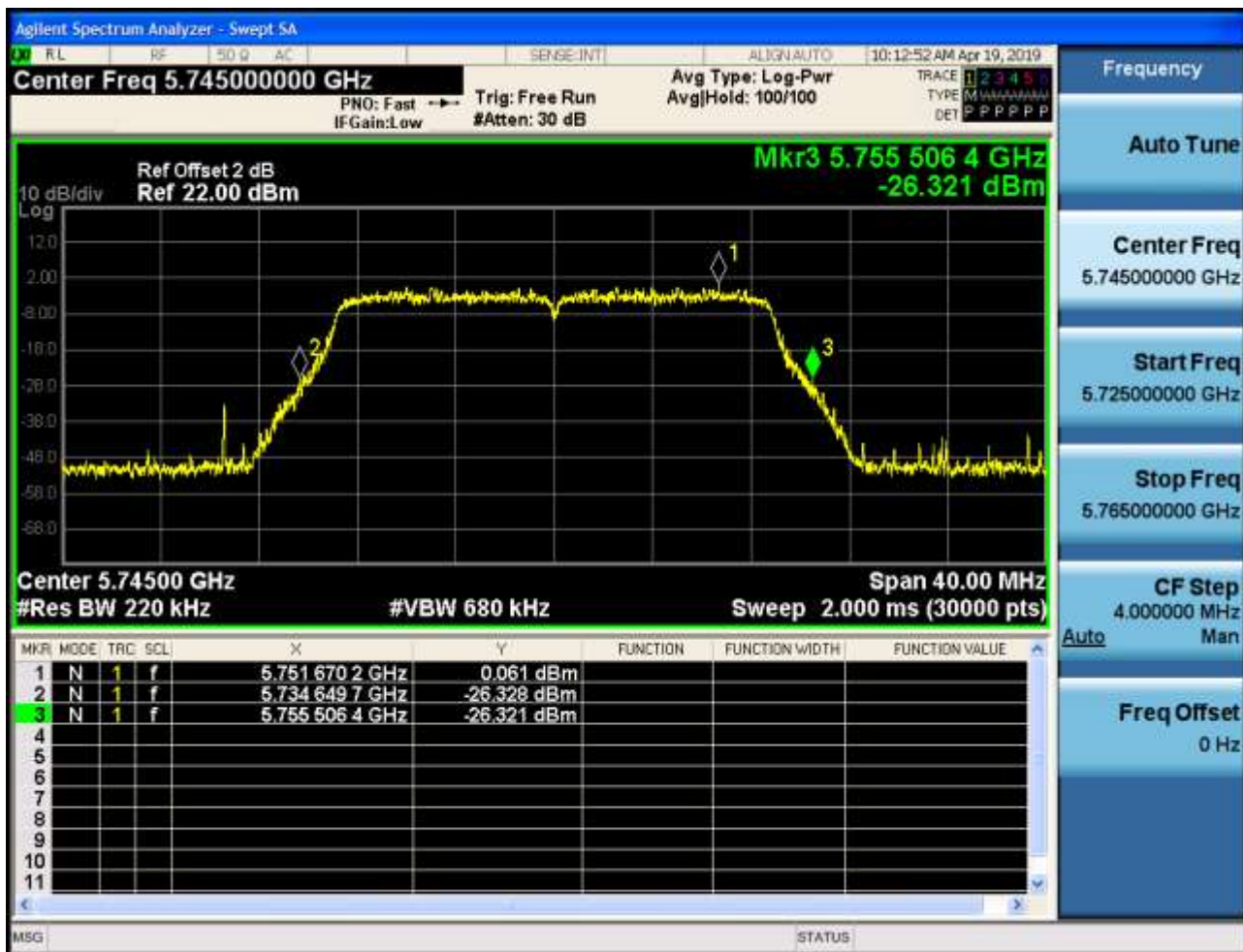


1.108 11AC20SISO_Ant2_5700





1.109 11AC20SISO_Ant1_5745



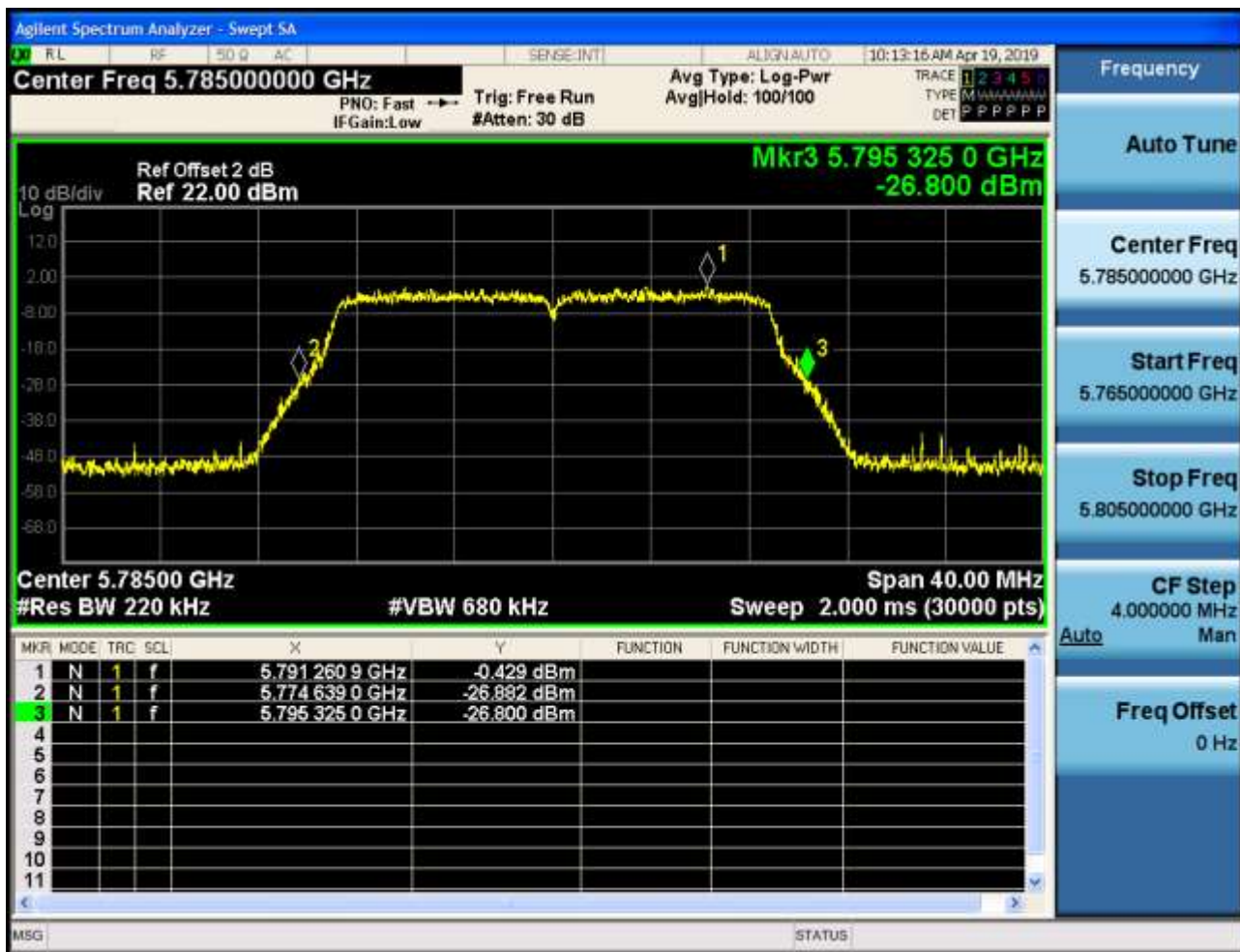


1.110 11AC20SISO_Ant2_5745



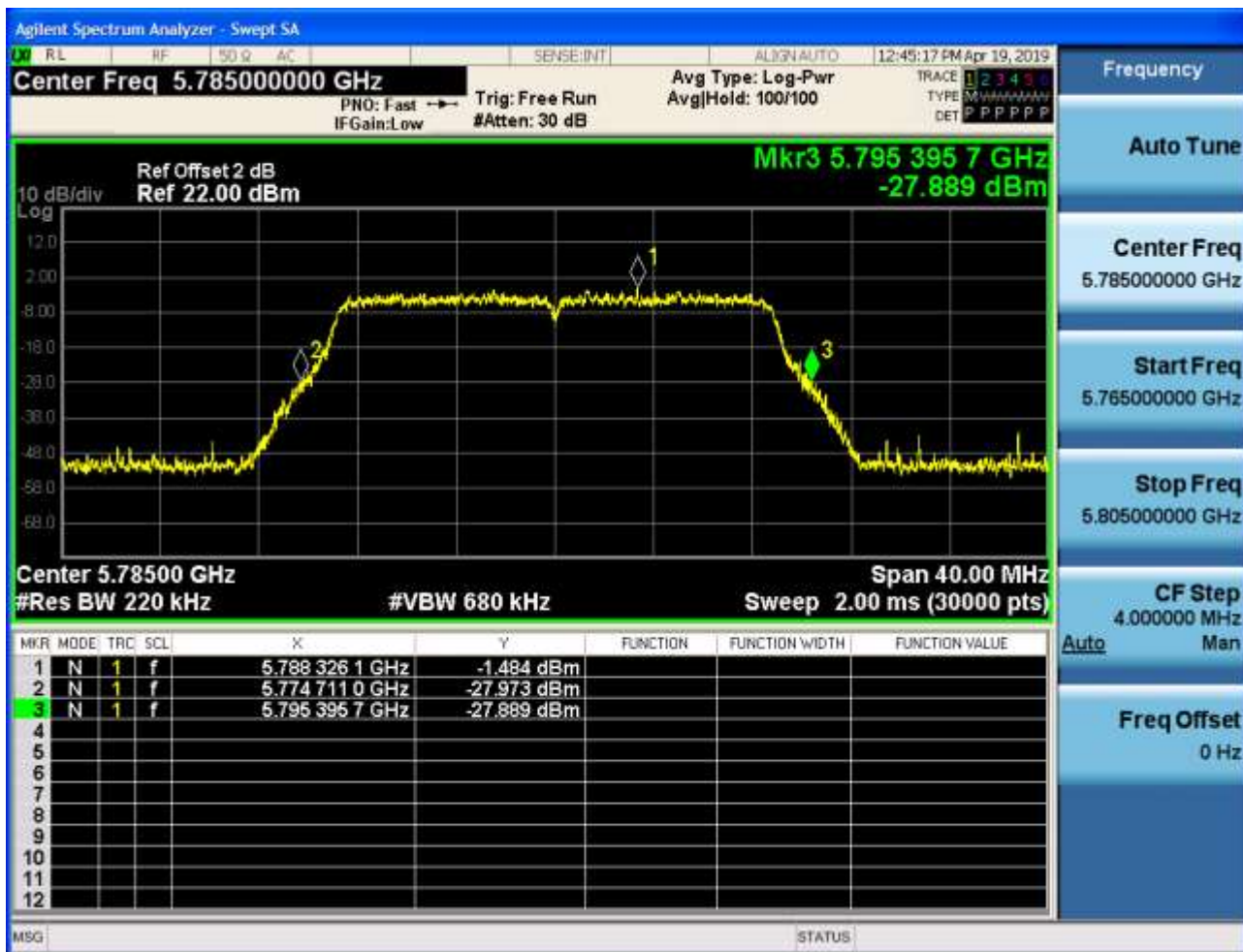


1.111 11AC20SISO_Ant1_5785



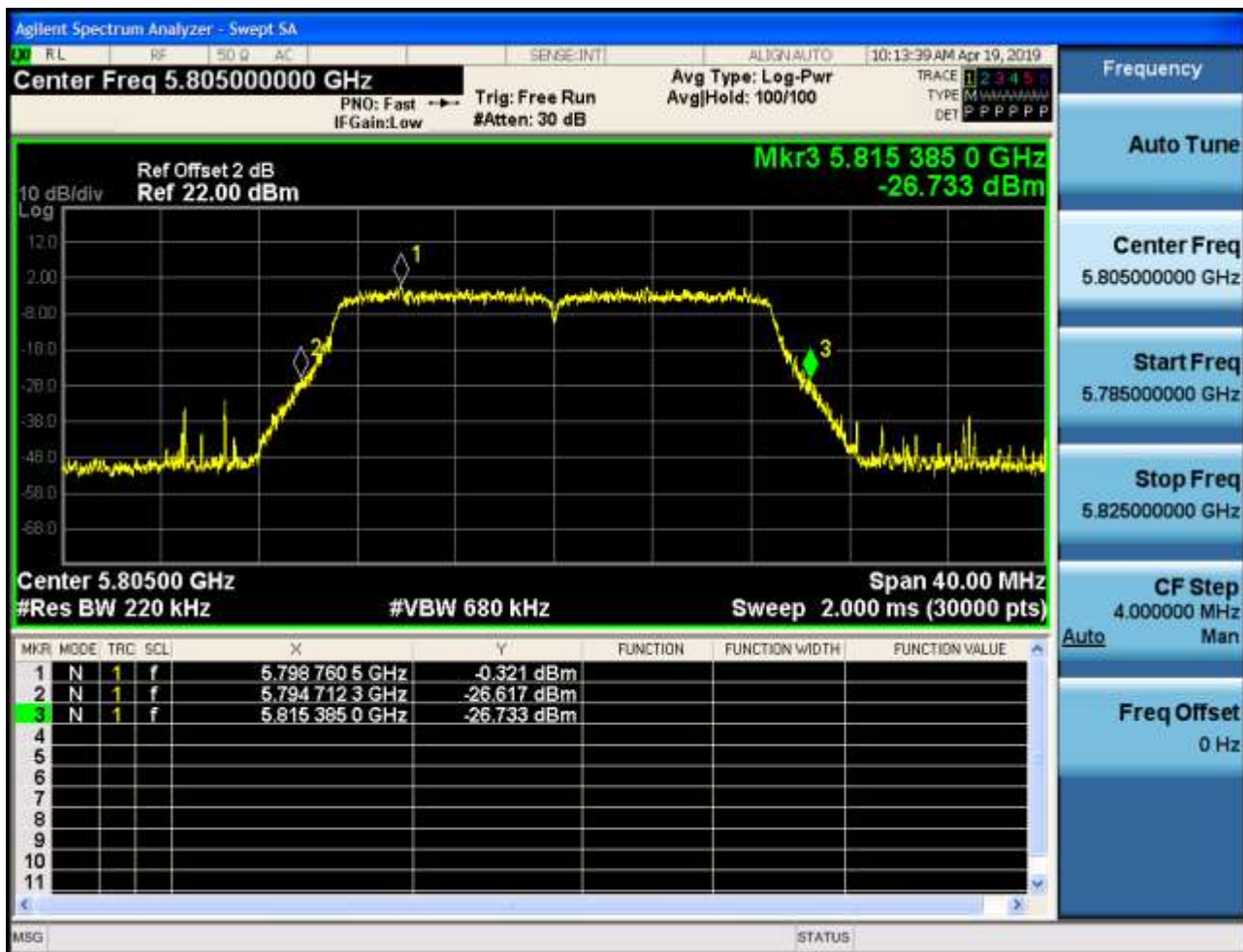


1.112 11AC20SISO_Ant2_5785





1.113 11AC20SISO_Ant1_5805



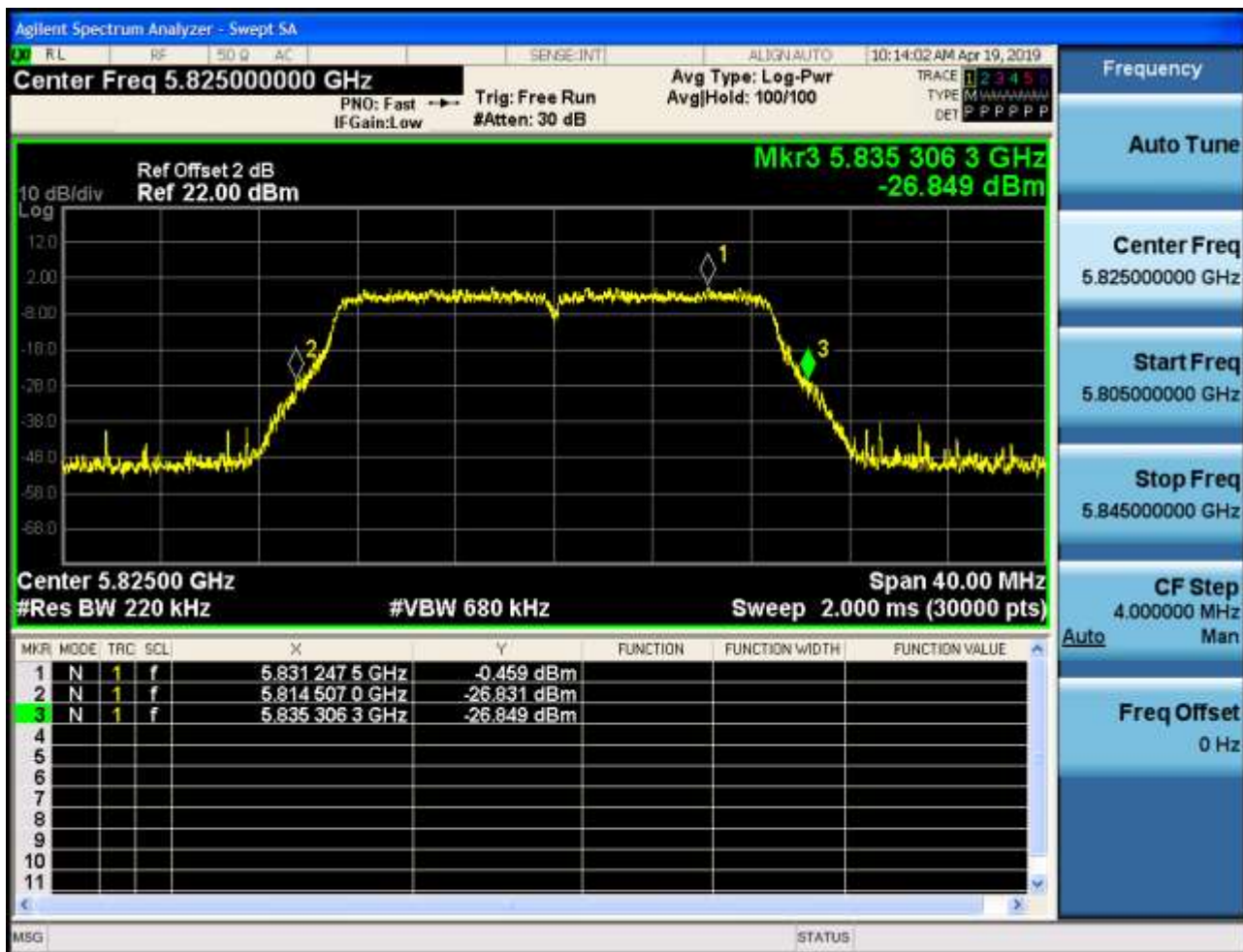


1.114 11AC20SISO_Ant2_5805



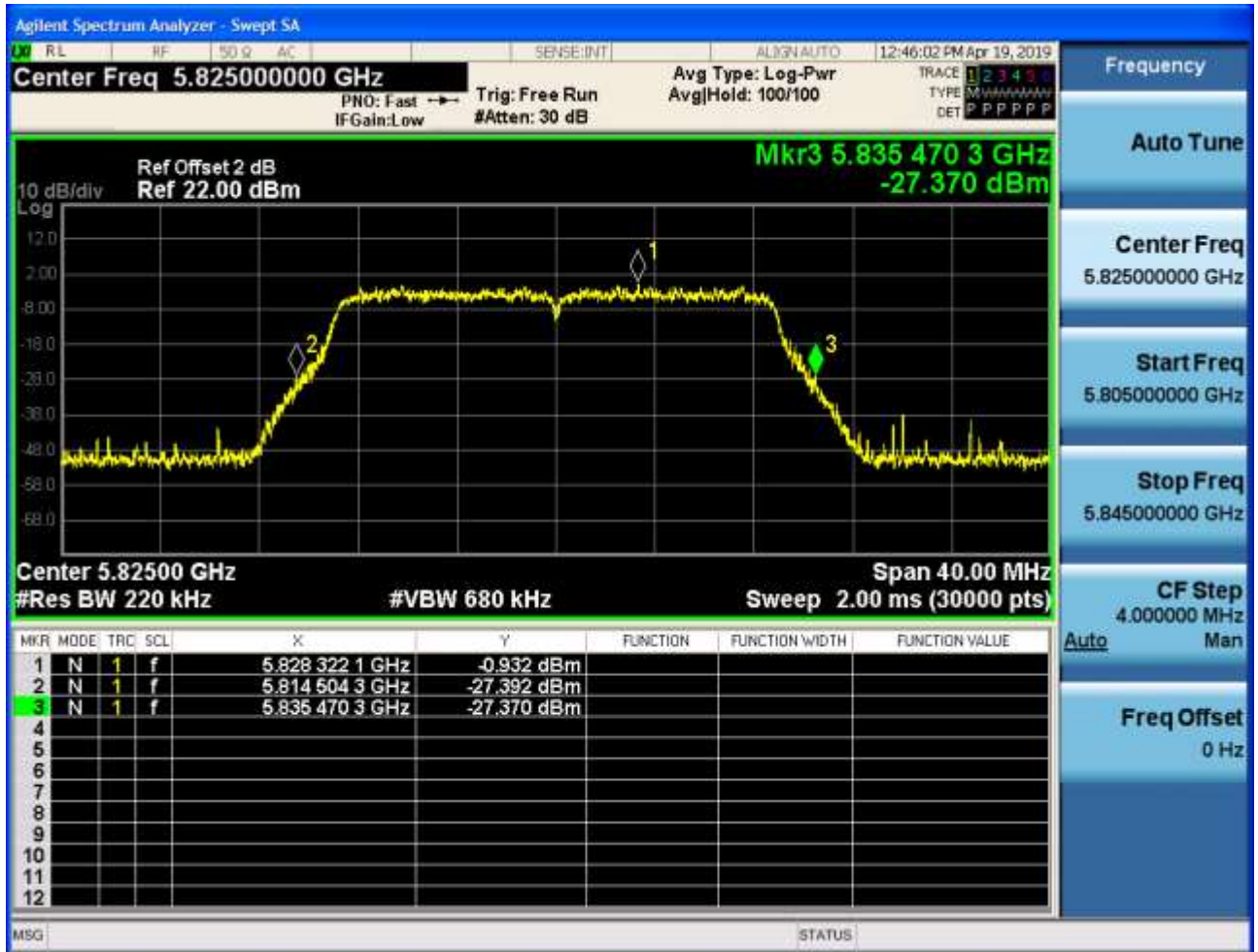


1.115 11AC20SISO_Ant1_5825



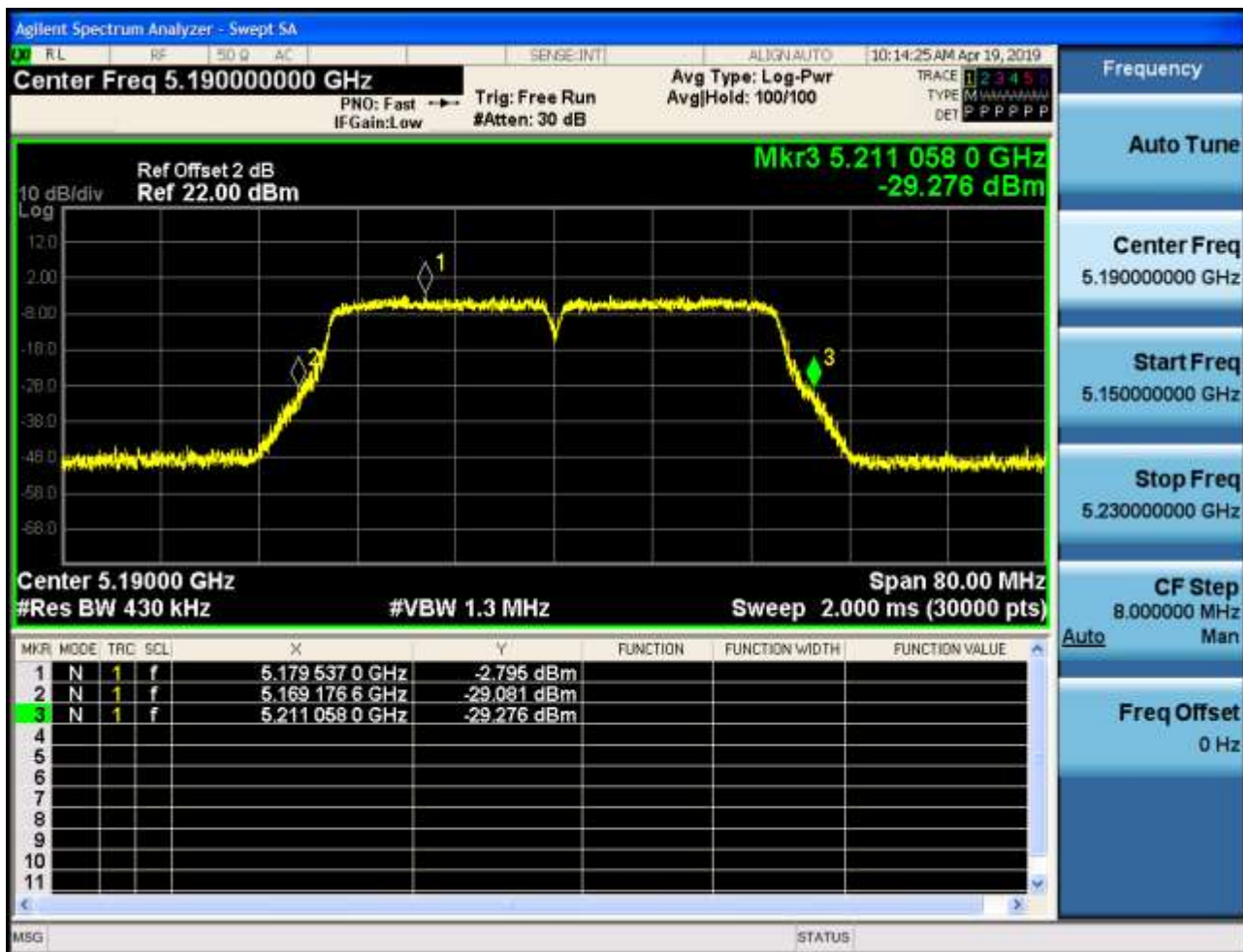


1.116 11AC20SISO_Ant2_5825





1.117 11AC40SISO_Ant1_5190



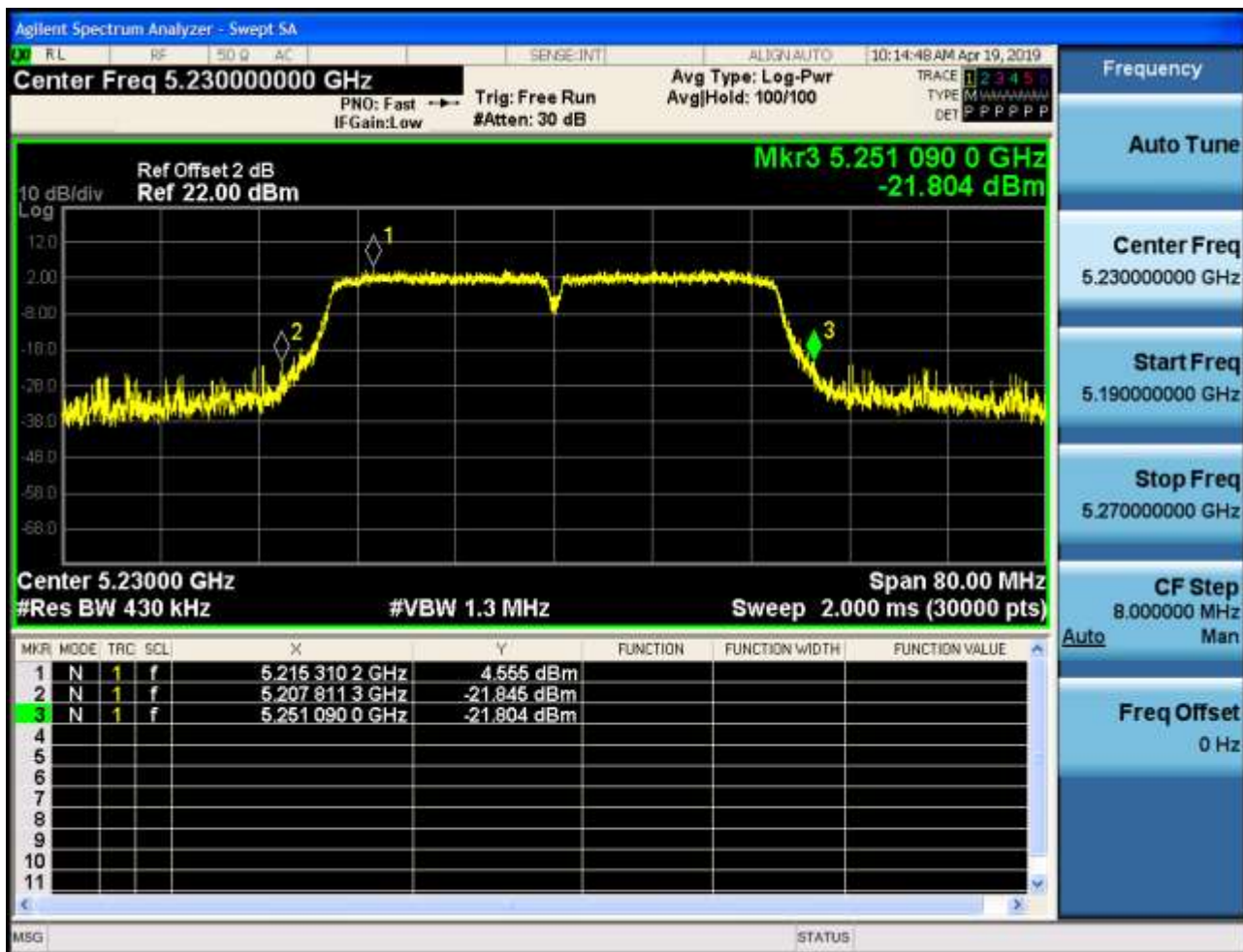


1.118 11AC40SISO_Ant2_5190



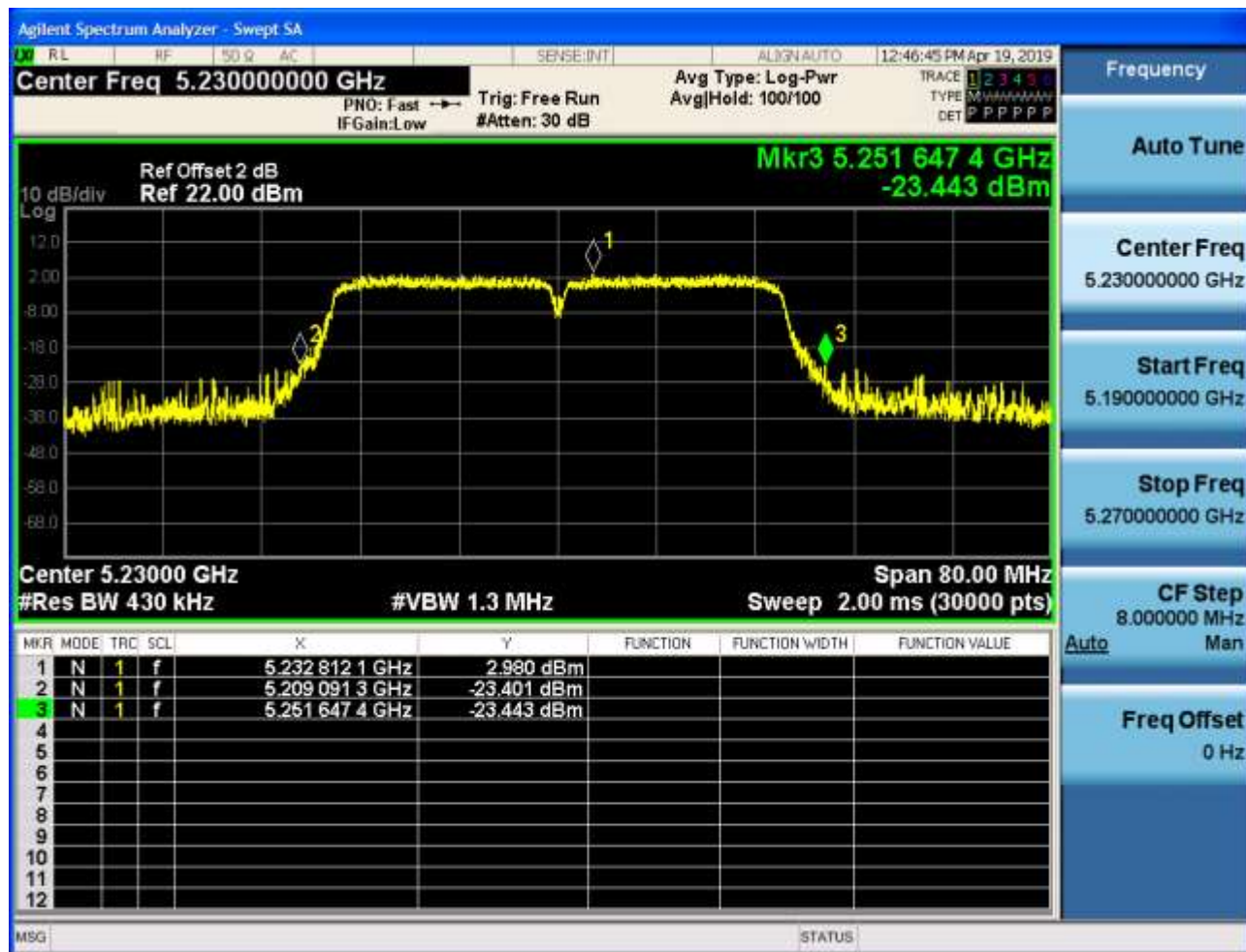


1.119 11AC40SISO_Ant1_5230



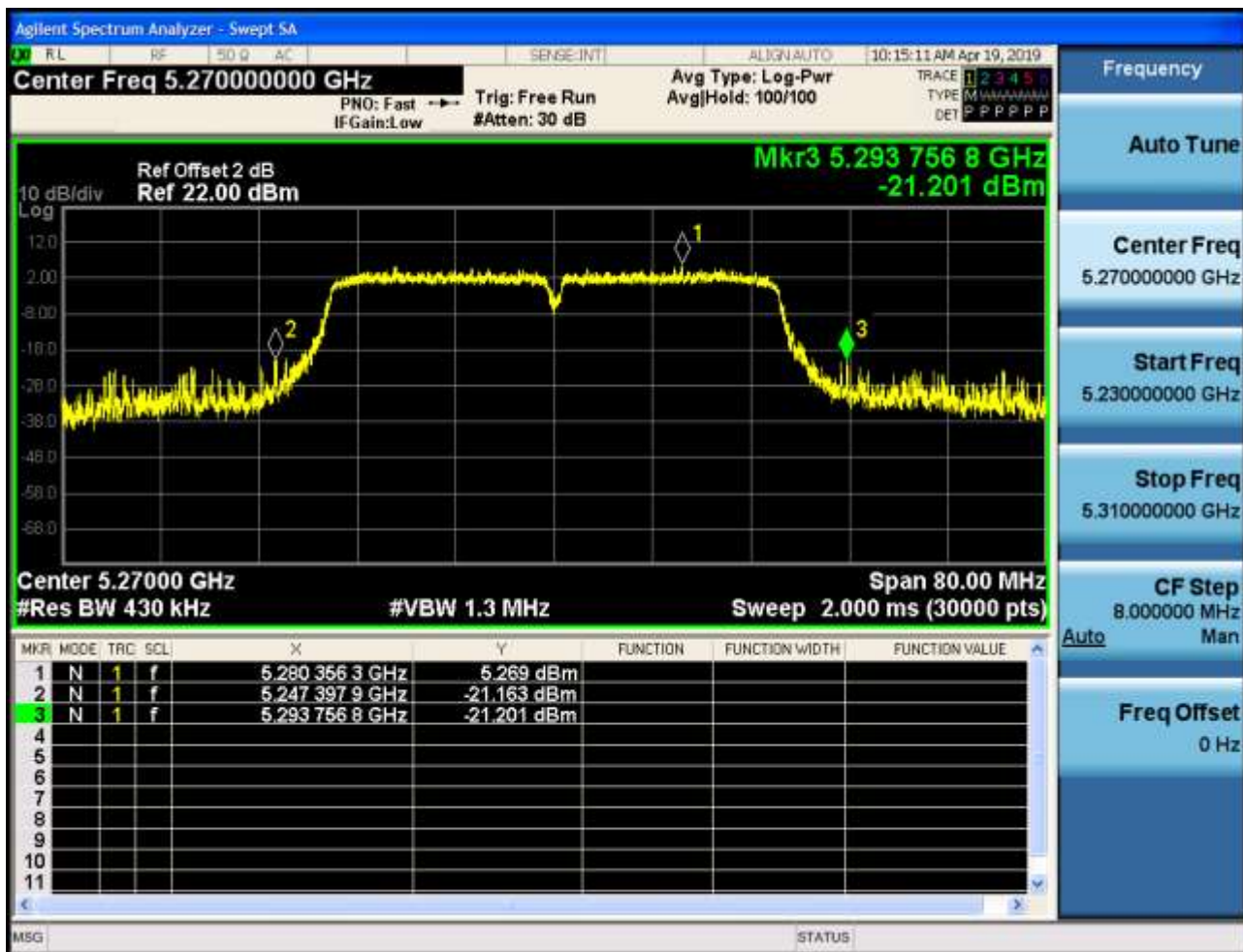


1.120 11AC40SISO_Ant2_5230



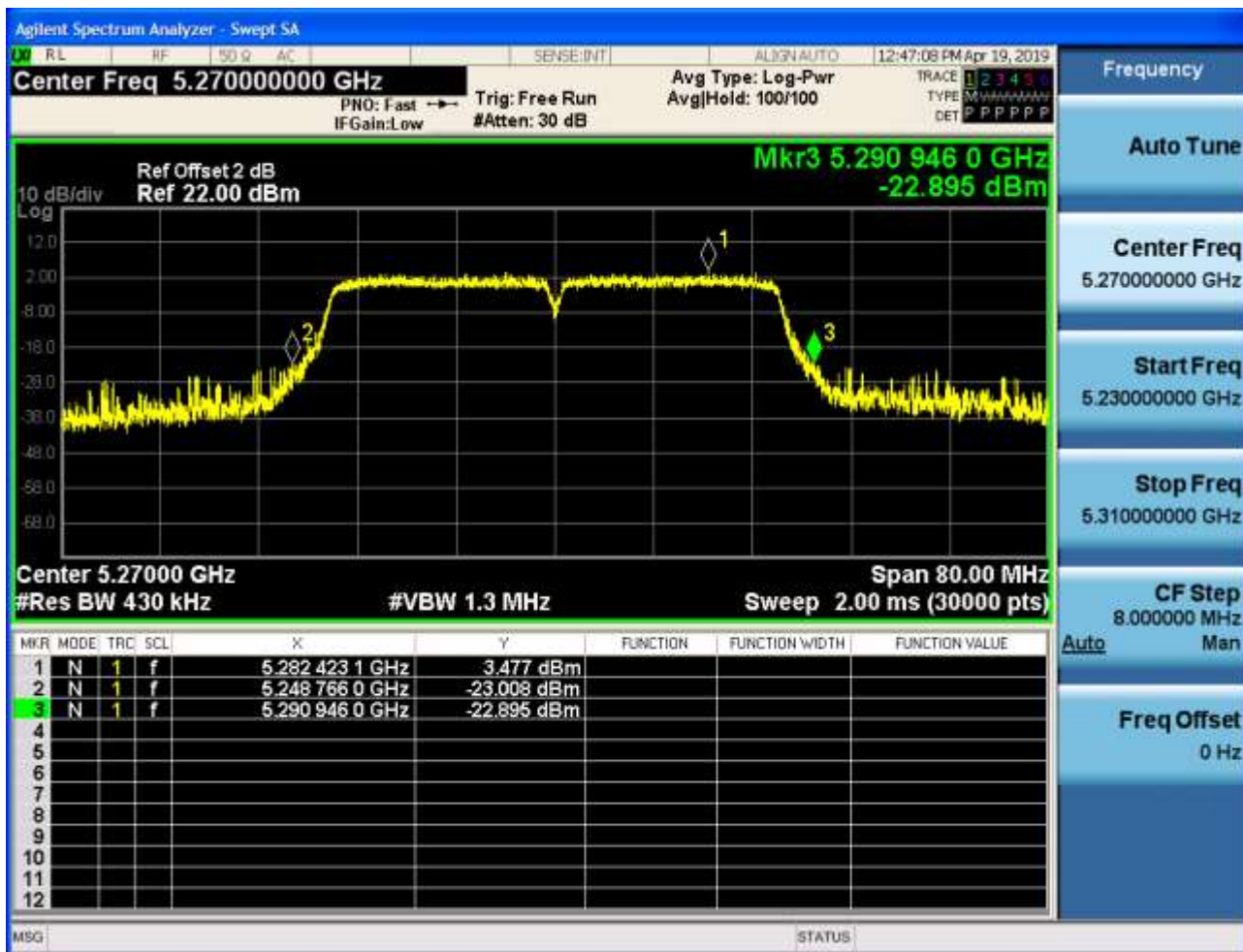


1.121 11AC40SISO_Ant1_5270



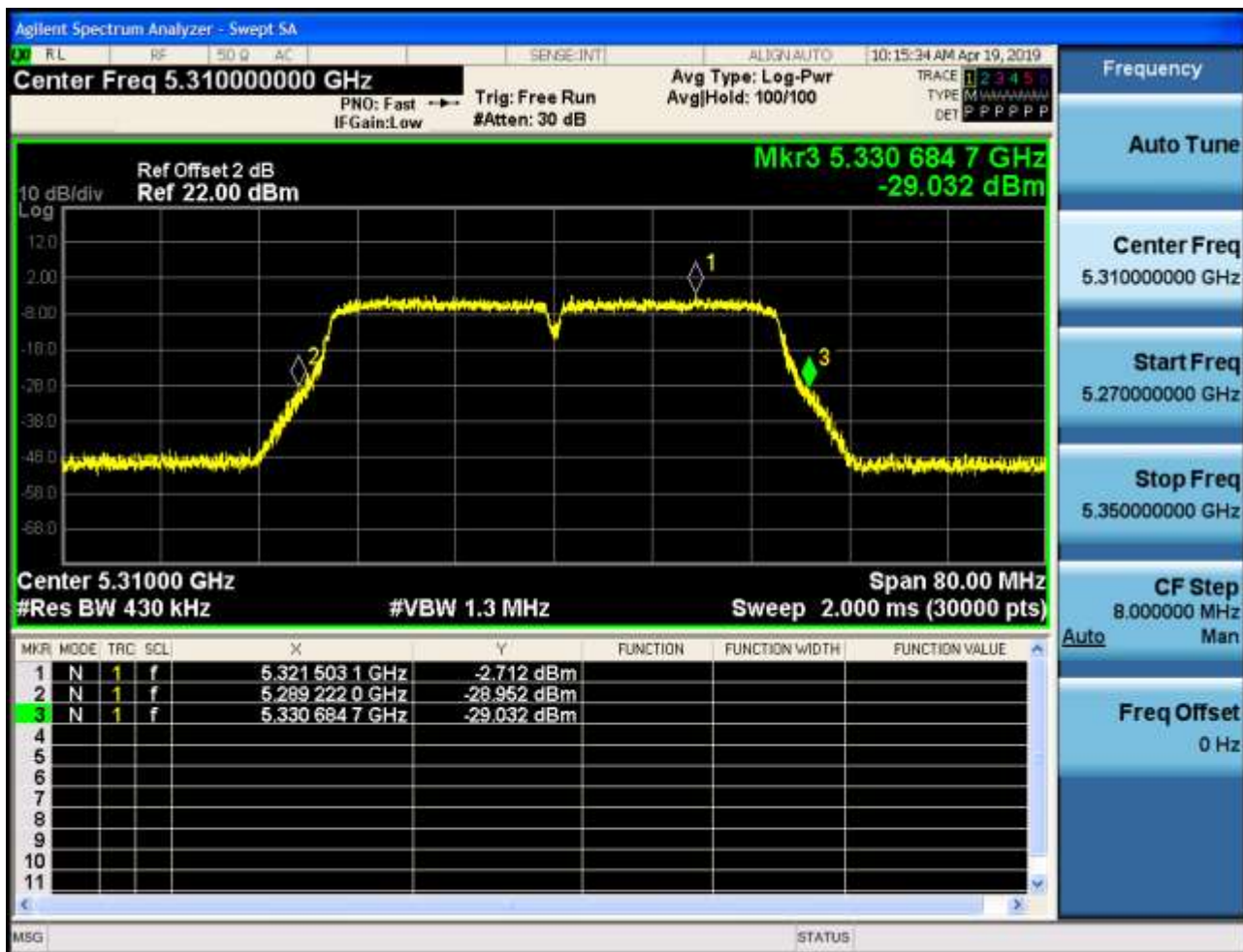


1.122 11AC40SISO_Ant2_5270



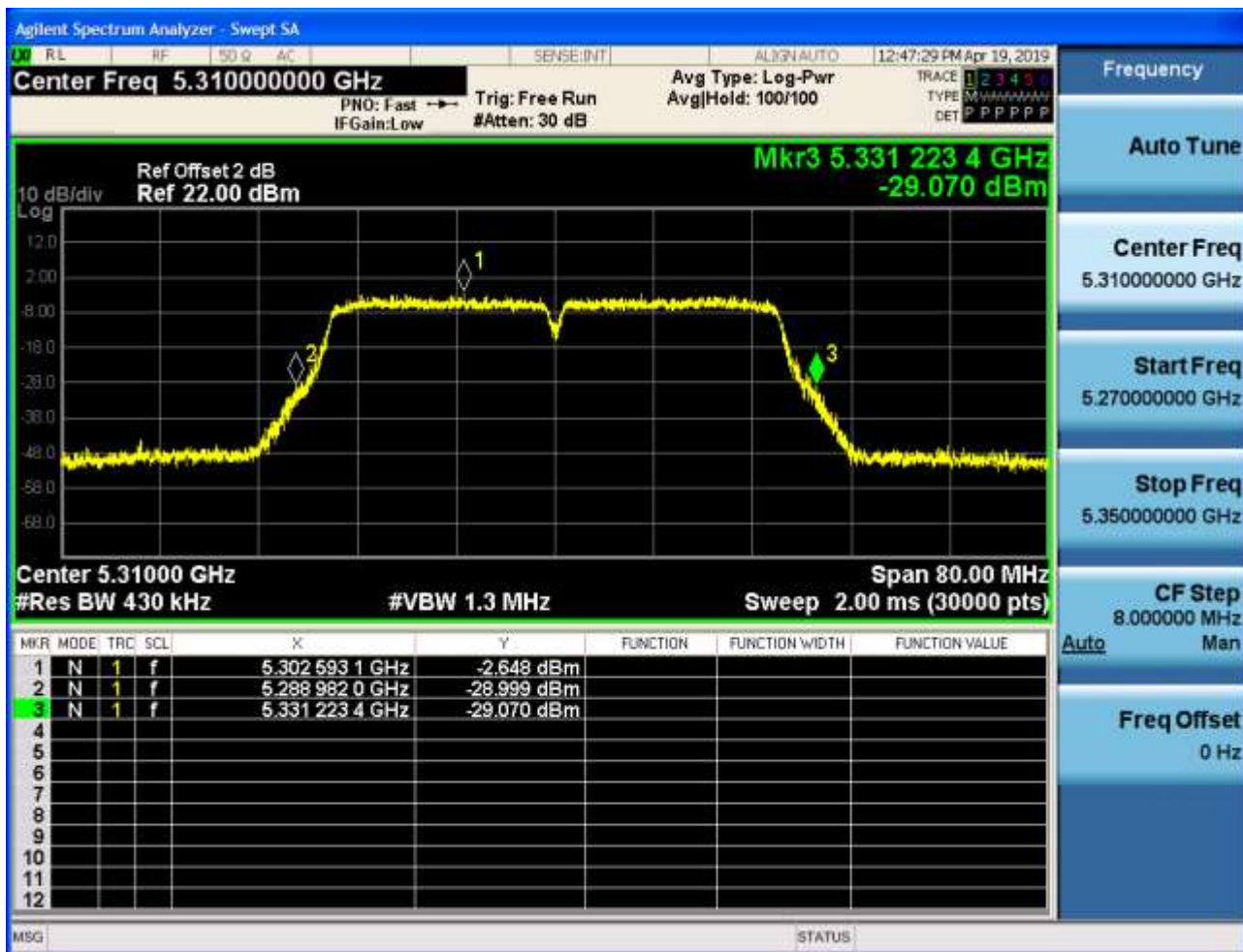


1.123 11AC40SISO_Ant1_5310



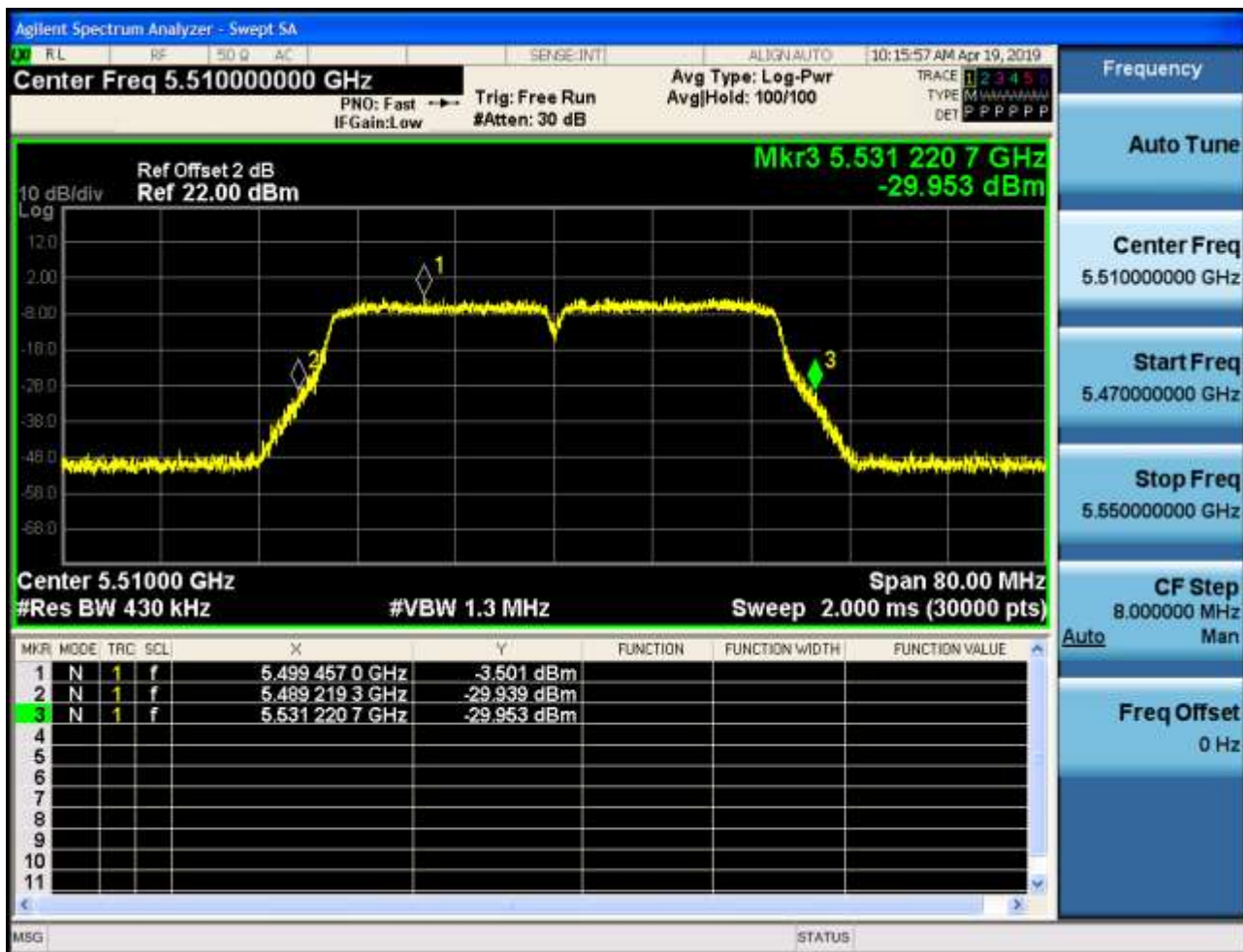


1.124 11AC40SISO_Ant2_5310



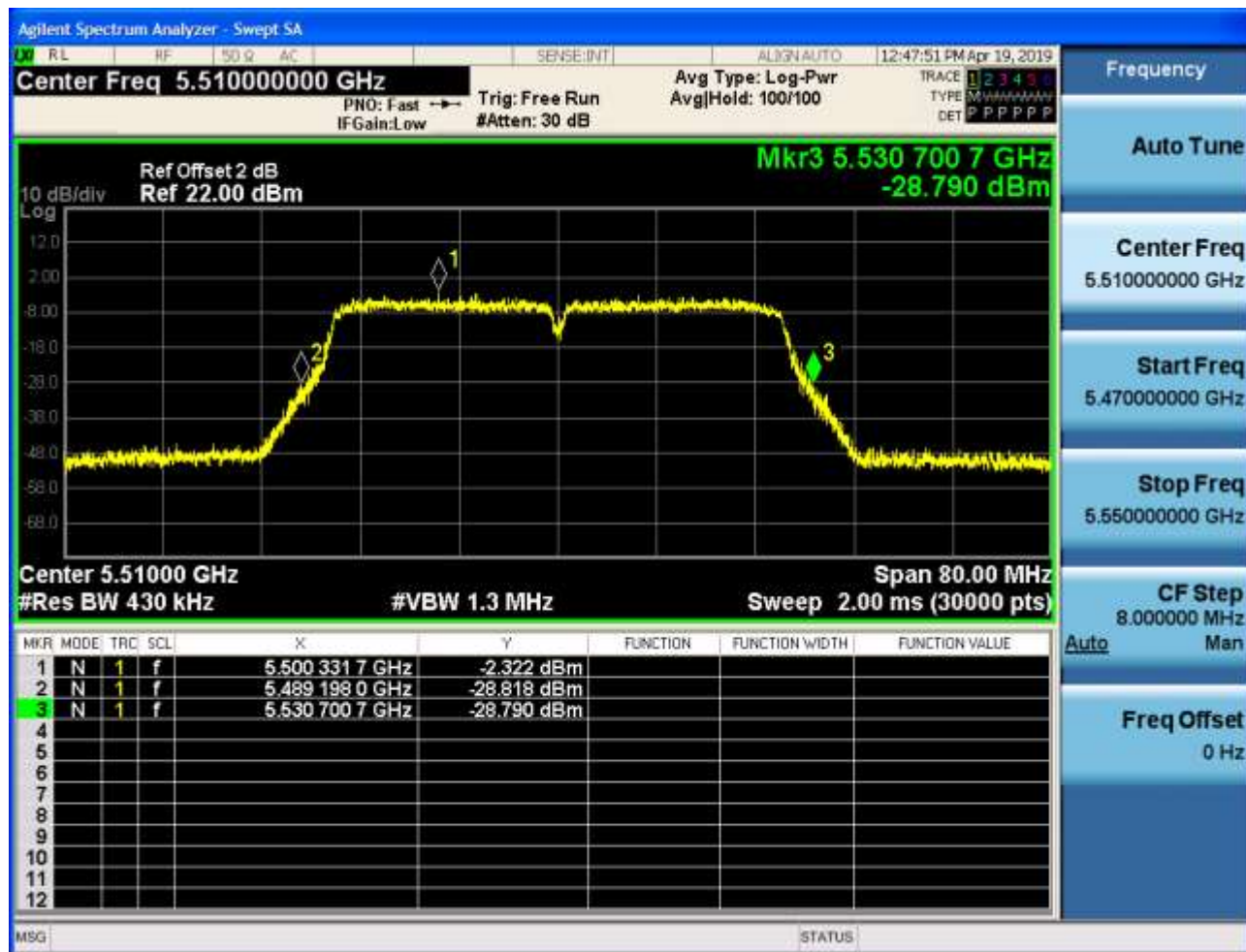


1.125 11AC40SISO_Ant1_5510



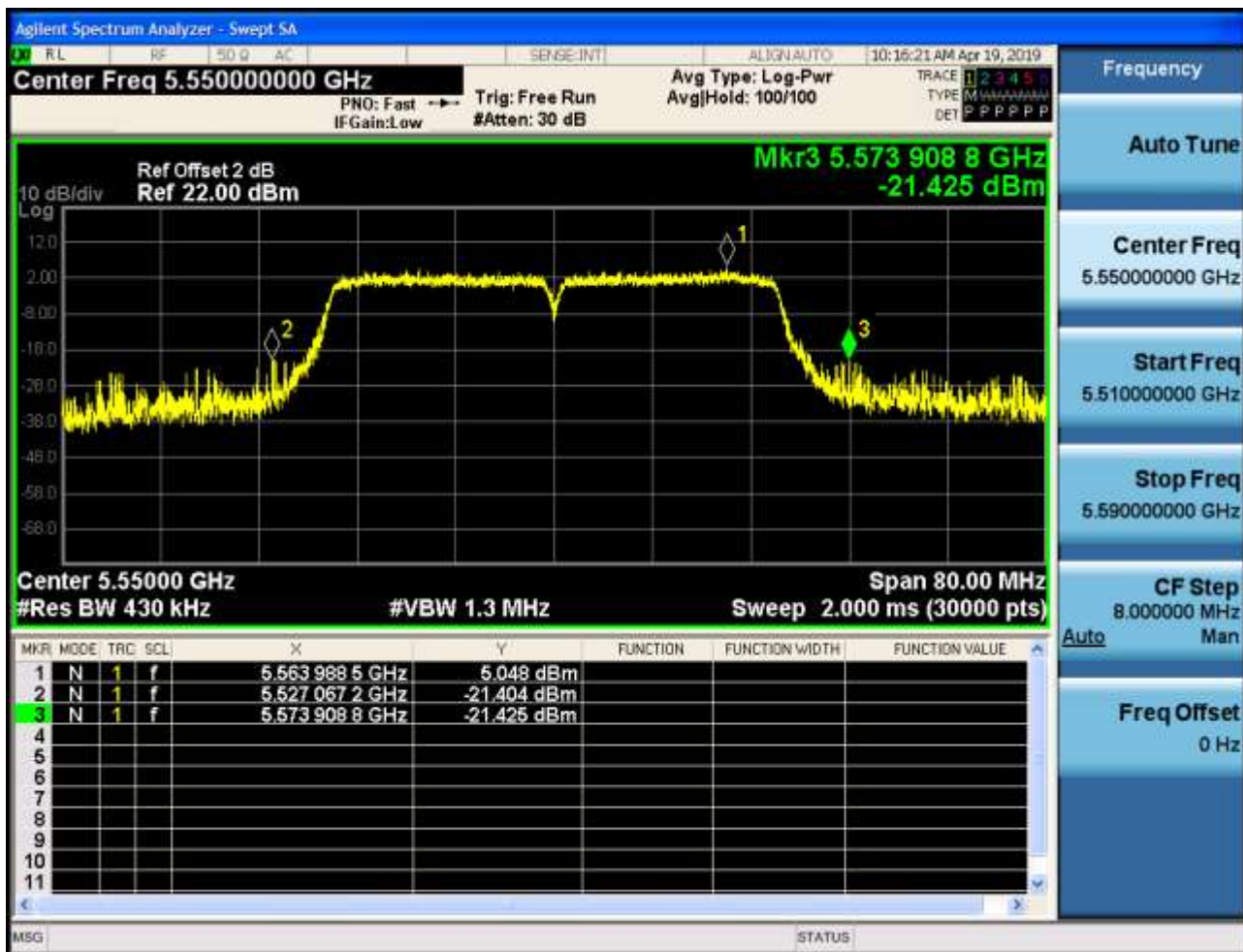


1.126 11AC40SISO_Ant2_5510



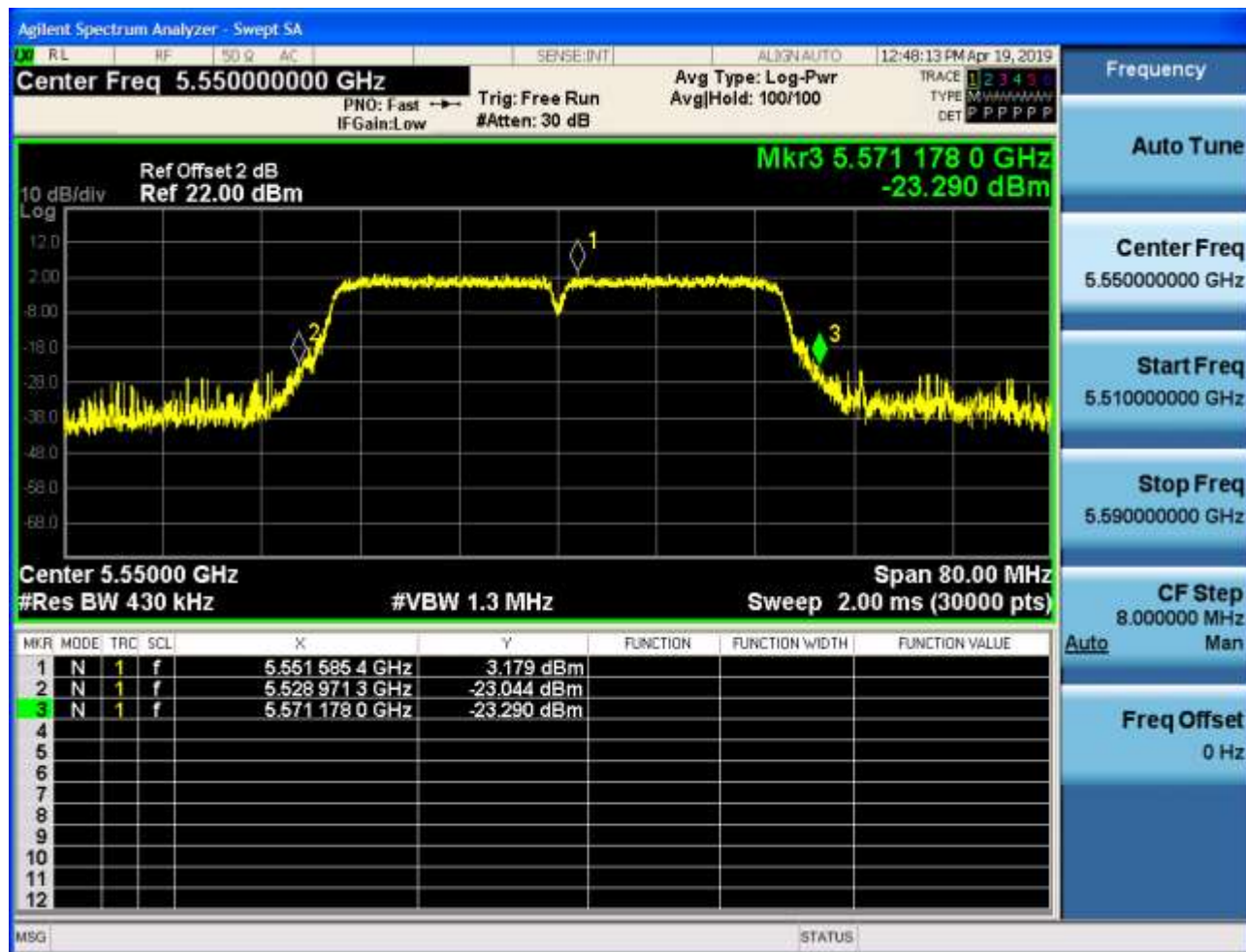


1.127 11AC40SISO_Ant1_5550



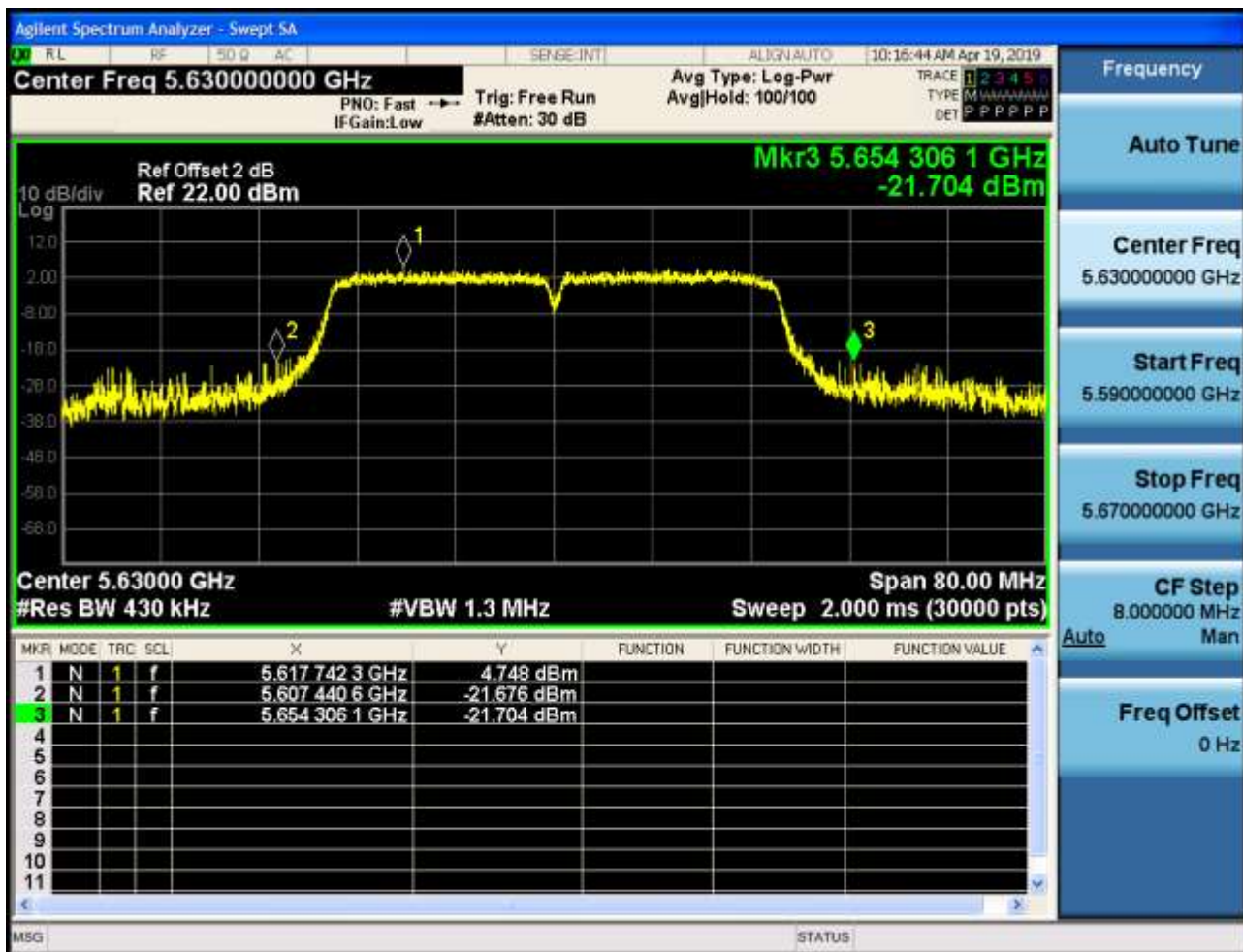


1.128 11AC40SISO_Ant2_5550



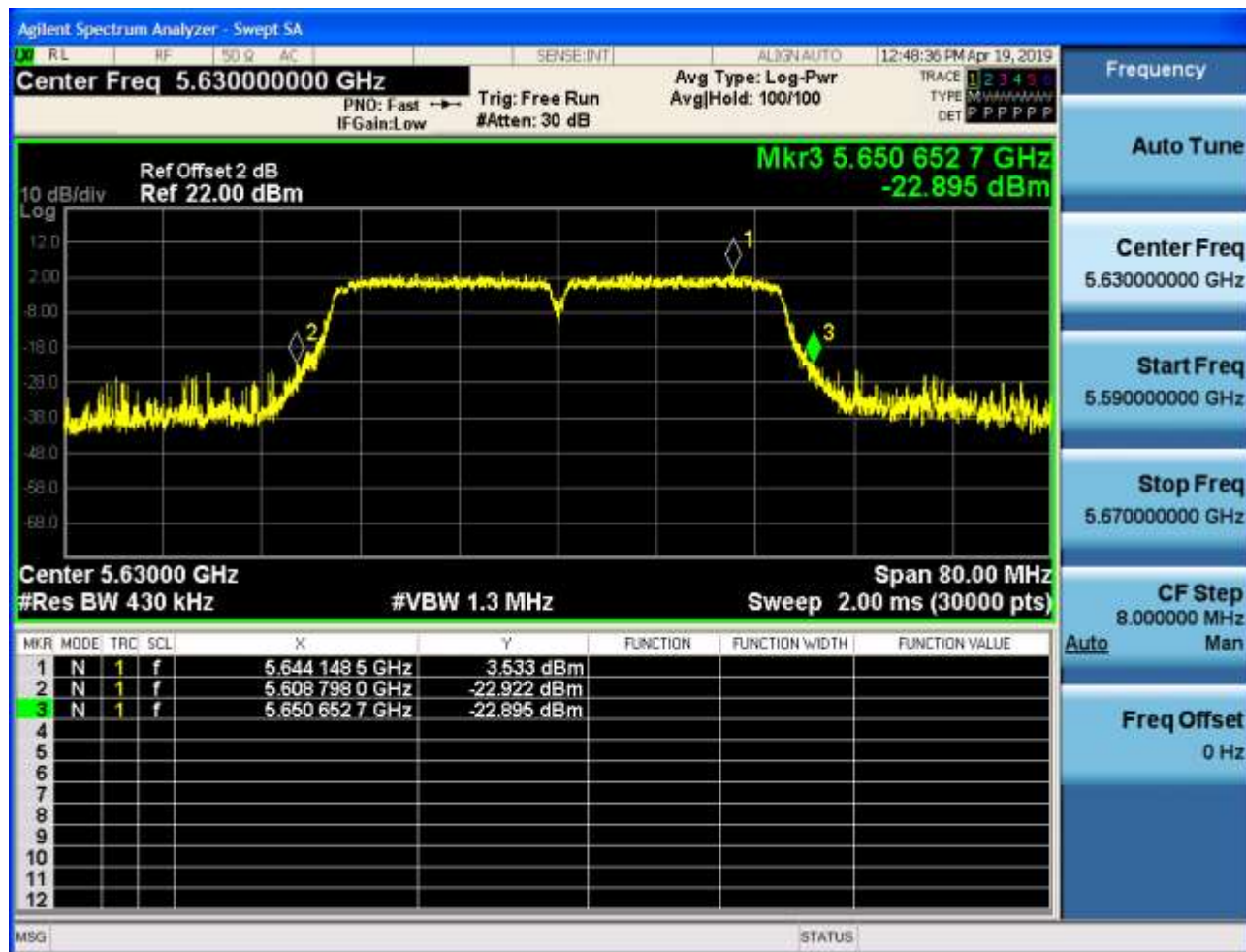


1.129 11AC40SISO_Ant1_5630



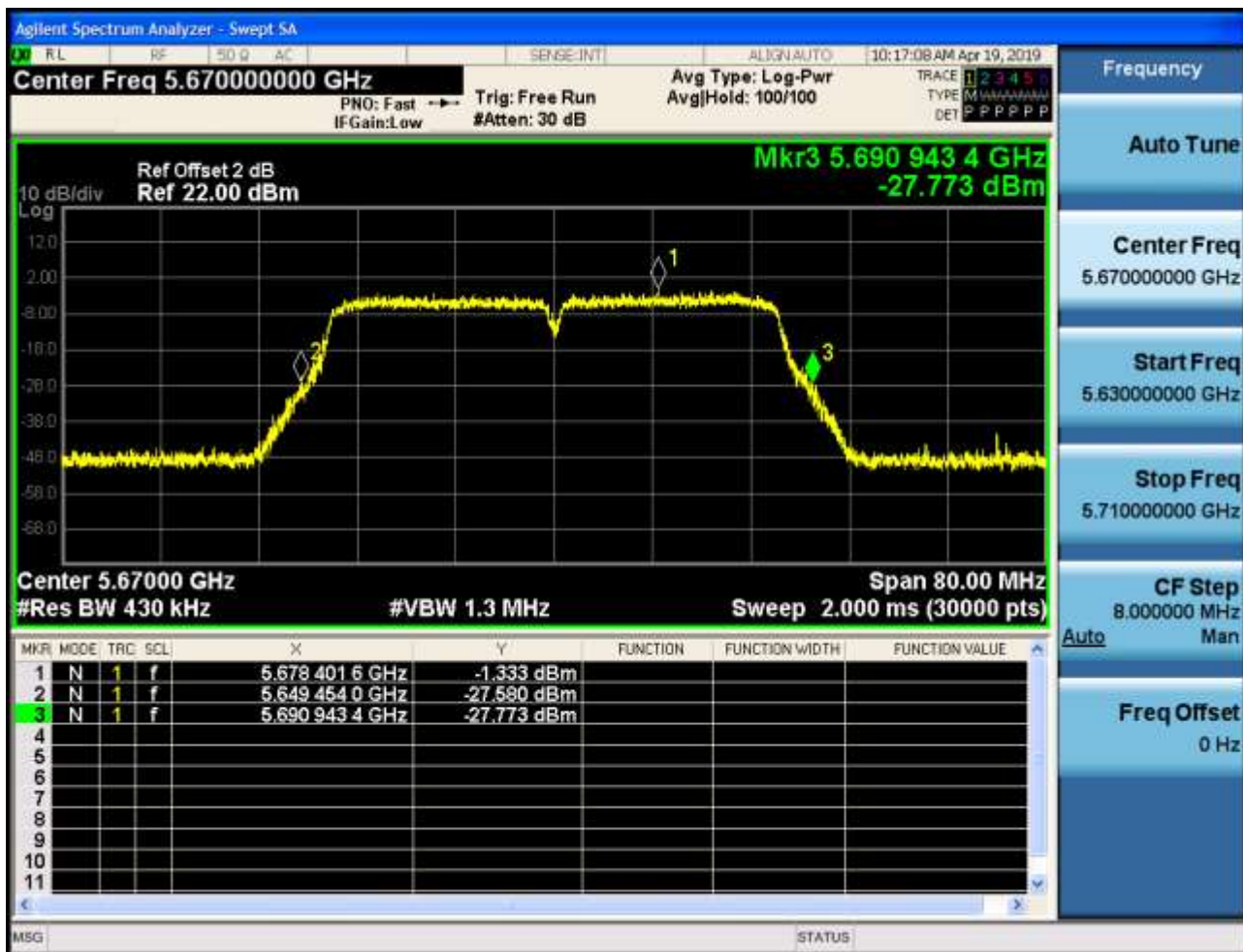


1.130 11AC40SISO_Ant2_5630



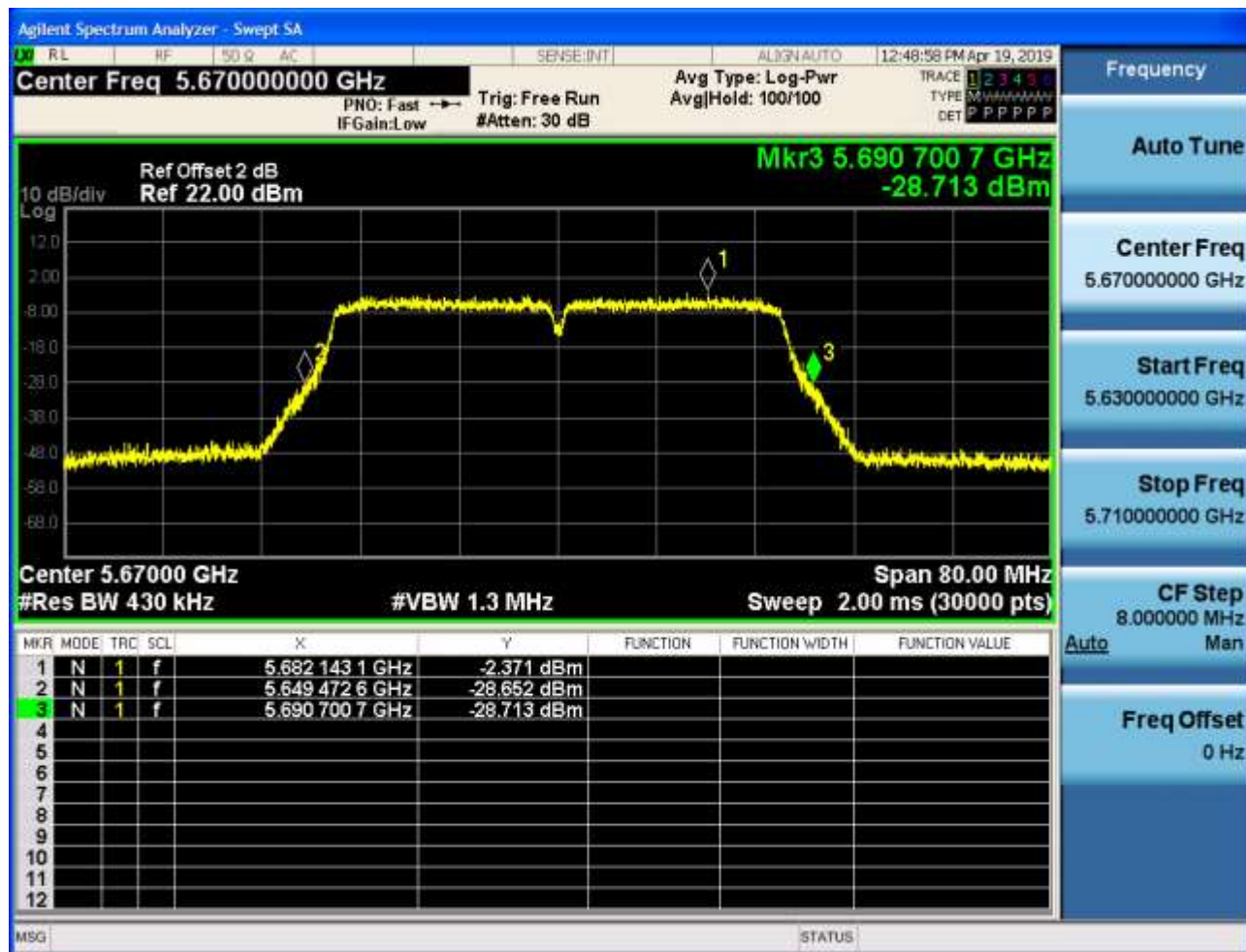


1.131 11AC40SISO_Ant1_5670



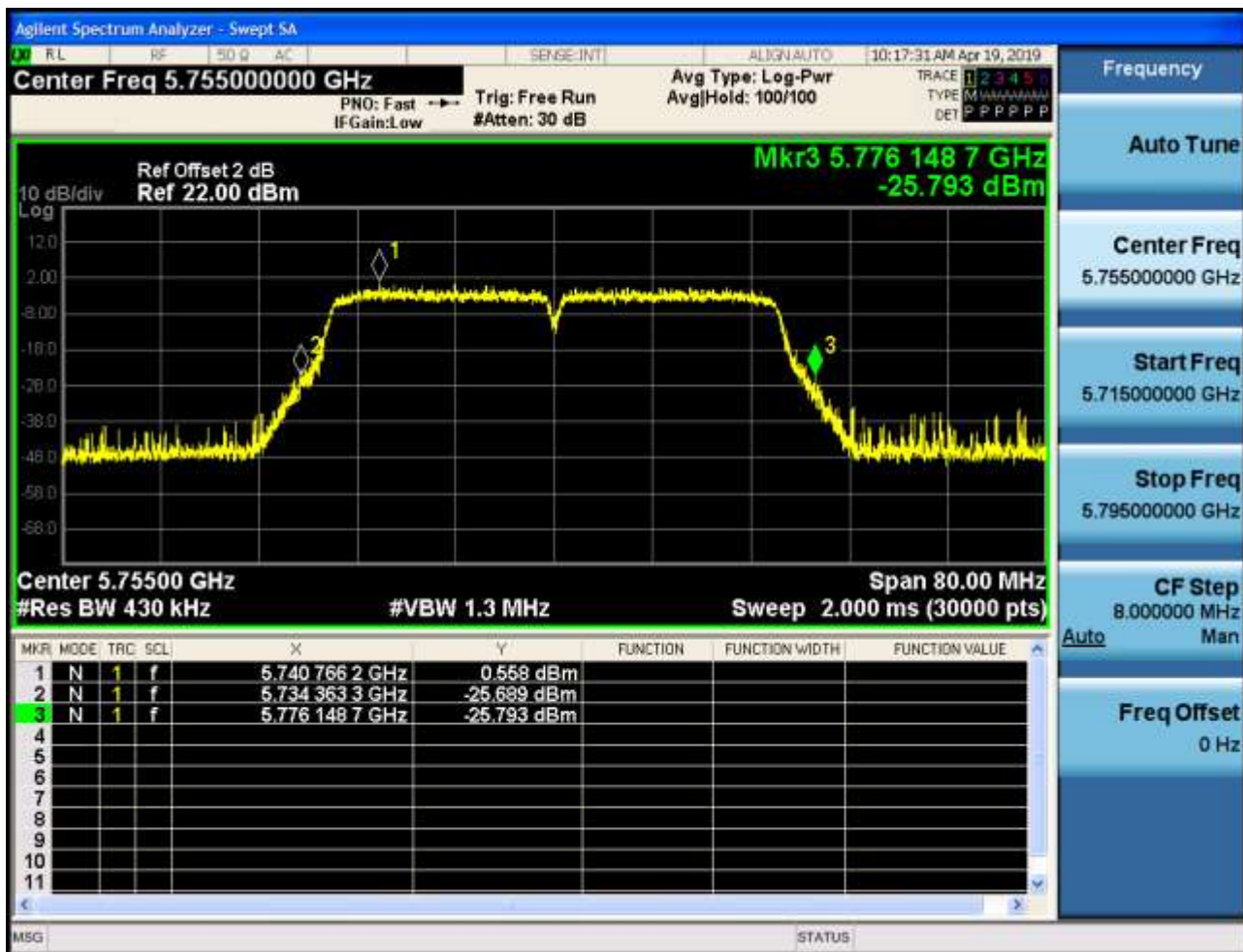


1.132 11AC40SISO_Ant2_5670



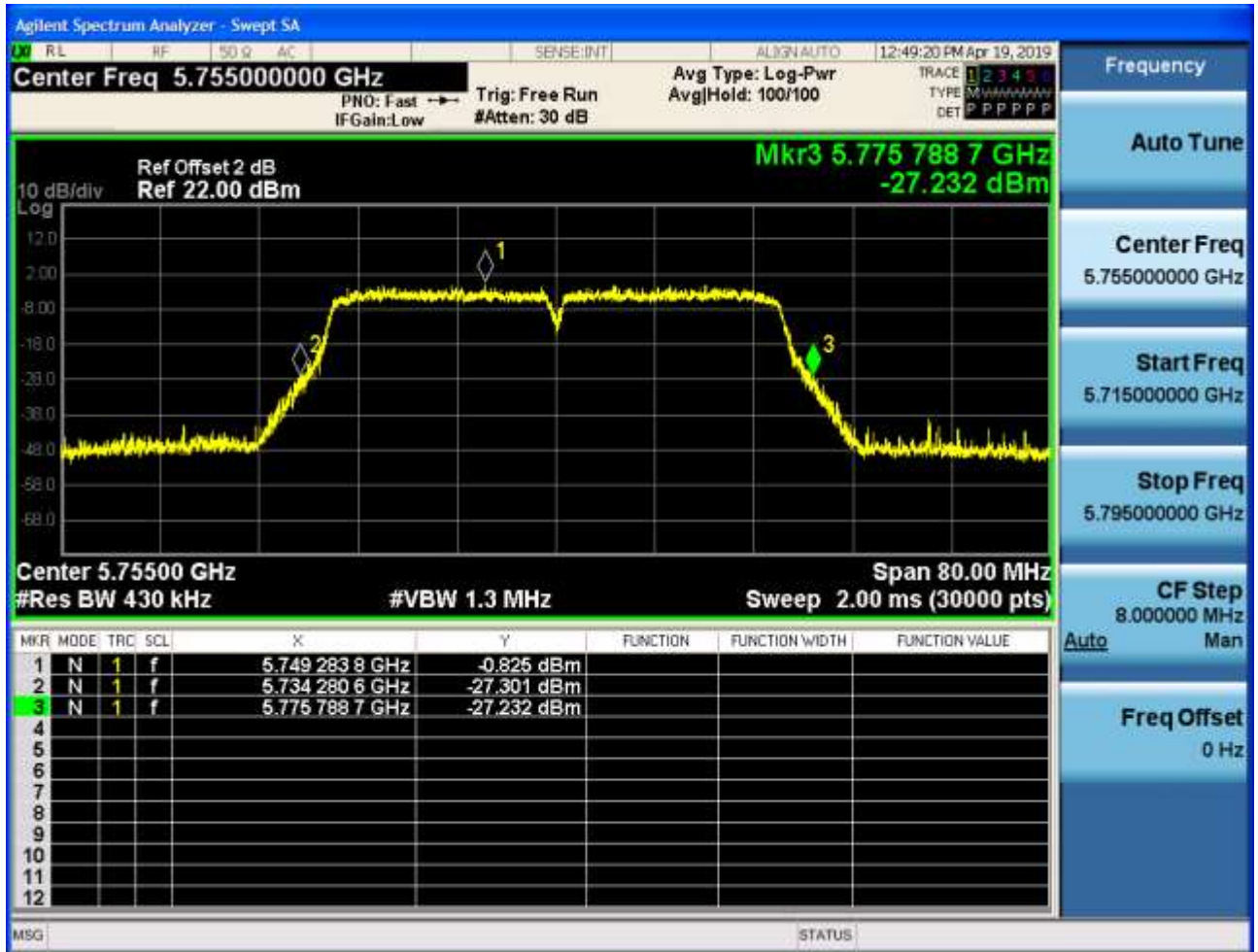


1.133 11AC40SISO_Ant1_5755



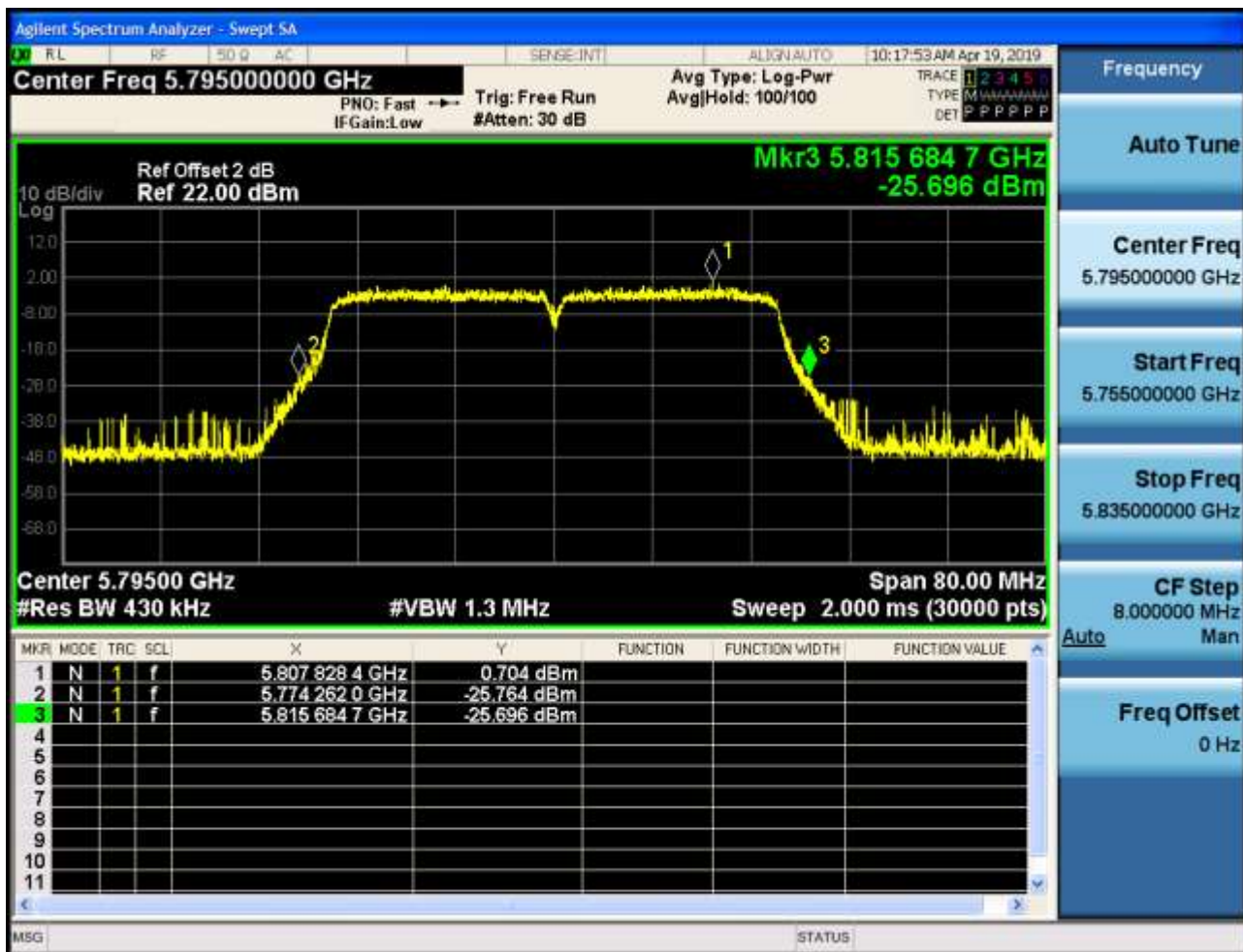


1.134 11AC40SISO_Ant2_5755



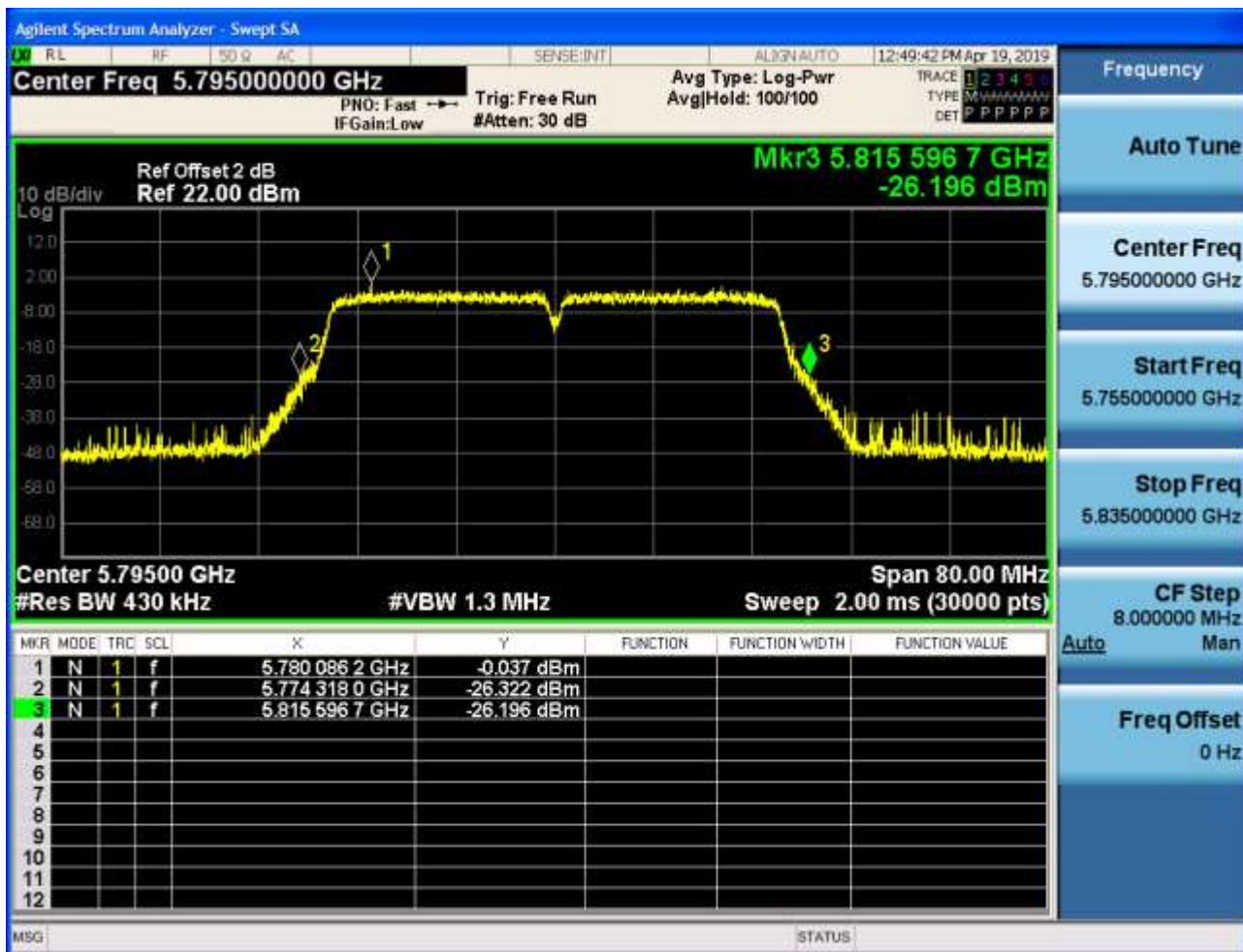


1.135 11AC40SISO_Ant1_5795



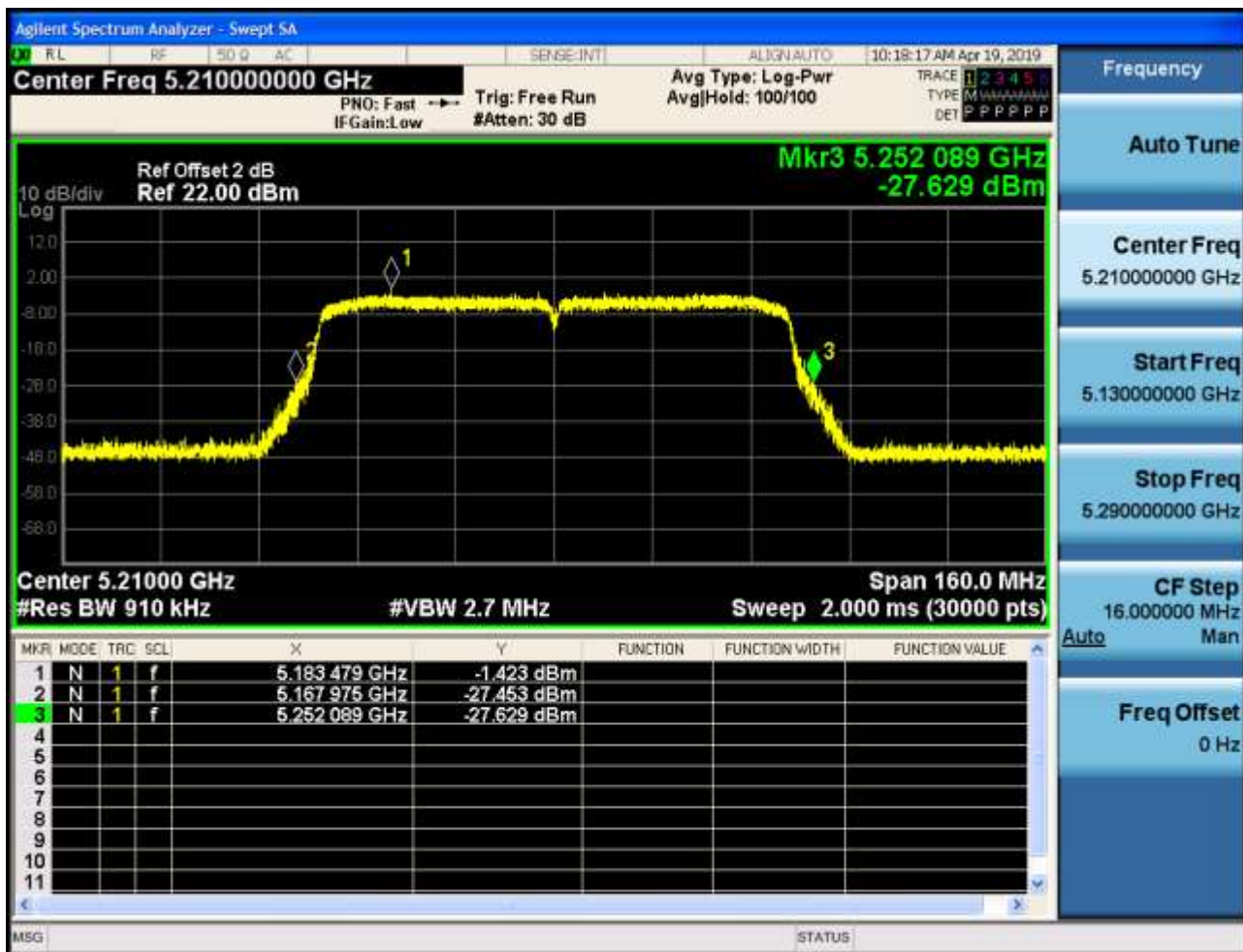


1.136 1AC40SISO_Ant2_5795





1.137 11AC80SISO_Ant1_5210



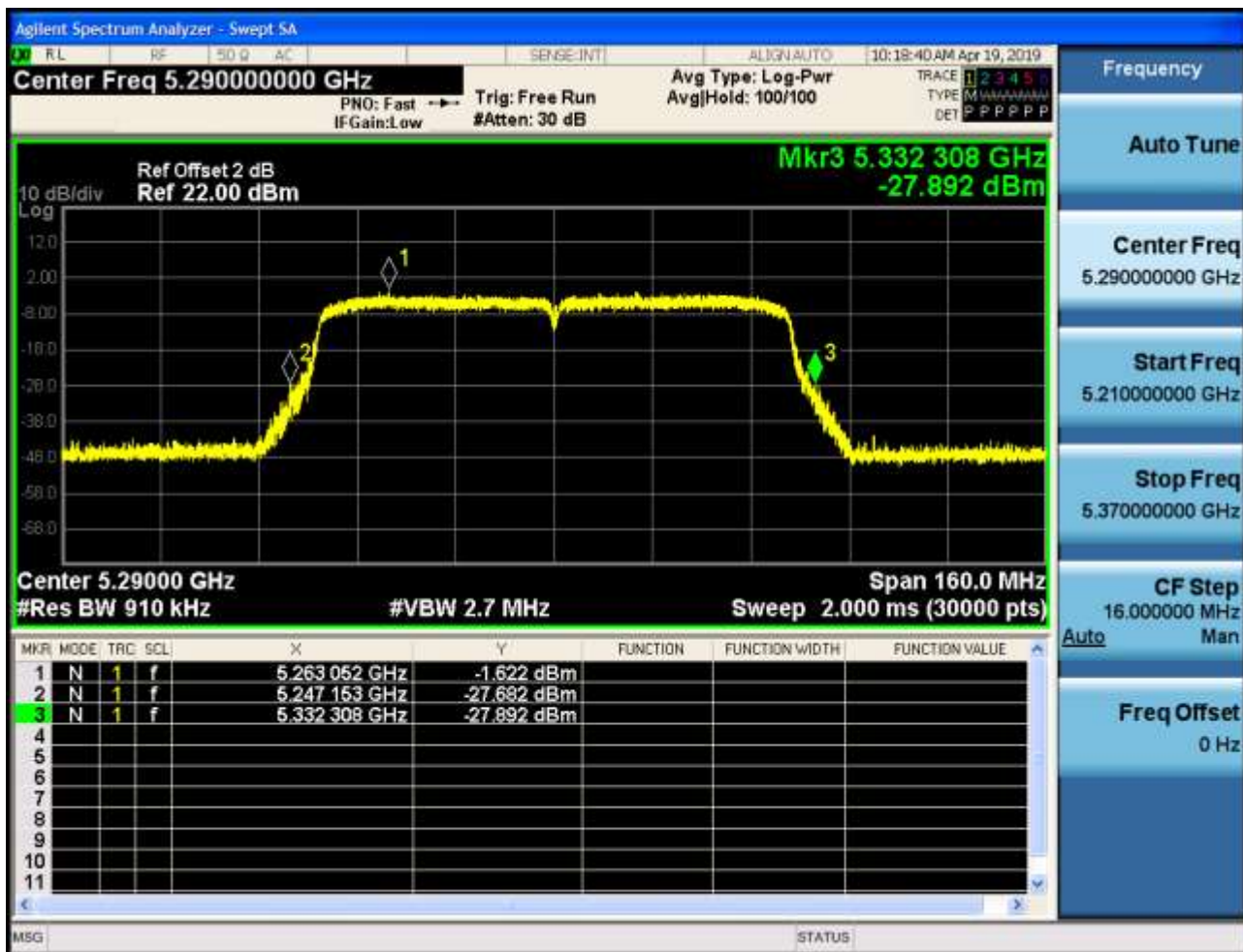


1.138 11AC80SISO_Ant2_5210





1.139 11AC80SISO_Ant1_5290



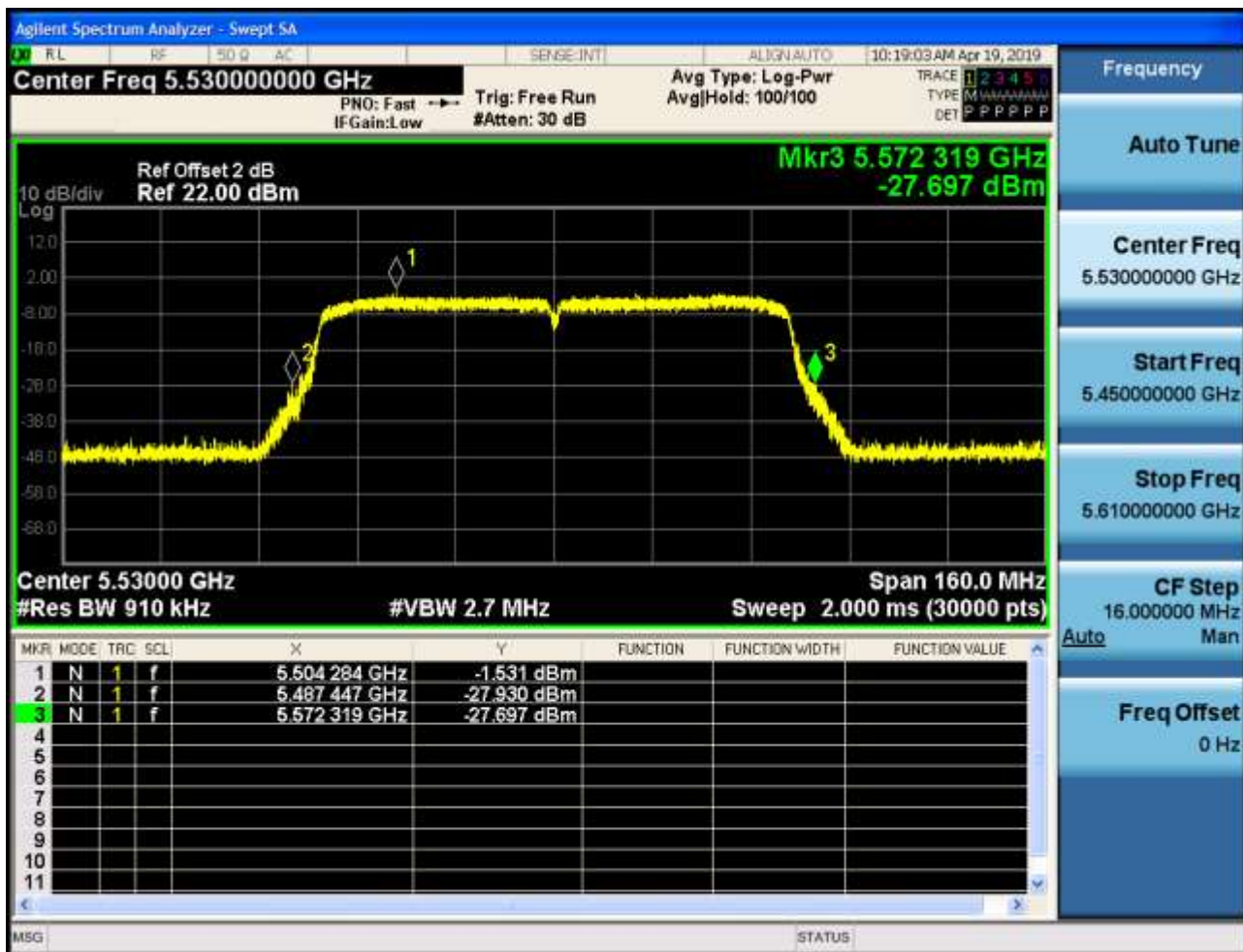


1.140 11AC80SISO_Ant2_5290



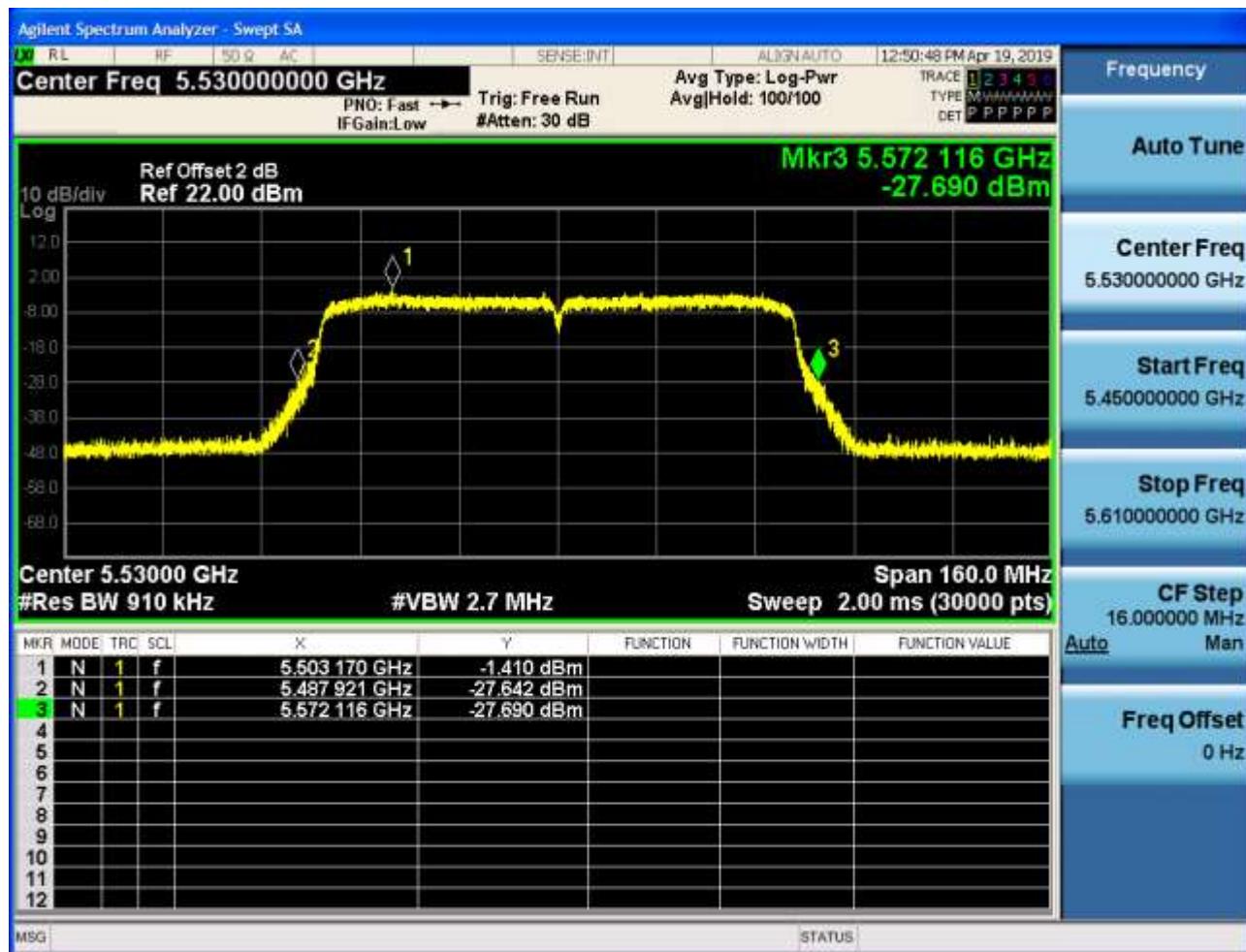


1.141 11AC80SISO_Ant1_5530





1.142 11AC80SISO_Ant2_5530





1.143 11AC80SISO_Ant1_5610



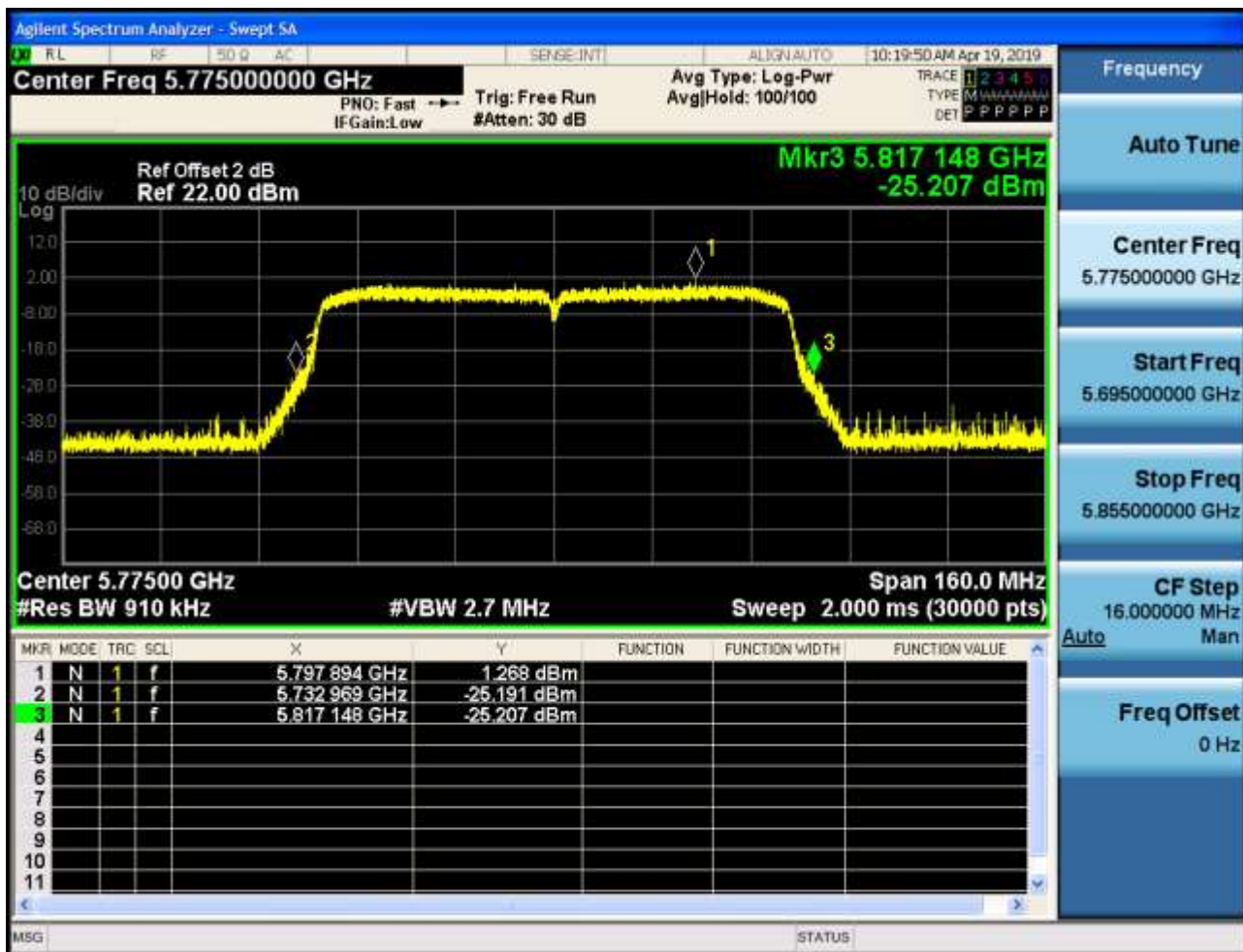


1.144 11AC80SISO_Ant2_5610



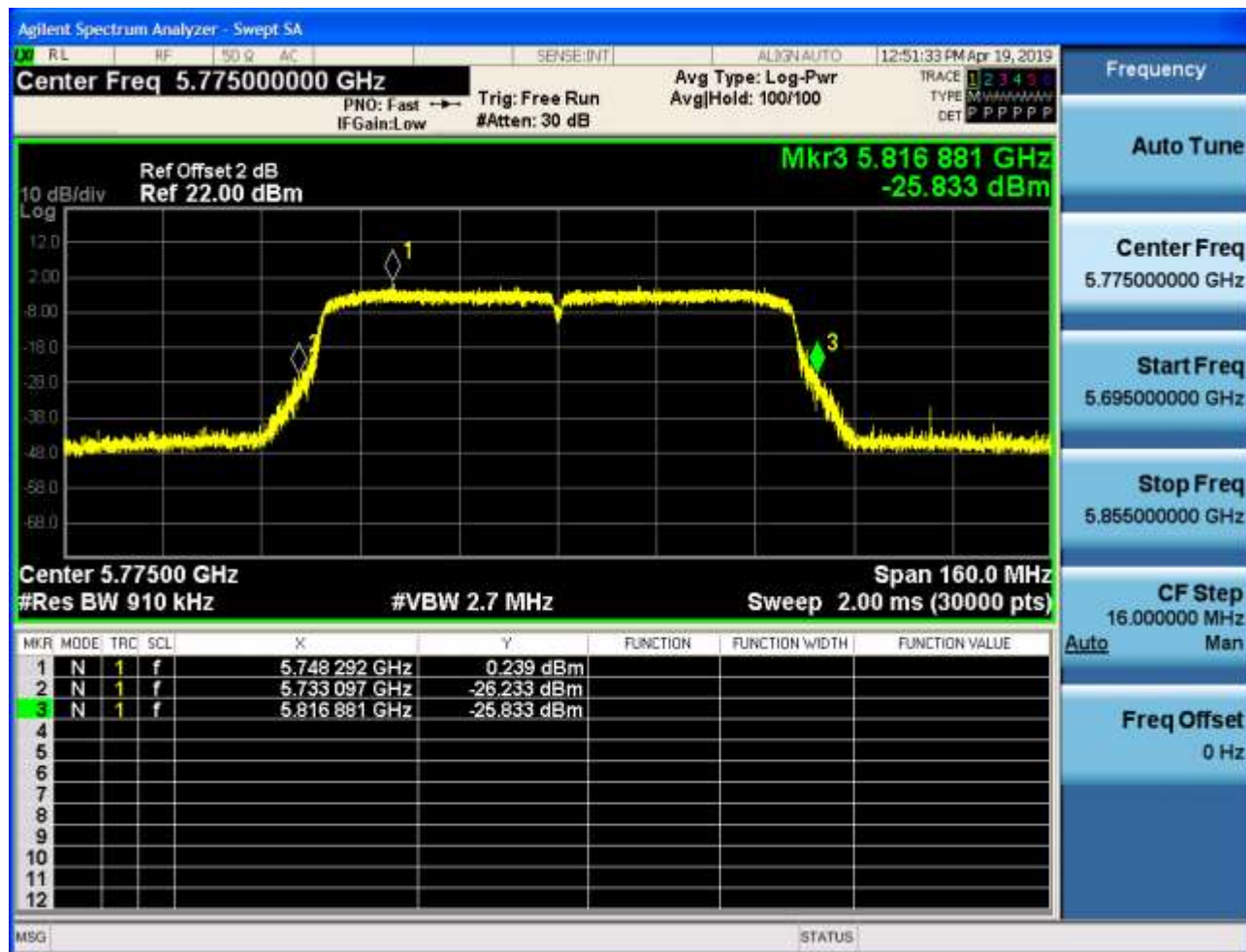


1.145 11AC80SISO_Ant1_5775





1.146 11AC80SISO_Ant2_5775



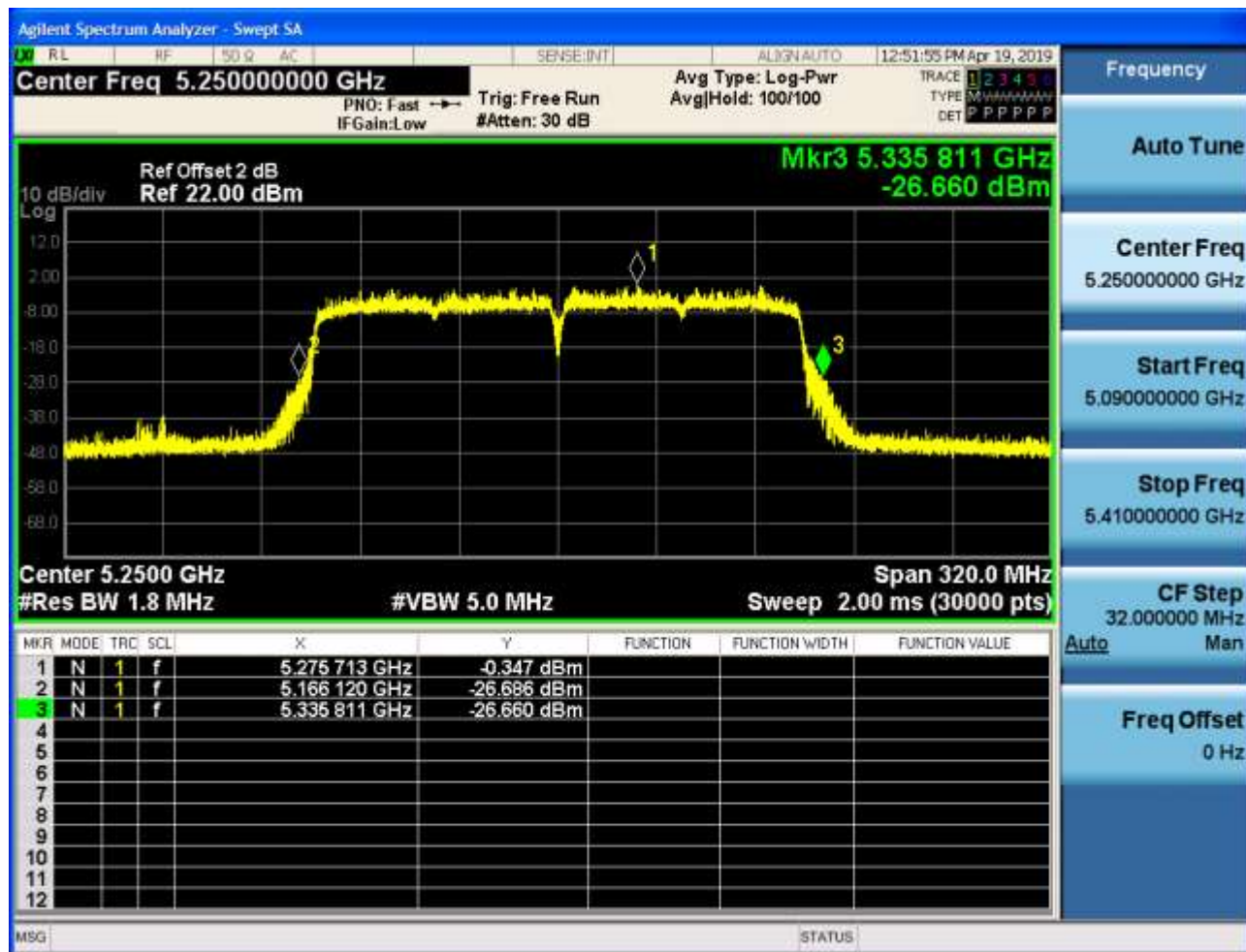


1.147 11AC160SISO_Ant1_5250



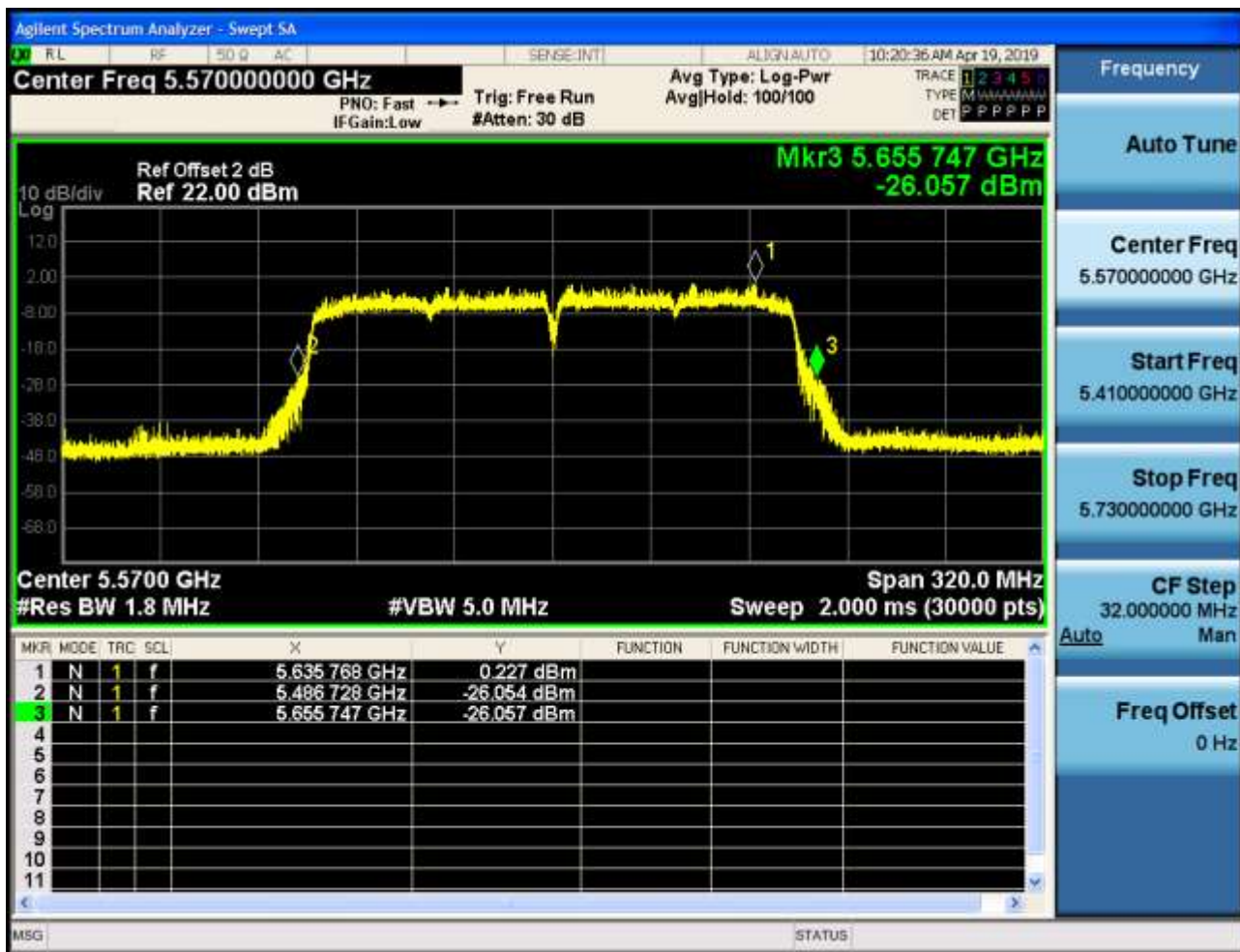


1.14811AC160SISO_Ant2_5250



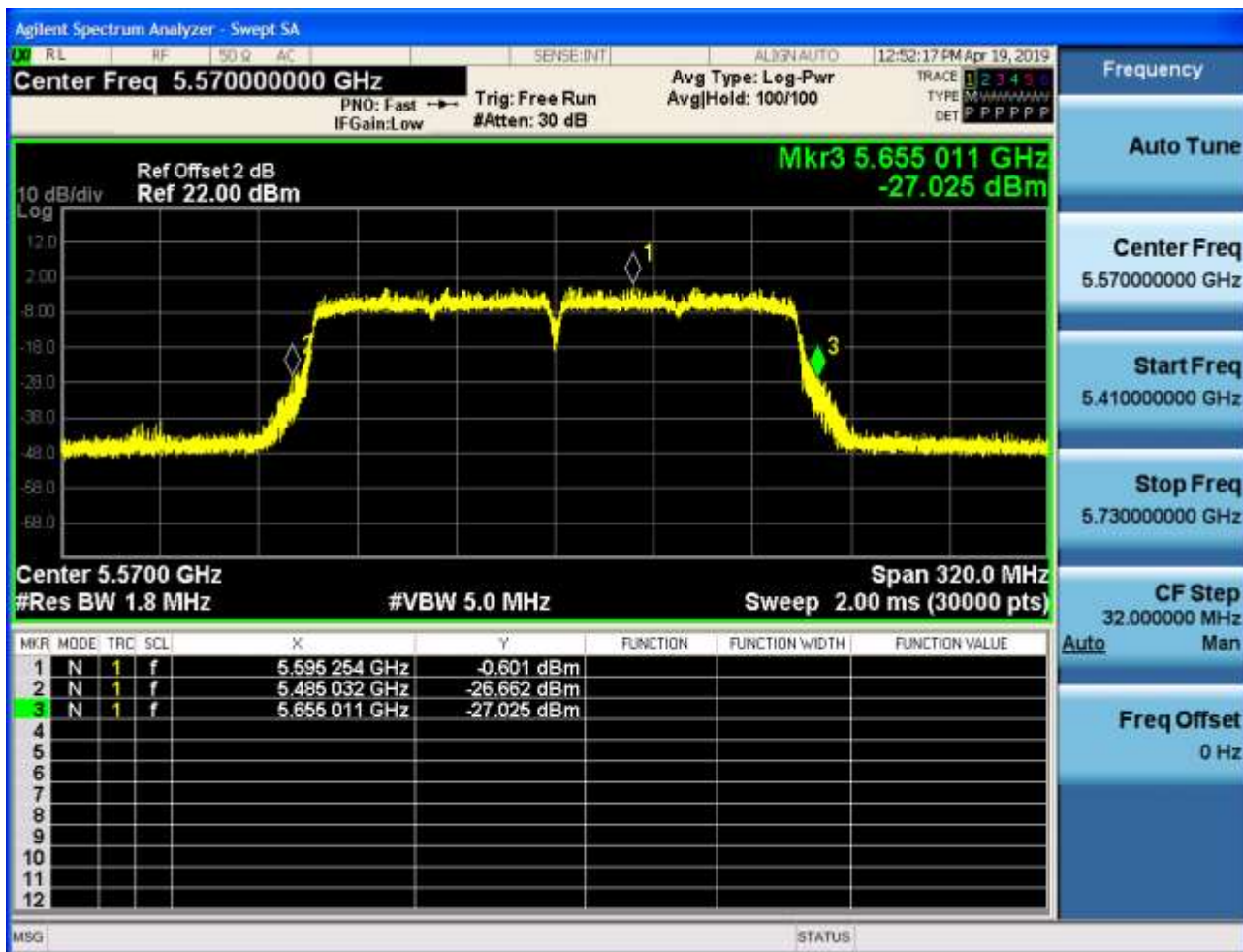


1.149 11AC160SISO_Ant1_5570



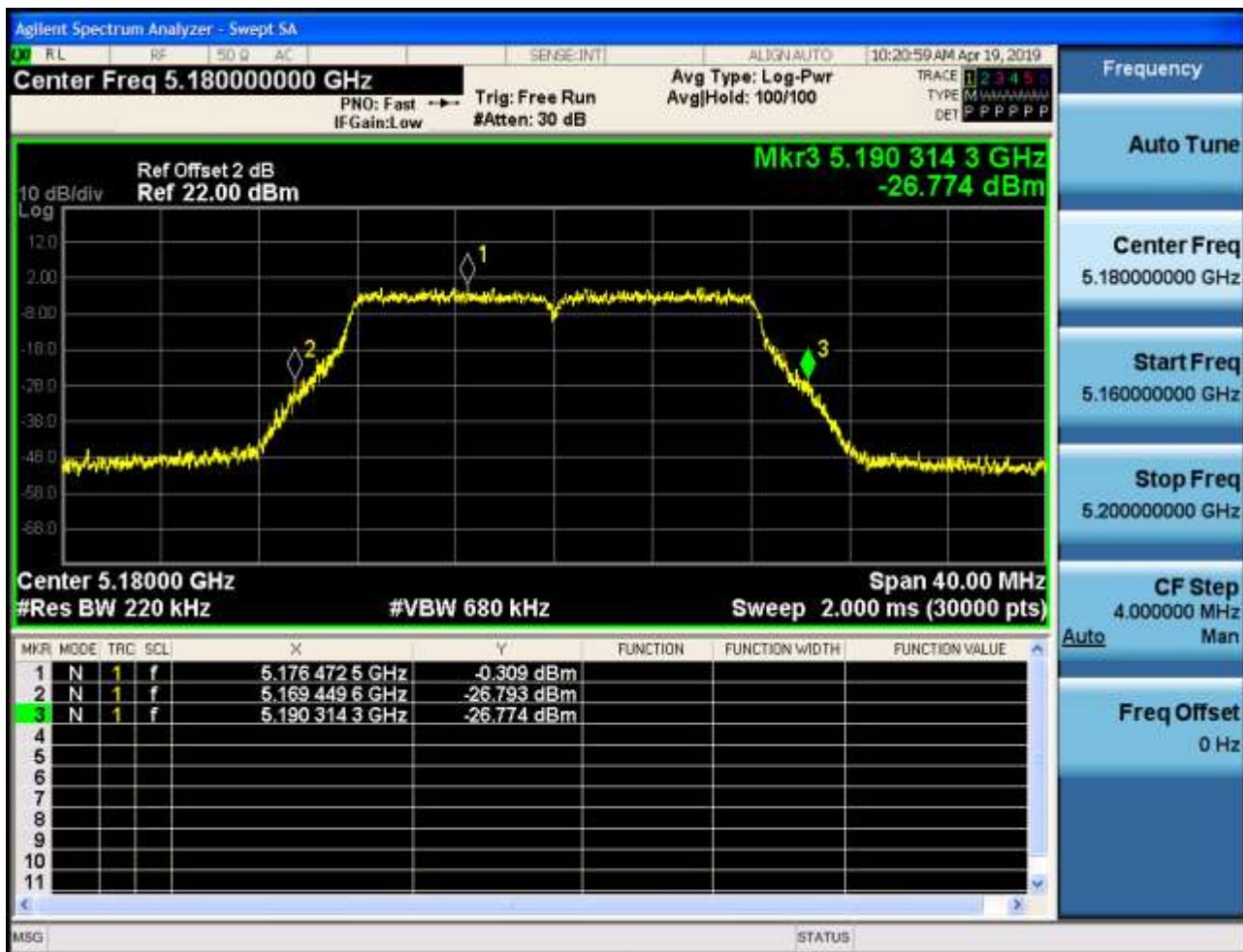


1.150 11AC160SISO_Ant2_5570





1.151 11A20CDD_Ant1_5180



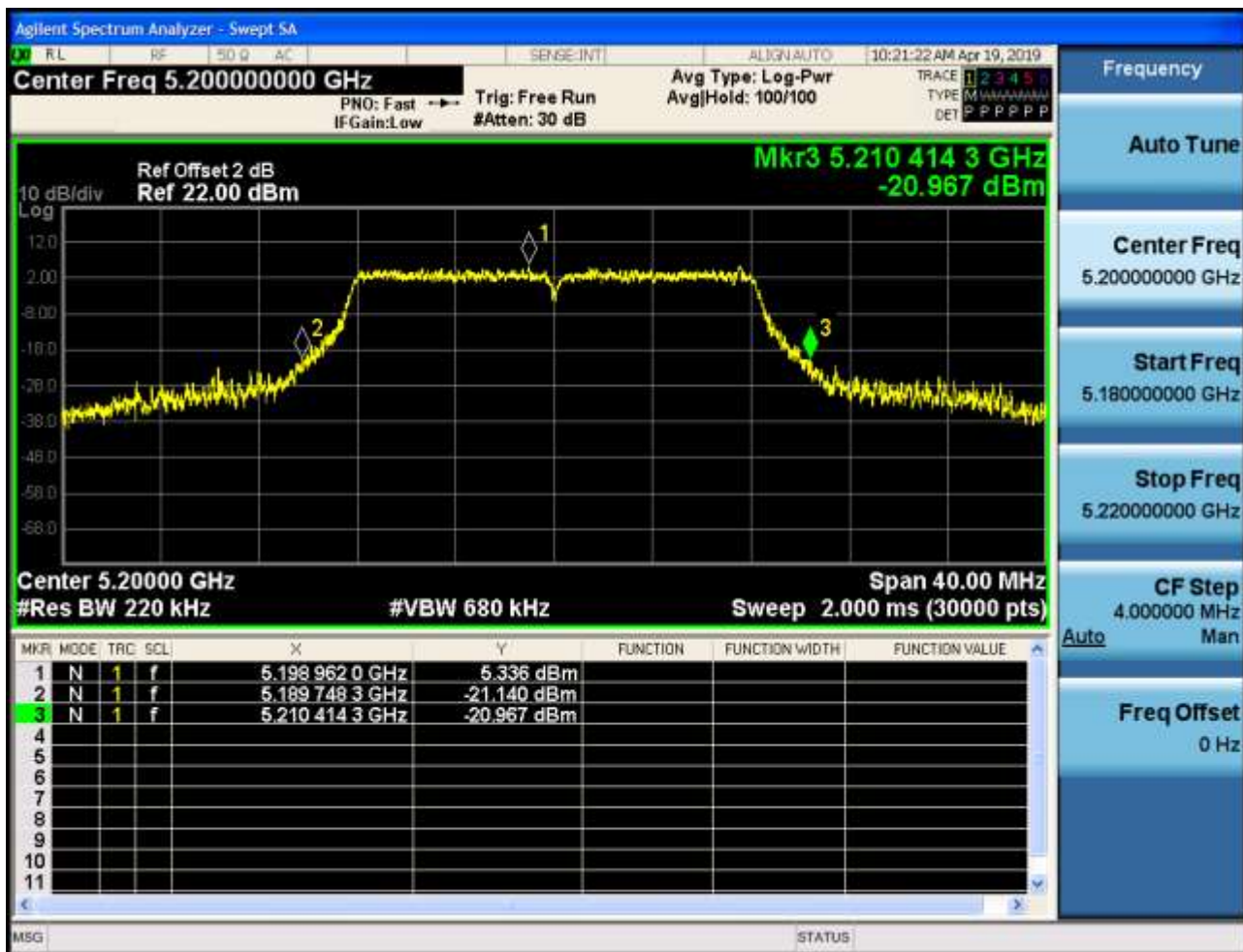


1.152 11A20CDD_Ant2_5180



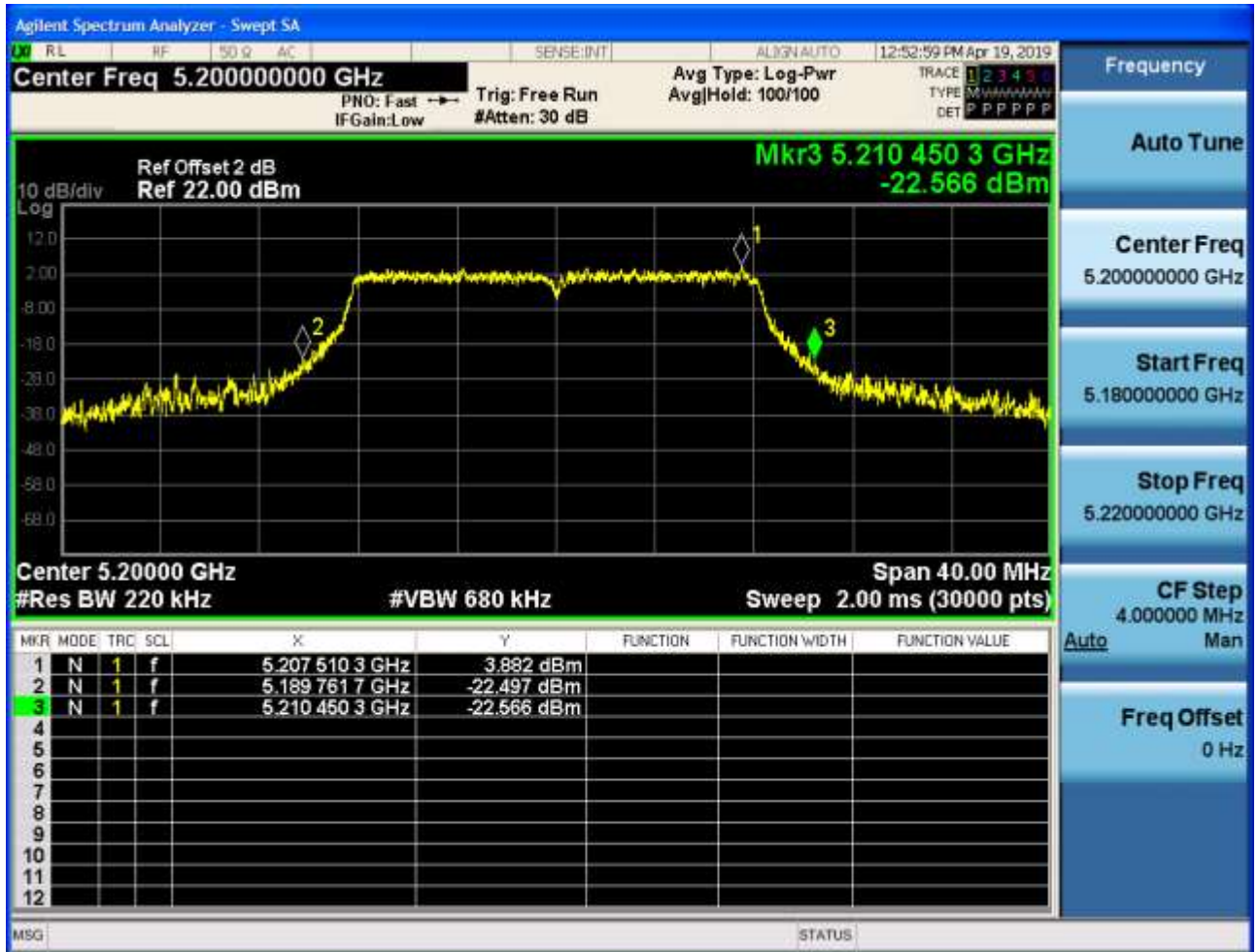


1.153 11A20CDD_Ant1_5200



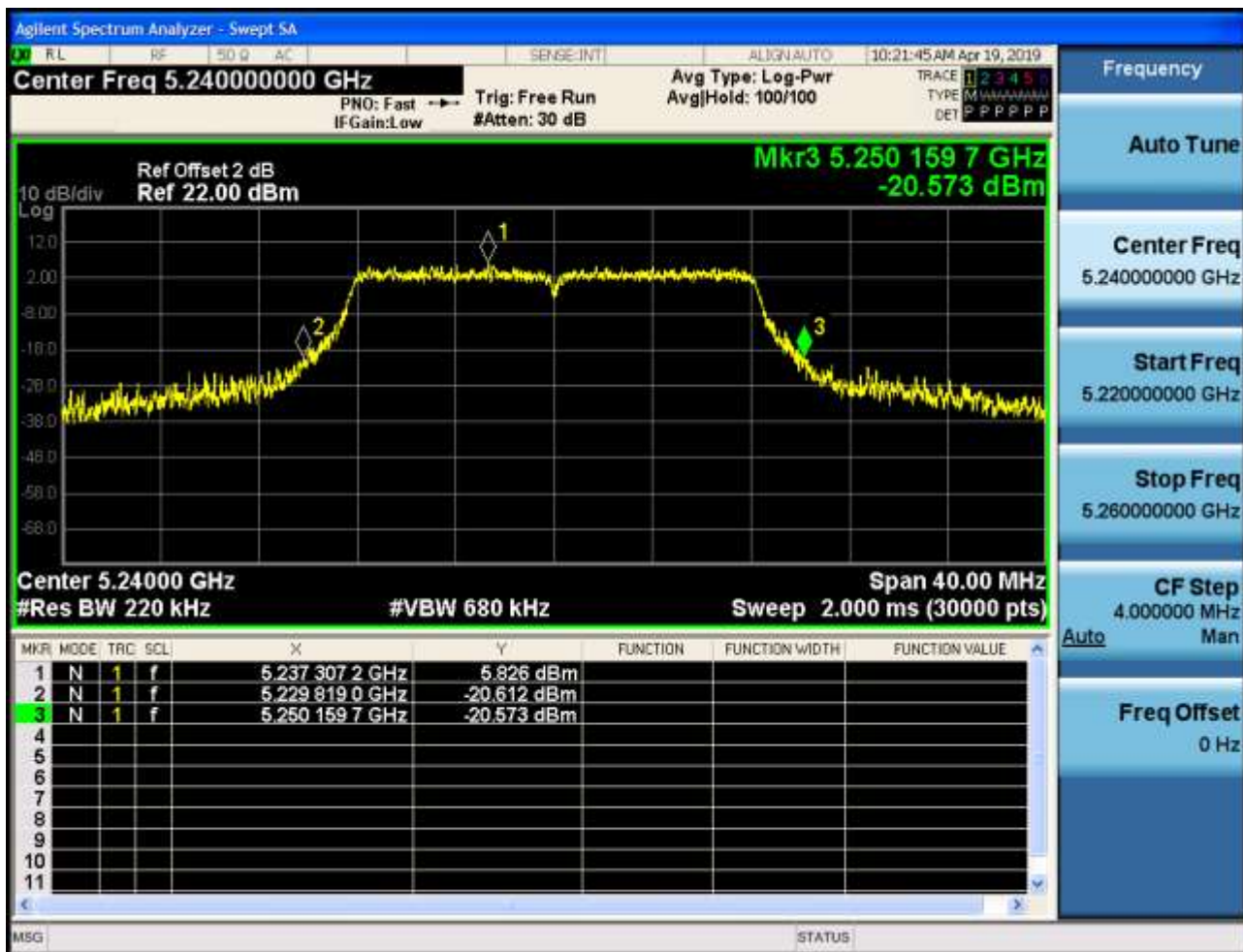


1.154 11A20CDD_Ant2_5200





1.155 11A20CDD_Ant1_5240



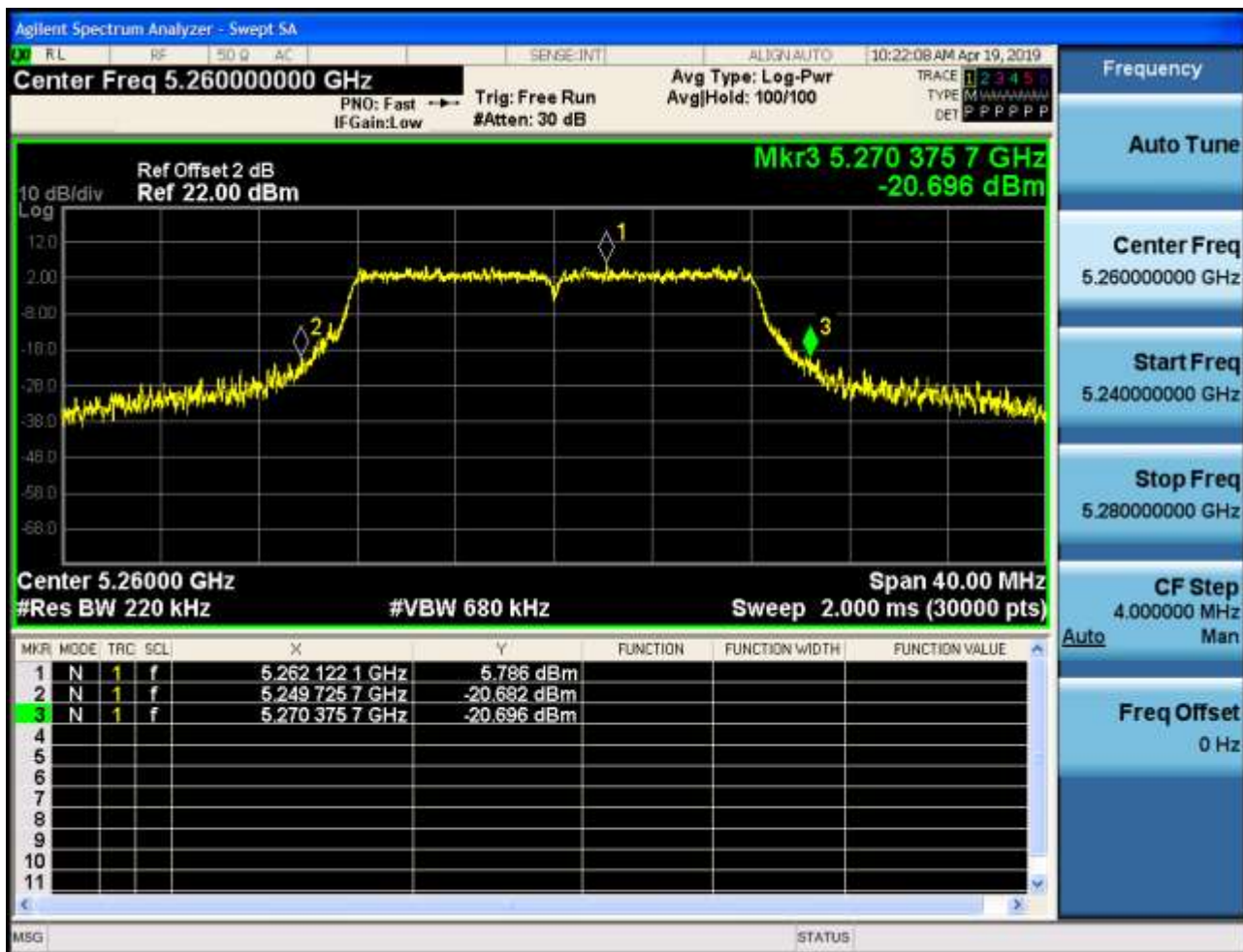


1.156 11A20CDD_Ant2_5240





1.157 11A20CDD_Ant1_5260



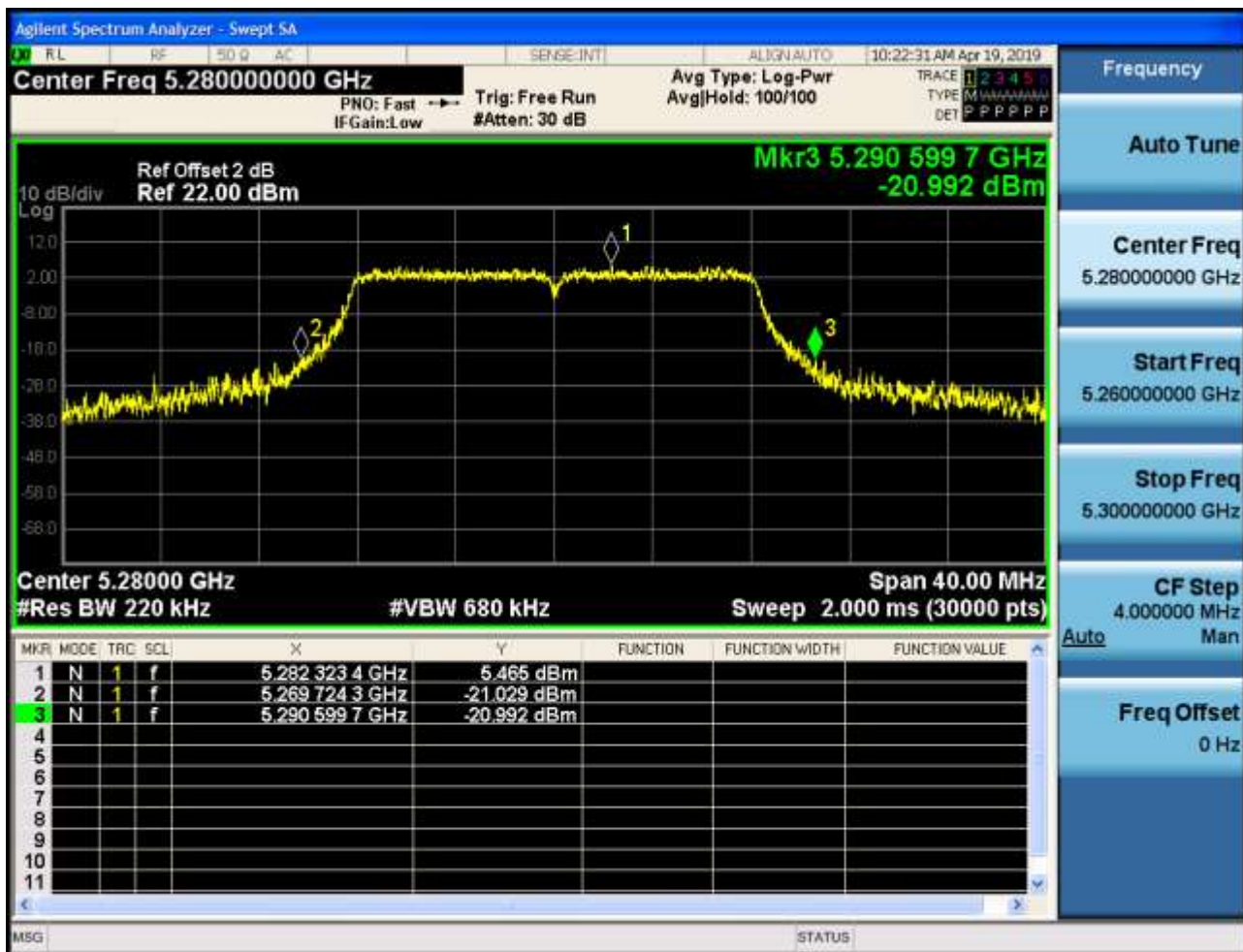


1.158 11A20CDD_Ant2_5260



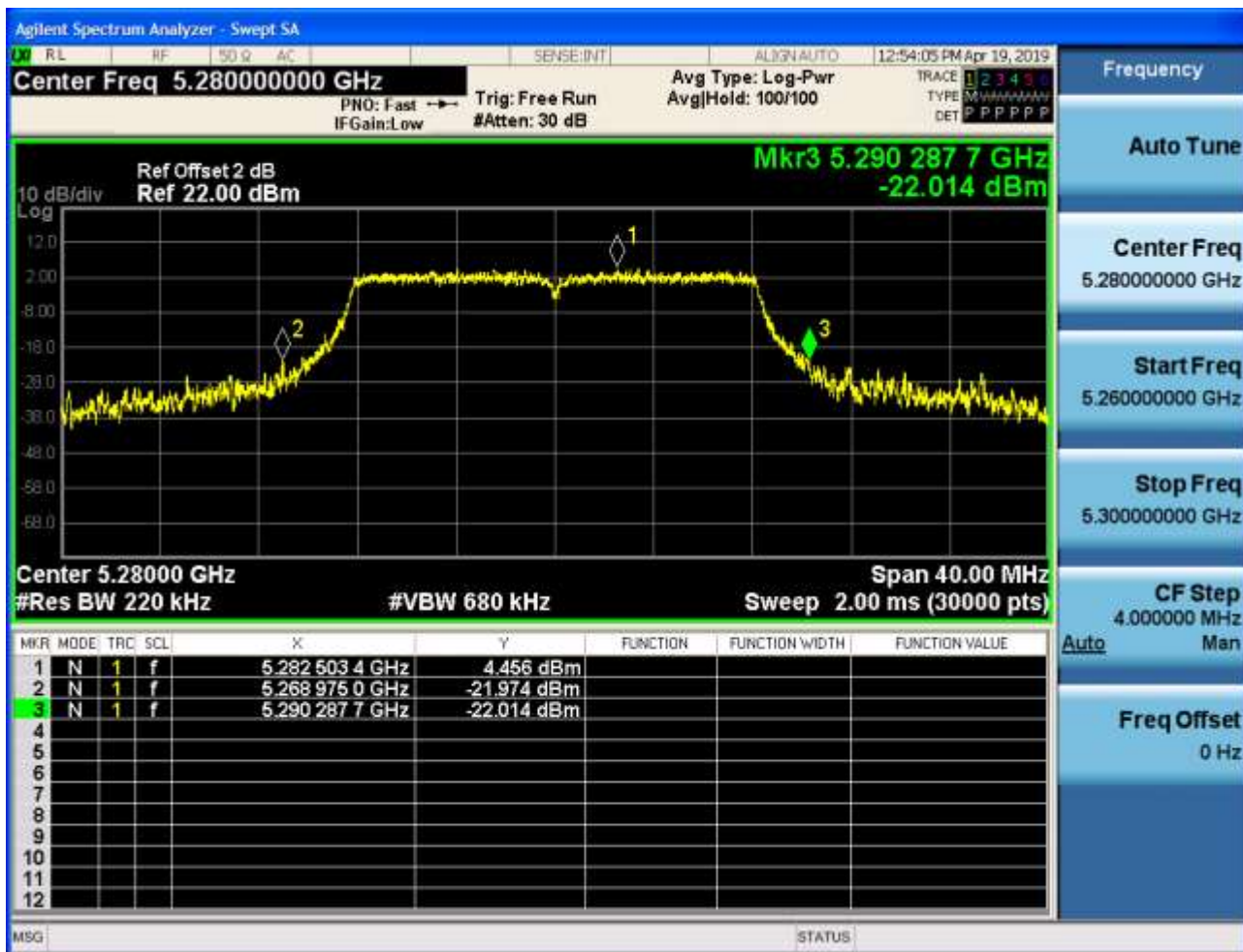


1.159 11A20CDD_Ant1_5280



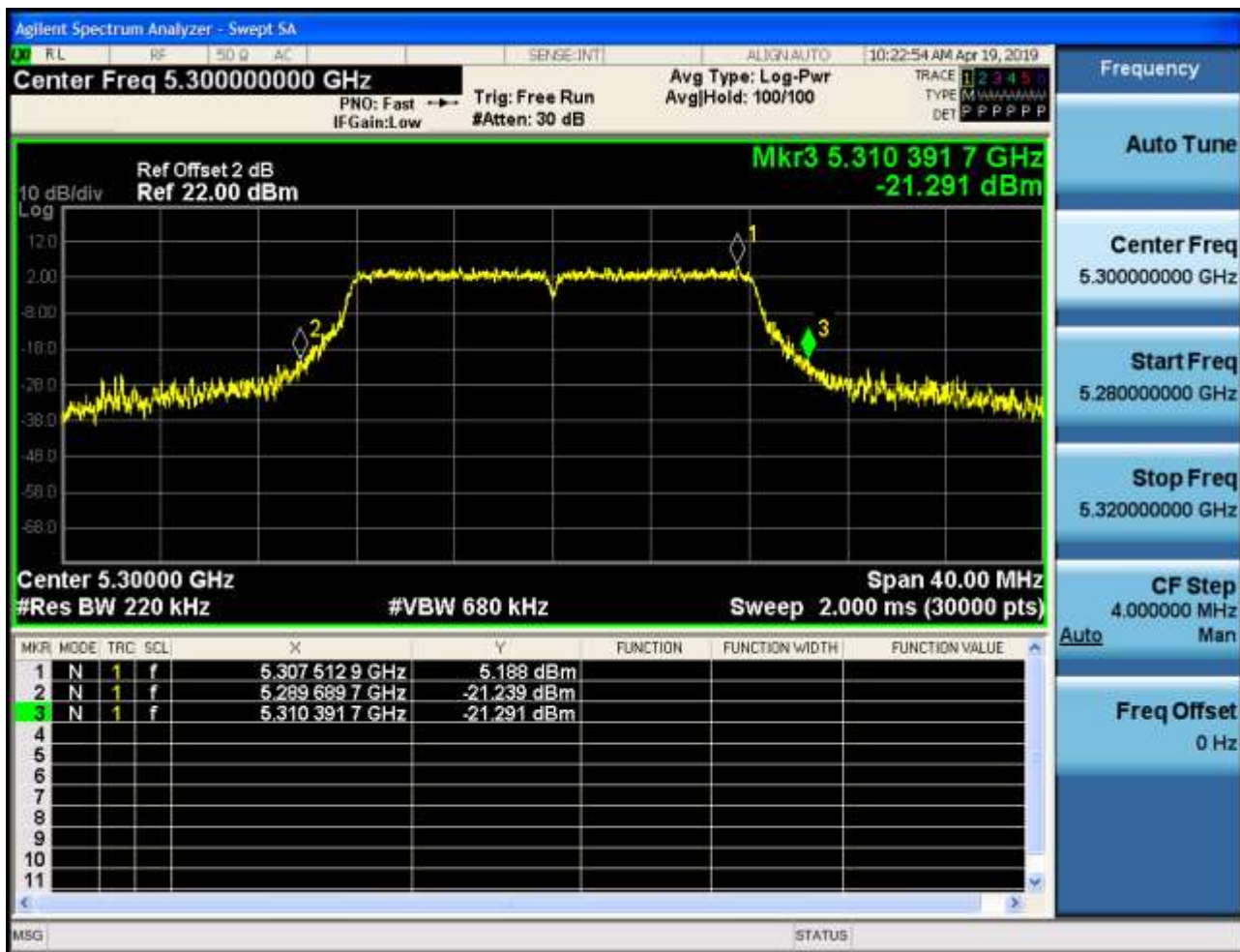


1.160 11A20CDD_Ant2_5280





1.161 11A20CDD_Ant1_5300





1.162 11A20CDD_Ant2_5300





1.163 11A20CDD_Ant1_5320





1.164 11A20CDD_Ant2_5320





1.165 11A20CDD_Ant1_5500



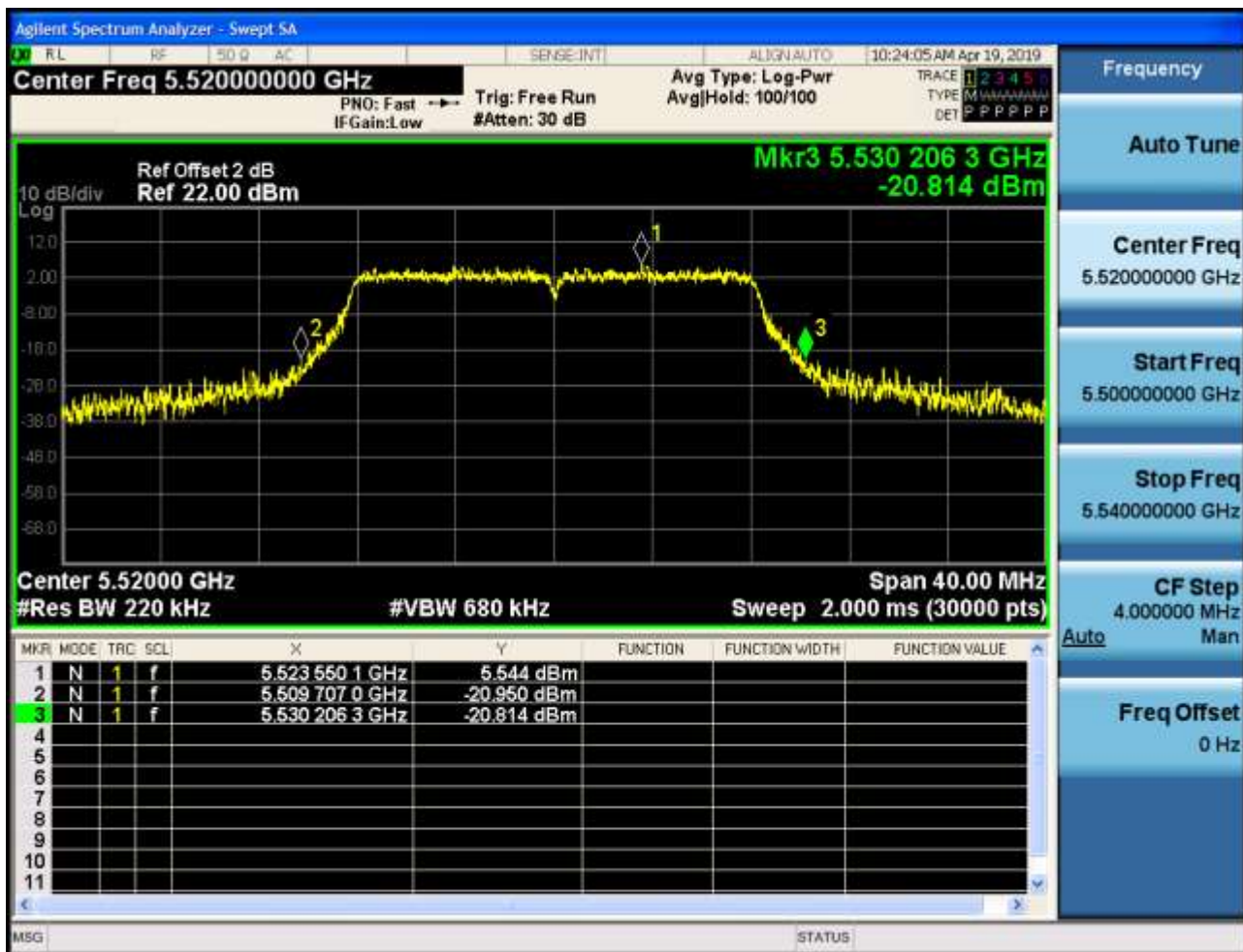


1.166 11A20CDD_Ant2_5500



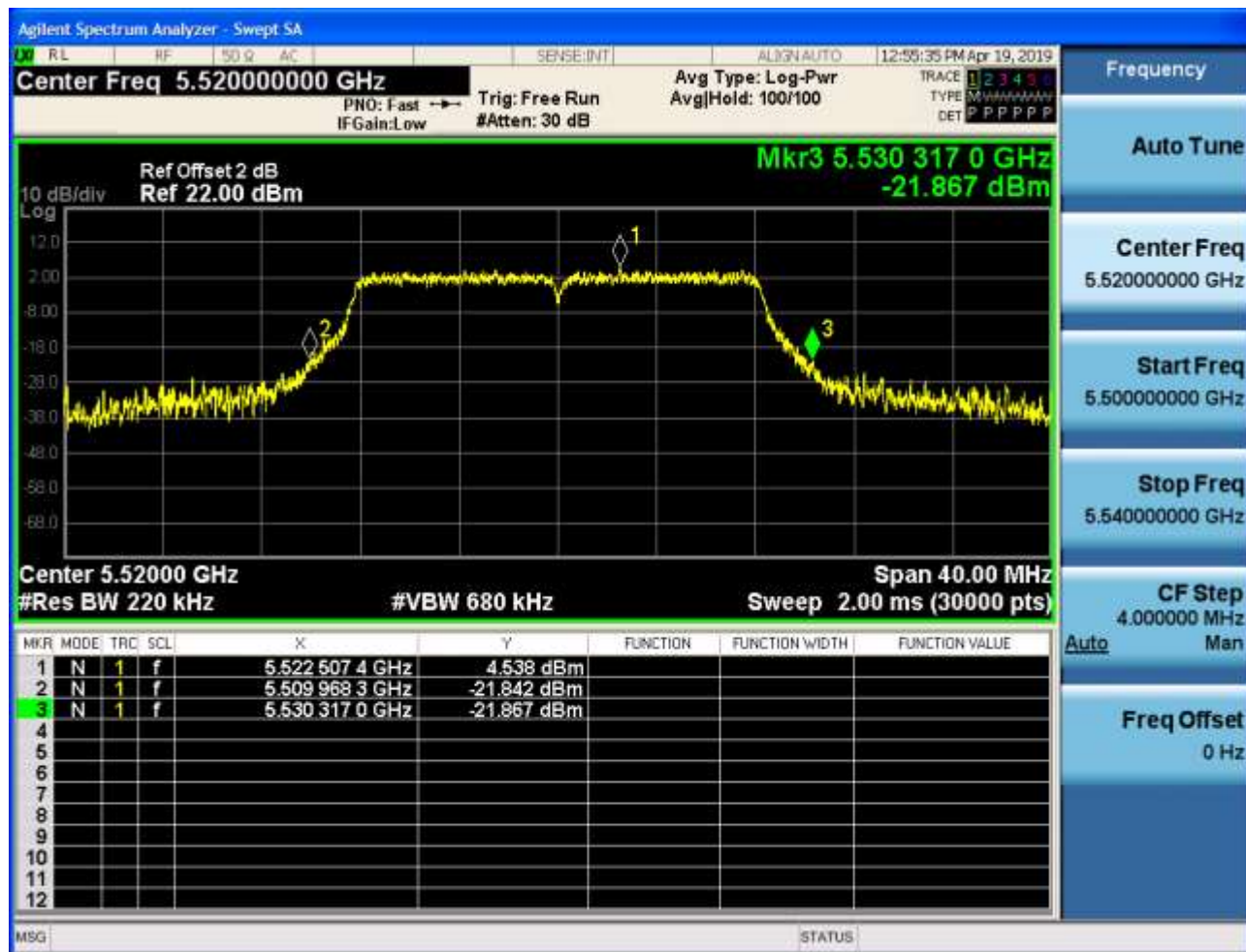


1.167 11A20CDD_Ant1_5520



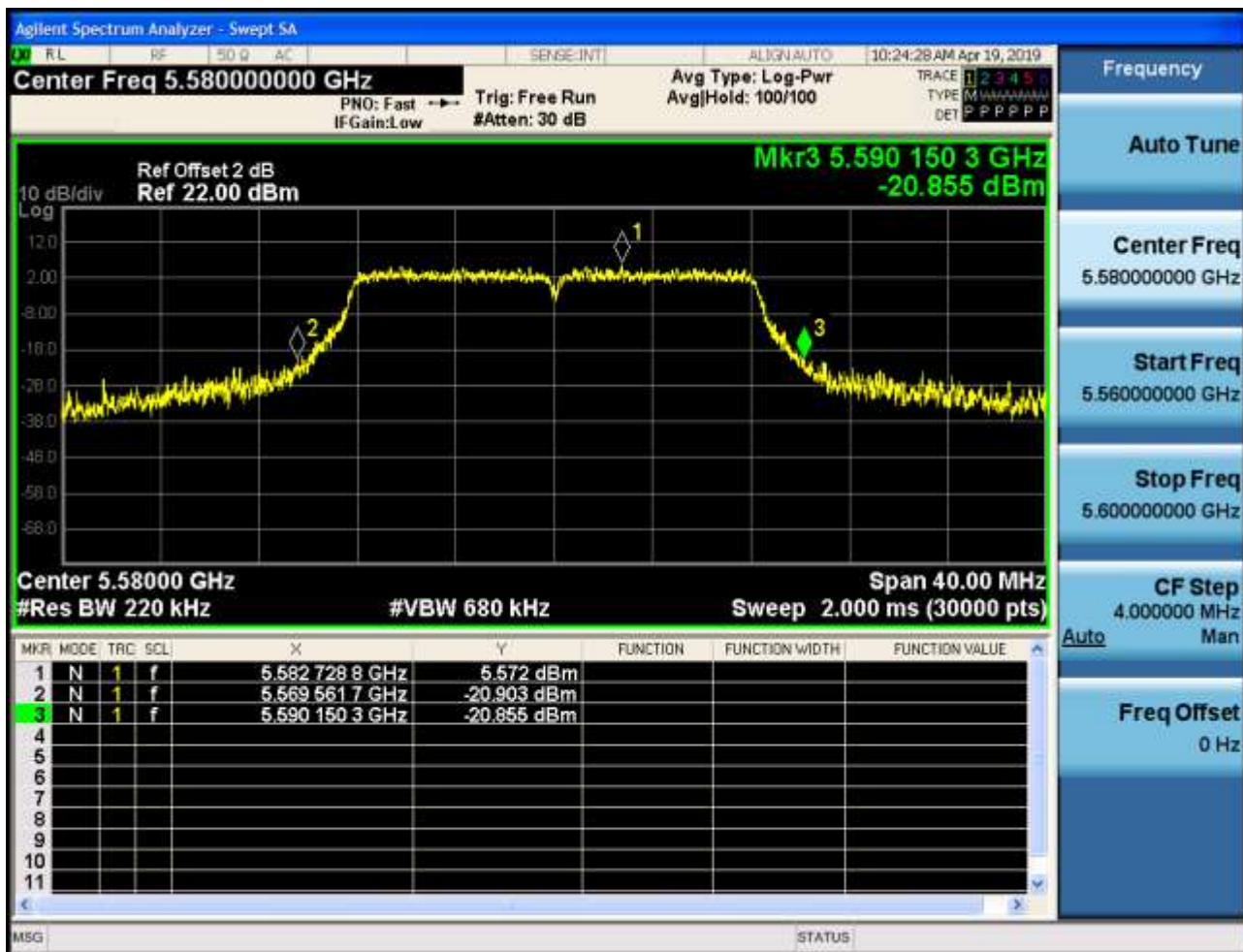


1.168 11A20CDD_Ant2_5520



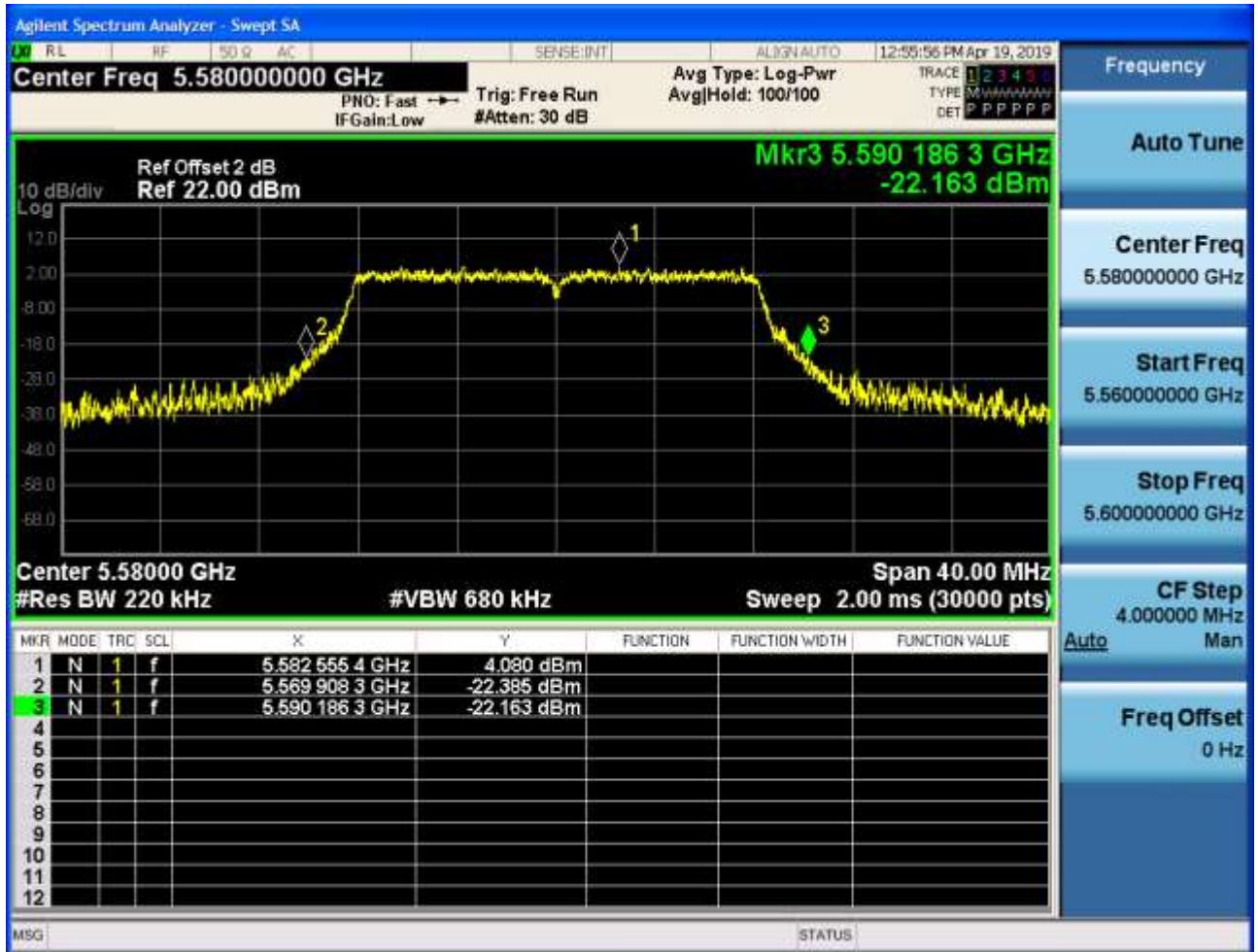


1.169 11A20CDD_Ant1_5580





1.170 11A20CDD_Ant2_5580



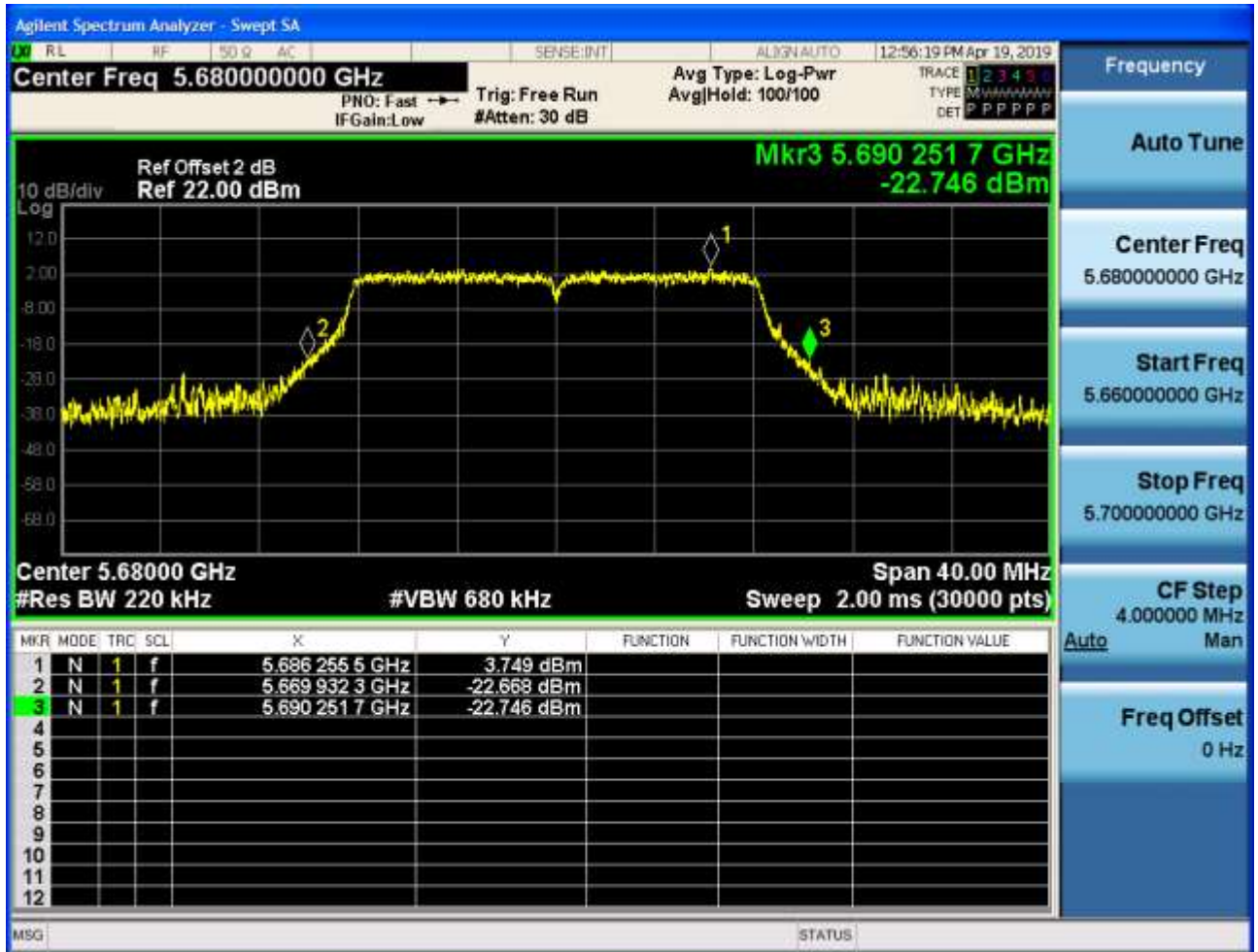


1.171 11A20CDD_Ant1_5680





1.172 11A20CDD_Ant2_5680





1.173 11A20CDD_Ant1_5700





1.174 11A20CDD_Ant2_5700



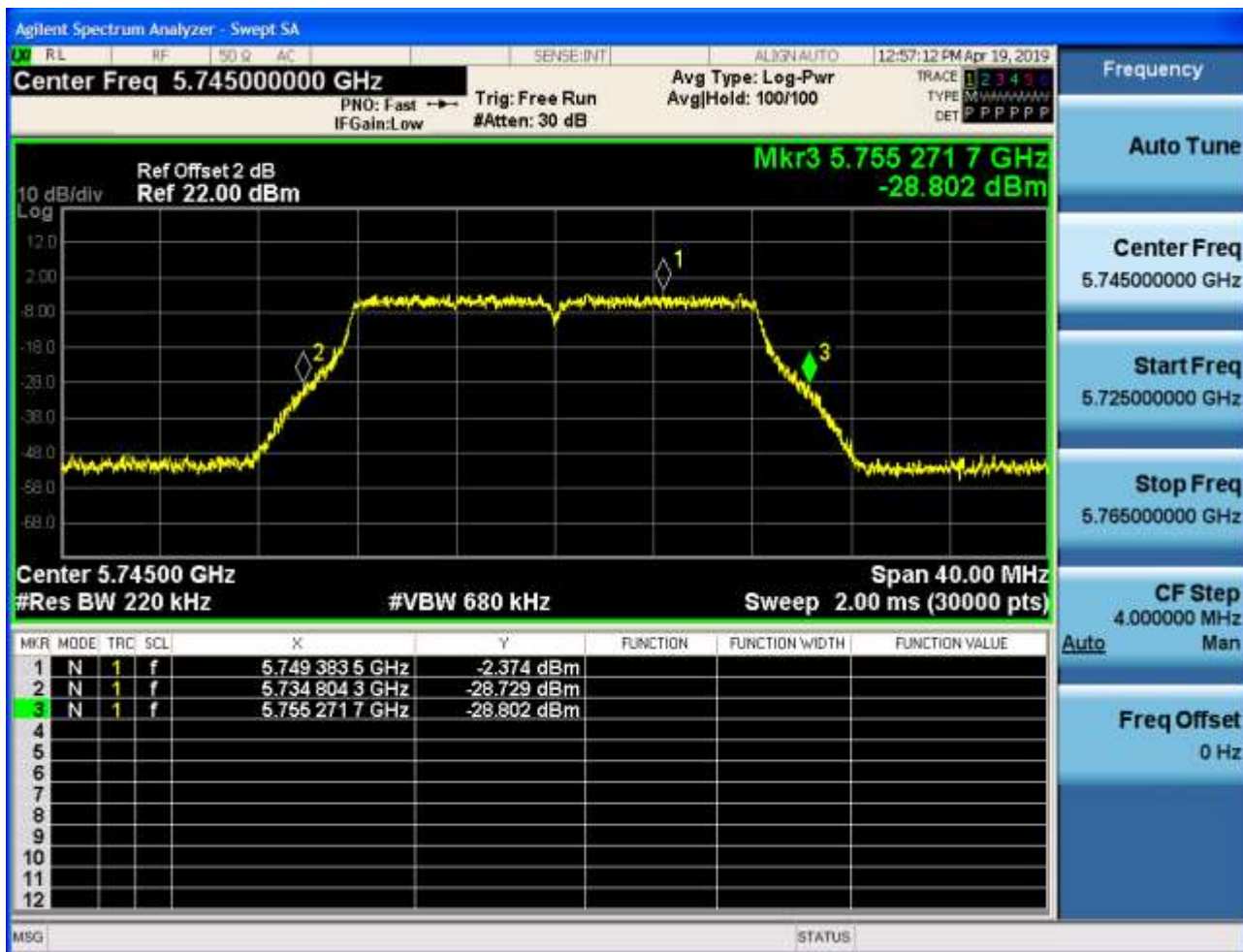


1.175 11A20CDD_Ant1_5745



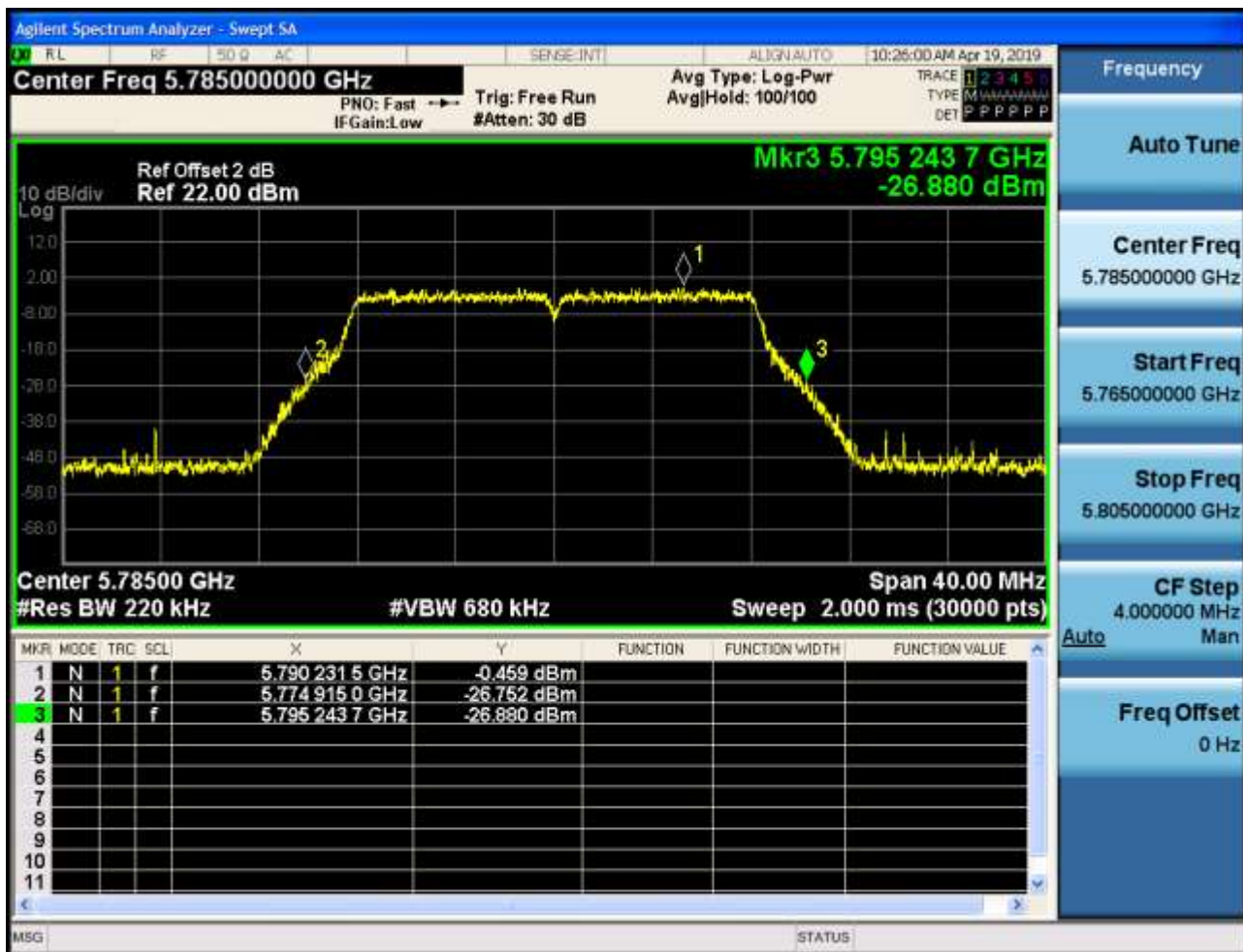


1.176 11A20CDD_Ant2_5745





1.177 11A20CDD_Ant1_5785



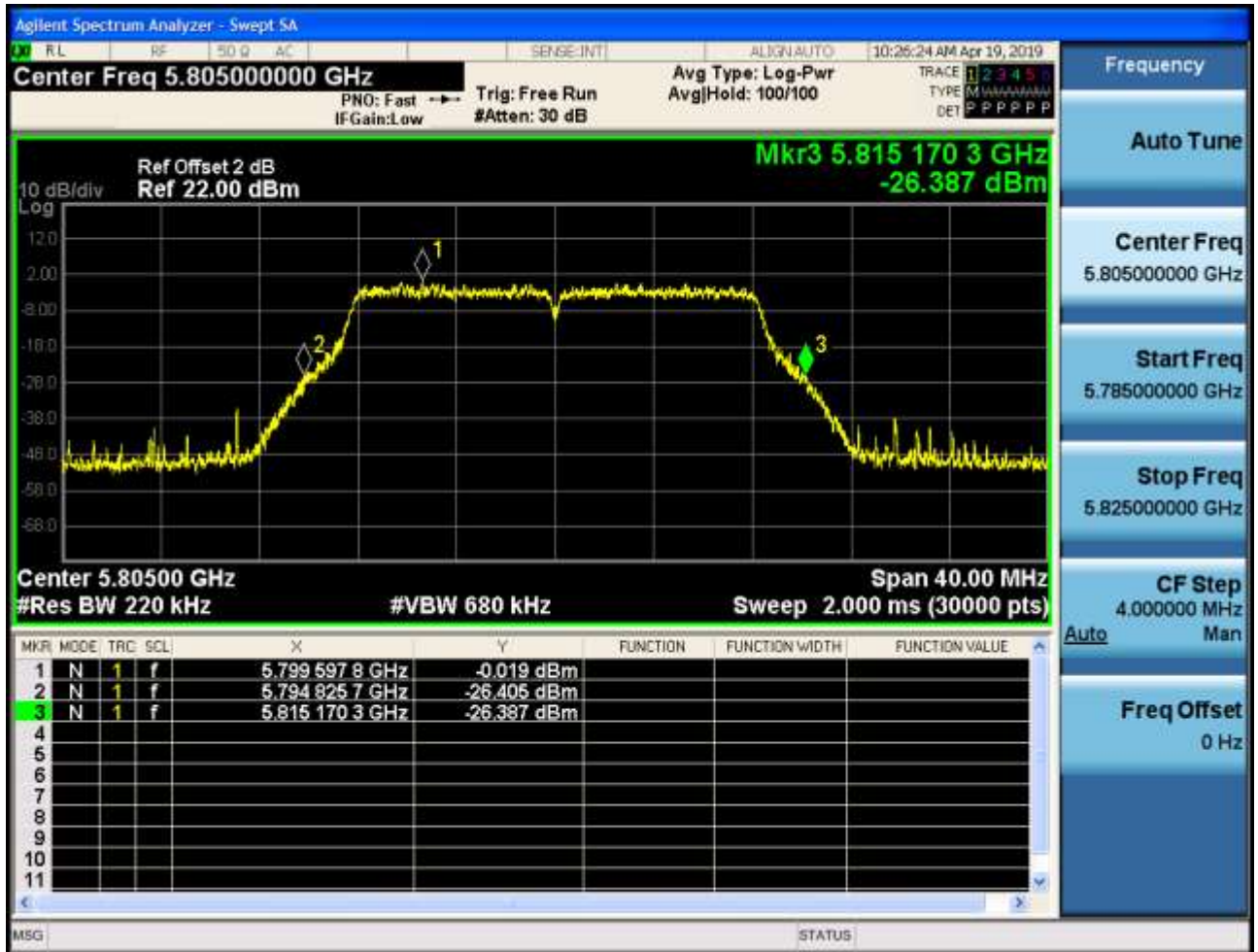


1.178 11A20CDD_Ant2_5785





1.179 11A20CDD_Ant1_5805





1.180 11A20CDD_Ant2_5805

