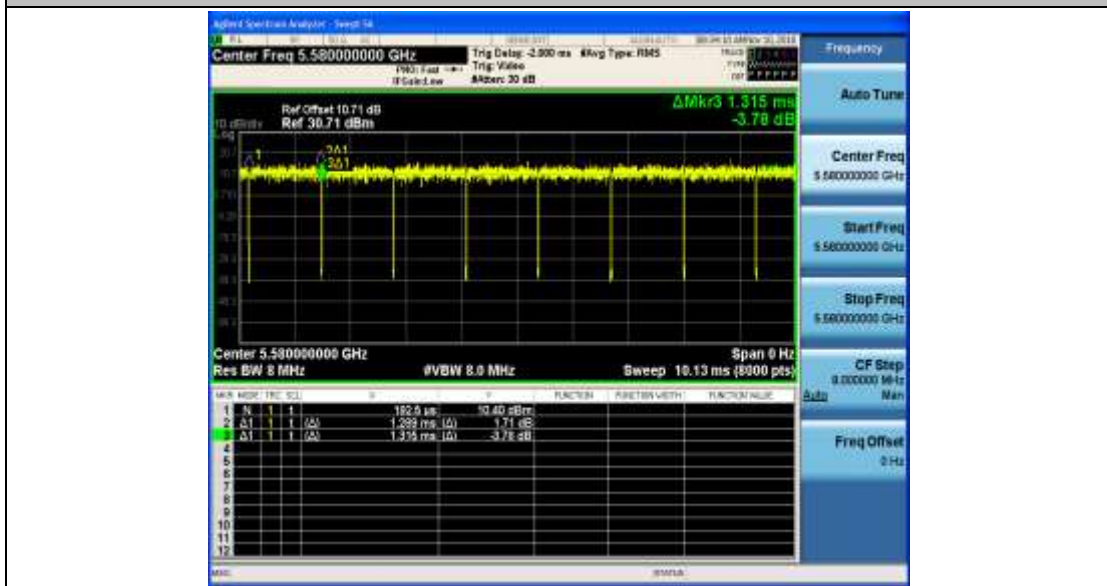
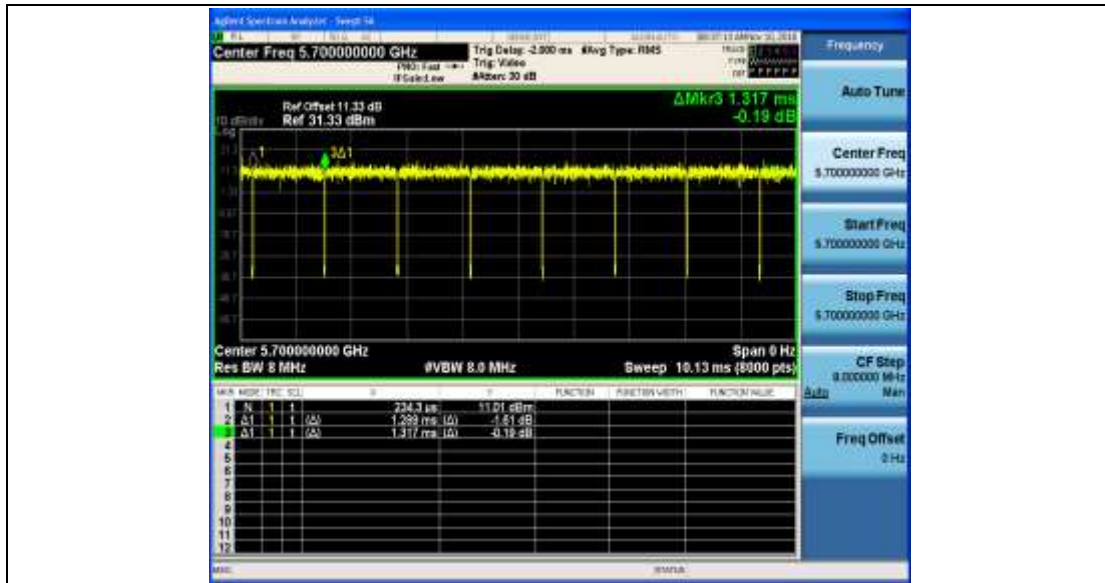


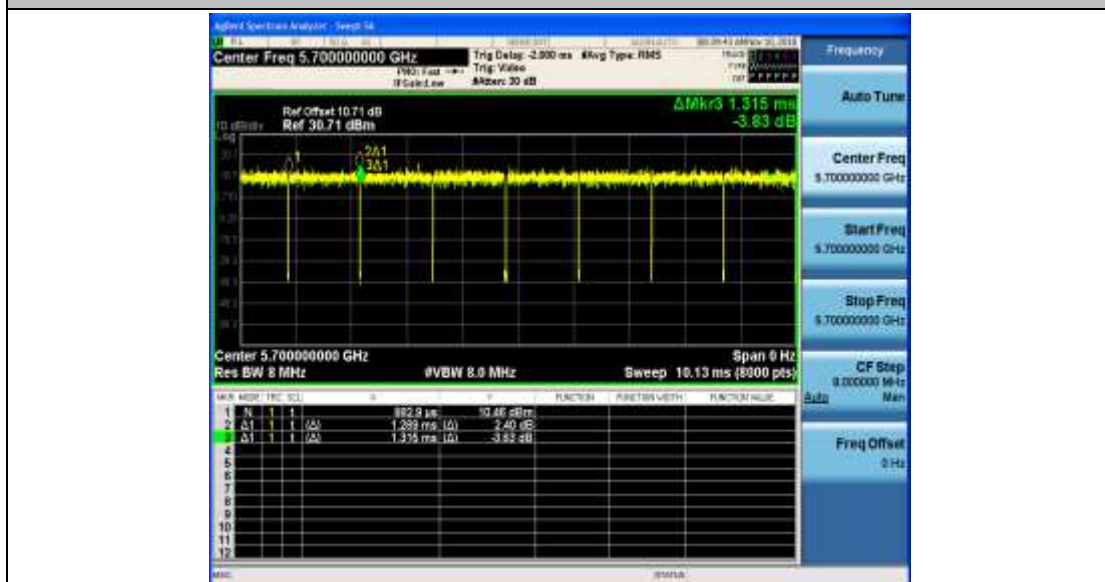
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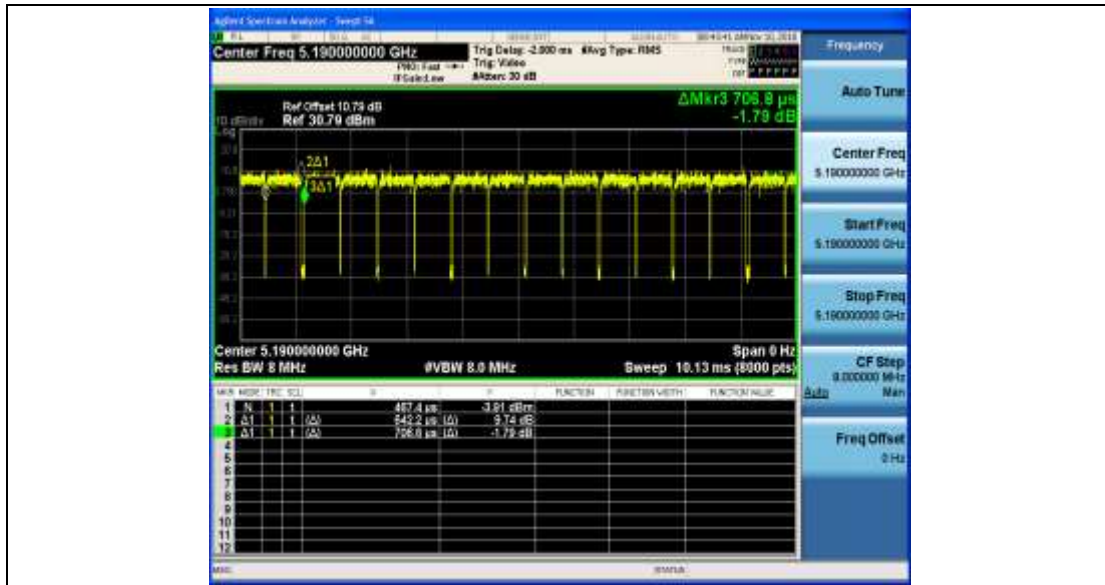
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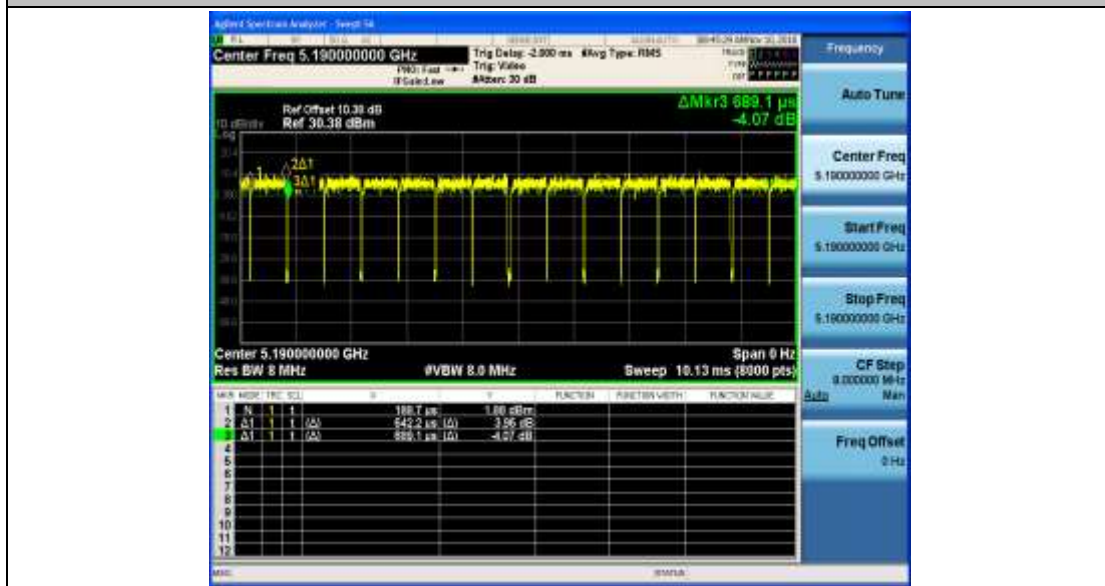
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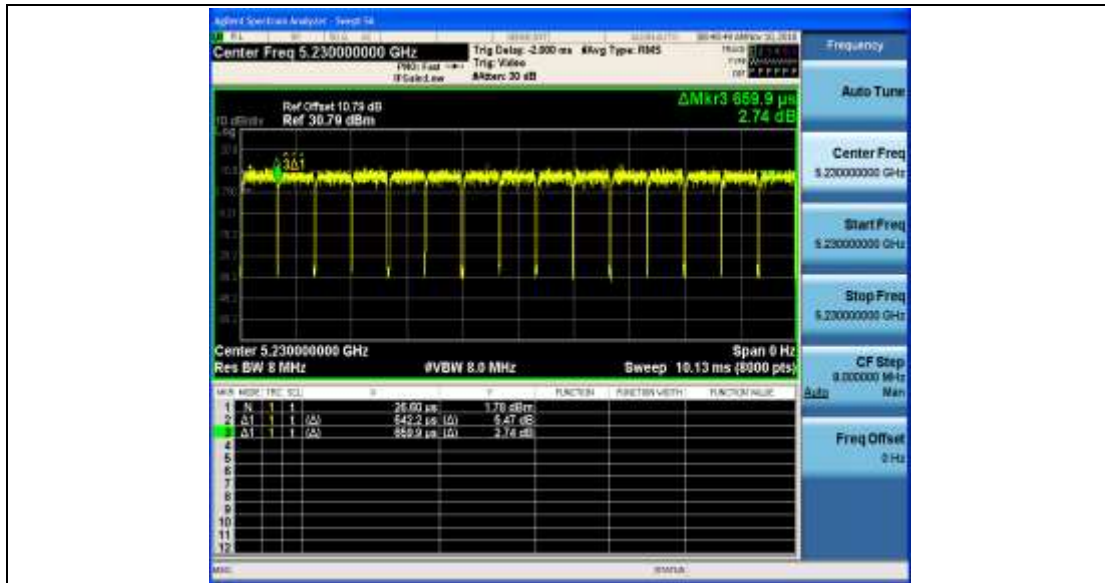
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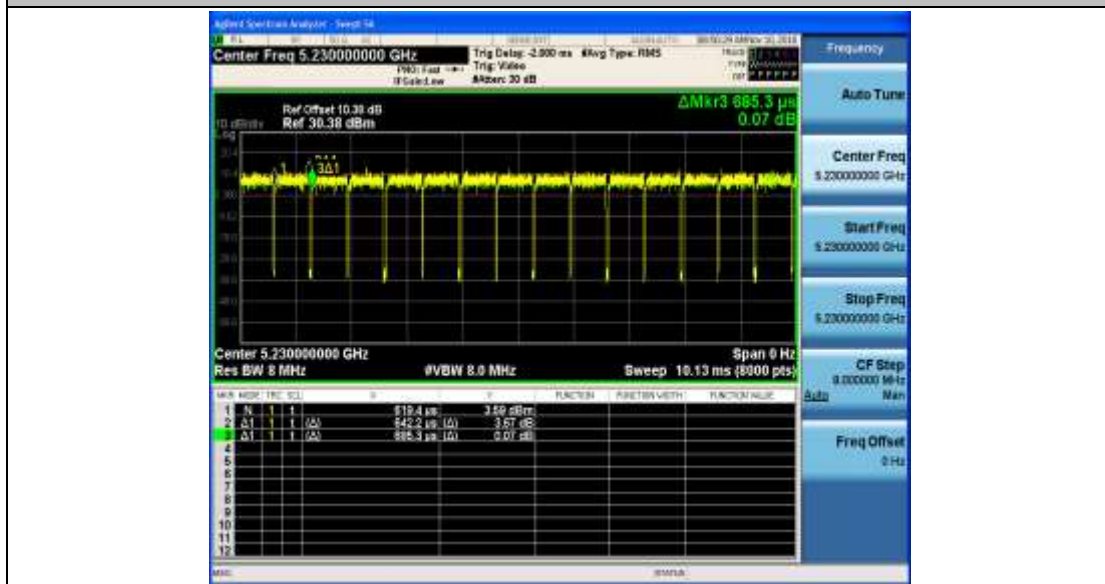
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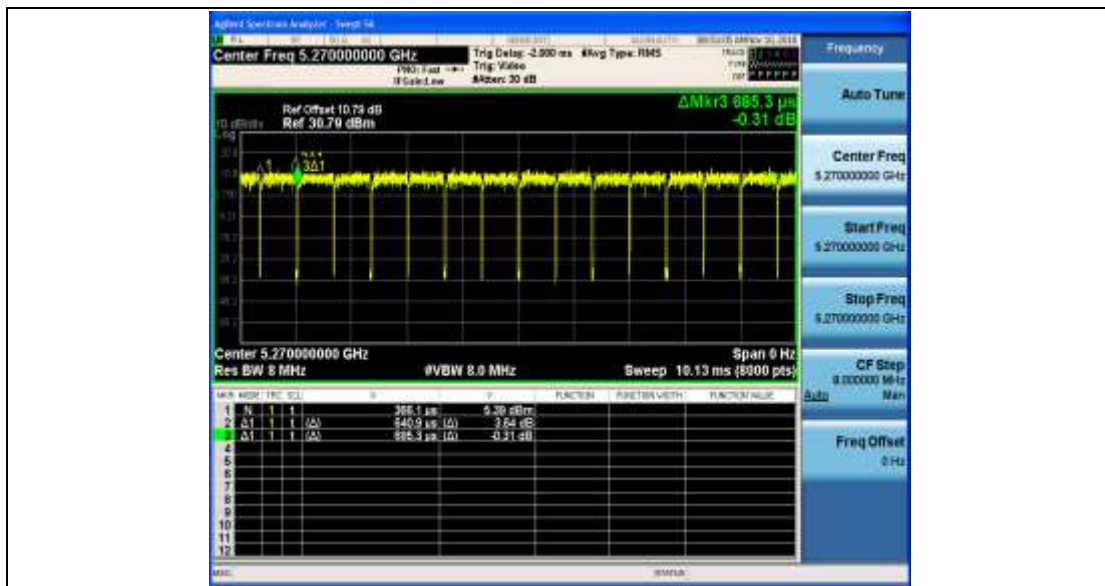
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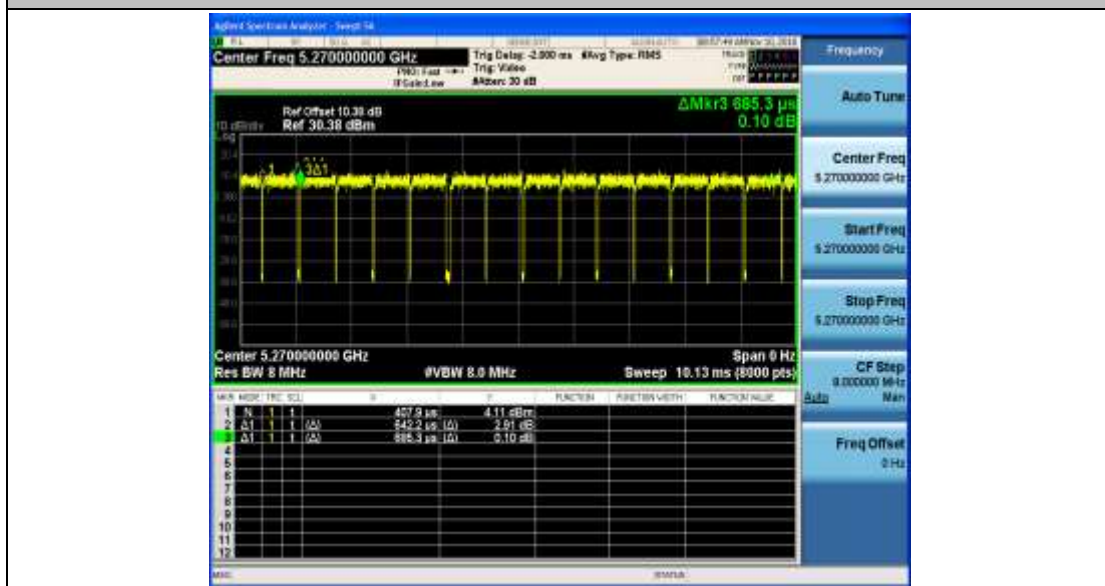
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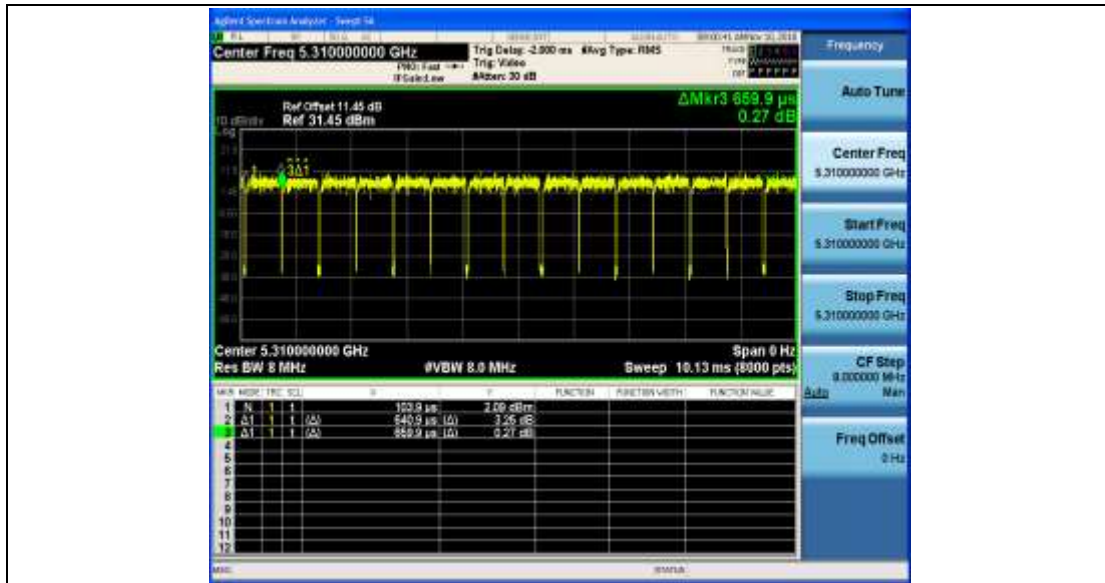
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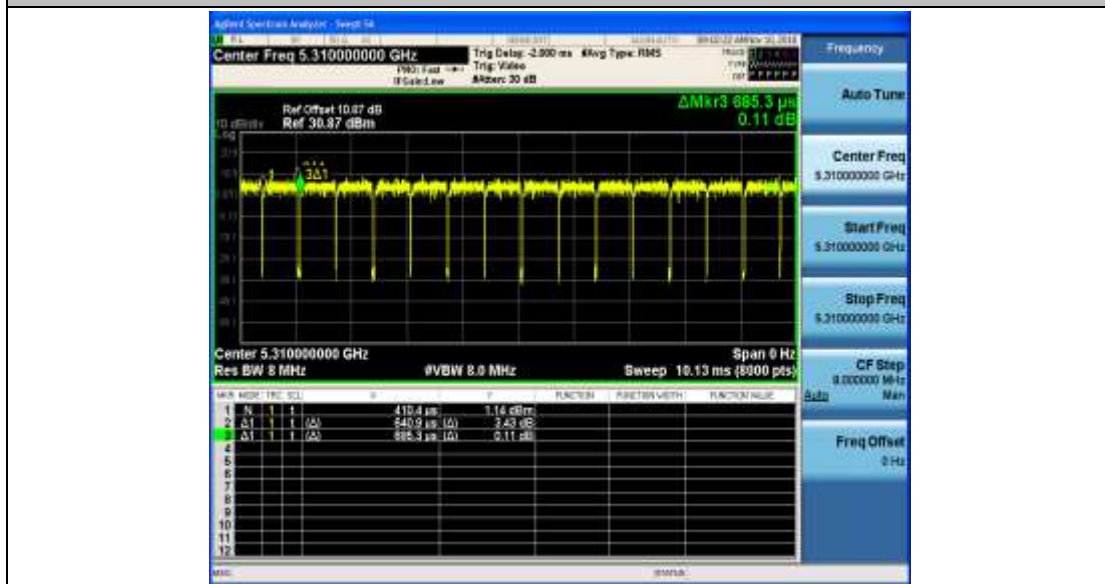
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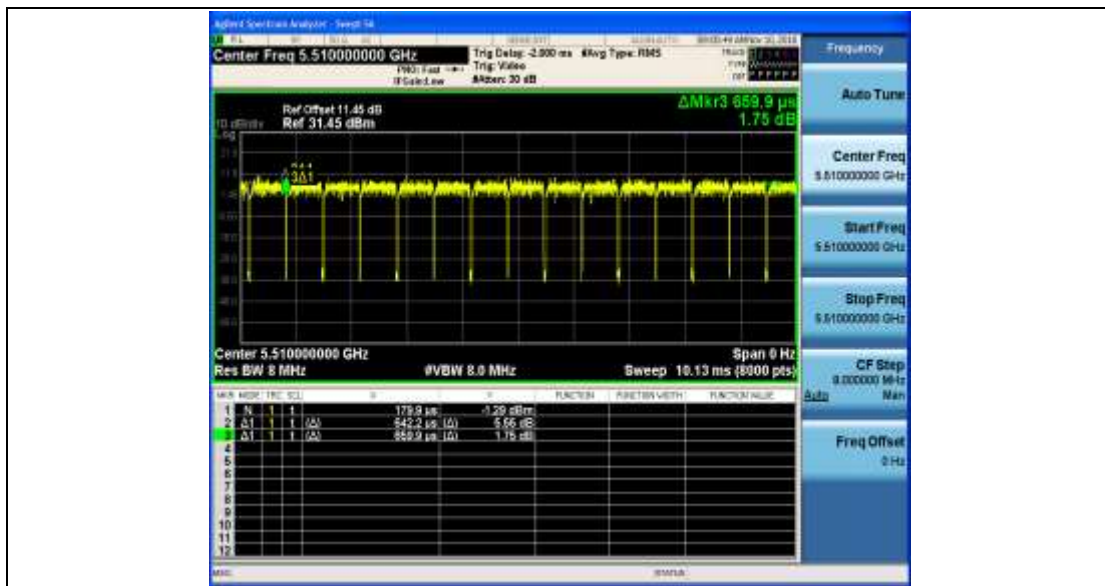
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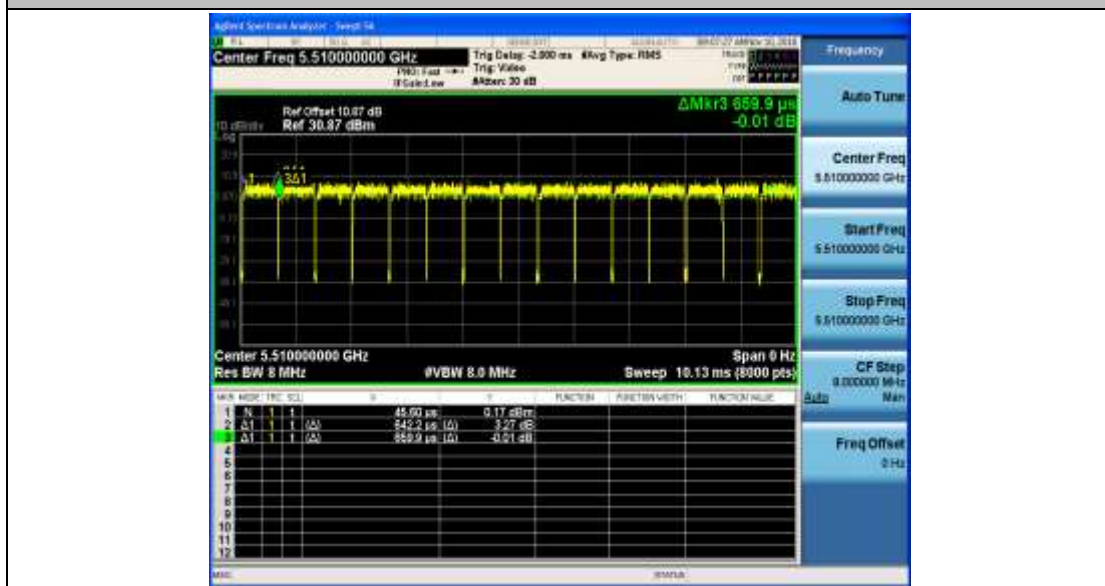
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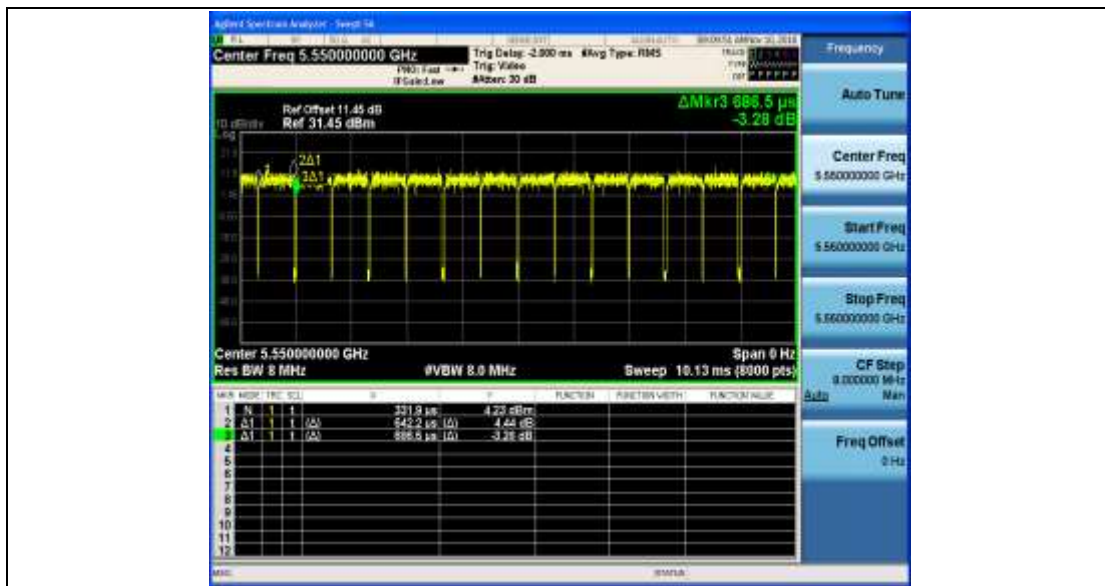
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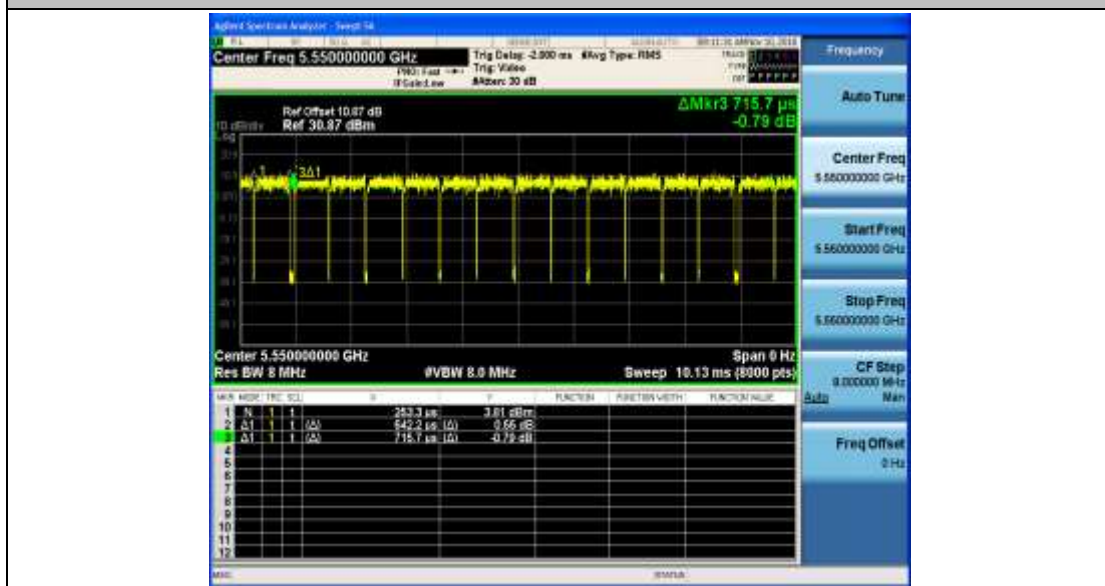
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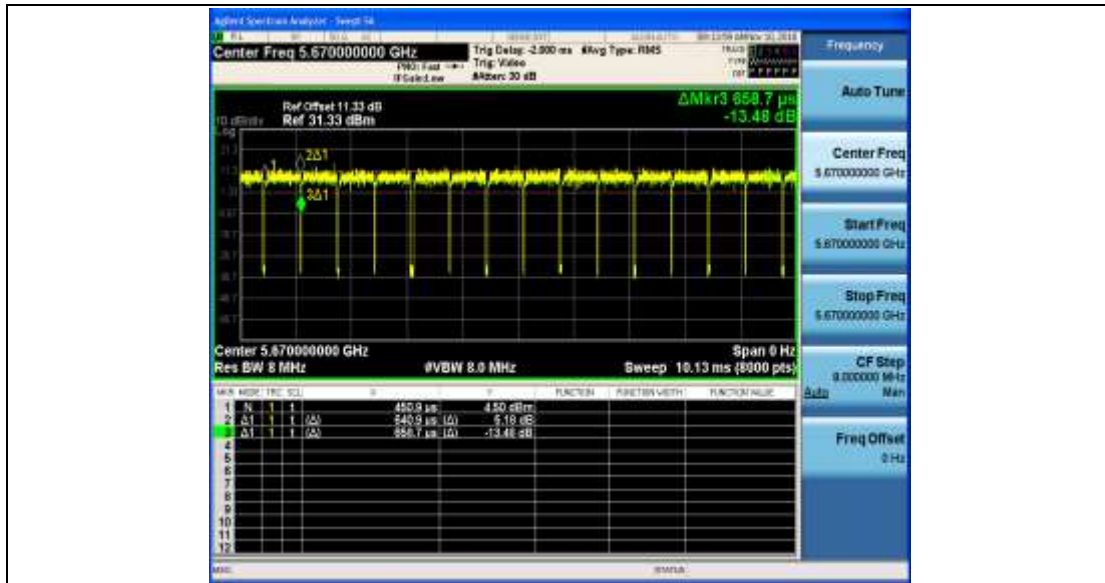
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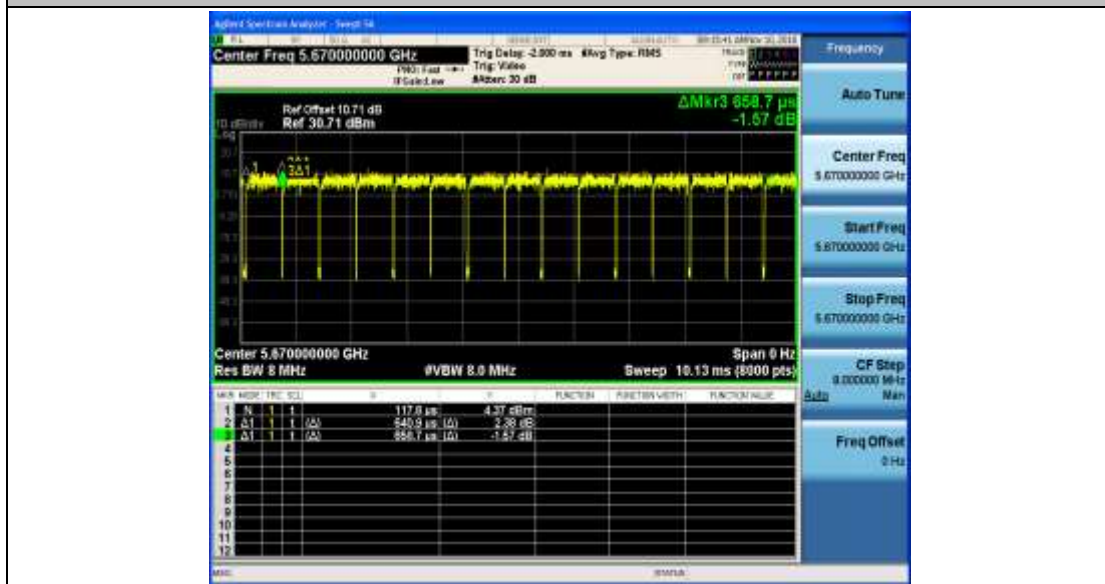
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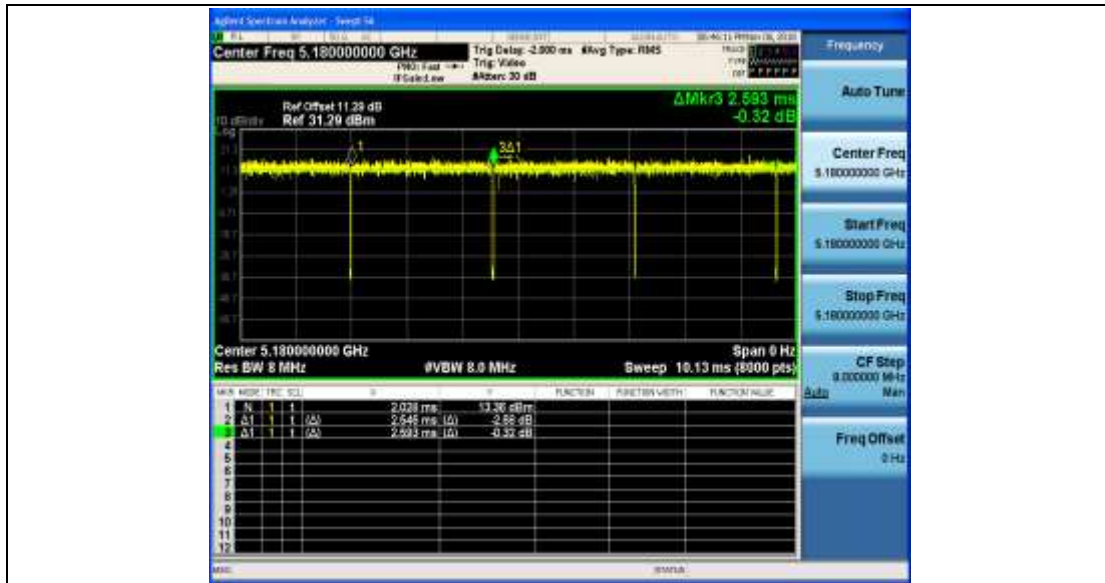
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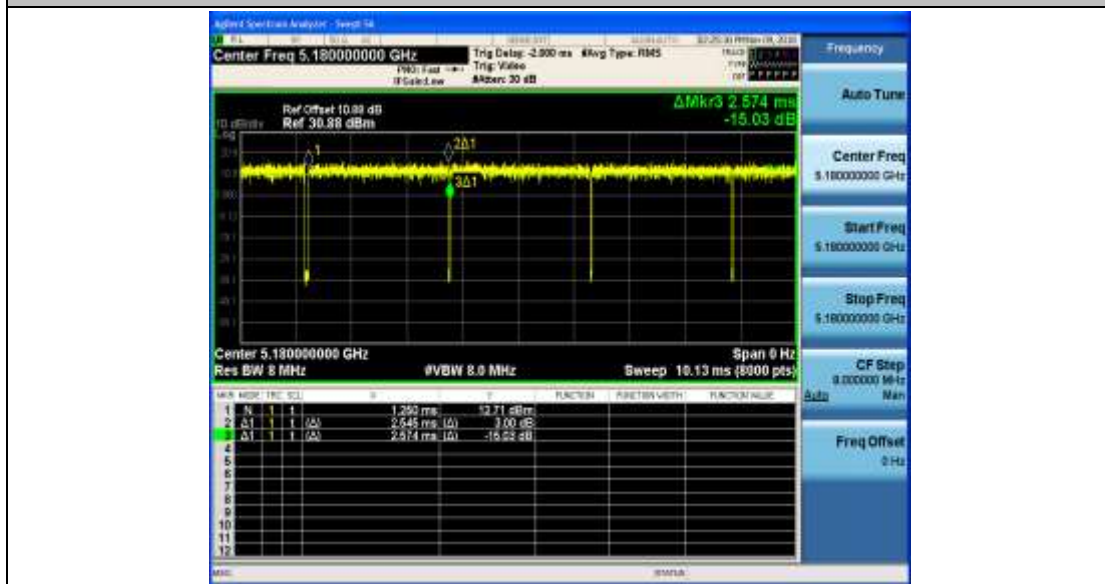
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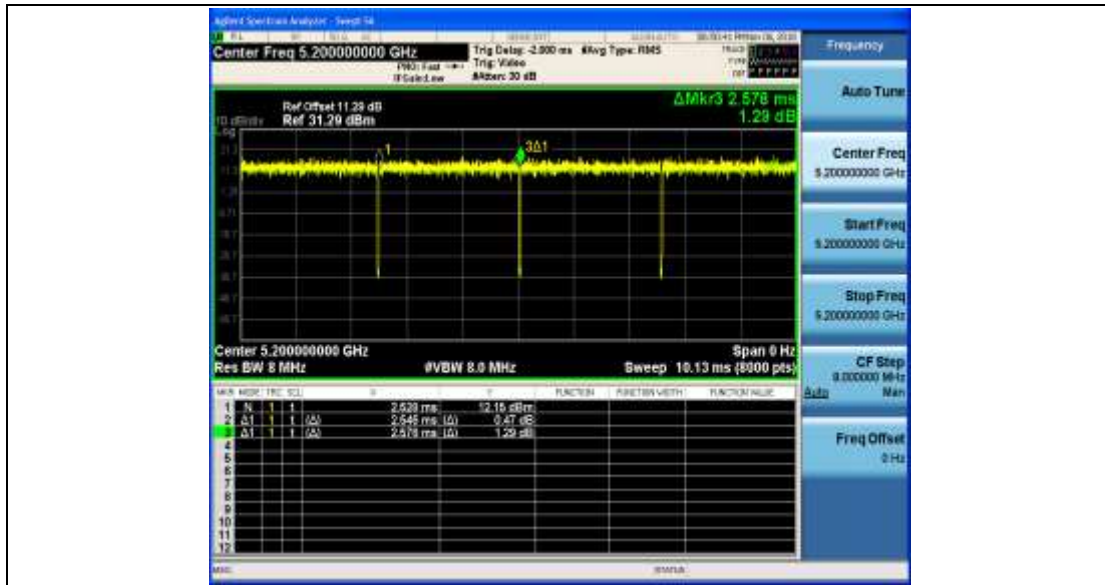
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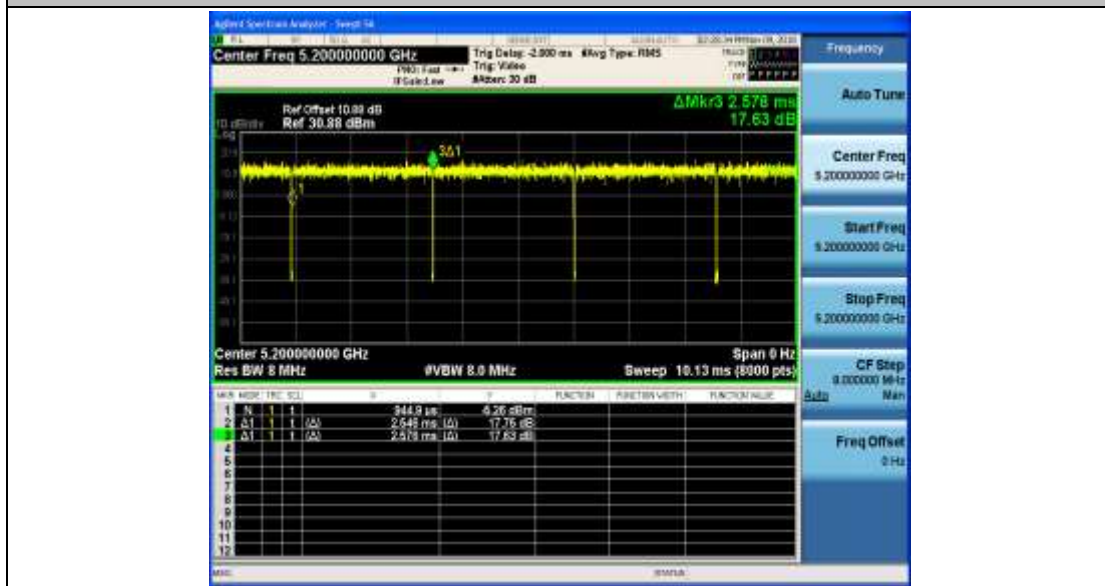
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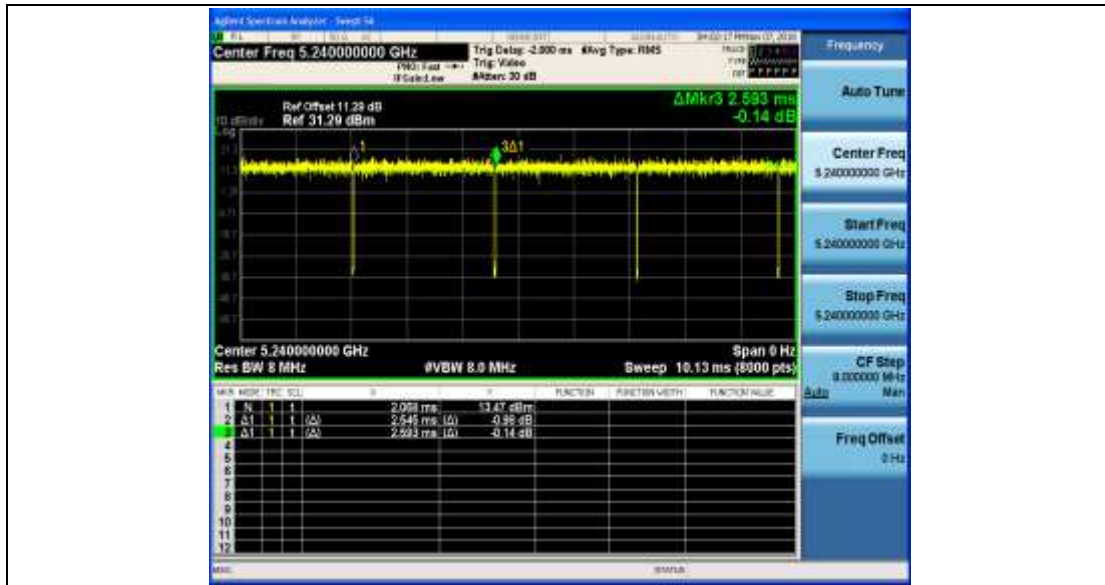
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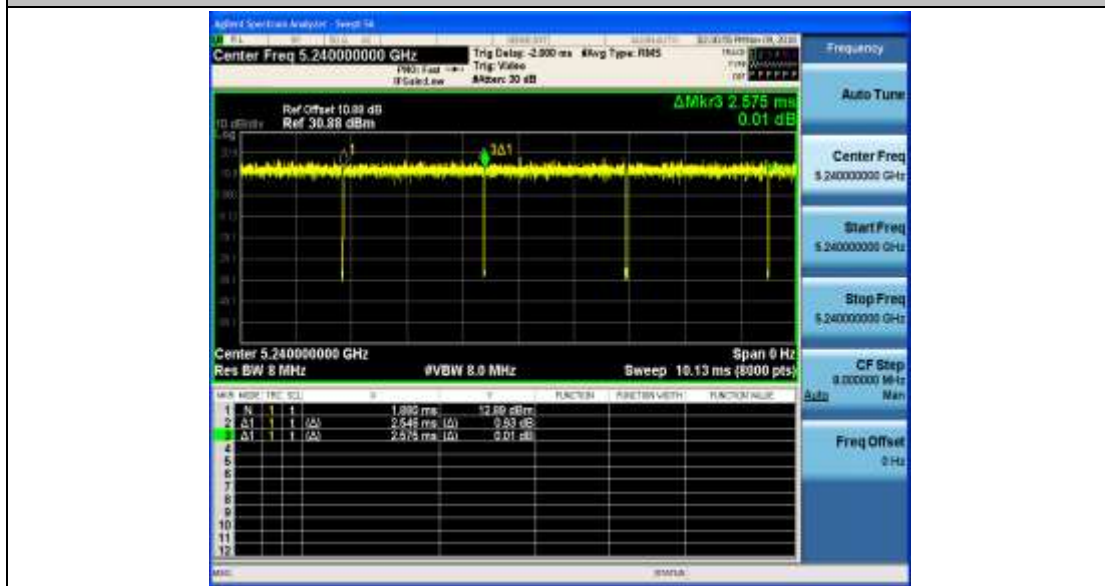
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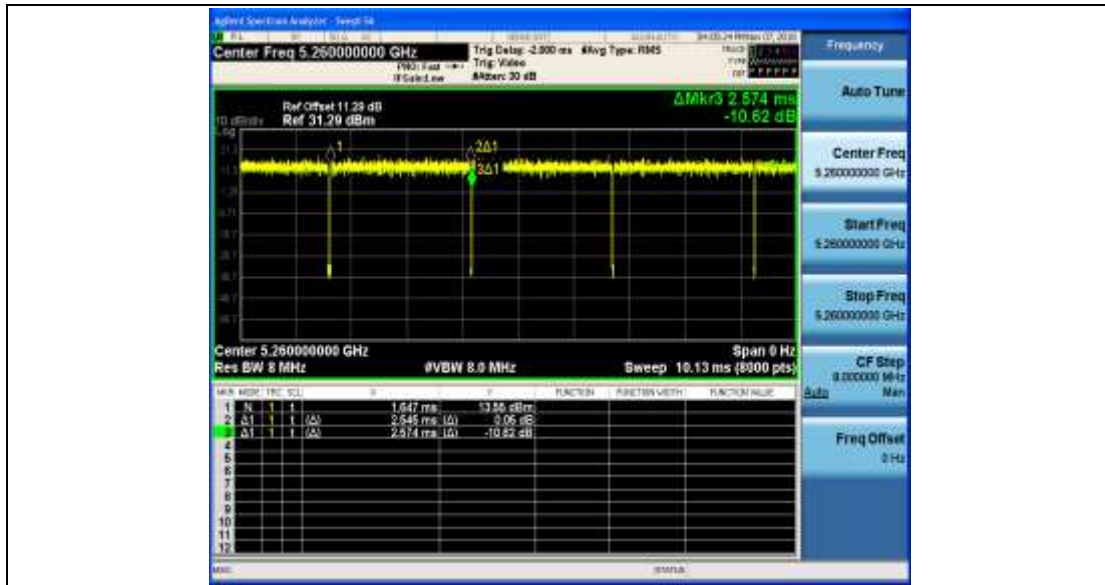
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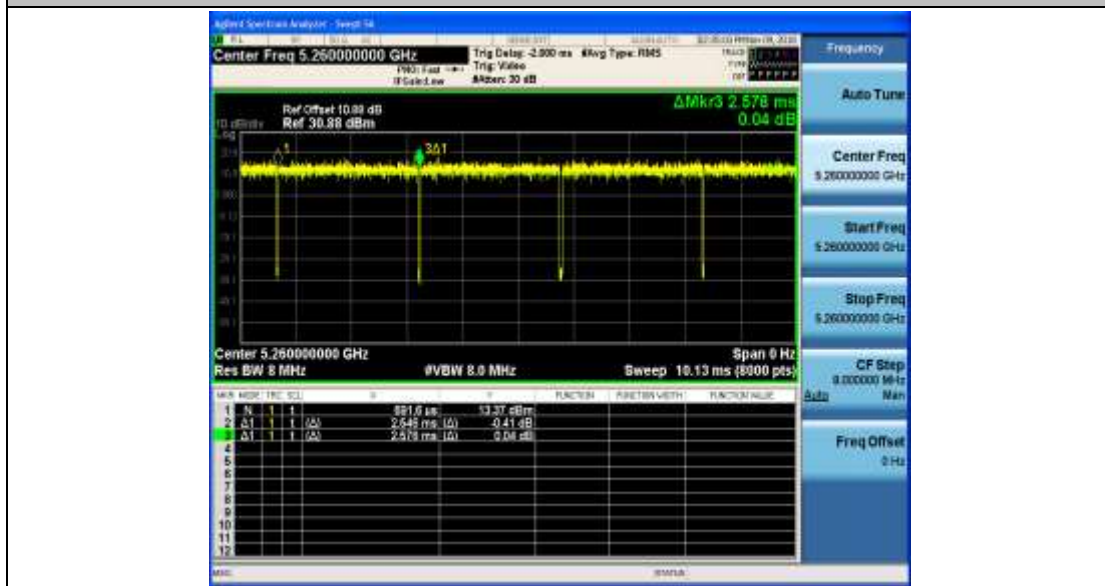
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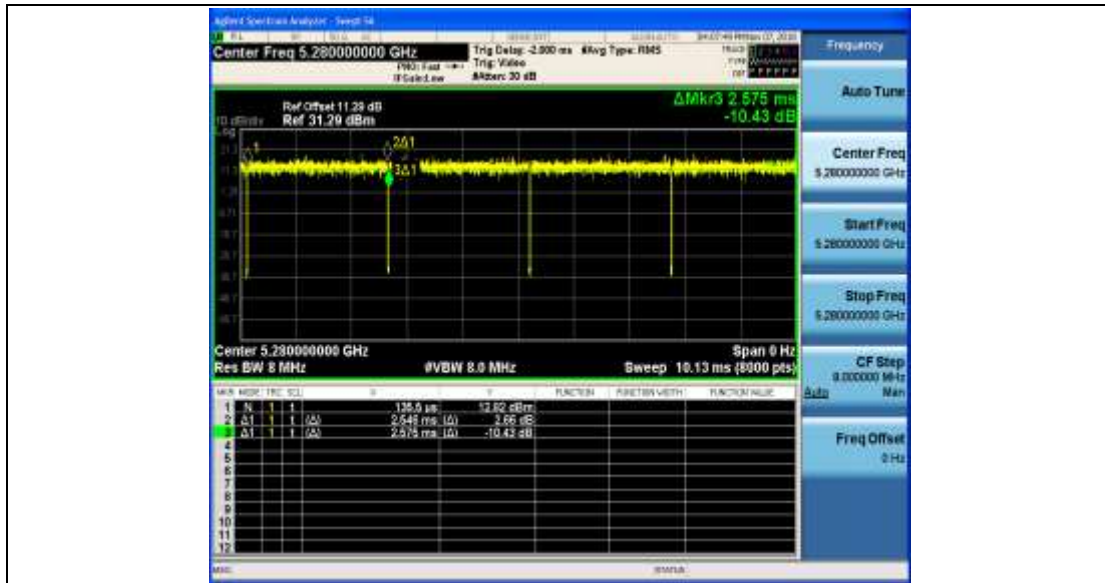
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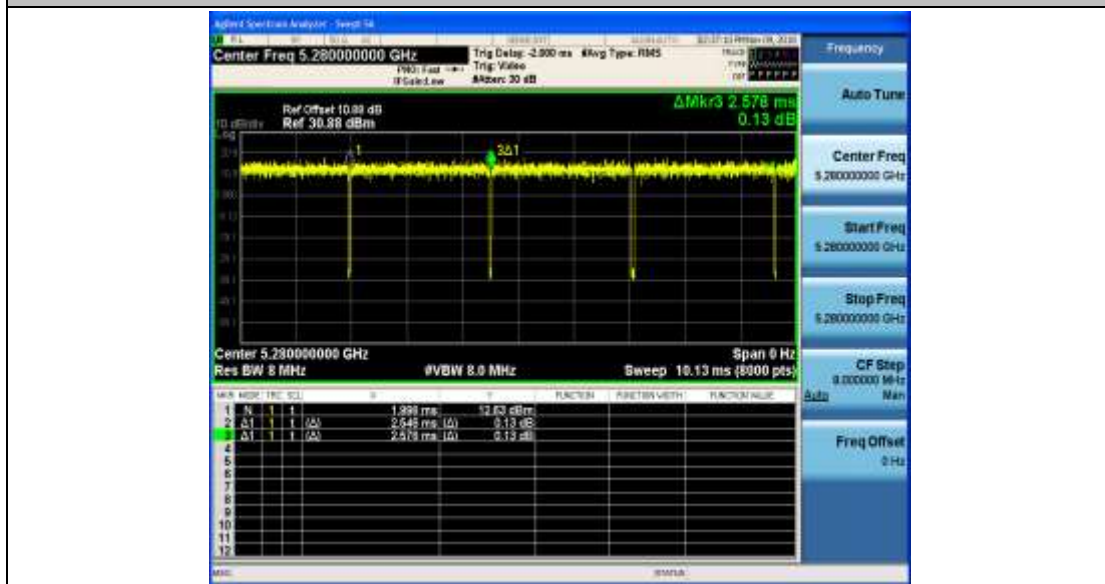
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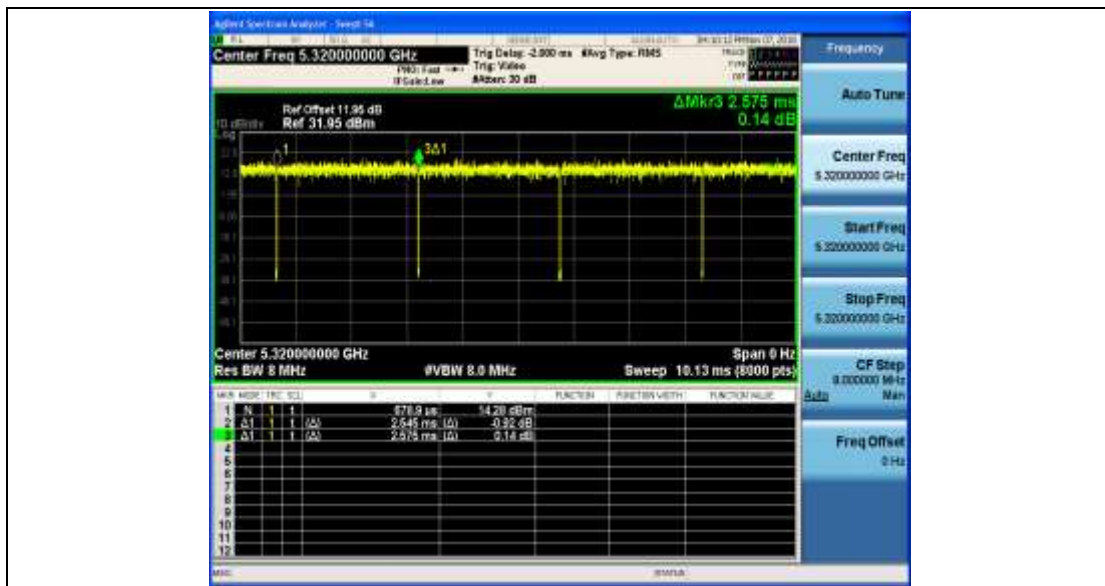
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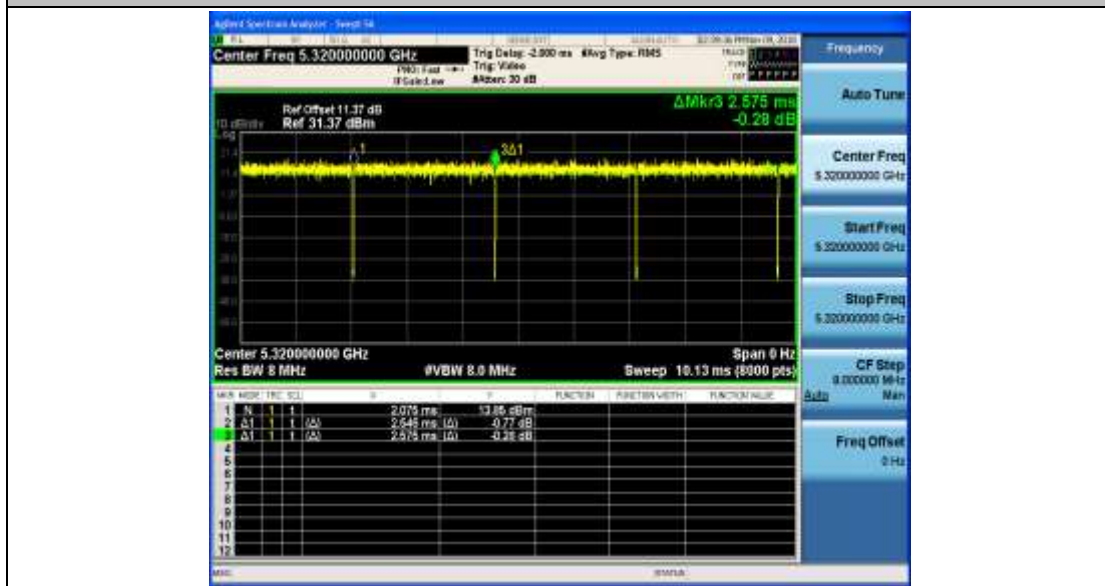
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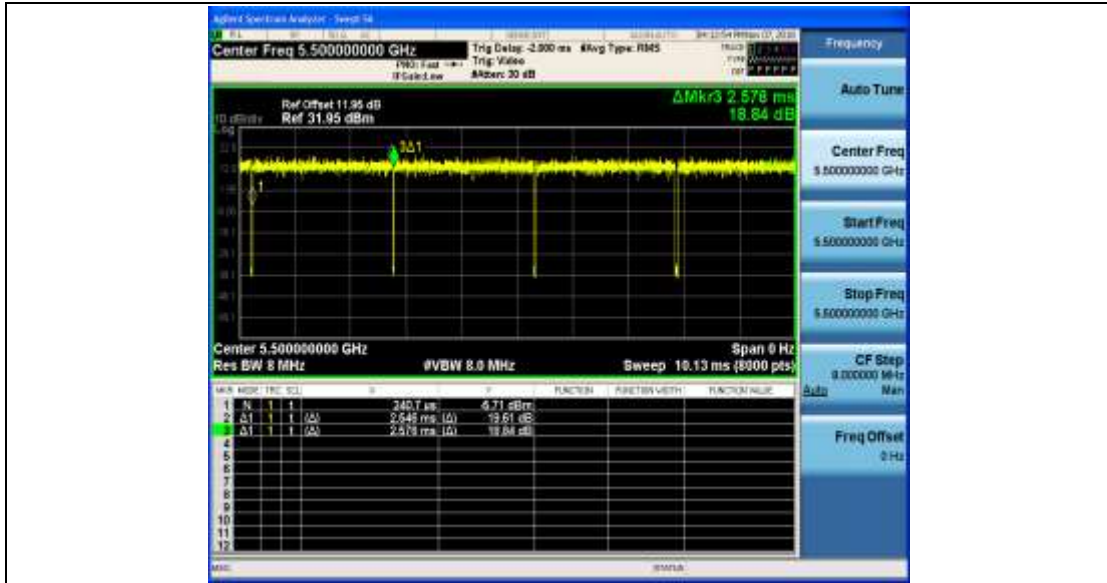
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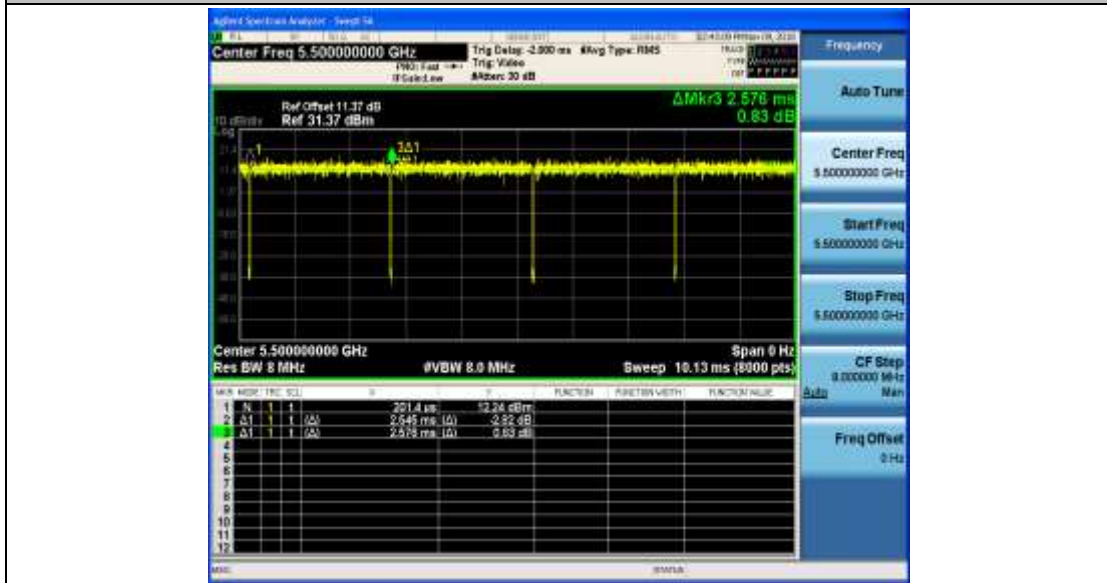
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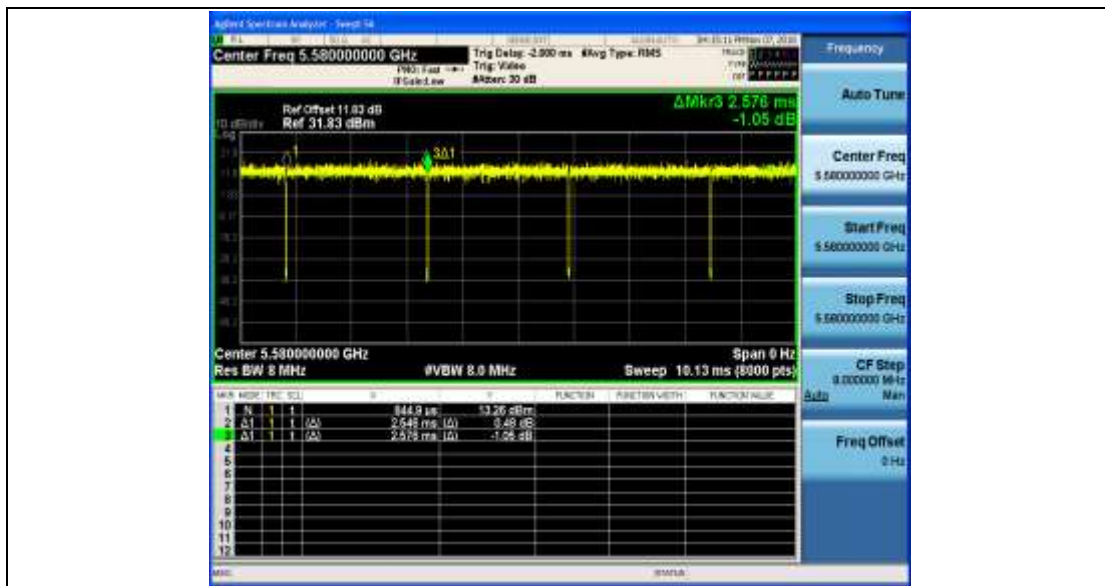
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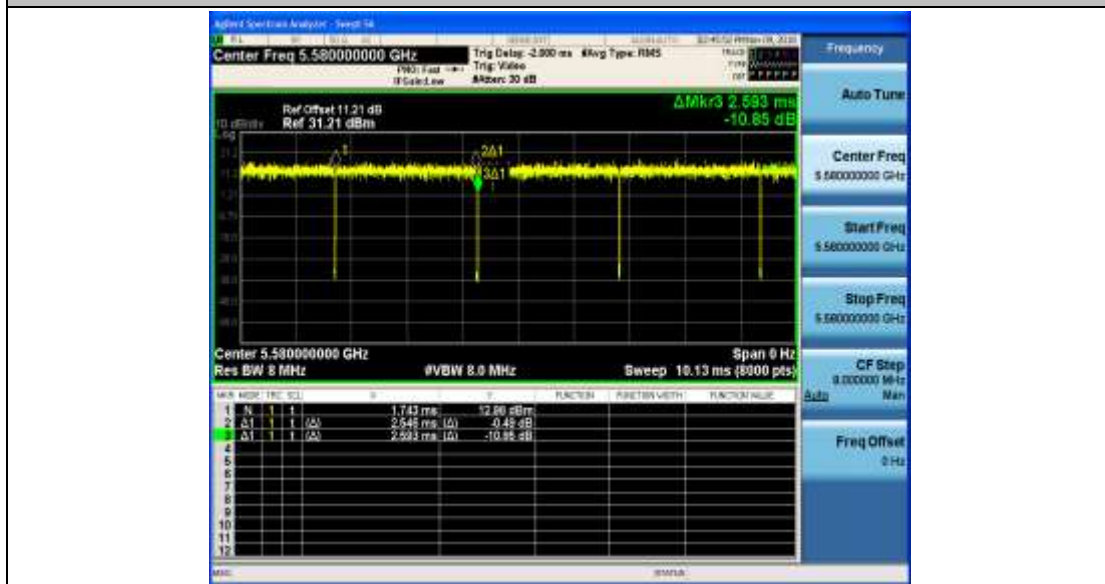
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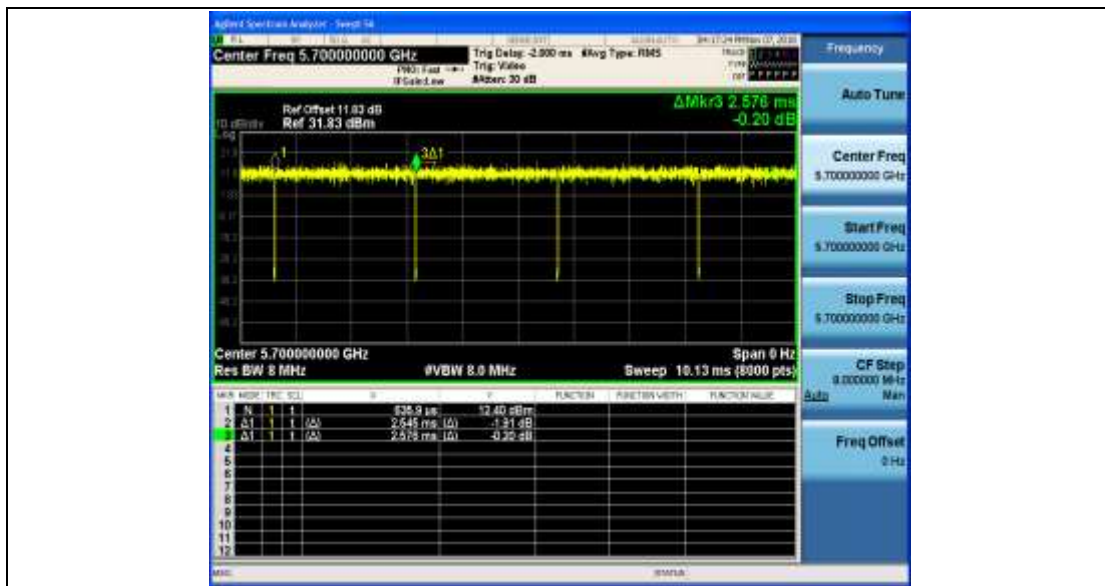
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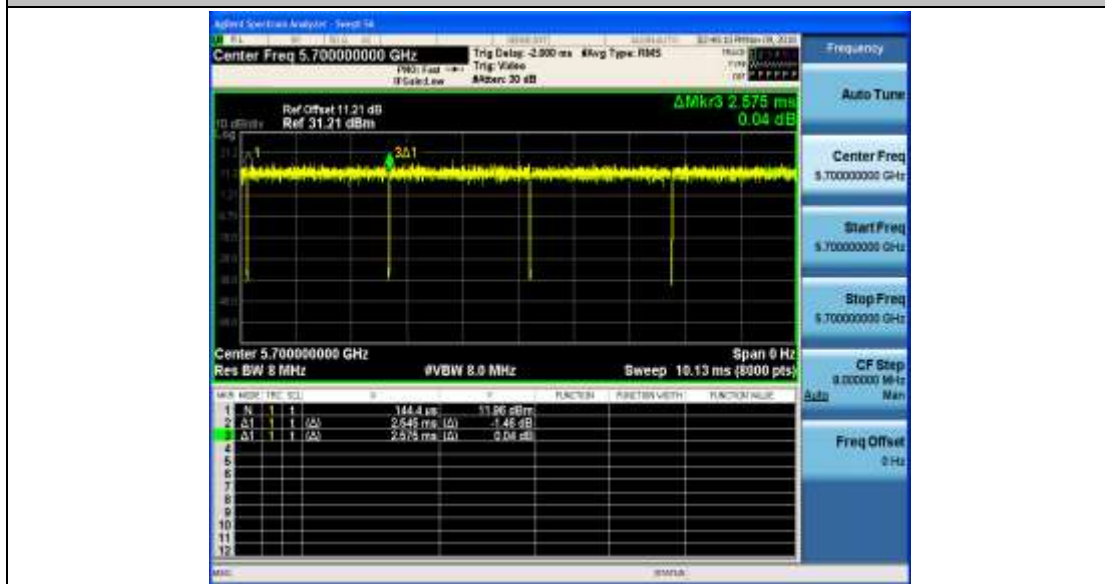
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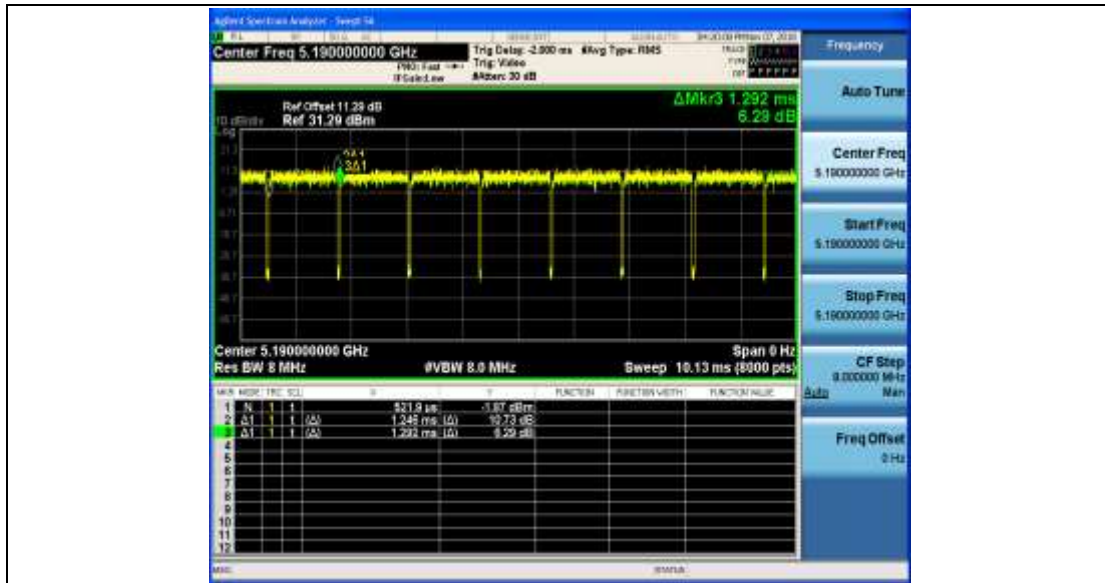
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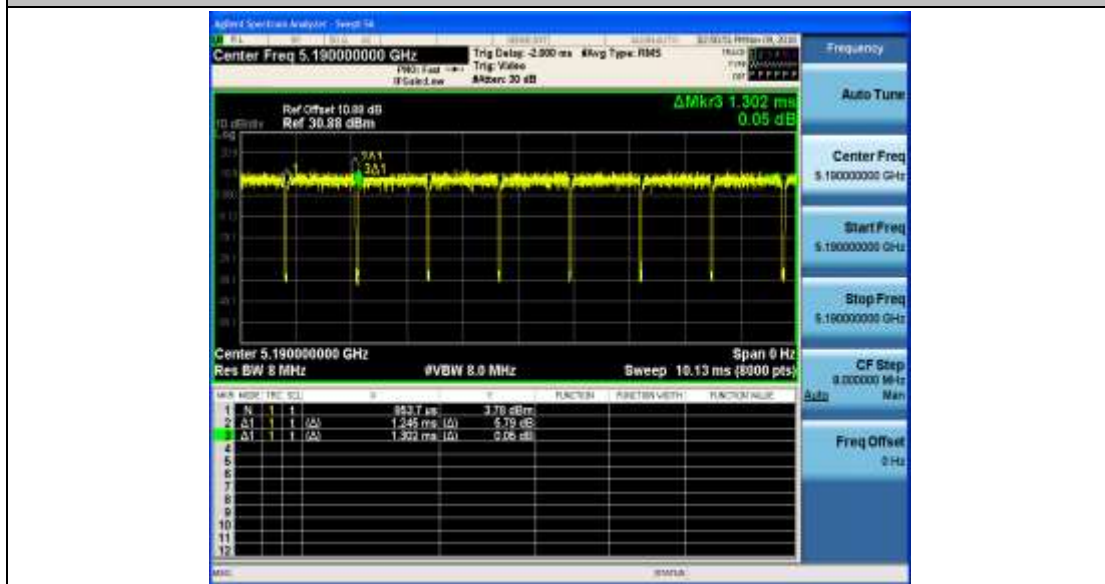
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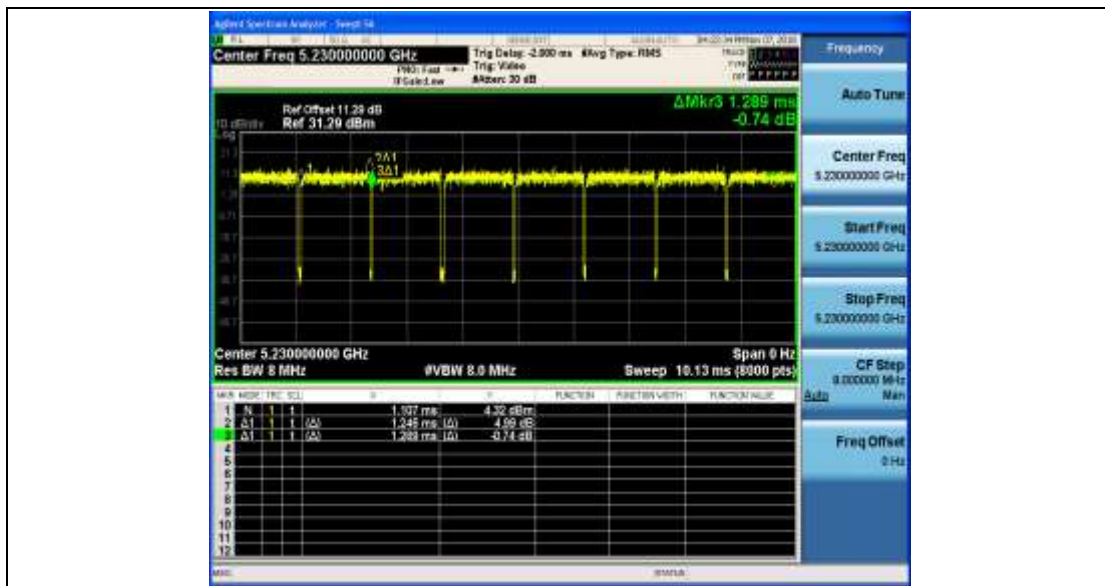
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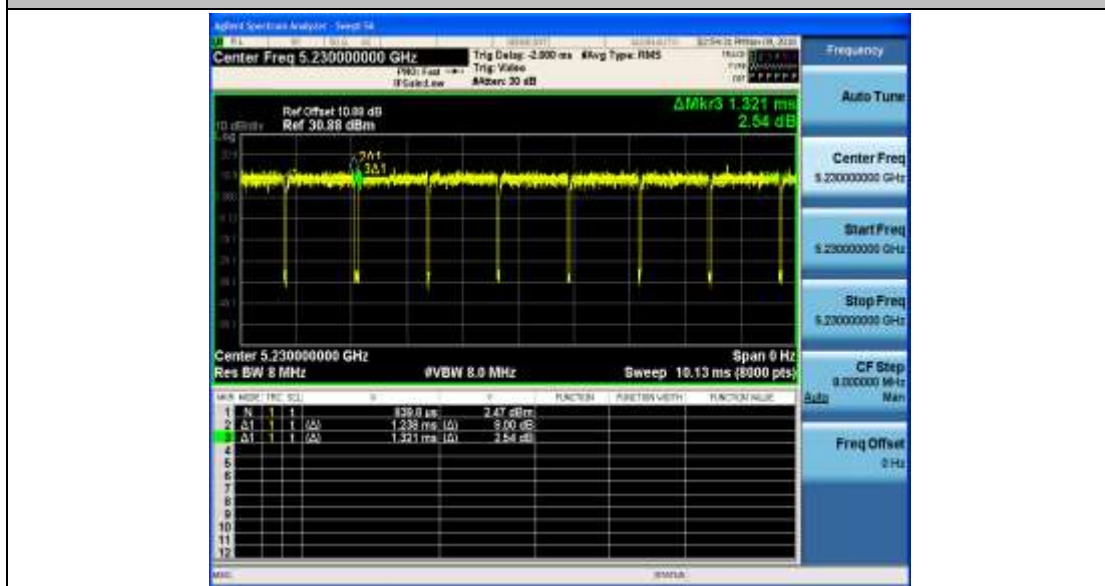
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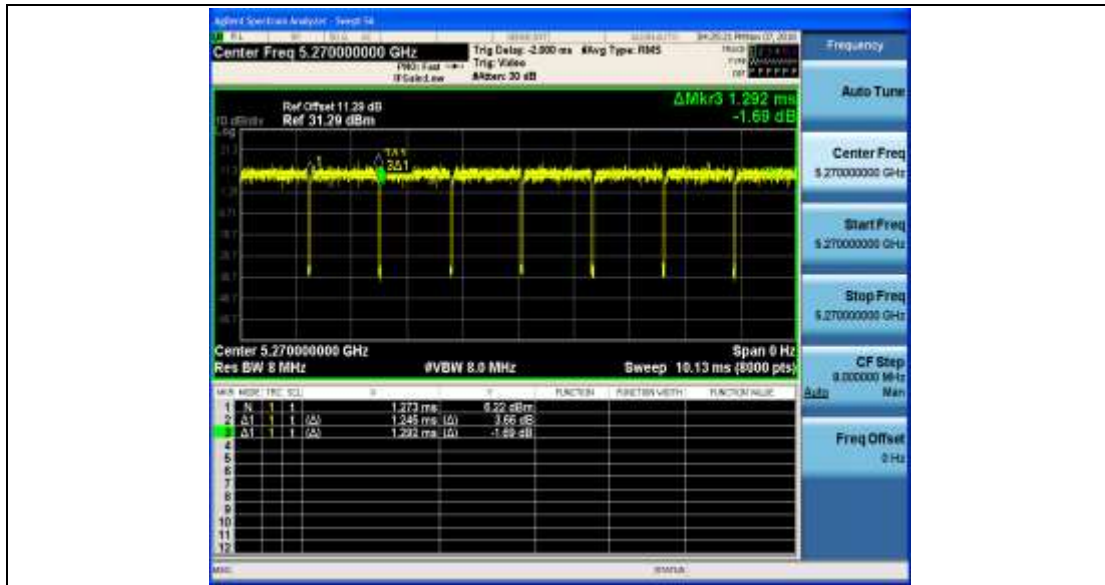
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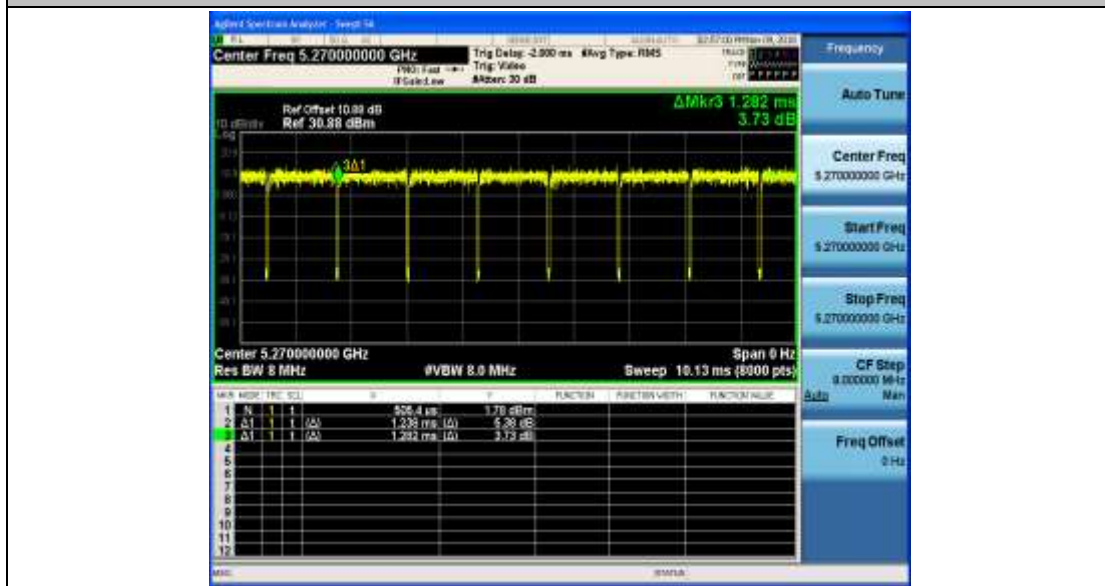
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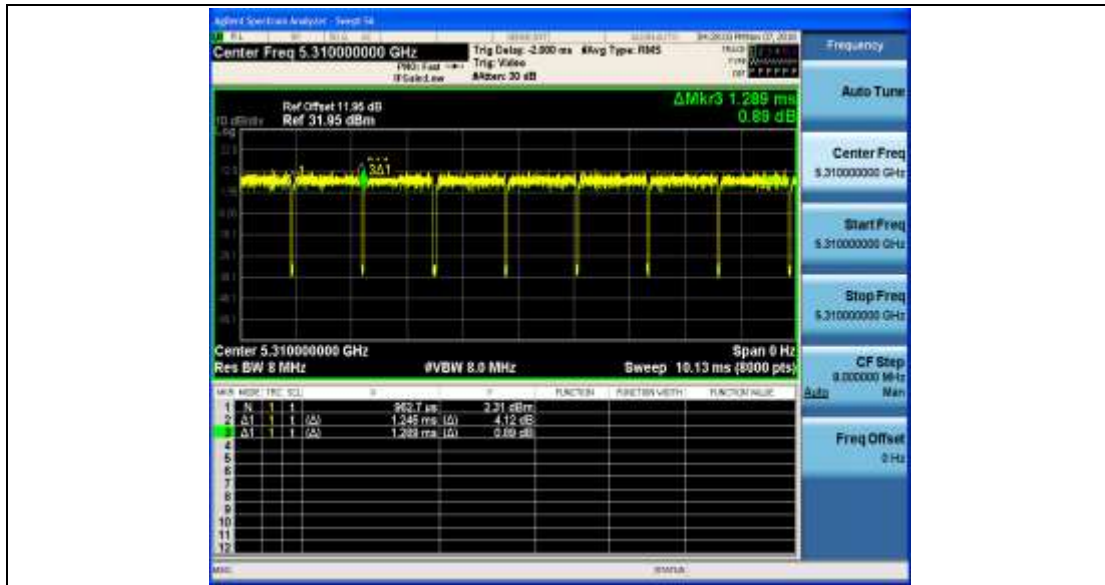
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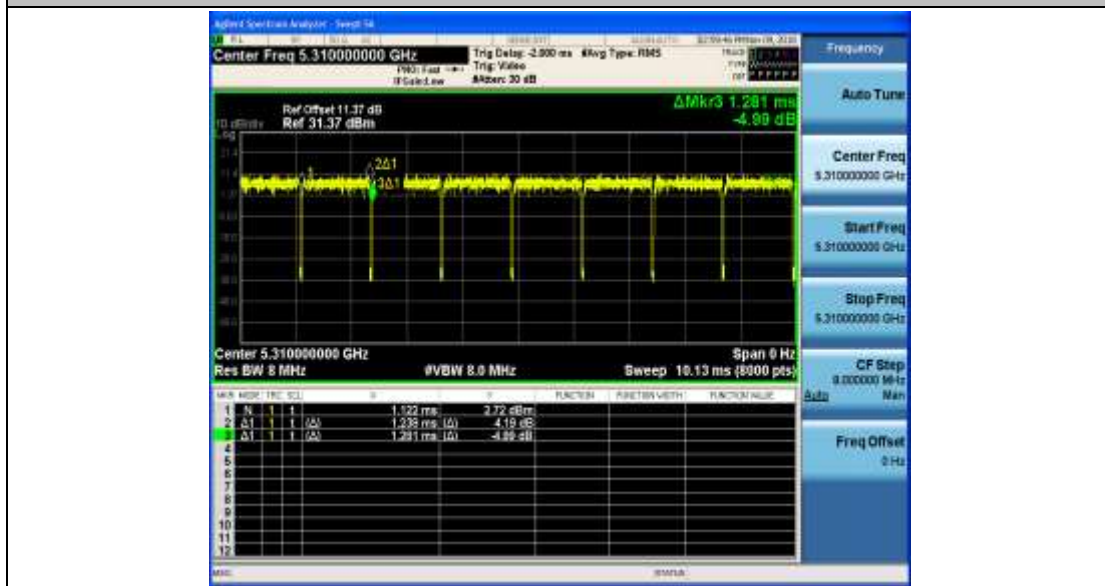
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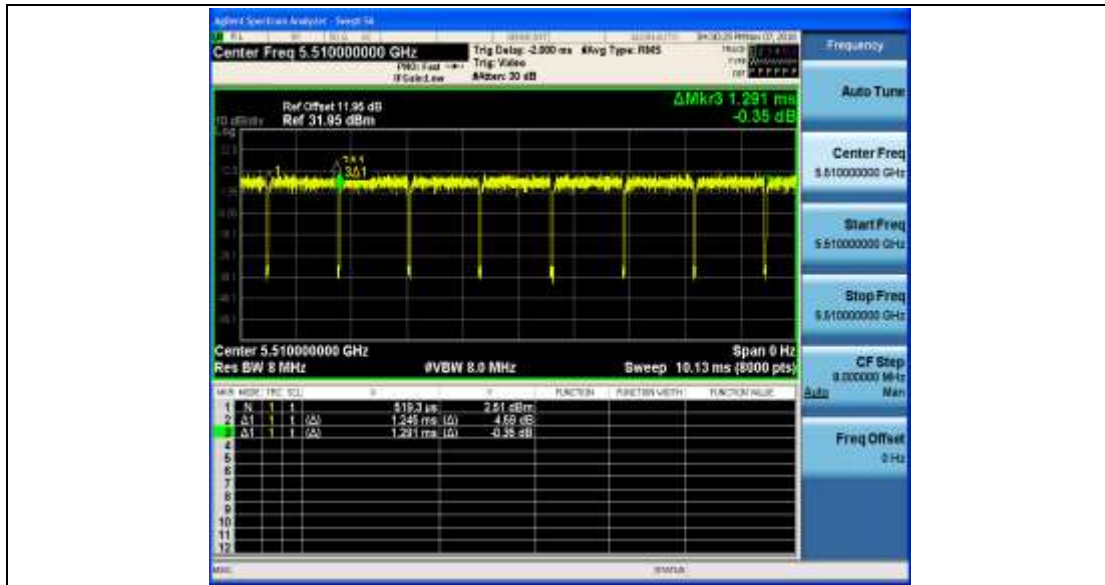
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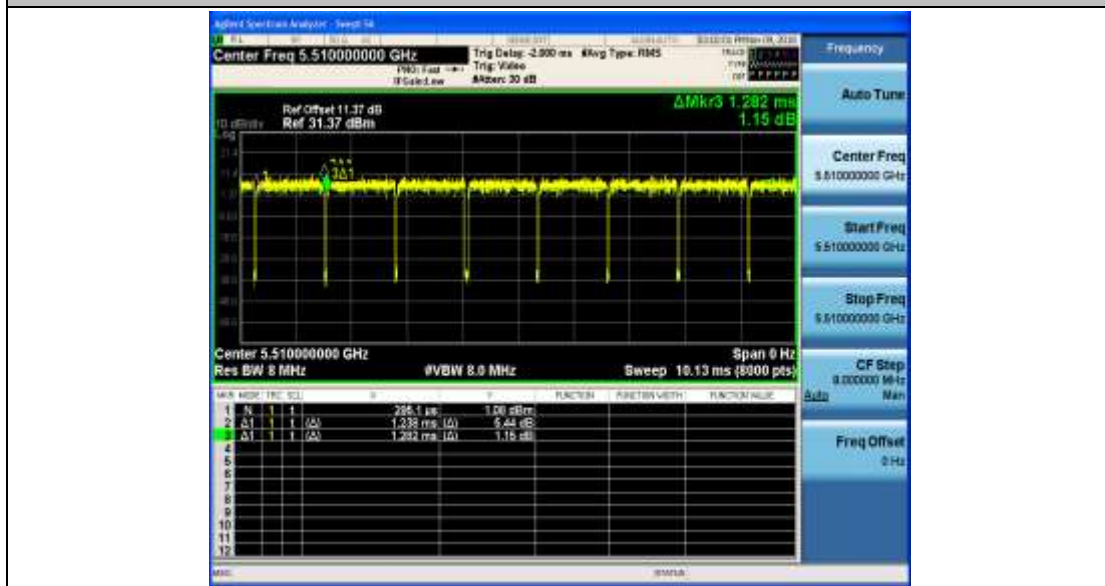
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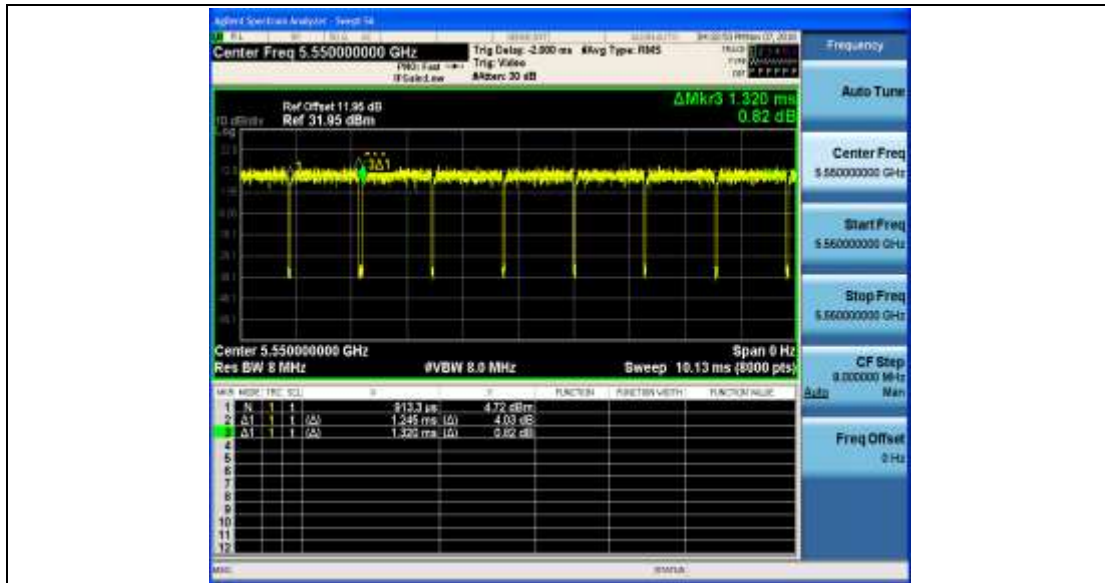
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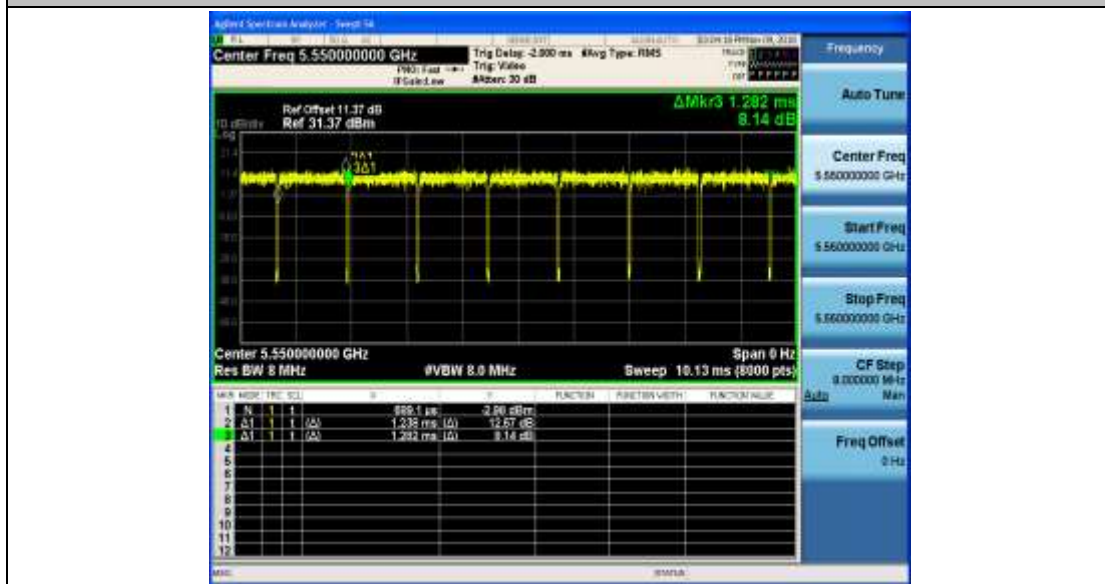
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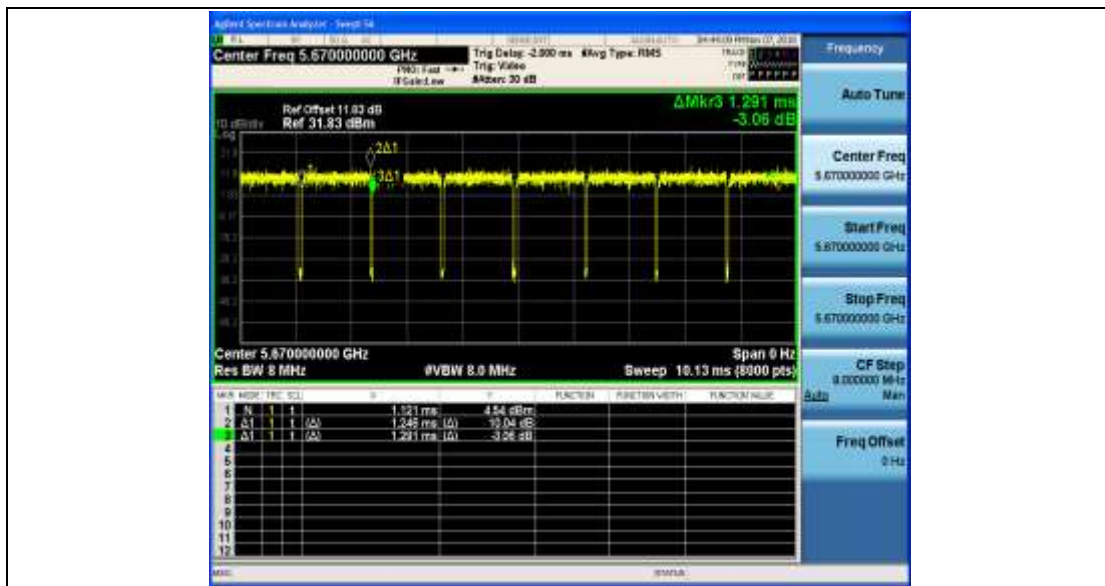
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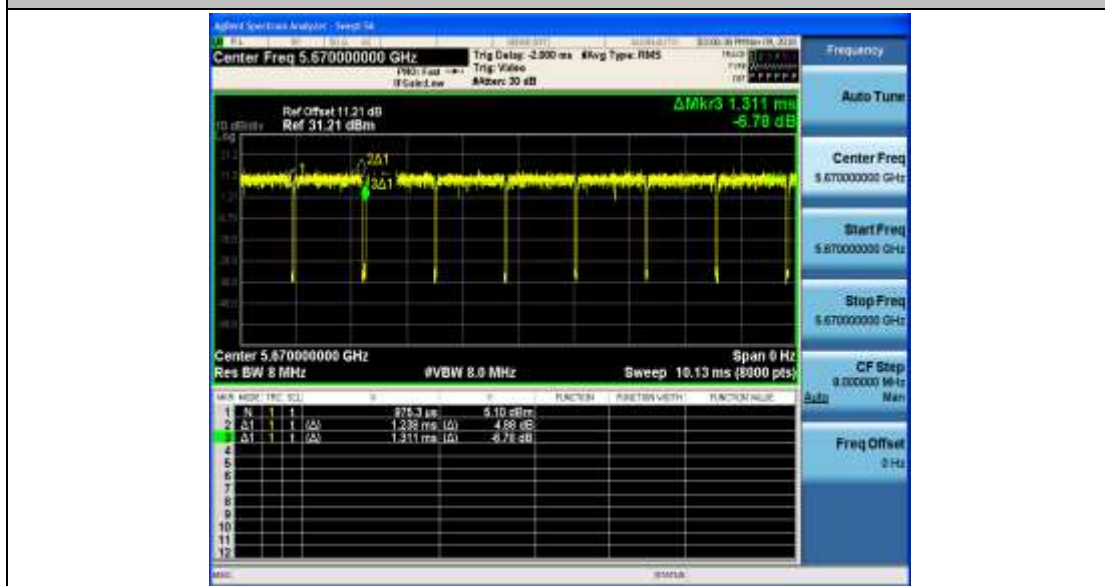
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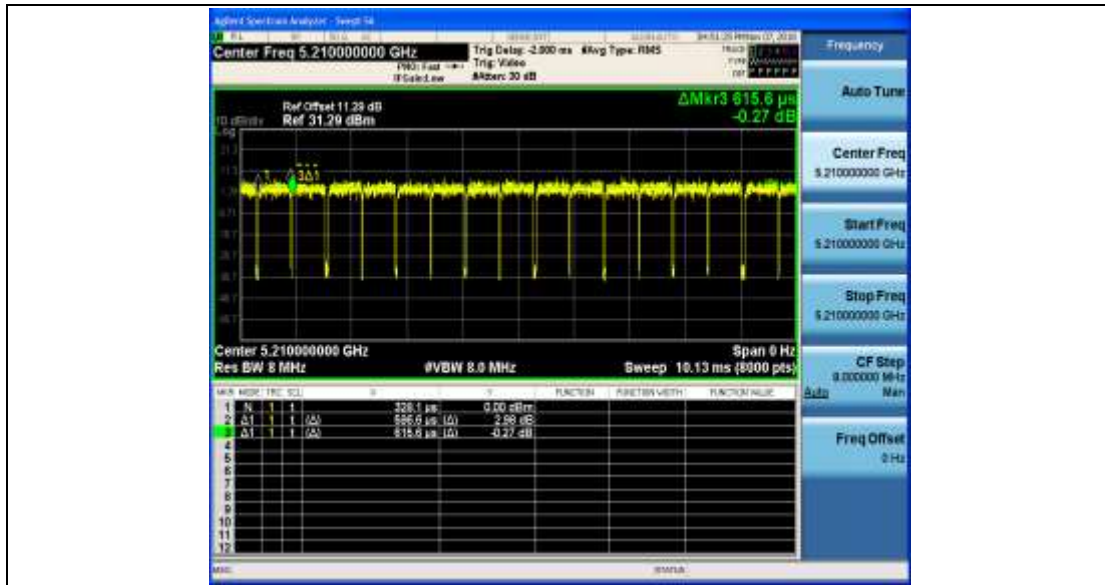
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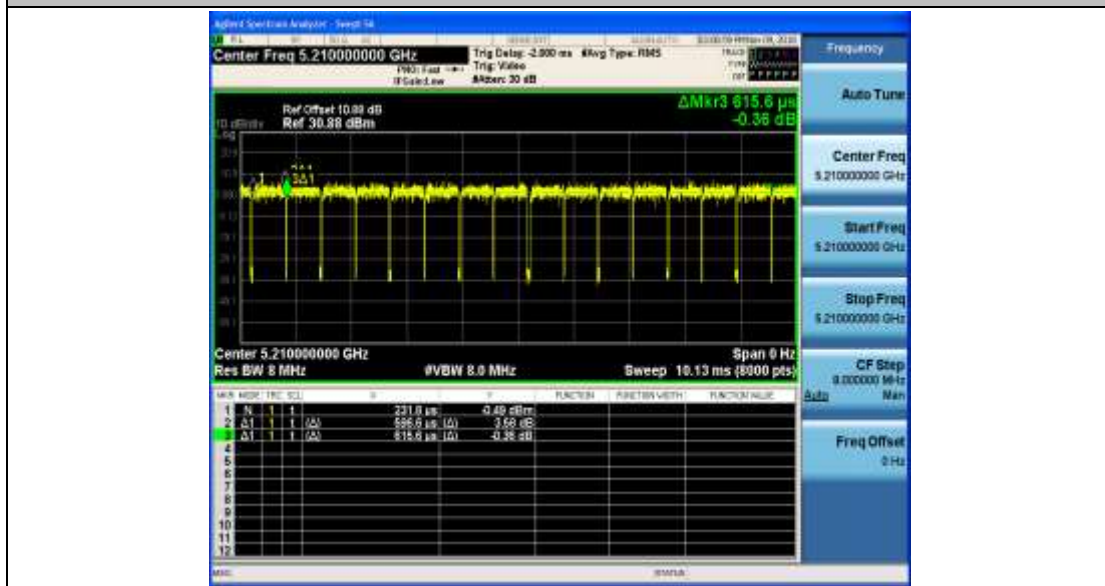
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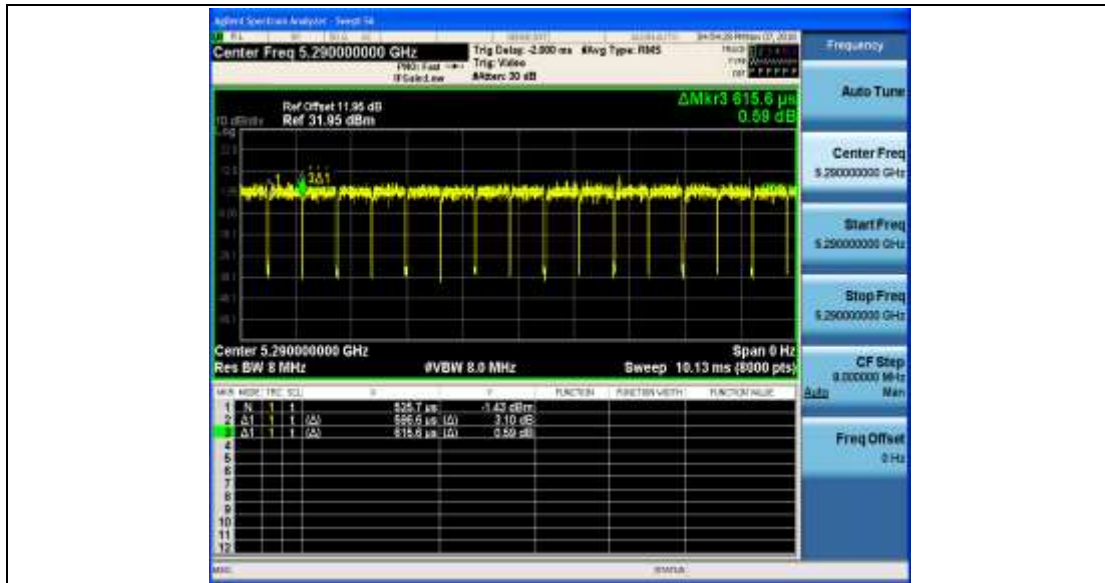
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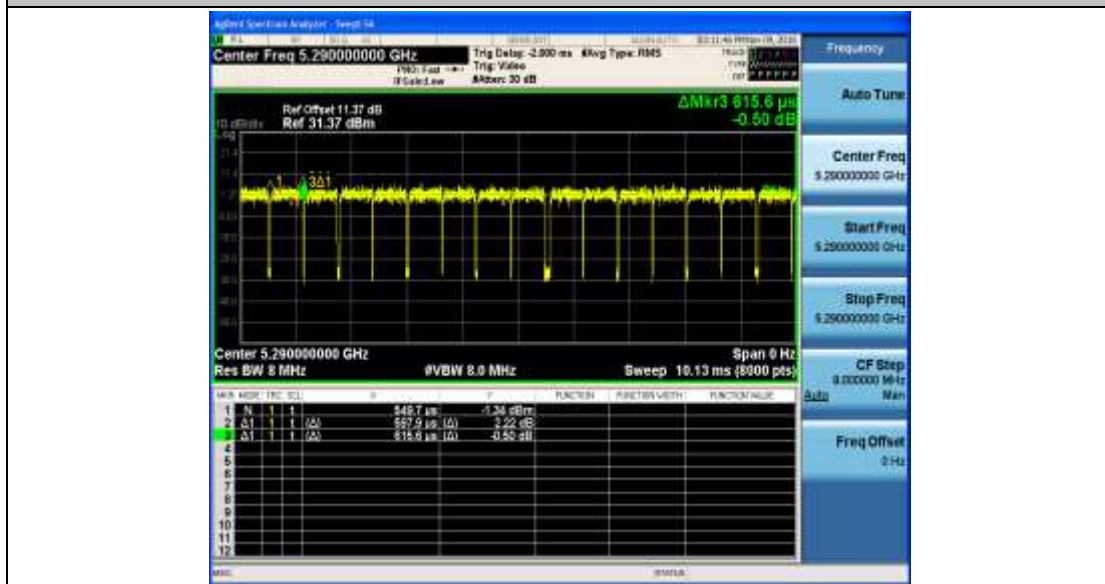
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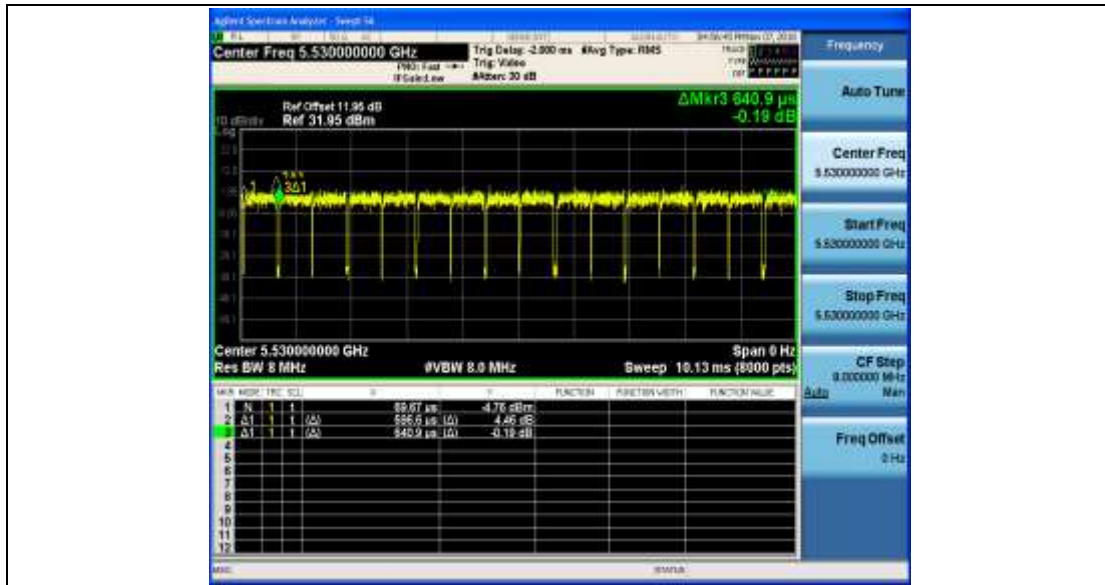
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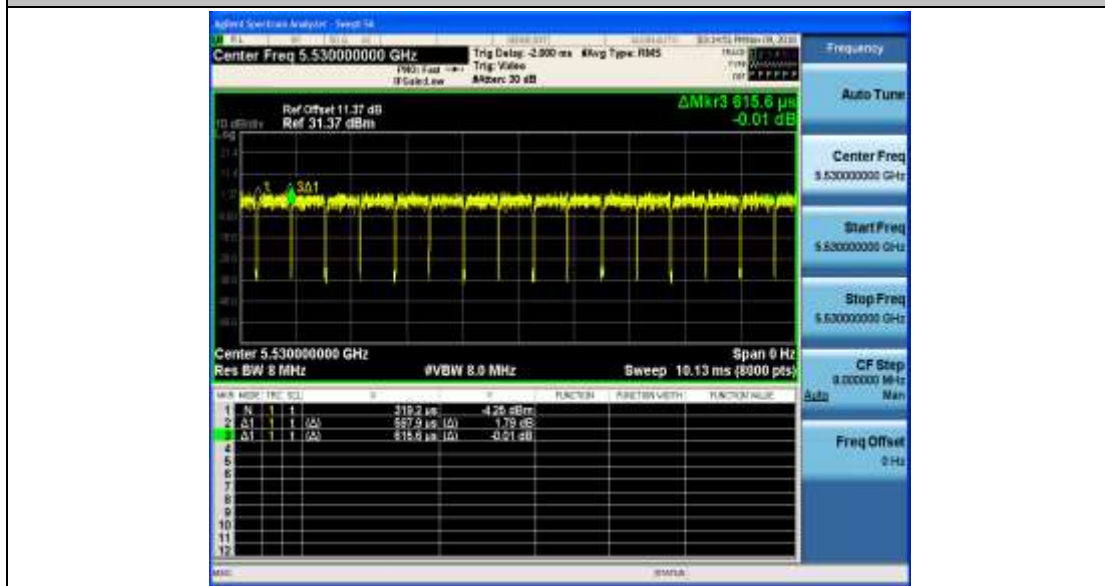
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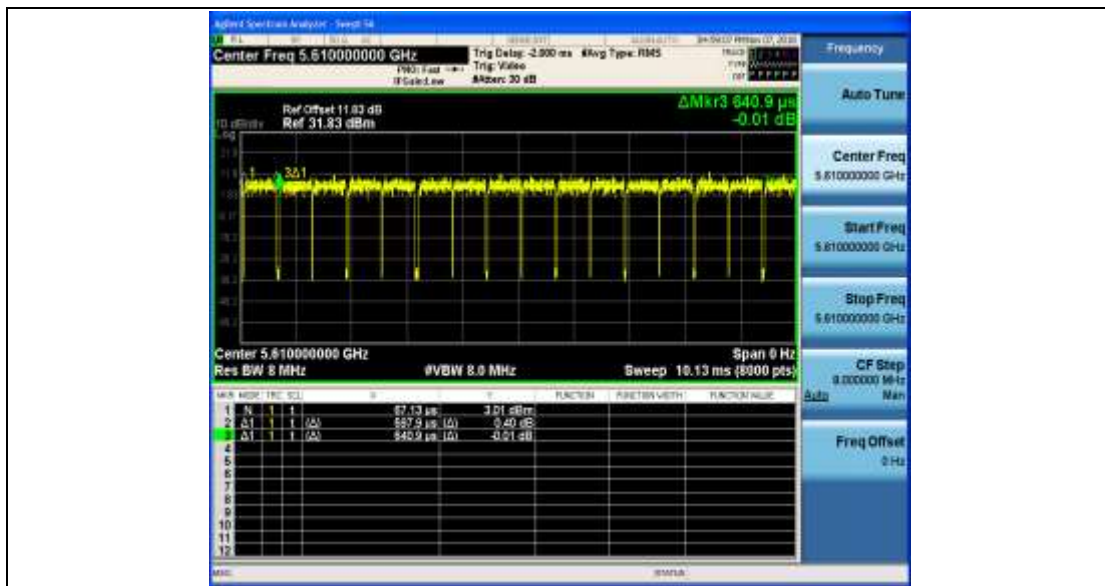
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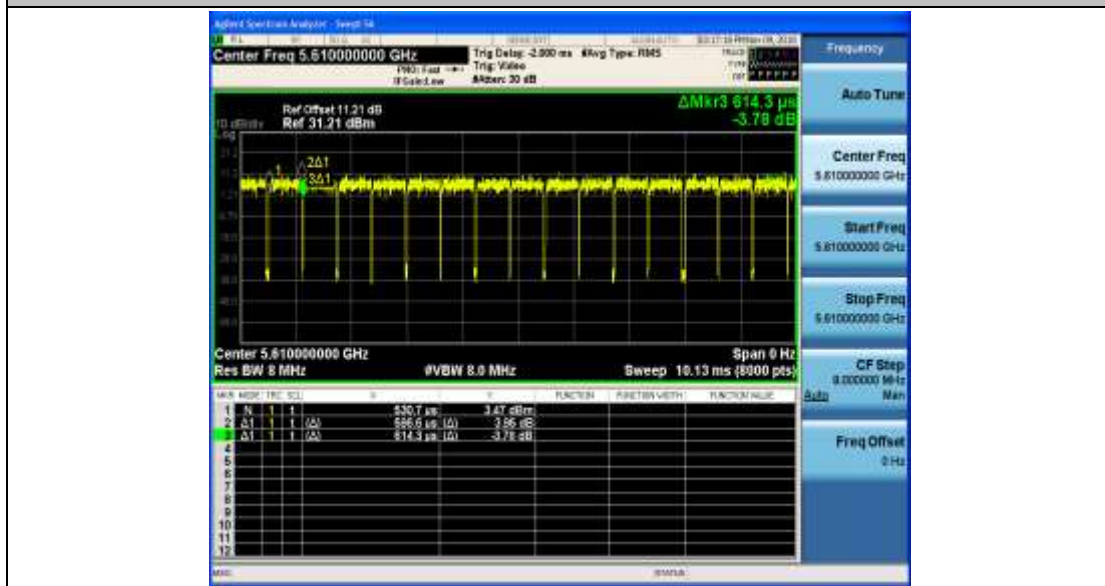
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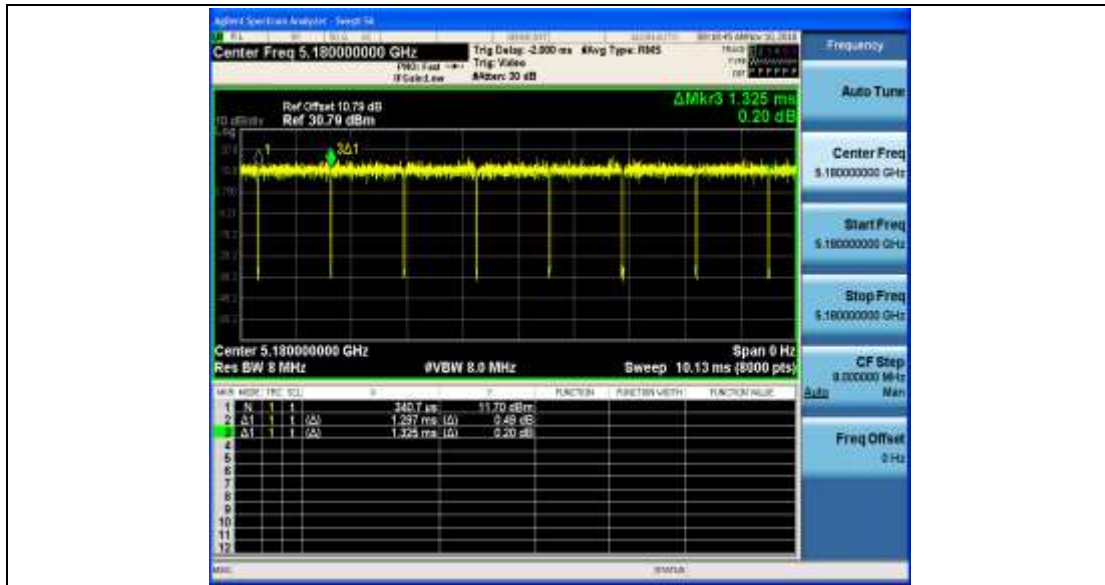
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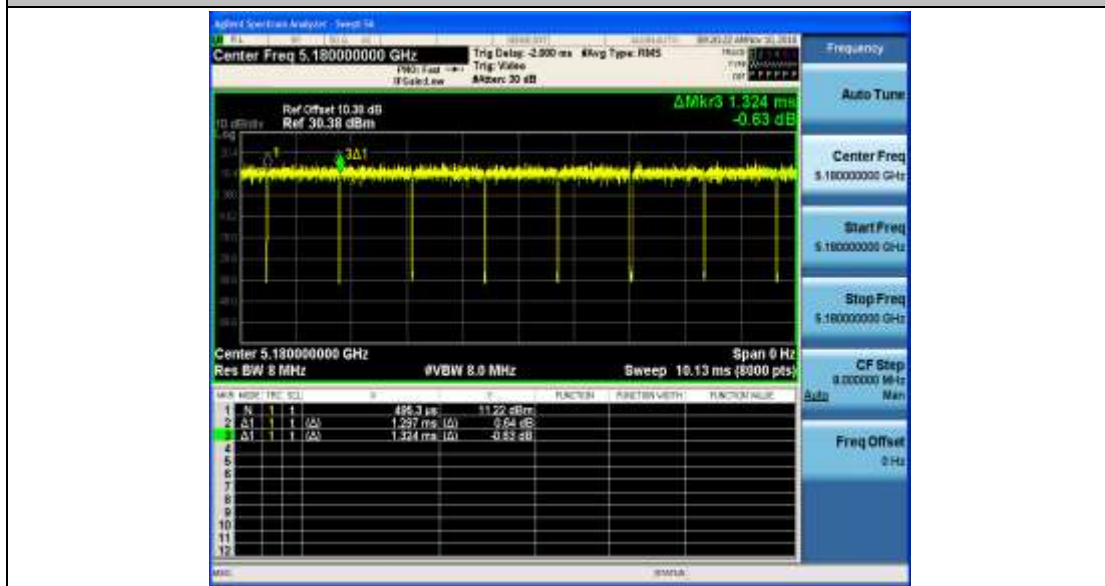
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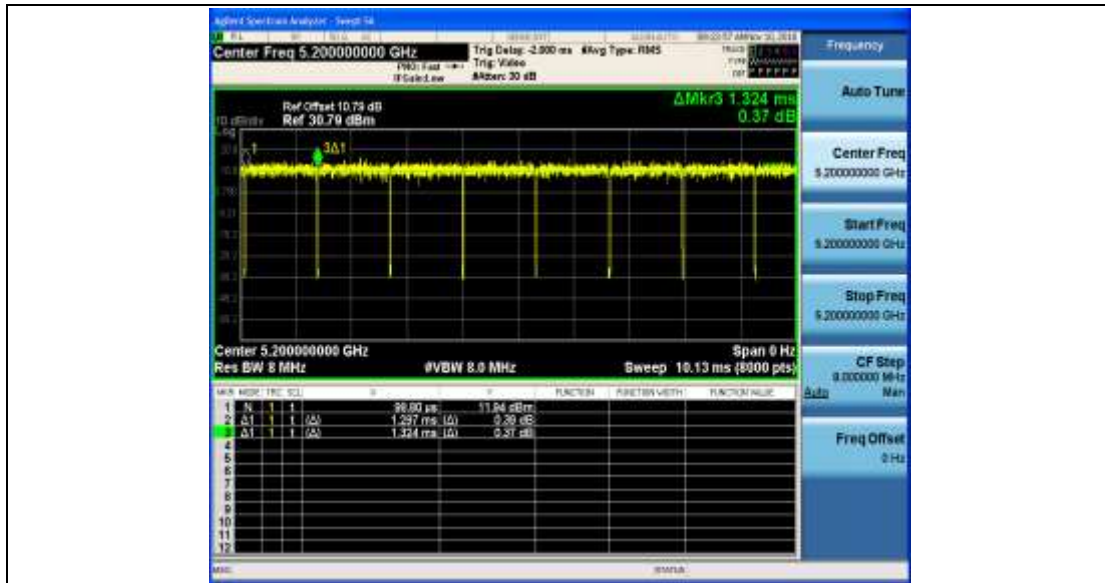
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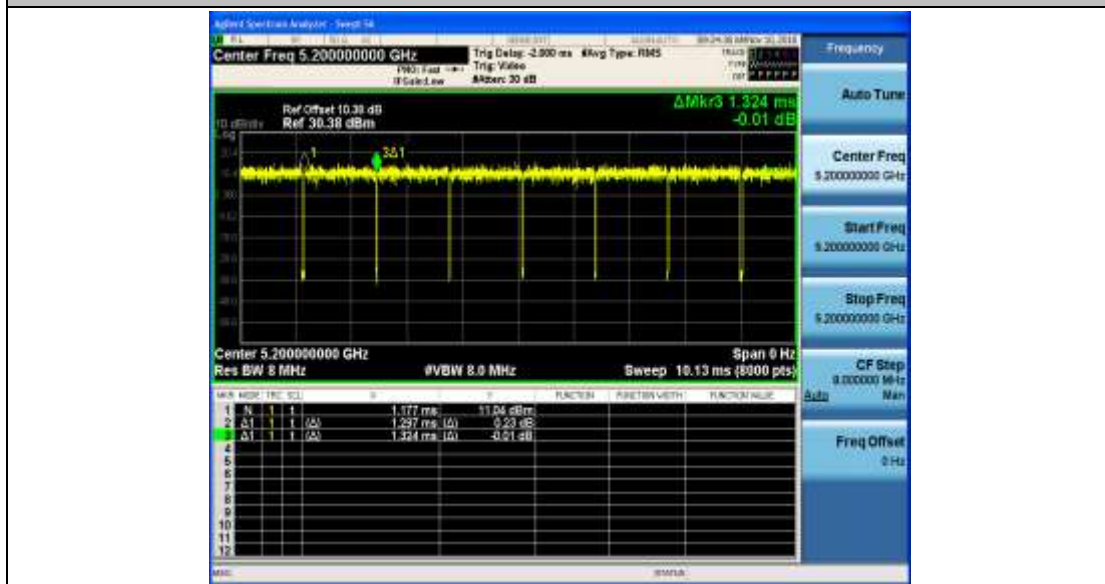
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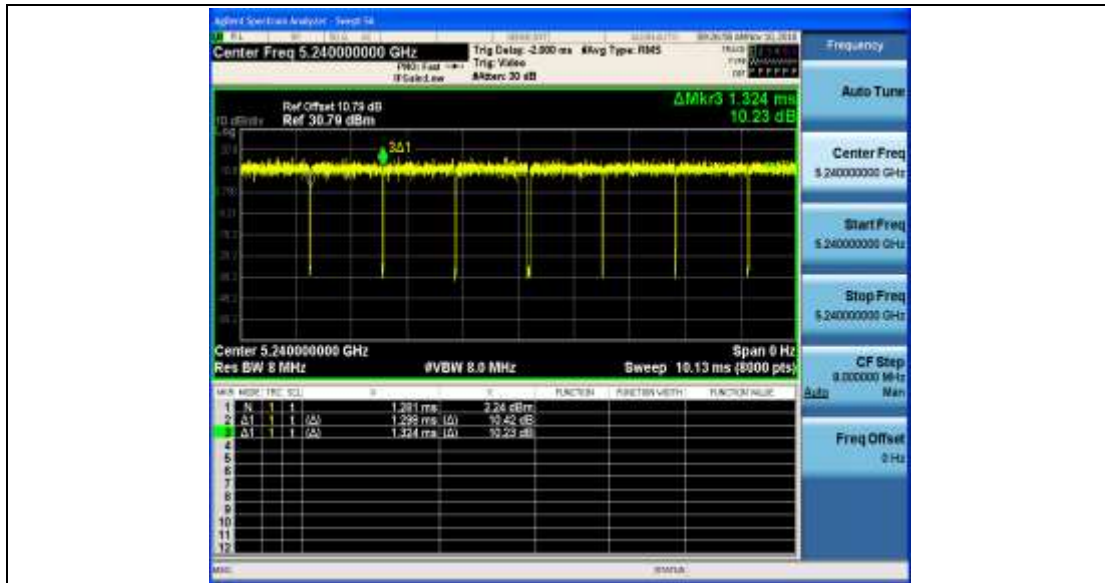
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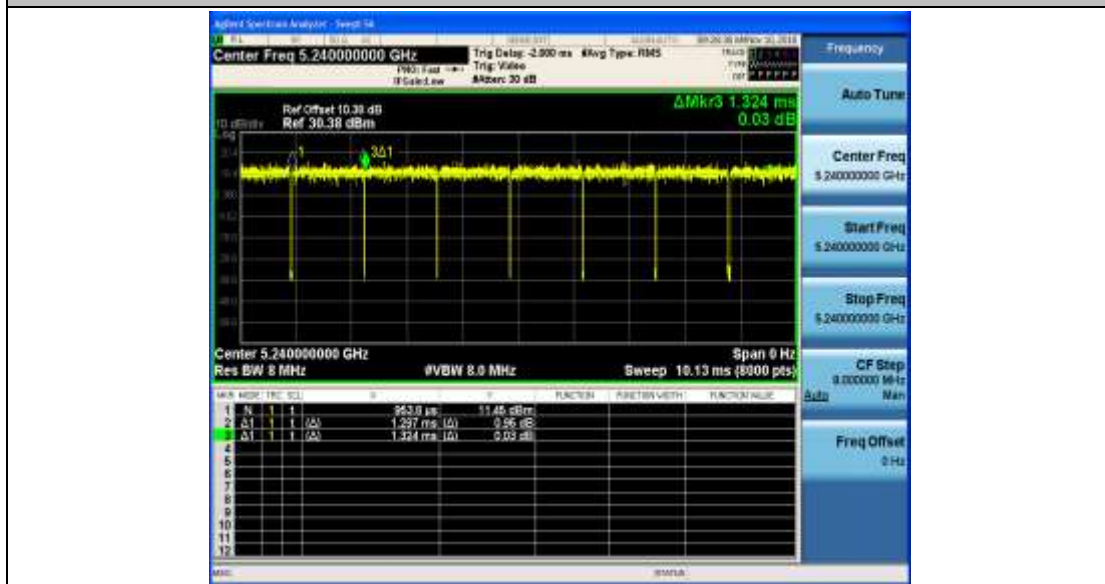
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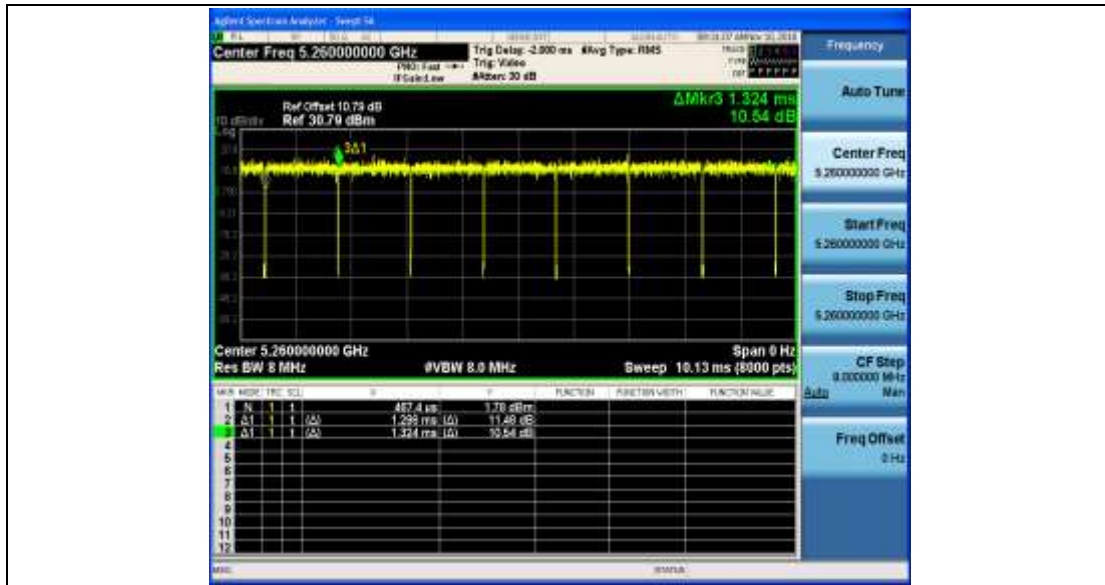
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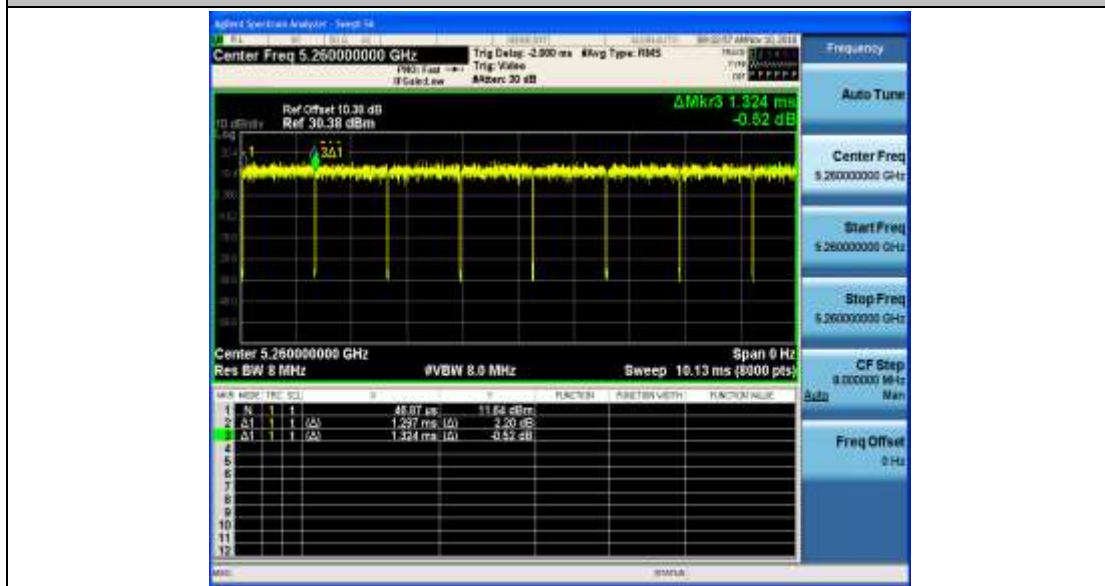
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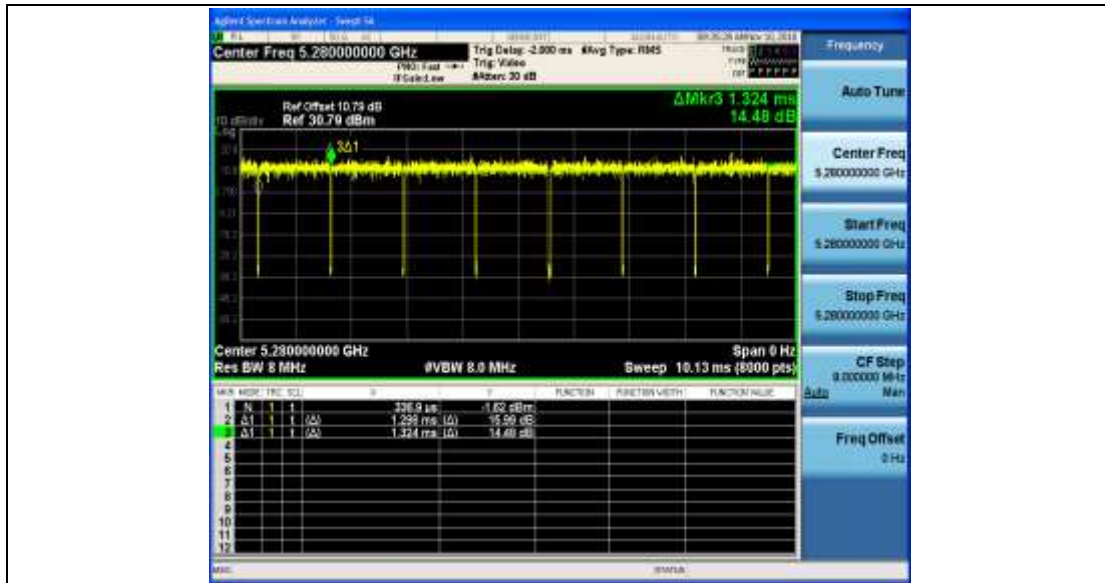
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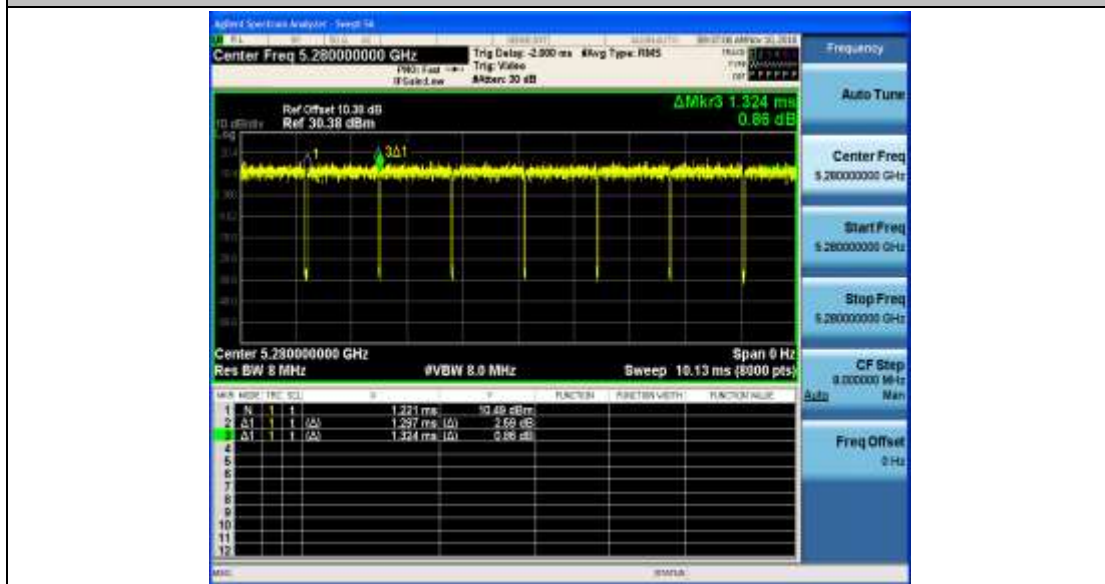
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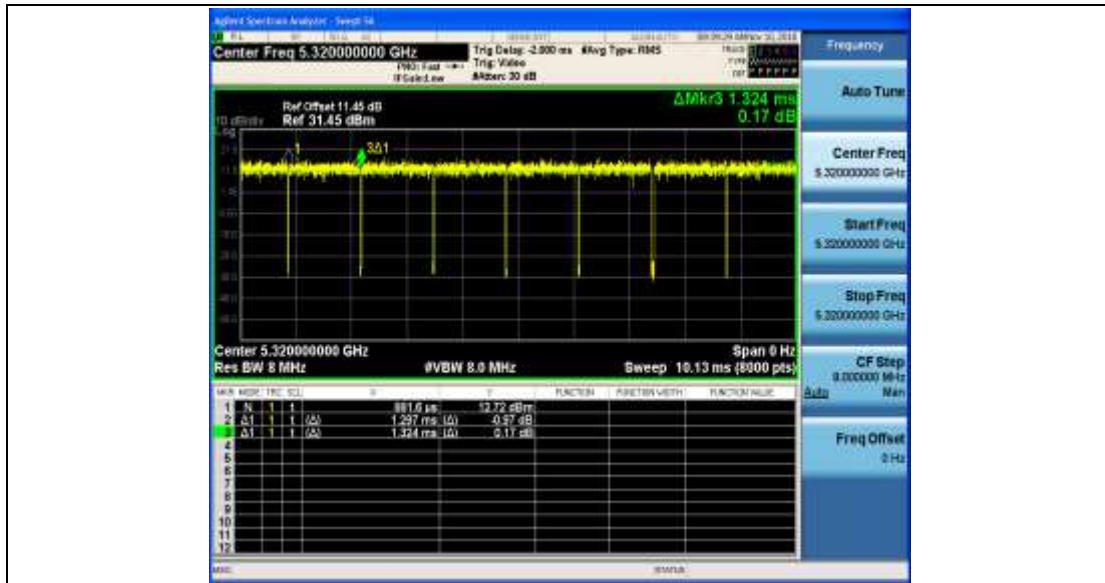
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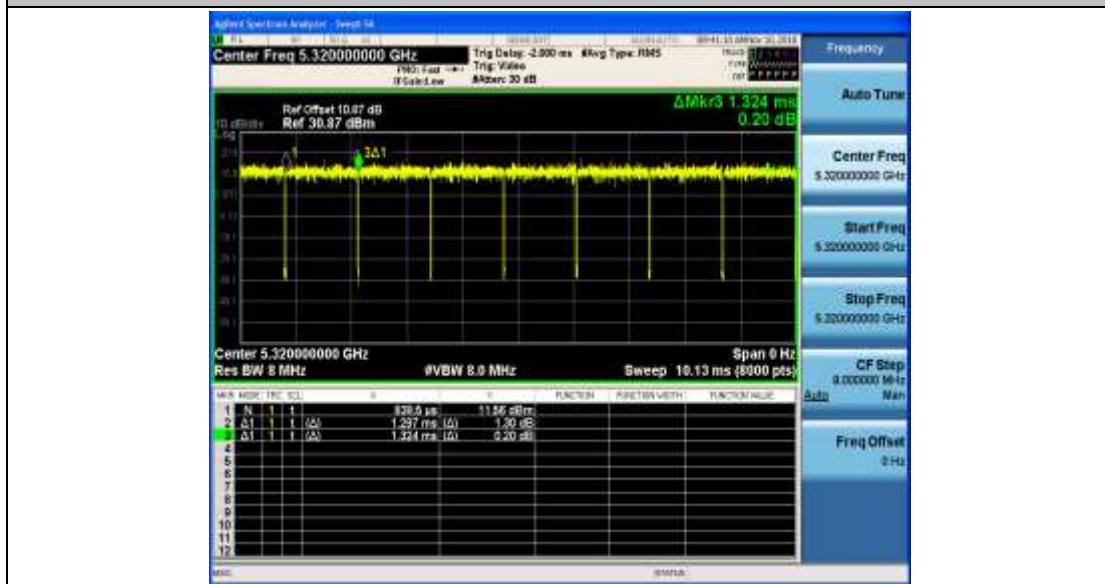
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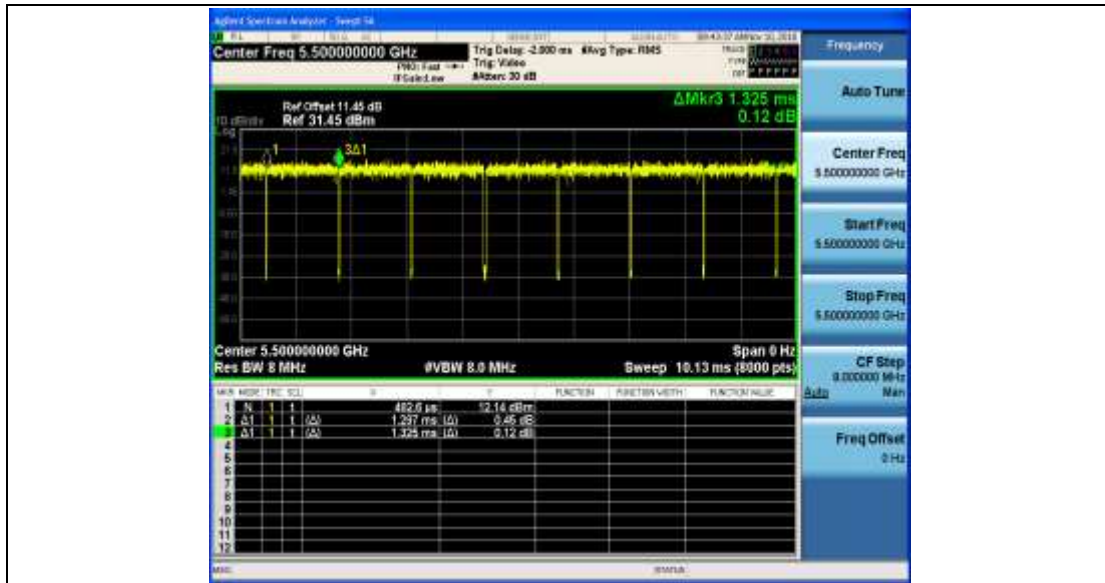
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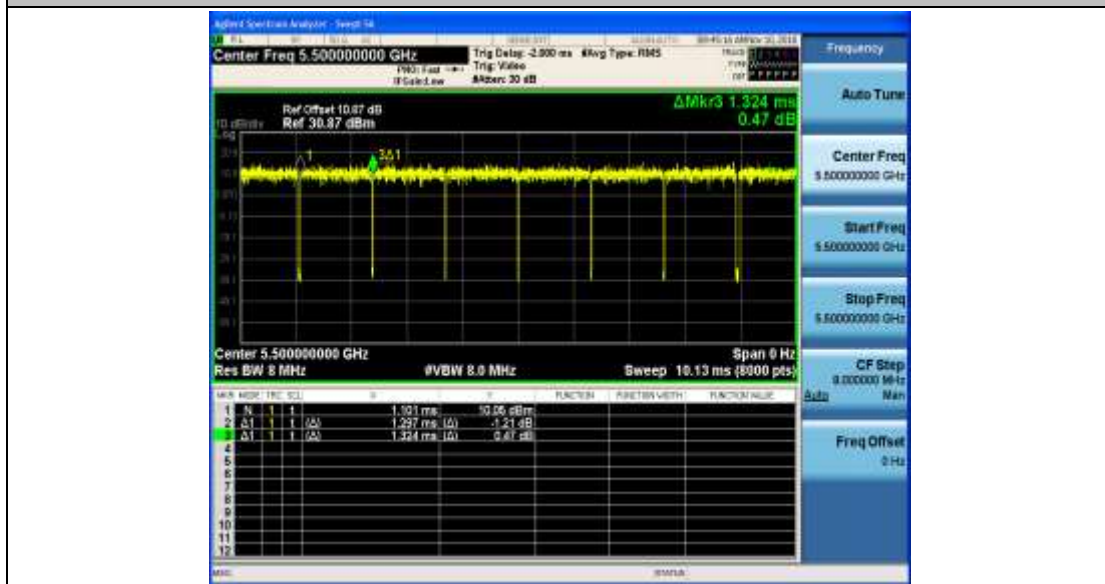
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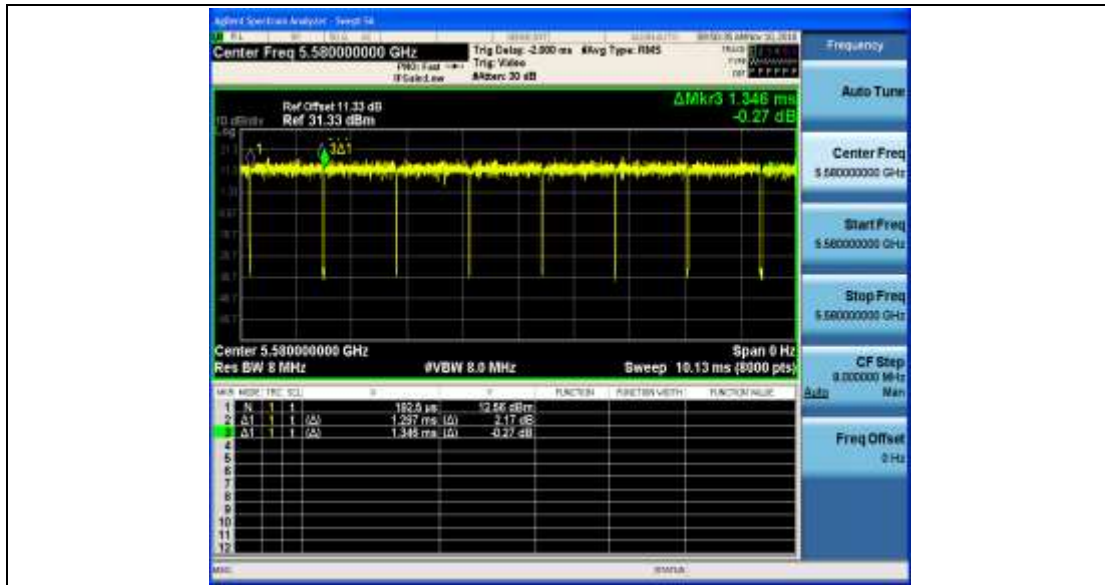
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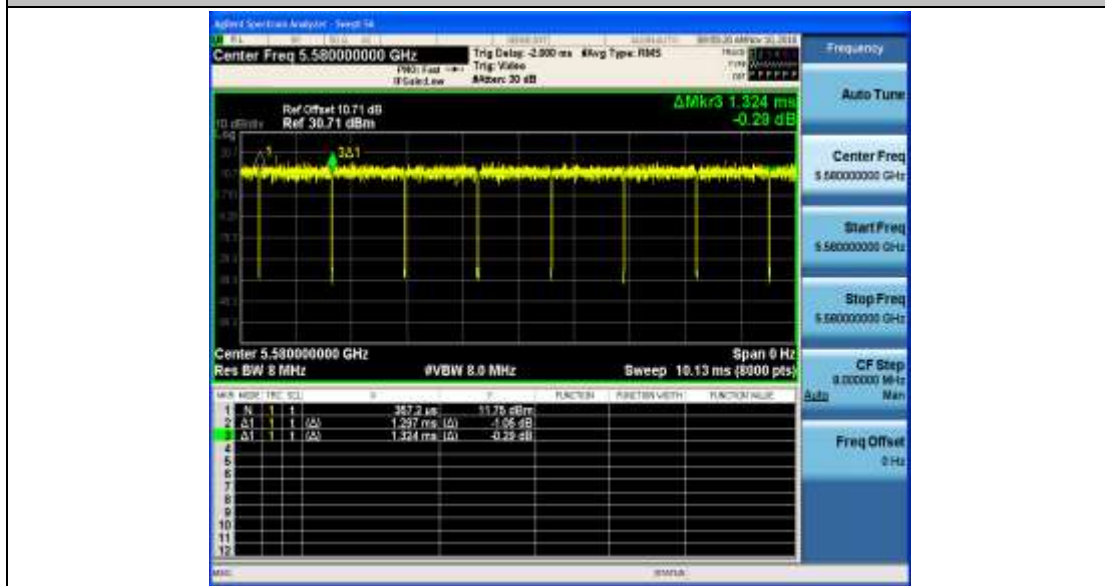
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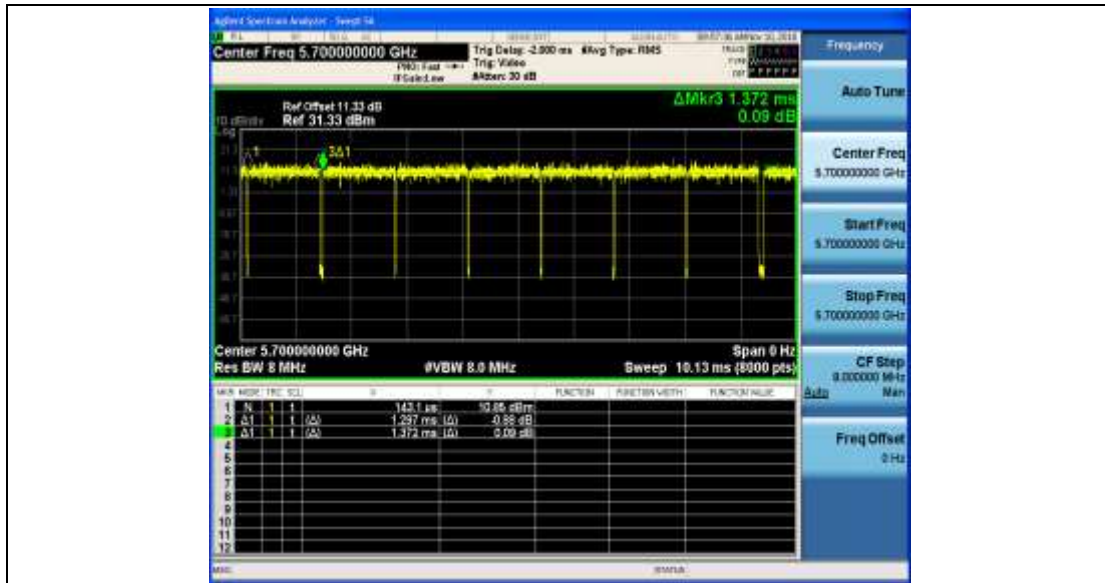
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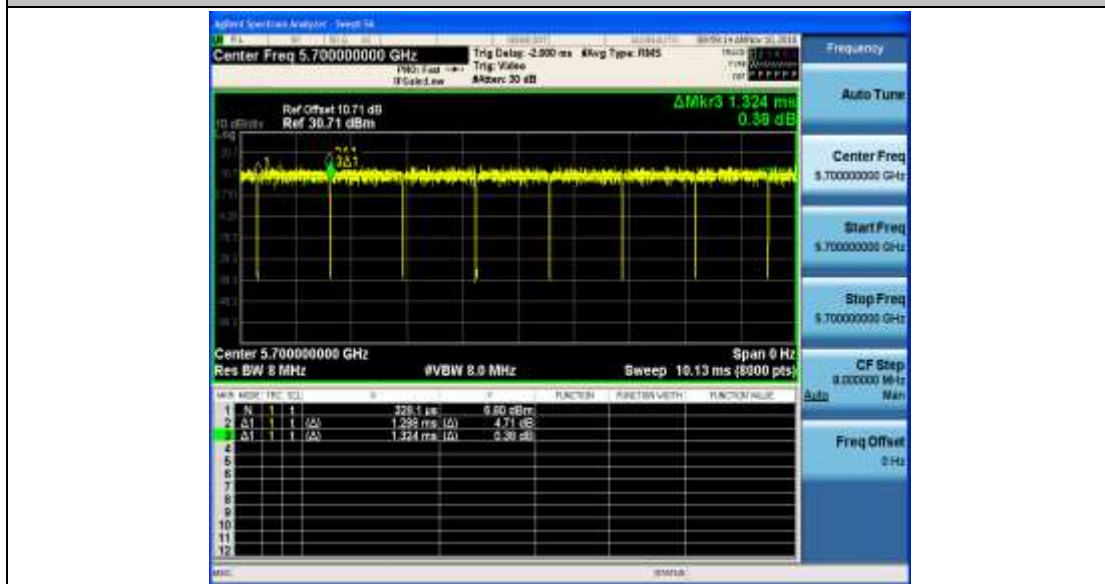
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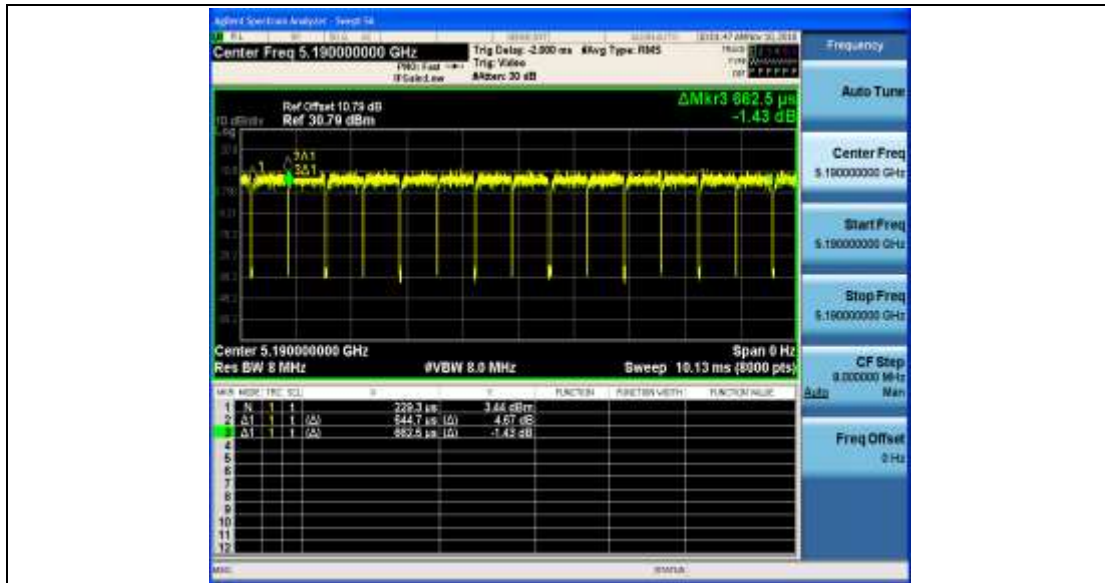
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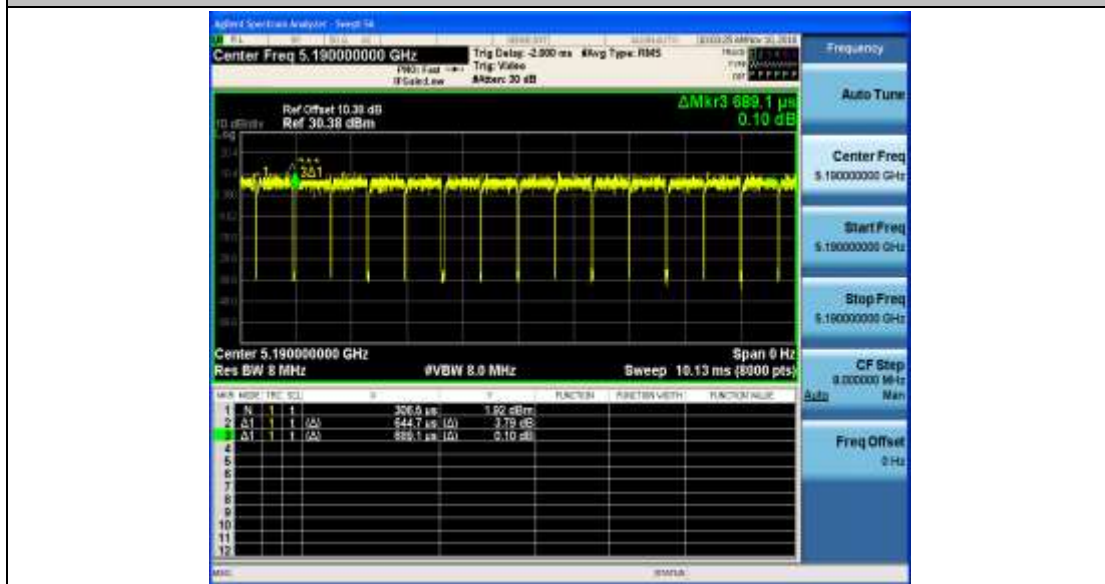
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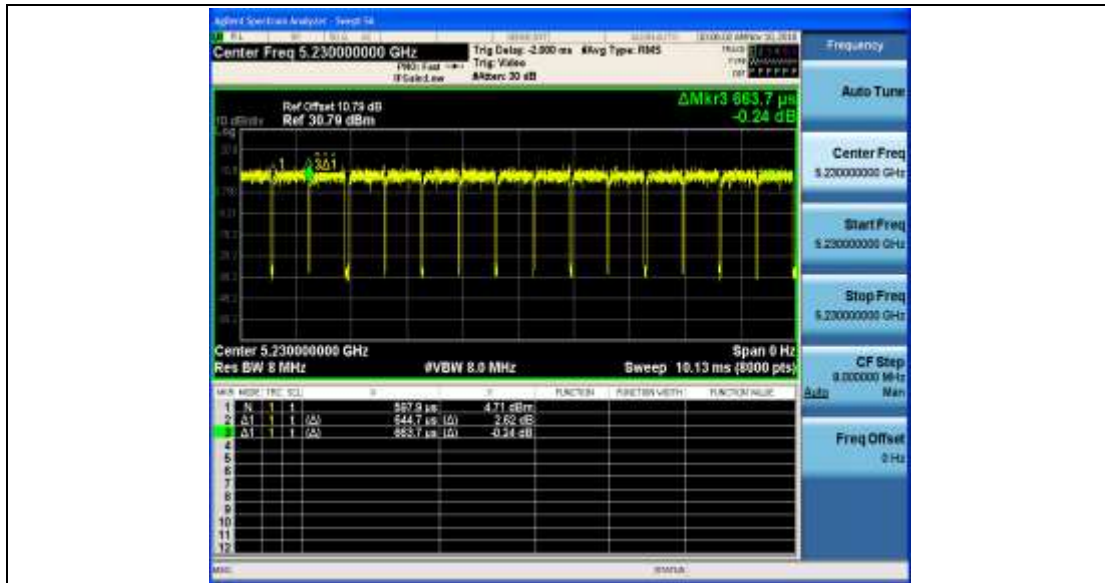
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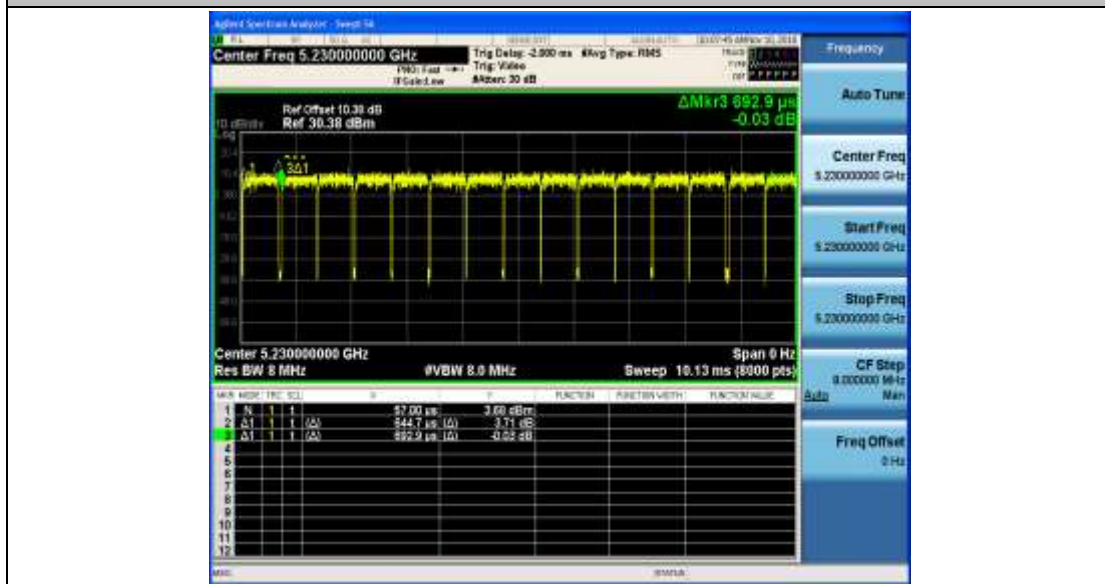
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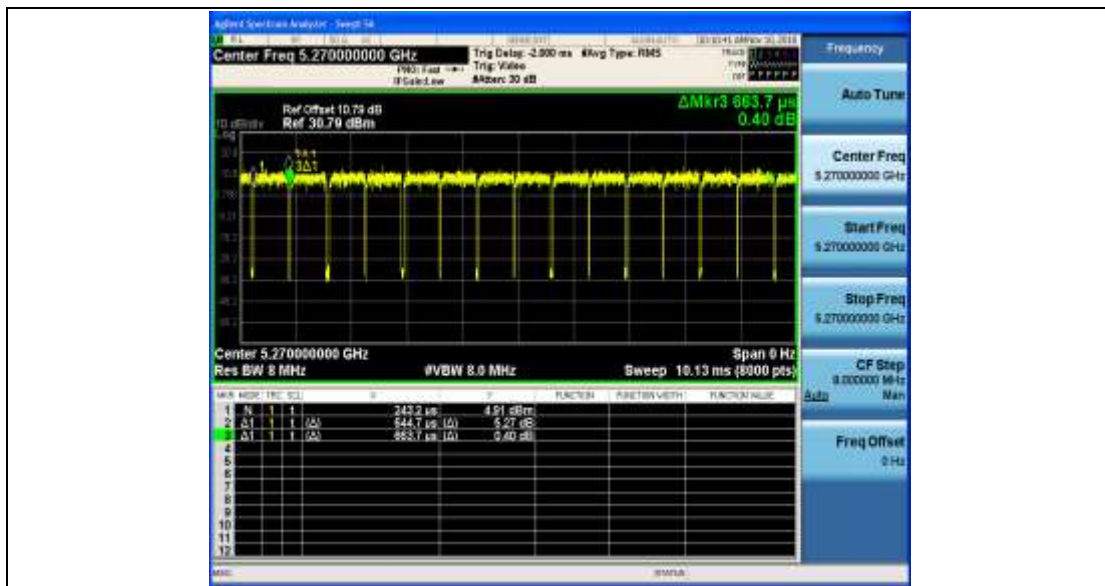
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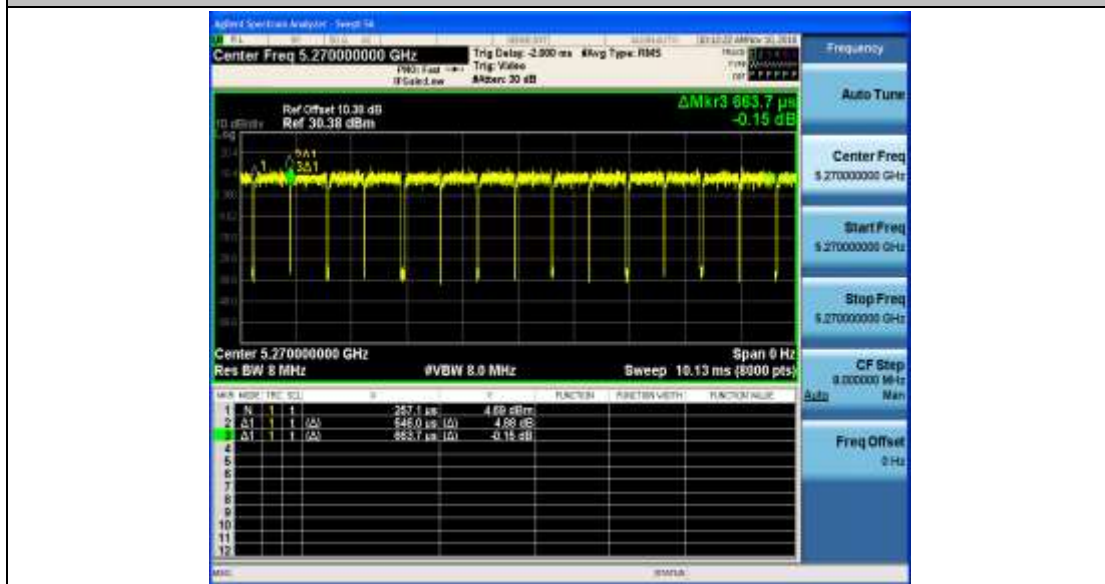
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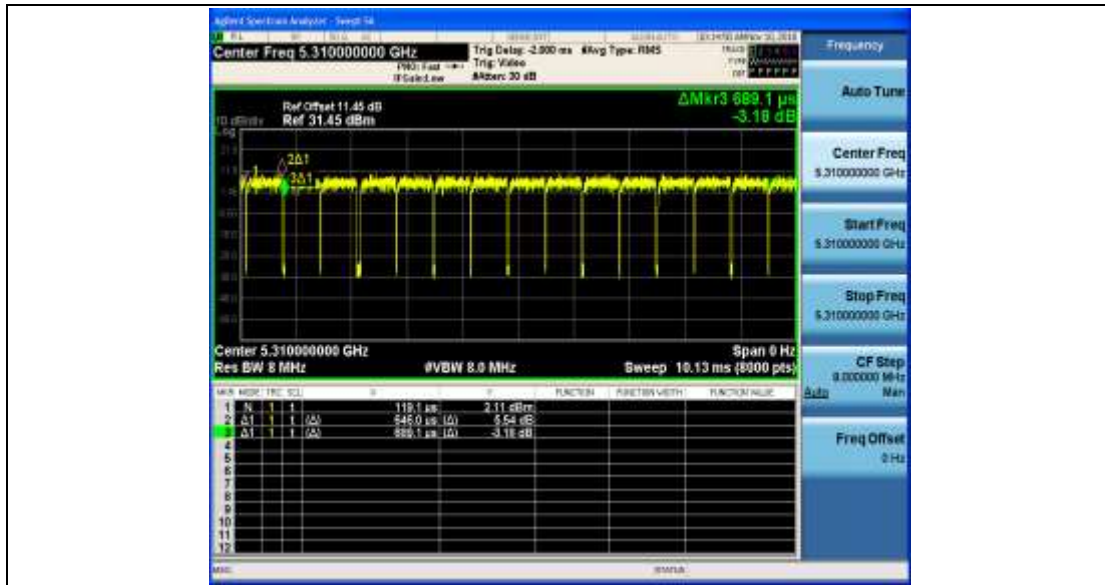
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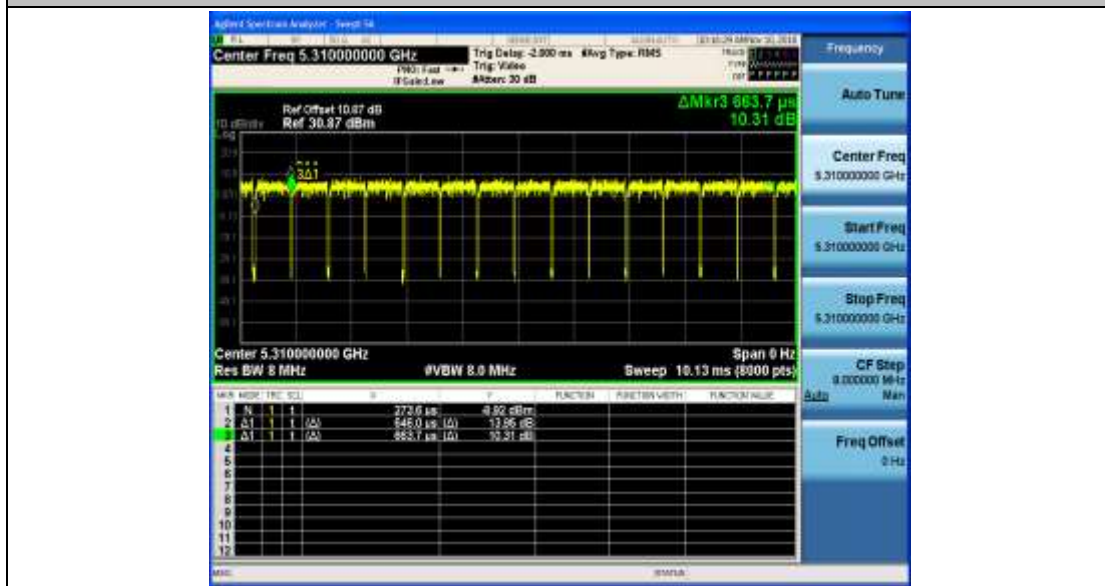
11AC40MIMO_Ant2_5270



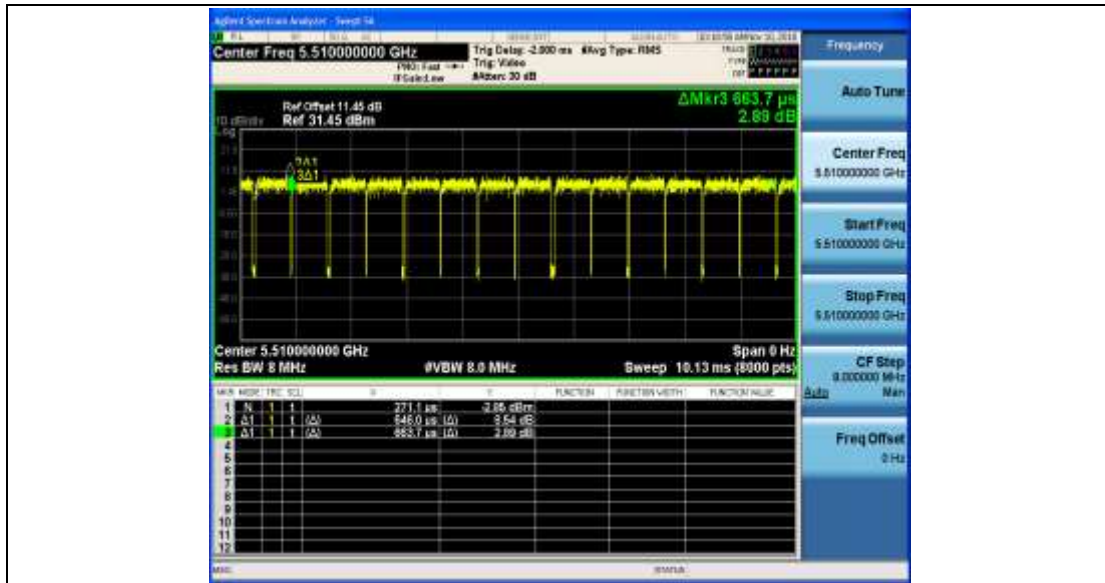
11AC40MIMO_Ant1_5310



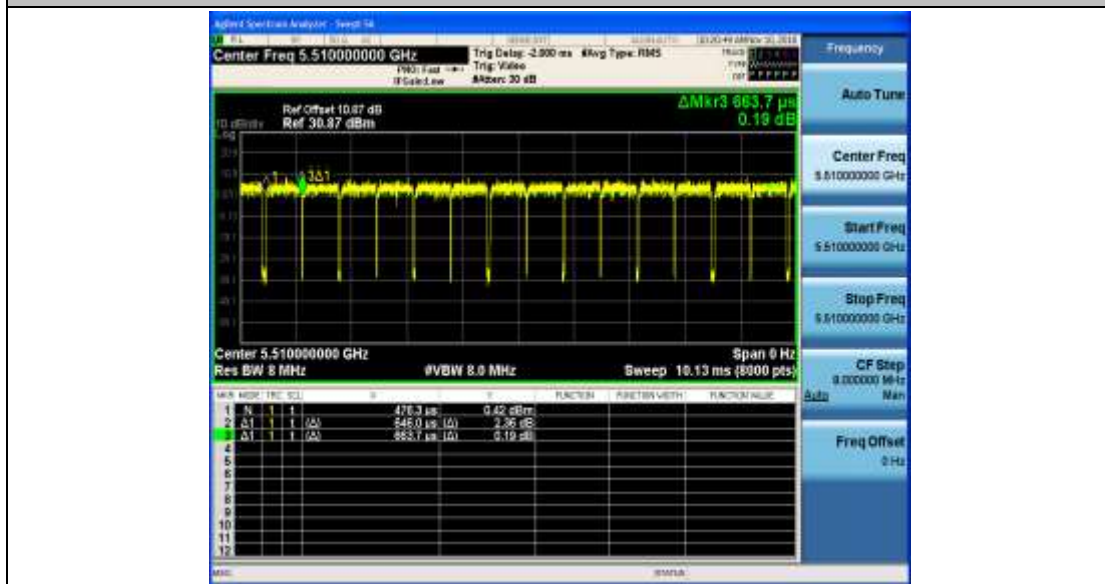
11AC40MIMO_Ant2_5310



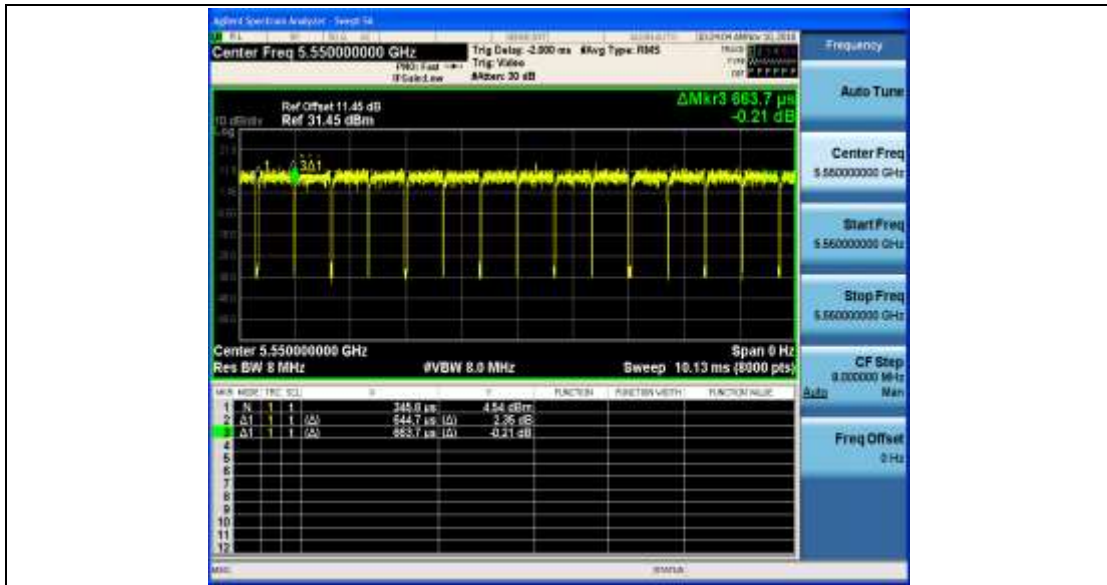
11AC40MIMO_Ant1_5510



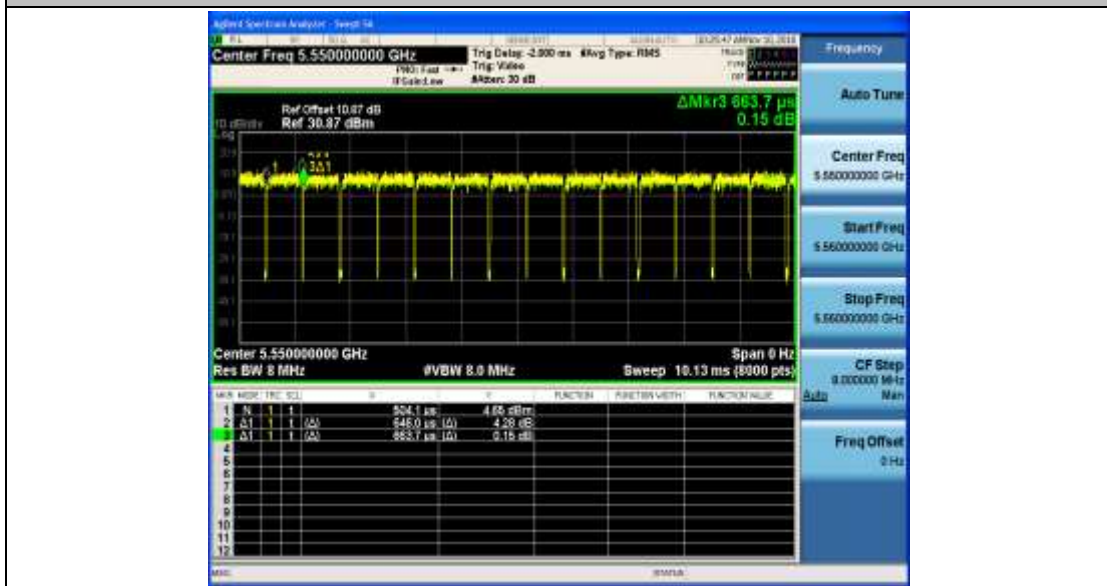
11AC40MIMO_Ant2_5510



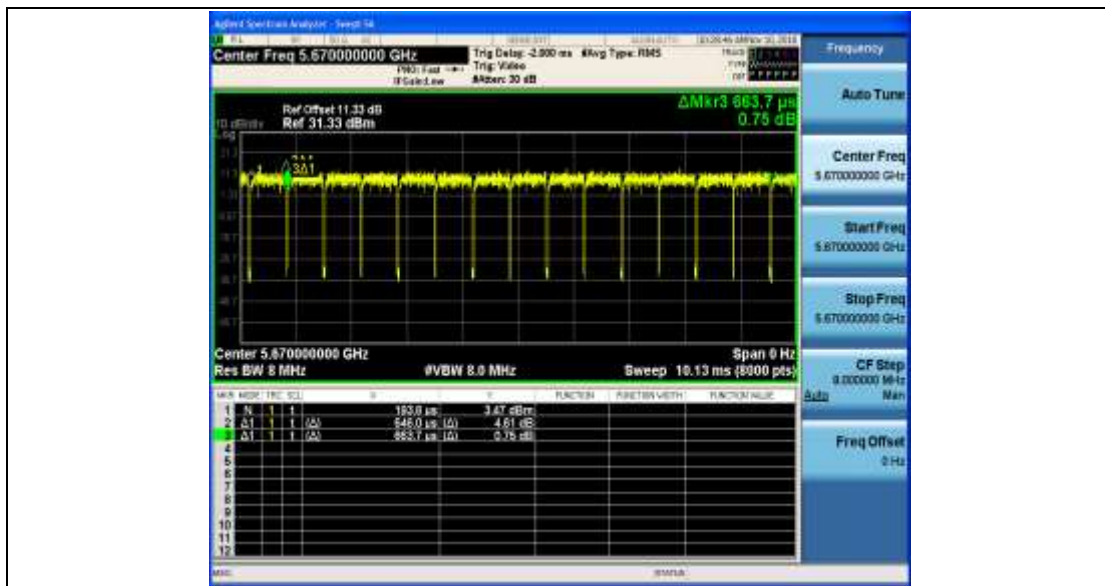
11AC40MIMO_Ant1_5550



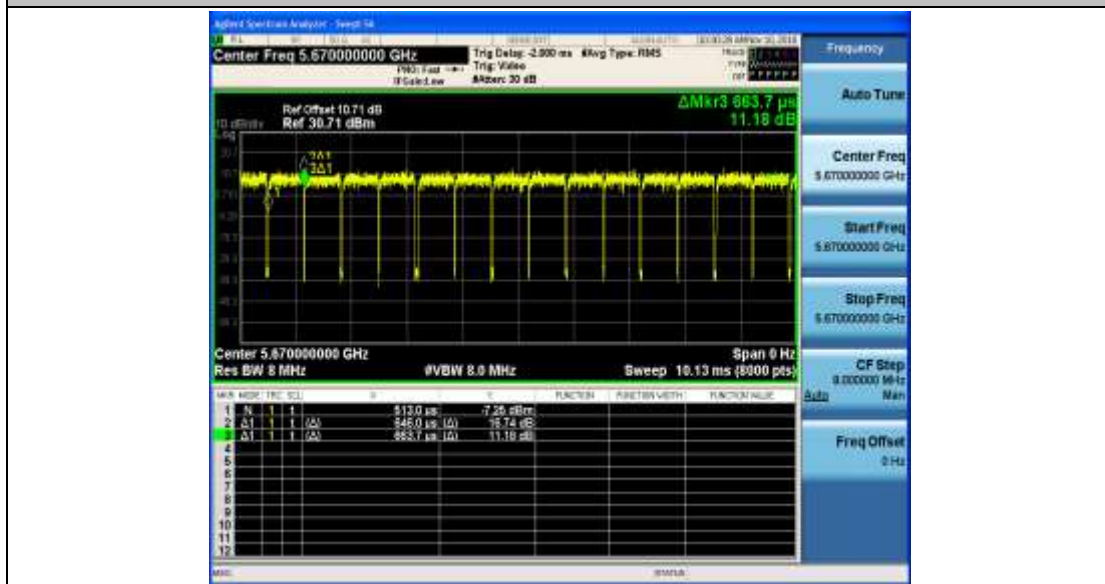
11AC40MIMO_Ant2_5550



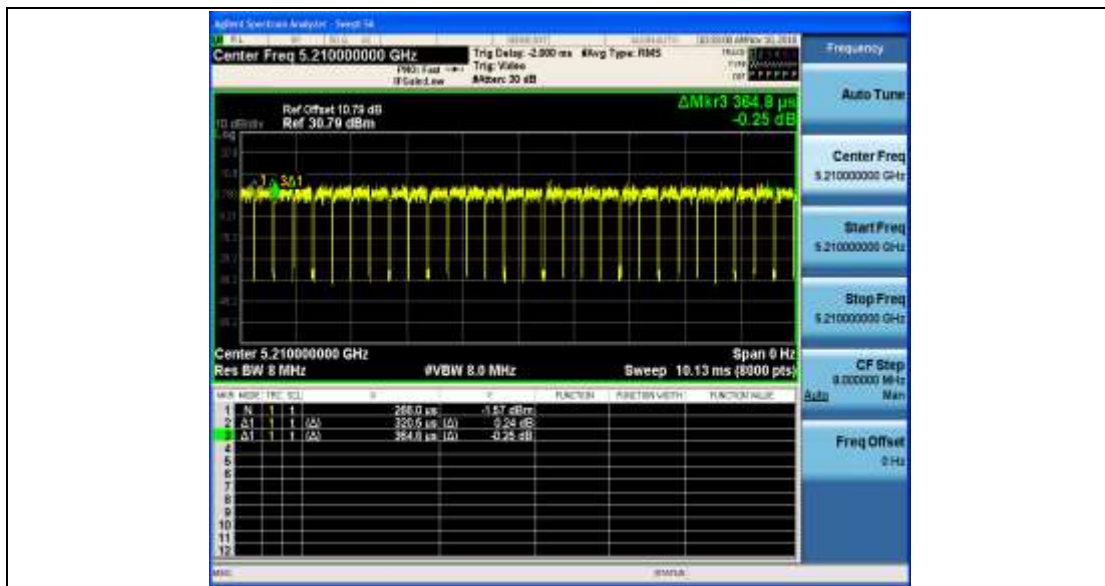
11AC40MIMO_Ant1_5670



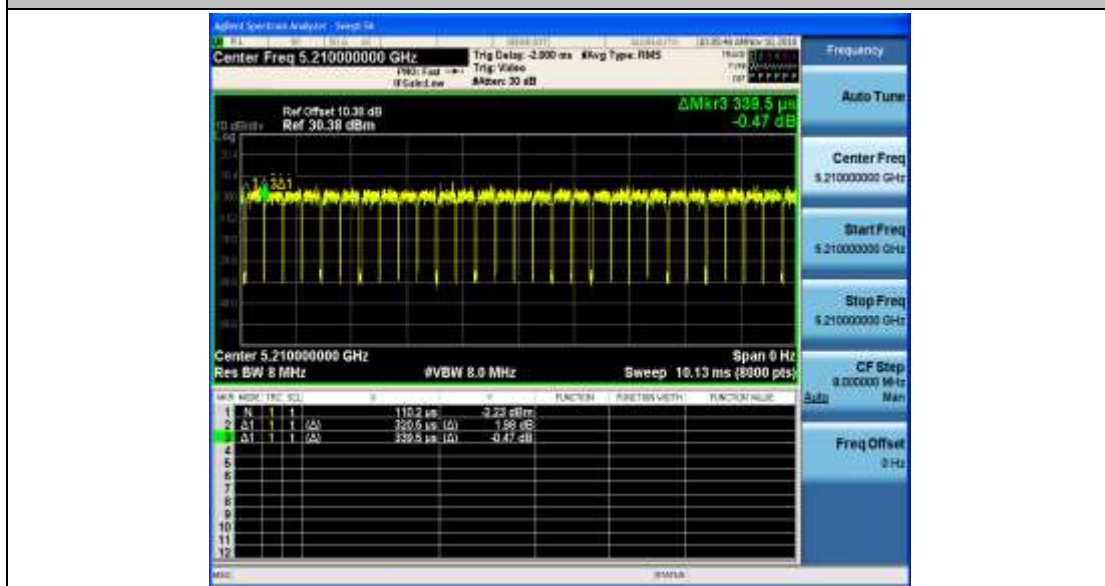
11AC40MIMO_Ant2_5670



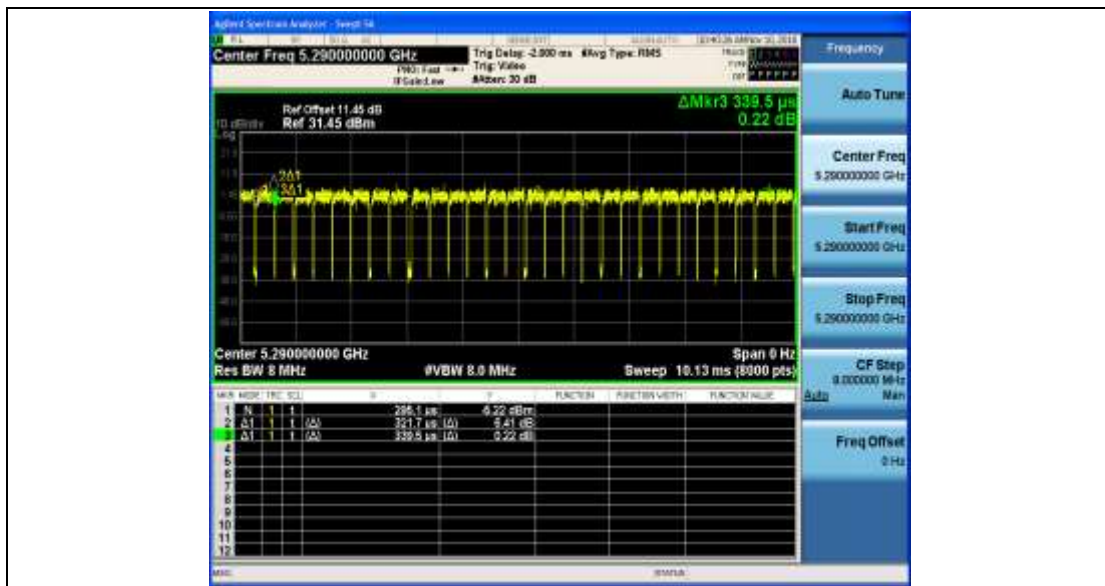
11AC80MIMO_Ant1_5210



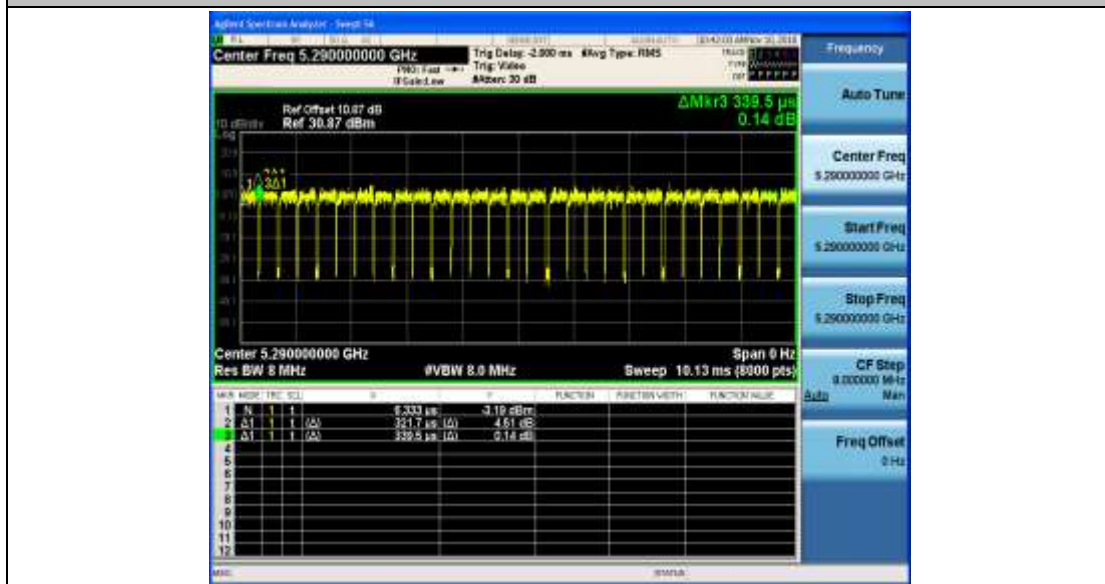
11AC80MIMO_Ant2_5210



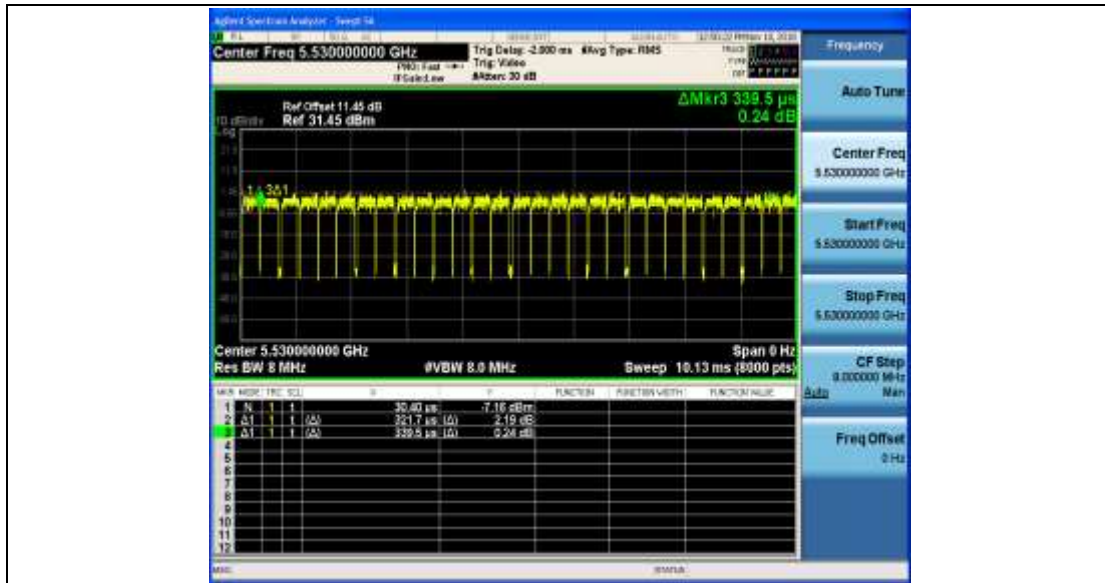
11AC80MIMO_Ant1_5290



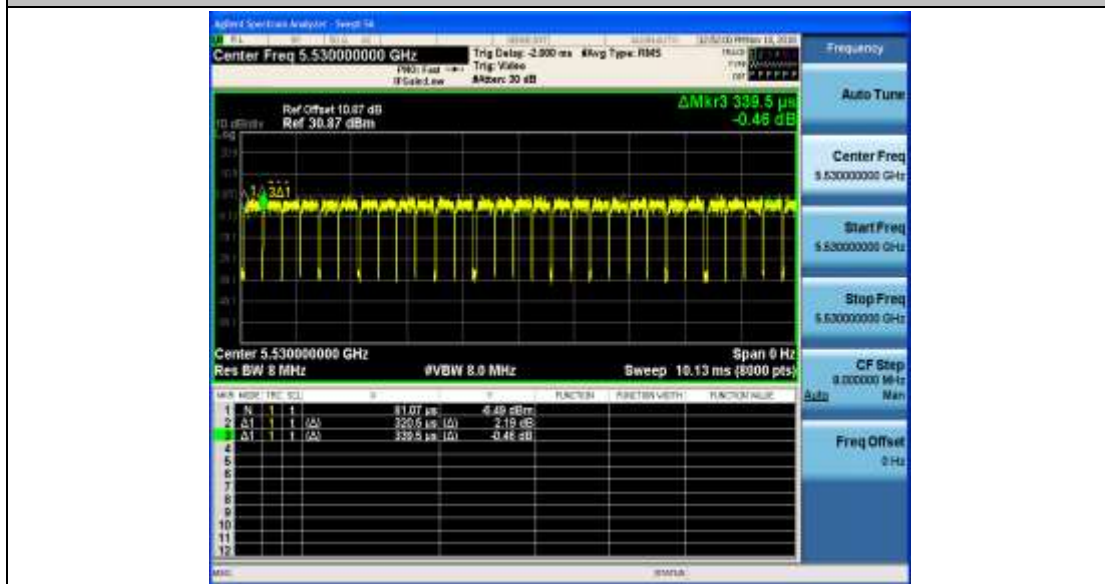
11AC80MIMO_Ant2_5290



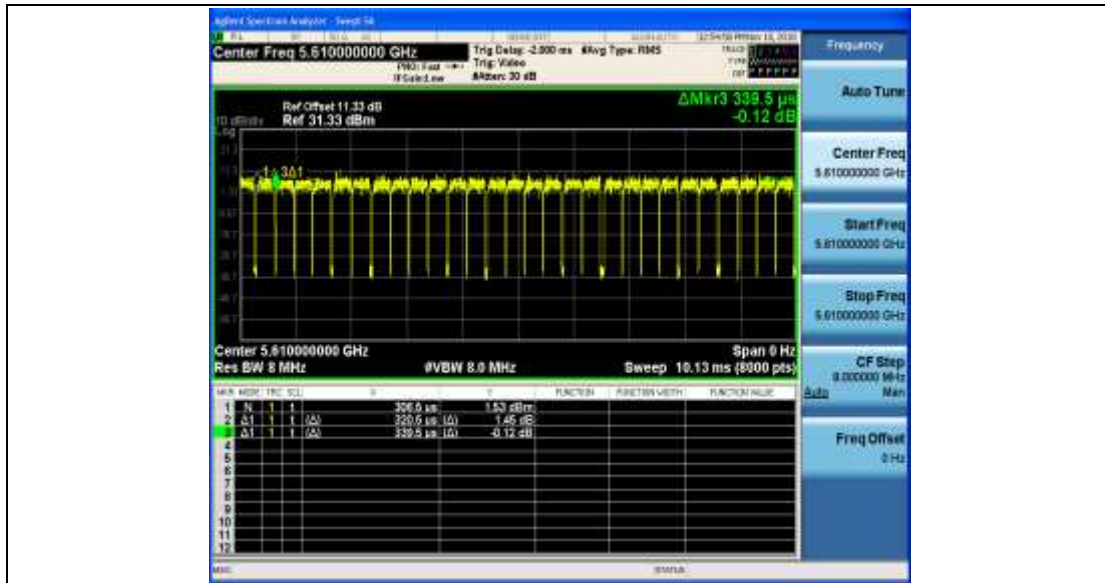
11AC80MIMO_Ant1_5530



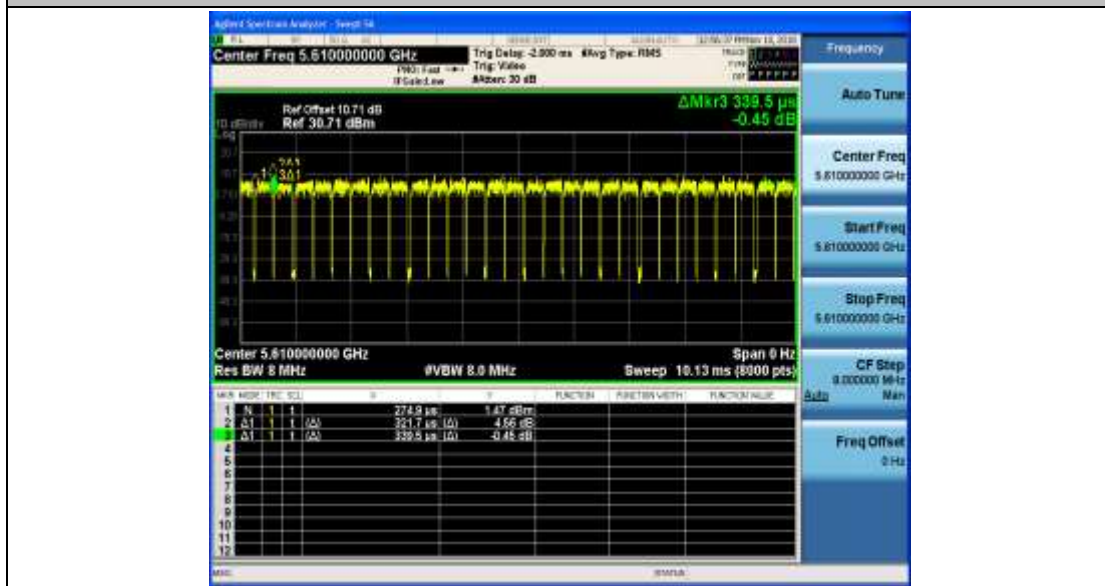
11AC80MIMO_Ant2_5530



11AC80MIMO_Ant1_5610



11AC80MIMO_Ant2_5610





Appendix D: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	5180	13.78	23.98	PASS
	Ant2	5180	13.41	23.98	PASS
	Ant1	5200	14.25	23.98	PASS
	Ant2	5200	13.79	23.98	PASS
	Ant1	5240	14.66	23.98	PASS
	Ant2	5240	14.05	23.98	PASS
	Ant1	5260	14.65	23.87	PASS
	Ant2	5260	14.18	23.97	PASS
	Ant1	5280	14.73	23.90	PASS
	Ant2	5280	14.23	23.98	PASS
	Ant1	5320	15.44	23.91	PASS
	Ant2	5320	14.77	23.97	PASS
	Ant1	5500	12.46	23.90	PASS
	Ant2	5500	11.49	23.95	PASS
	Ant1	5580	14.95	23.89	PASS
	Ant2	5580	14.13	23.98	PASS
	Ant1	5700	13.31	23.91	PASS
	Ant2	5700	12.30	23.96	PASS
11N20SISO	Ant1	5180	11.91	23.98	PASS
	Ant2	5180	11.20	23.98	PASS
	Ant1	5200	11.93	23.98	PASS
	Ant2	5200	11.34	23.98	PASS
	Ant1	5240	12.30	23.98	PASS
	Ant2	5240	11.52	23.98	PASS
	Ant1	5260	12.38	23.98	PASS
	Ant2	5260	11.92	23.98	PASS
	Ant1	5280	12.30	23.98	PASS
	Ant2	5280	11.93	23.98	PASS
	Ant1	5320	13.00	23.98	PASS
	Ant2	5320	12.38	23.98	PASS
	Ant1	5500	12.66	23.98	PASS



	Ant2	5500	11.47	23.98	PASS
	Ant1	5580	12.60	23.98	PASS
	Ant2	5580	11.78	23.98	PASS
	Ant1	5700	11.08	23.98	PASS
	Ant2	5700	10.33	23.98	PASS
11N40SISO	Ant1	5190	9.93	23.98	PASS
	Ant2	5190	9.98	23.98	PASS
	Ant1	5230	11.61	23.98	PASS
	Ant2	5230	11.57	23.98	PASS
	Ant1	5270	11.84	23.98	PASS
	Ant2	5270	11.70	23.98	PASS
	Ant1	5310	9.38	23.98	PASS
	Ant2	5310	8.98	23.98	PASS
	Ant1	5510	8.69	23.98	PASS
	Ant2	5510	8.24	23.98	PASS
	Ant1	5550	11.81	23.98	PASS
	Ant2	5550	11.38	23.98	PASS
	Ant1	5670	11.68	23.98	PASS
	Ant2	5670	11.20	23.98	PASS
11A-CDD	Ant1	5180	11.71	23.98	PASS
	Ant2	5180	12.40	23.98	PASS
	total	5180	15.1	23.98	PASS
	Ant1	5200	12.12	23.98	PASS
	Ant2	5200	12.57	23.98	PASS
	total	5200	15.4	23.98	PASS
	Ant1	5240	12.39	23.98	PASS
	Ant2	5240	12.95	23.98	PASS
	total	5240	15.7	23.98	PASS
	Ant1	5260	13.32	23.94	PASS
	Ant2	5260	12.58	23.94	PASS
	total	5260	16.0	23.94	PASS
	Ant1	5280	13.36	23.96	PASS
	Ant2	5280	12.76	23.91	PASS
	total	5280	16.1	23.91	PASS
	Ant1	5320	14.00	23.92	PASS
	Ant2	5320	13.47	23.93	PASS
	total	5320	16.8	23.93	PASS



	Ant1	5500	11.02	23.93	PASS
	Ant2	5500	10.35	23.92	PASS
	total	5500	13.7	23.92	PASS
	Ant1	5580	13.25	23.90	PASS
	Ant2	5580	13.07	23.93	PASS
	total	5580	16.2	23.93	PASS
	Ant1	5700	12.21	23.92	PASS
	Ant2	5700	11.38	23.91	PASS
	total	5700	14.8	23.91	PASS
11N20MIMO	Ant1	5180	9.87	23.98	PASS
	Ant2	5180	10.65	23.98	PASS
	total	5180	13.3	23.98	PASS
	Ant1	5200	9.80	23.98	PASS
	Ant2	5200	10.45	23.98	PASS
	total	5200	13.1	23.98	PASS
	Ant1	5240	10.42	23.98	PASS
	Ant2	5240	10.67	23.98	PASS
	total	5240	13.6	23.98	PASS
	Ant1	5260	10.48	23.98	PASS
	Ant2	5260	10.97	23.98	PASS
	total	5260	13.7	23.98	PASS
	Ant1	5280	11.02	23.98	PASS
	Ant2	5280	10.03	23.98	PASS
	total	5280	13.6	23.98	PASS
	Ant1	5320	11.35	23.98	PASS
	Ant2	5320	10.28	23.98	PASS
	total	5320	13.9	23.98	PASS
	Ant1	5500	11.15	23.98	PASS
	Ant2	5500	9.88	23.98	PASS
	total	5500	13.6	23.98	PASS
	Ant1	5580	10.88	23.98	PASS
	Ant2	5580	10.29	23.98	PASS
	total	5580	13.6	23.98	PASS
Ant1	5700	9.48	23.98	PASS	
Ant2	5700	8.69	23.98	PASS	
total	5700	12.1	23.98	PASS	
11N40MIMO	Ant1	5190	8.86	23.98	PASS



	Ant2	5190	7.94	23.98	PASS
	total	5190	11.4	23.98	PASS
	Ant1	5230	10.12	23.98	PASS
	Ant2	5230	9.67	23.98	PASS
	total	5230	12.9	23.98	PASS
	Ant1	5270	10.58	23.98	PASS
	Ant2	5270	10.20	23.98	PASS
	total	5270	13.4	23.98	PASS
	Ant1	5310	7.86	23.98	PASS
	Ant2	5310	6.98	23.98	PASS
	total	5310	10.5	23.98	PASS
	Ant1	5510	7.17	23.98	PASS
	Ant2	5510	6.22	23.98	PASS
	total	5510	9.7	23.98	PASS
	Ant1	5550	10.77	23.98	PASS
	Ant2	5550	9.91	23.98	PASS
	total	5550	13.4	23.98	PASS
	Ant1	5670	10.26	23.98	PASS
	Ant2	5670	9.38	23.98	PASS
	total	5670	12.9	23.98	PASS
11AC20SISO	Ant1	5180	11.85	23.98	PASS
	Ant2	5180	11.24	23.98	PASS
	Ant1	5200	11.94	23.98	PASS
	Ant2	5200	11.24	23.98	PASS
	Ant1	5240	11.98	23.98	PASS
	Ant2	5240	11.50	23.98	PASS
	Ant1	5260	11.95	23.98	PASS
	Ant2	5260	11.79	23.98	PASS
	Ant1	5280	12.02	23.98	PASS
	Ant2	5280	11.80	23.98	PASS
	Ant1	5320	12.87	23.98	PASS
	Ant2	5320	12.41	23.98	PASS
	Ant1	5500	12.13	23.98	PASS
	Ant2	5500	11.61	23.98	PASS
	Ant1	5580	12.08	23.98	PASS
	Ant2	5580	11.76	23.98	PASS
	Ant1	5700	10.84	23.98	PASS



	Ant2	5700	10.35	23.98	PASS
11AC40SISO	Ant1	5190	10.28	23.98	PASS
	Ant2	5190	10.01	23.98	PASS
	Ant1	5230	11.61	23.98	PASS
	Ant2	5230	11.64	23.98	PASS
	Ant1	5270	11.75	23.98	PASS
	Ant2	5270	11.65	23.98	PASS
	Ant1	5310	9.50	23.98	PASS
	Ant2	5310	8.95	23.98	PASS
	Ant1	5510	8.65	23.98	PASS
	Ant2	5510	8.22	23.98	PASS
	Ant1	5550	11.90	23.98	PASS
	Ant2	5550	11.42	23.98	PASS
	Ant1	5670	11.77	23.98	PASS
	Ant2	5670	11.35	23.98	PASS
	11AC80SISO	Ant1	5210	8.71	23.98
Ant2		5210	8.31	23.98	PASS
Ant1		5290	7.74	23.98	PASS
Ant2		5290	7.24	23.98	PASS
Ant1		5530	4.83	23.98	PASS
Ant2		5530	4.47	23.98	PASS
Ant1		5610	12.14	23.98	PASS
Ant2		5610	11.74	23.98	PASS
11AC20MIMO	Ant1	5180	10.17	23.98	PASS
	Ant2	5180	9.76	23.98	PASS
	total	5180	13.0	23.98	PASS
	Ant1	5200	10.30	23.98	PASS
	Ant2	5200	9.65	23.98	PASS
	total	5200	13.0	23.98	PASS
	Ant1	5240	10.72	23.98	PASS
	Ant2	5240	9.99	23.98	PASS
	total	5240	13.4	23.98	PASS
	Ant1	5260	10.89	23.98	PASS
	Ant2	5260	10.45	23.98	PASS
	total	5260	13.7	23.98	PASS
	Ant1	5280	11.04	23.98	PASS
	Ant2	5280	10.26	23.98	PASS



	total	5280	13.7	23.98	PASS
	Ant1	5320	11.44	23.98	PASS
	Ant2	5320	10.73	23.98	PASS
	total	5320	14.1	23.98	PASS
	Ant1	5500	11.02	23.98	PASS
	Ant2	5500	9.91	23.98	PASS
	total	5500	13.5	23.98	PASS
	Ant1	5580	11.28	23.98	PASS
	Ant2	5580	10.51	23.98	PASS
	total	5580	13.9	23.98	PASS
	Ant1	5700	9.83	23.98	PASS
	Ant2	5700	8.80	23.98	PASS
	total	5700	12.4	23.98	PASS
11AC40MIMO	Ant1	5190	8.70	23.98	PASS
	Ant2	5190	8.19	23.98	PASS
	total	5190	11.5	23.98	PASS
	Ant1	5230	10.43	23.98	PASS
	Ant2	5230	9.90	23.98	PASS
	total	5230	13.2	23.98	PASS
	Ant1	5270	10.66	23.98	PASS
	Ant2	5270	10.34	23.98	PASS
	total	5270	13.5	23.98	PASS
	Ant1	5310	7.92	23.98	PASS
	Ant2	5310	6.89	23.98	PASS
	total	5310	10.4	23.98	PASS
	Ant1	5510	7.23	23.98	PASS
	Ant2	5510	6.19	23.98	PASS
	total	5510	9.8	23.98	PASS
	Ant1	5550	10.55	23.98	PASS
	Ant2	5550	10.04	23.98	PASS
	total	5550	13.3	23.98	PASS
	Ant1	5670	10.56	23.98	PASS
	Ant2	5670	9.88	23.98	PASS
total	5670	13.2	23.98	PASS	
11AC80MIMO	Ant1	5210	7.61	23.98	PASS
	Ant2	5210	6.57	23.98	PASS
	total	5210	10.1	23.98	PASS

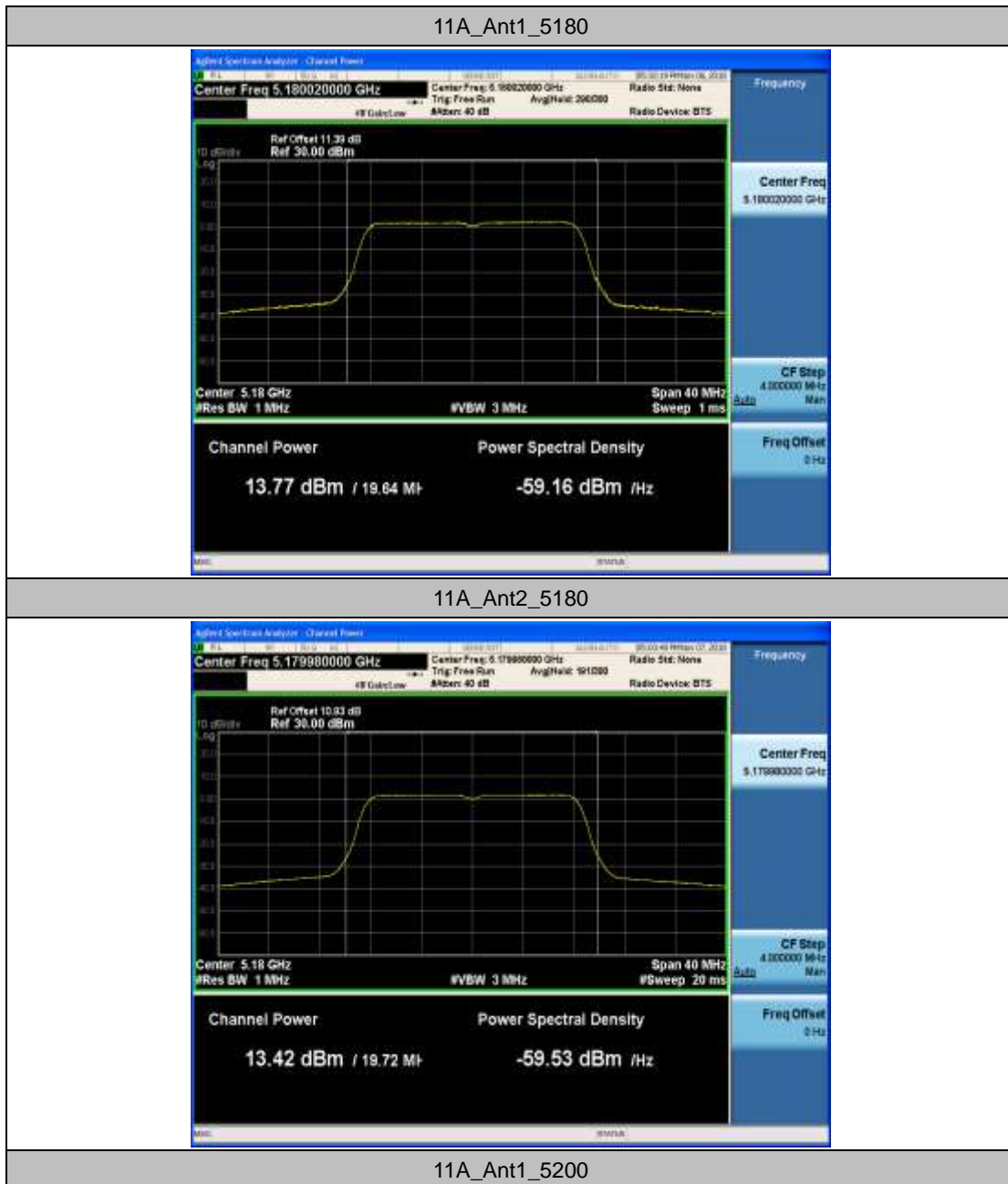


	Ant1	5290	6.53	23.98	PASS
	Ant2	5290	5.75	23.98	PASS
	total	5290	9.2	23.98	PASS
	Ant1	5530	2.71	23.98	PASS
	Ant2	5530	2.05	23.98	PASS
	total	5530	5.4	23.98	PASS
	Ant1	5610	10.69	23.98	PASS
	Ant2	5610	10.18	23.98	PASS
	total	5610	13.5	23.98	PASS

Note: The Duty Cycle Factor is compensated in the graph.



Test Graphs





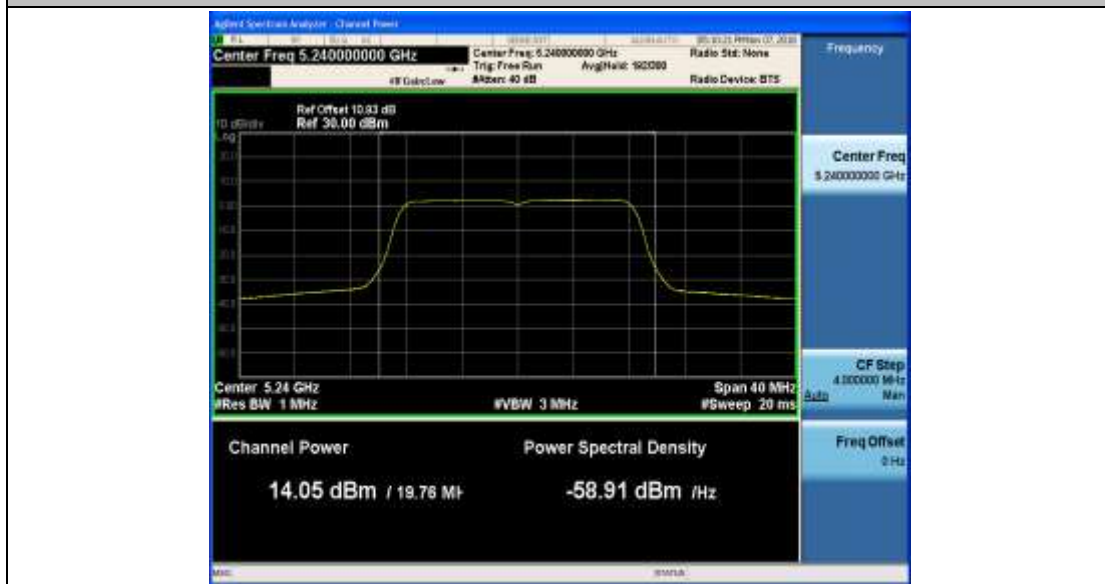
11A_Ant2_5200



11A_Ant1_5240



11A_Ant2_5240



11A_Ant1_5260



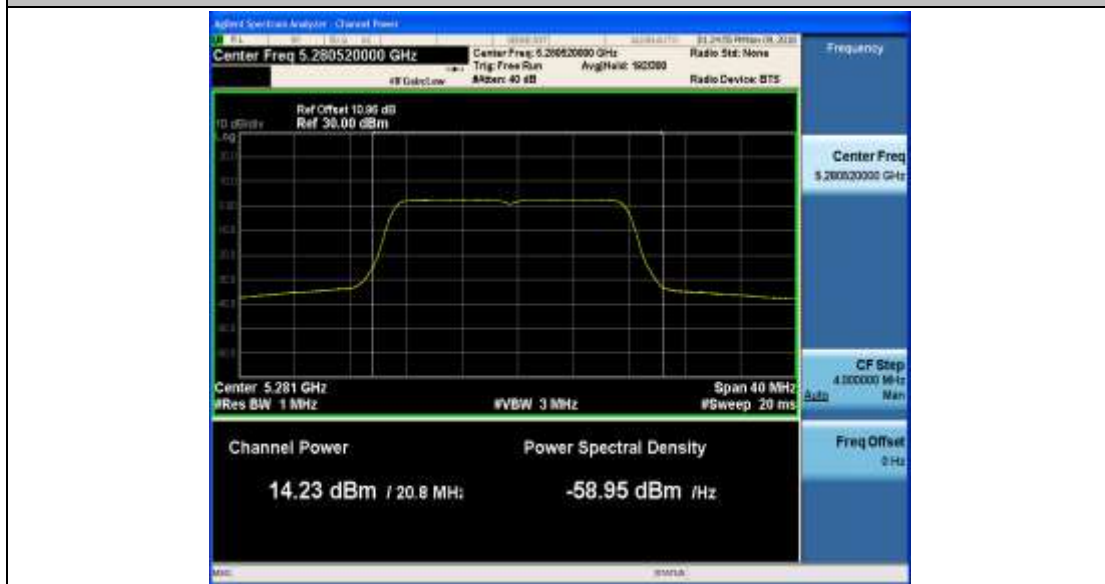
11A_Ant2_5260



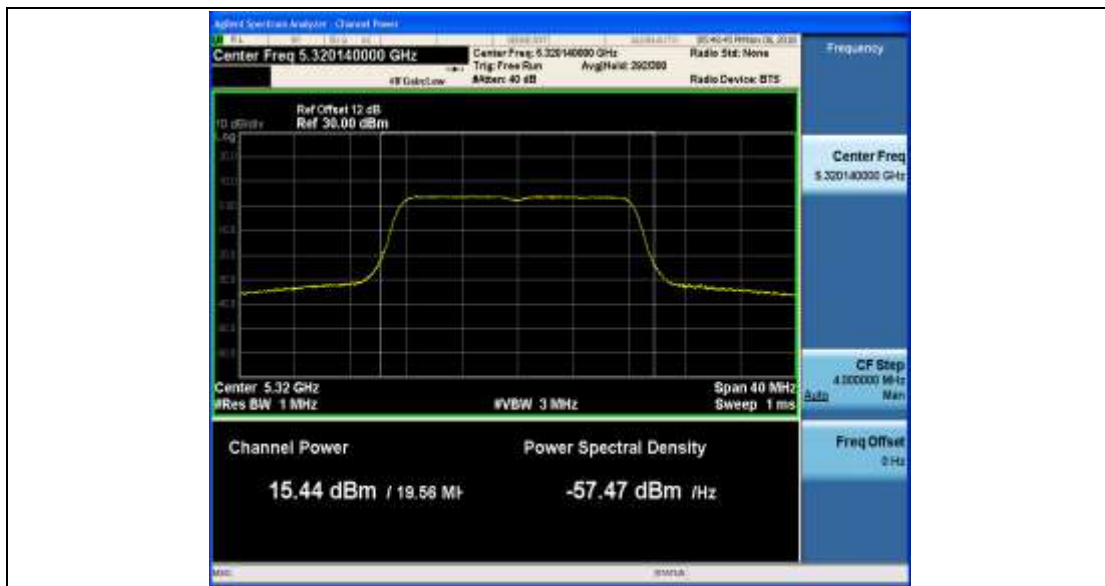
11A_Ant1_5280



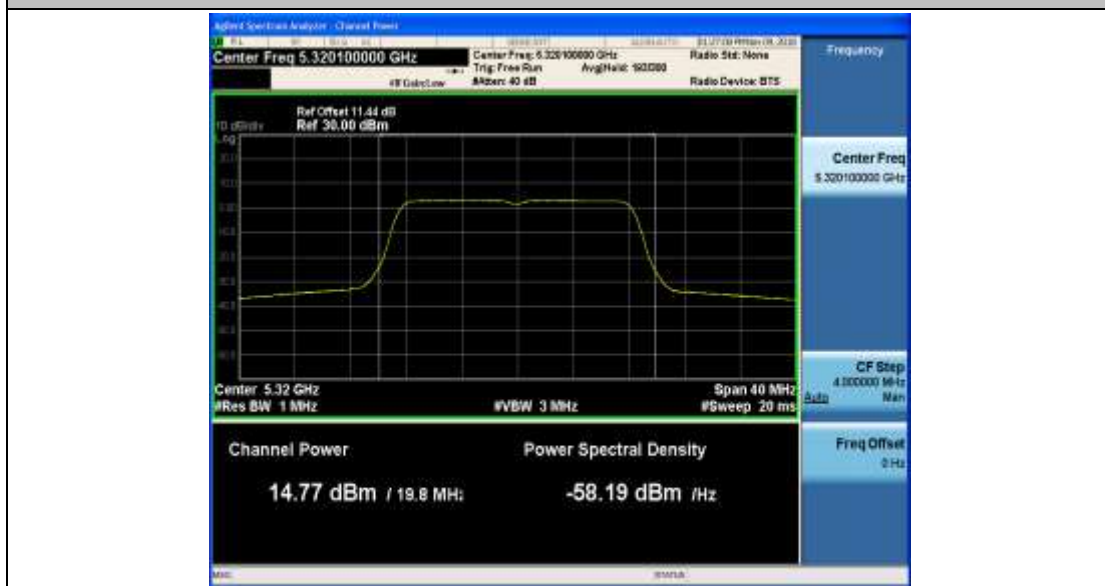
11A_Ant2_5280



11A_Ant1_5320



11A_Ant2_5320



11A_Ant1_5500



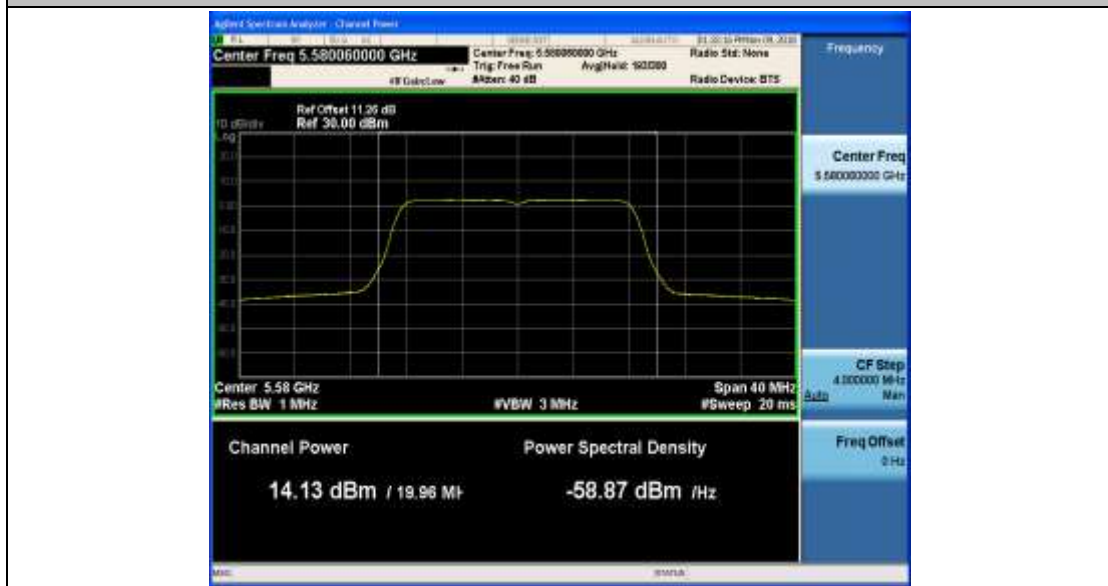
11A_Ant2_5500



11A_Ant1_5580



11A_Ant2_5580



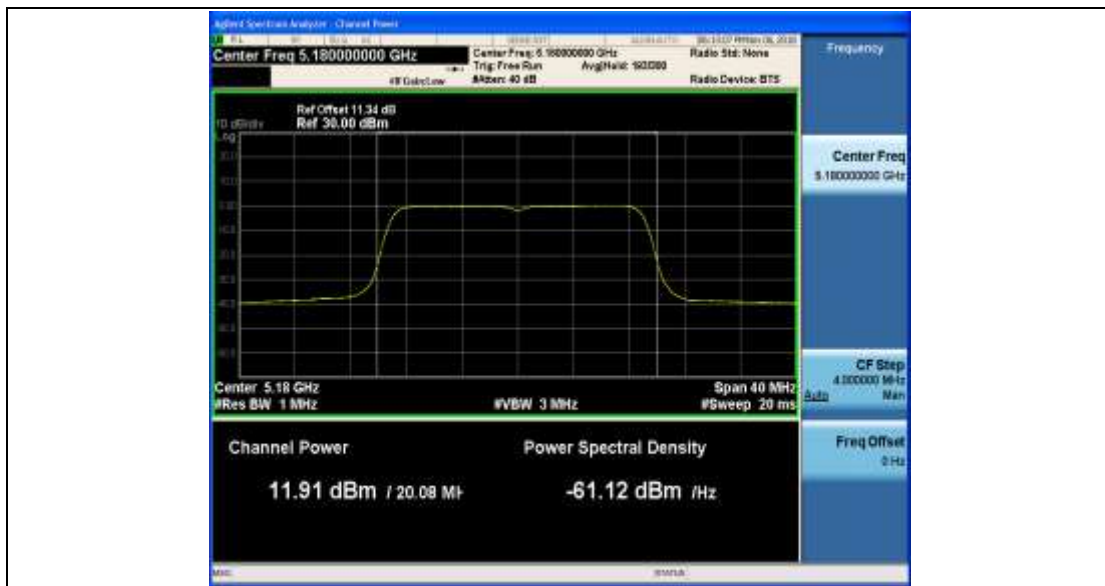
11A_Ant1_5700



11A_Ant2_5700



11N20SISO_Ant1_5180



11N20SISO_Ant2_5180



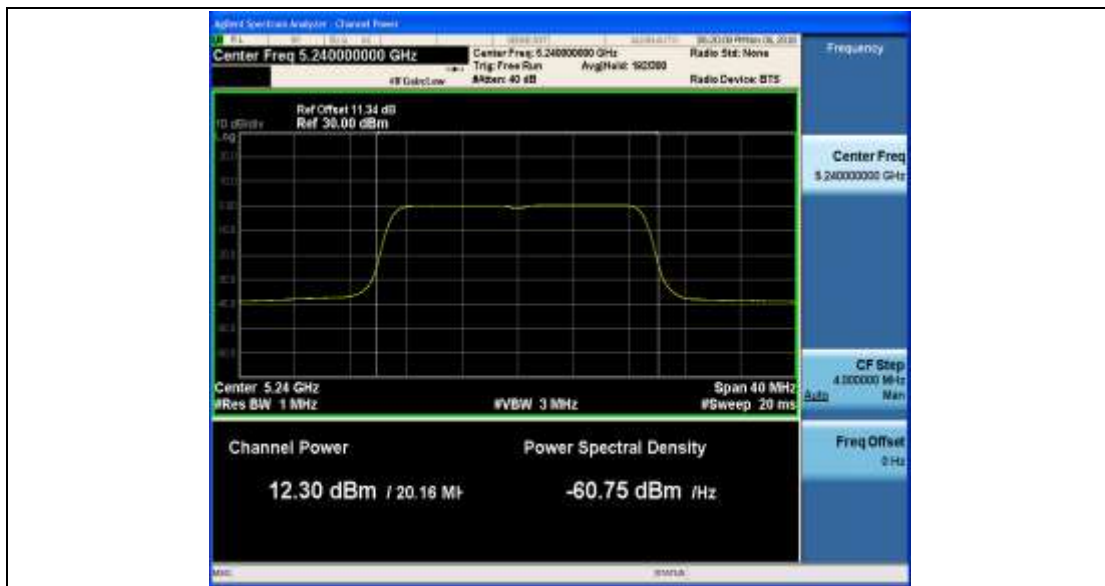
11N20SISO_Ant1_5200



11N20SISO_Ant2_5200



11N20SISO_Ant1_5240



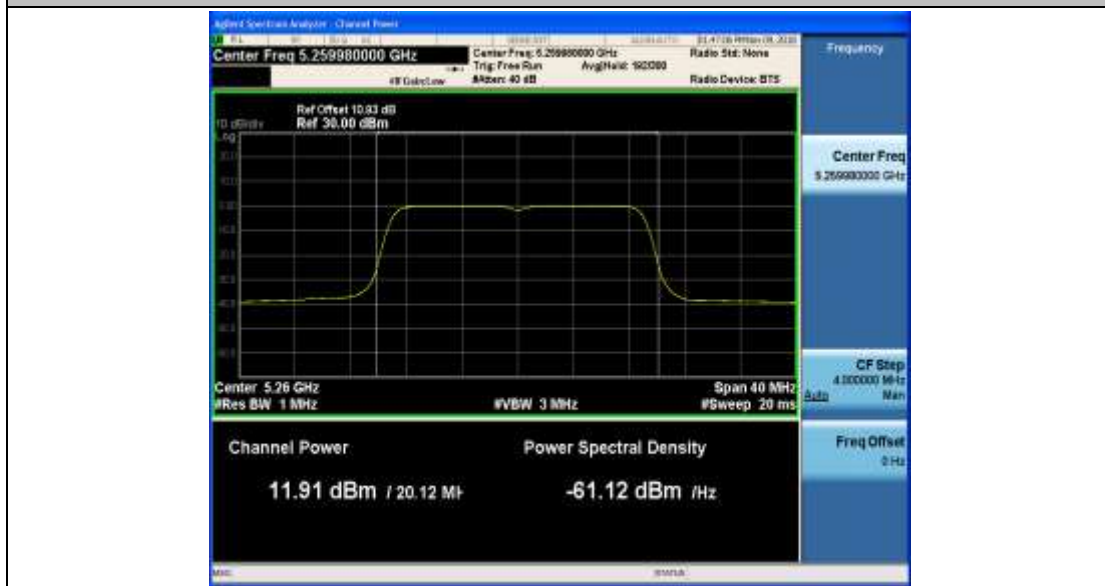
11N20SISO_Ant2_5240



11N20SISO_Ant1_5260



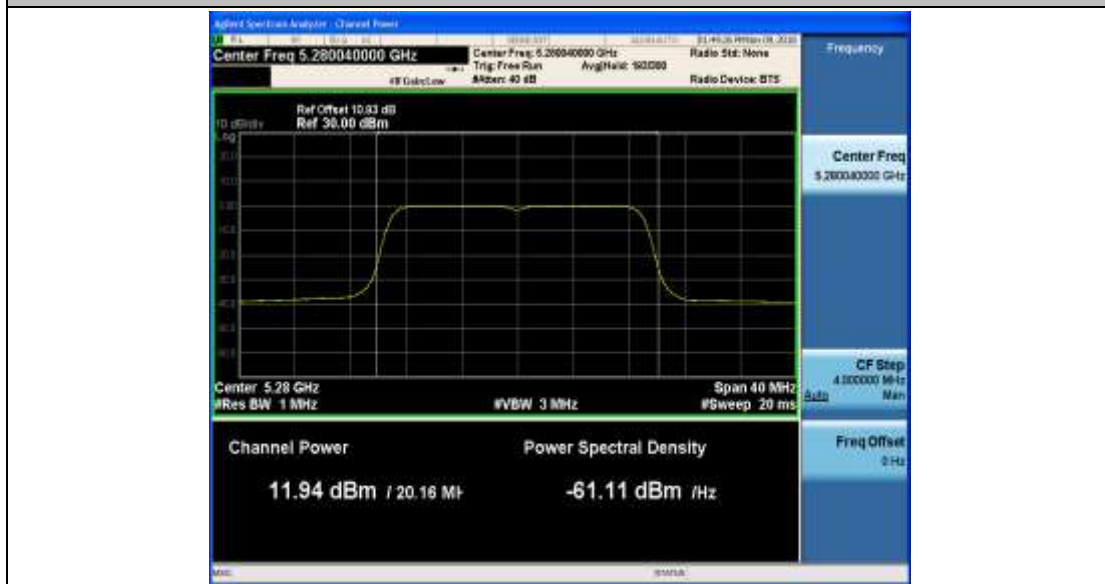
11N20SISO_Ant2_5260



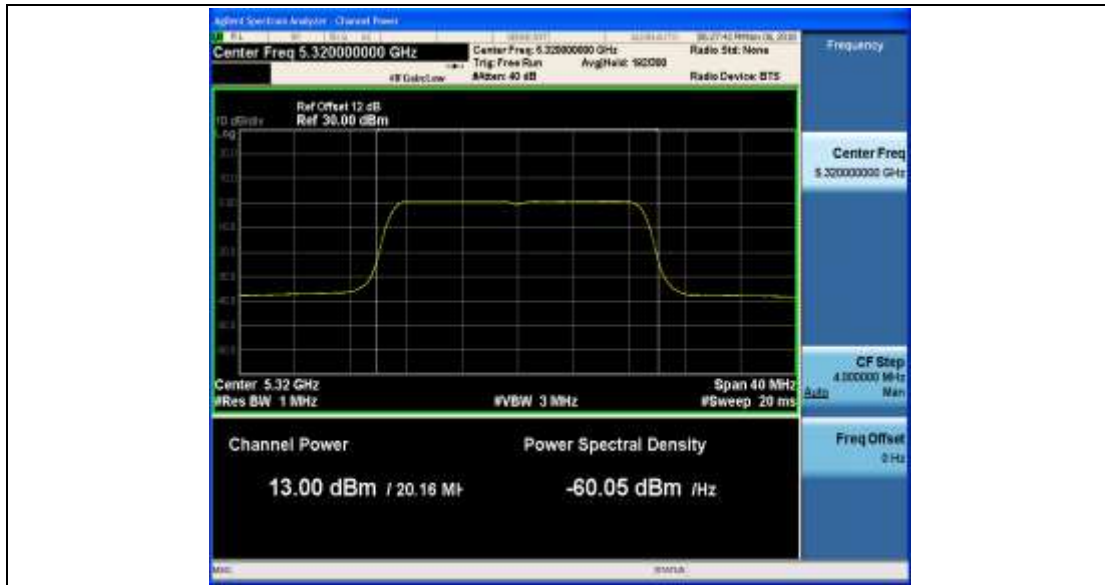
11N20SISO_Ant1_5280



11N20SISO_Ant2_5280



11N20SISO_Ant1_5320



11N20SISO_Ant2_5320



11N20SISO_Ant1_5500



11N20SISO_Ant2_5500



11N20SISO_Ant1_5580



11N20SISO_Ant2_5580



11N20SISO_Ant1_5700



11N20SISO_Ant2_5700



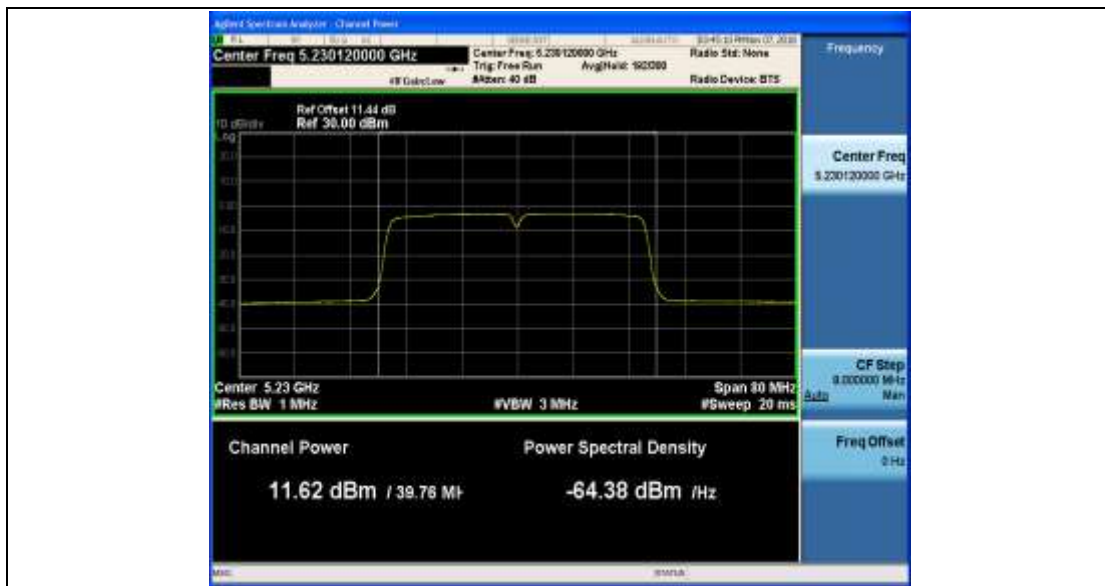
11N40SISO_Ant1_5190



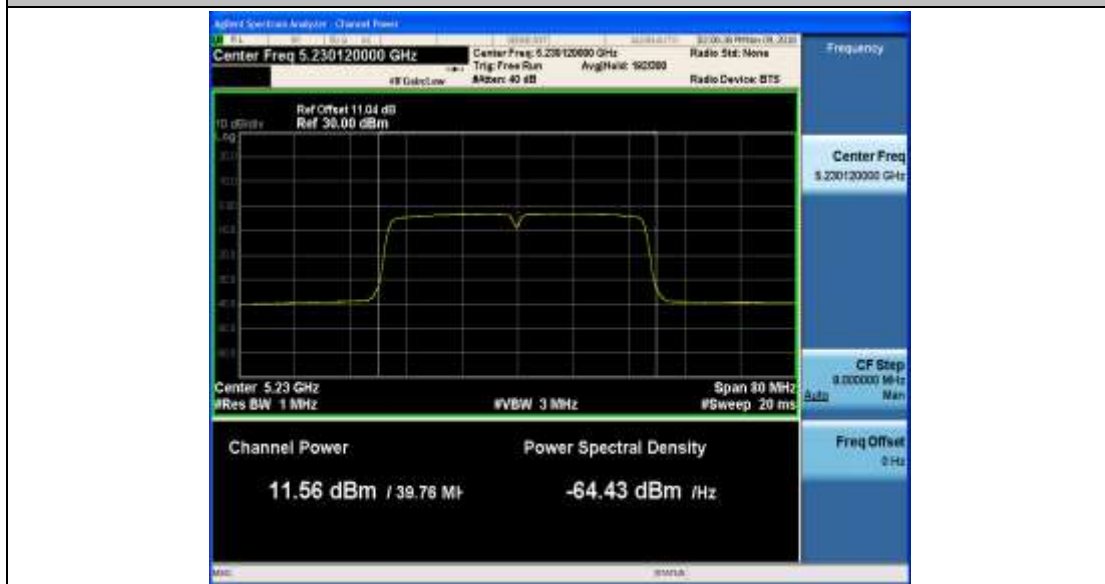
11N40SISO_Ant2_5190



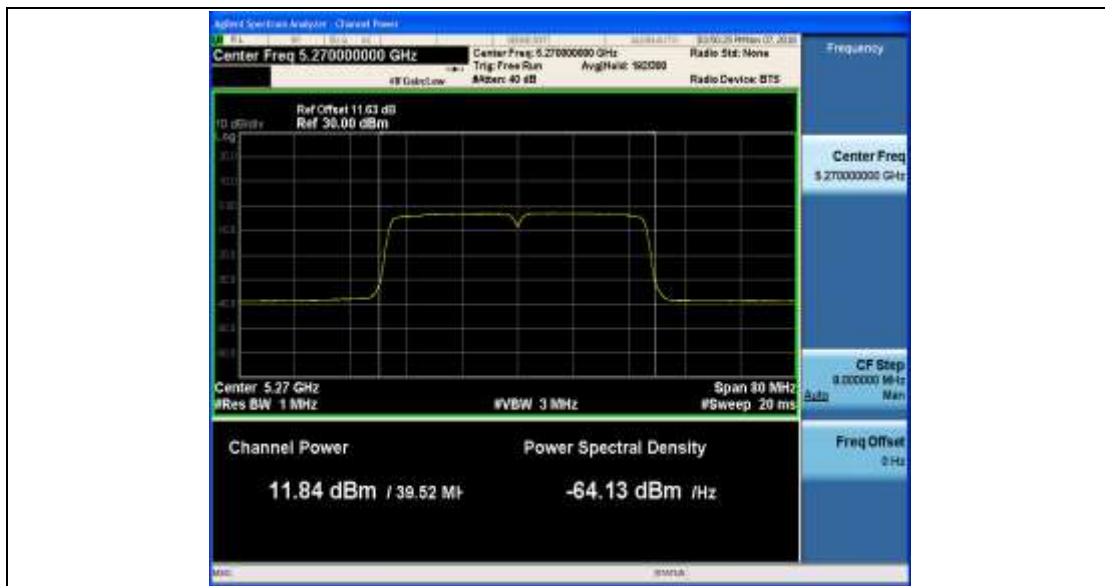
11N40SISO_Ant1_5230



11N40SISO_Ant2_5230



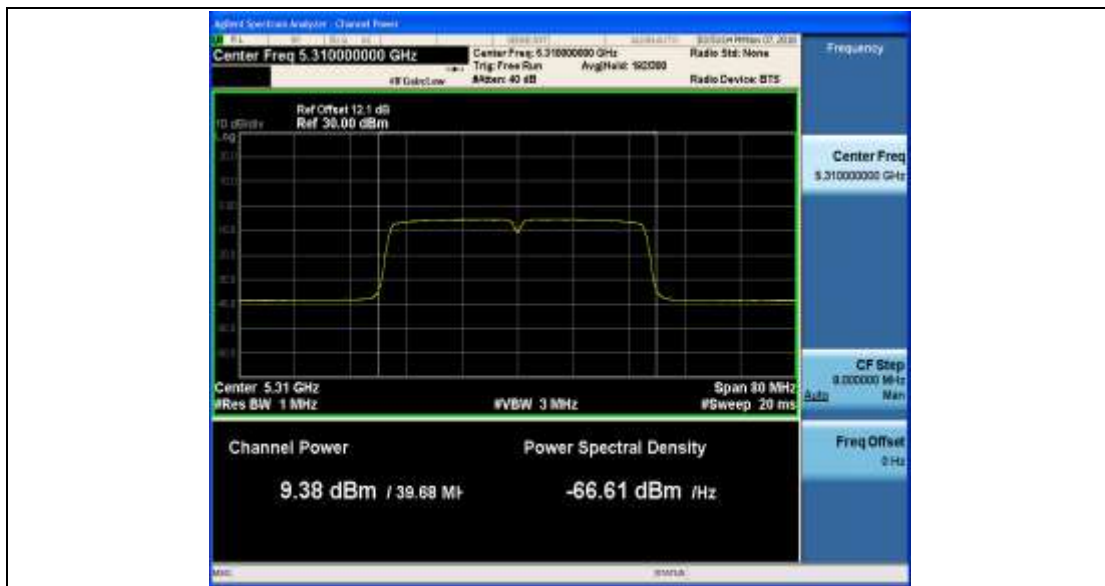
11N40SISO_Ant1_5270



11N40SISO_Ant2_5270



11N40SISO_Ant1_5310



11N40SISO_Ant2_5310



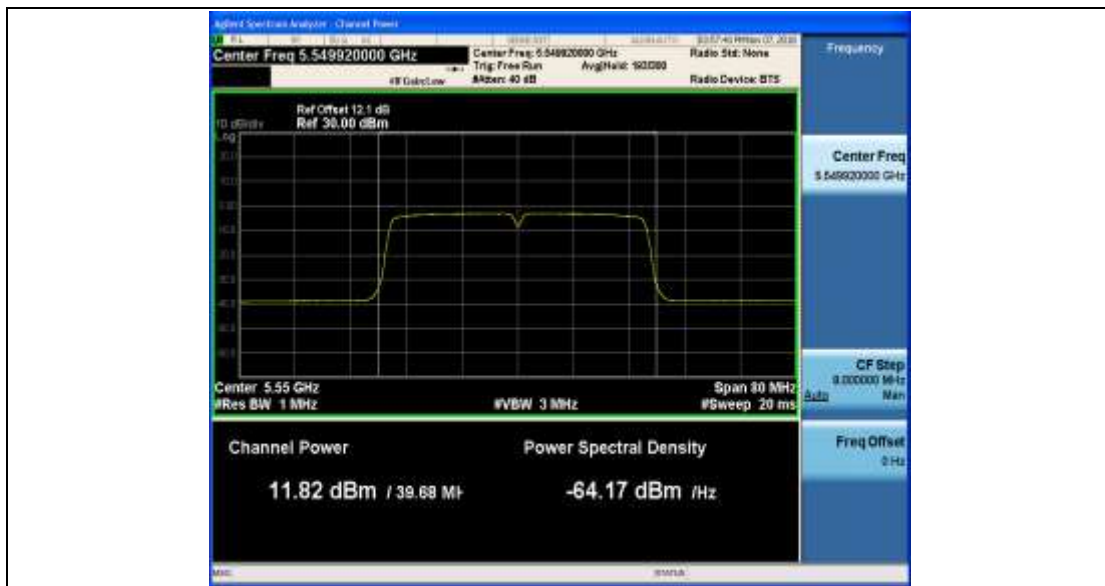
11N40SISO_Ant1_5510



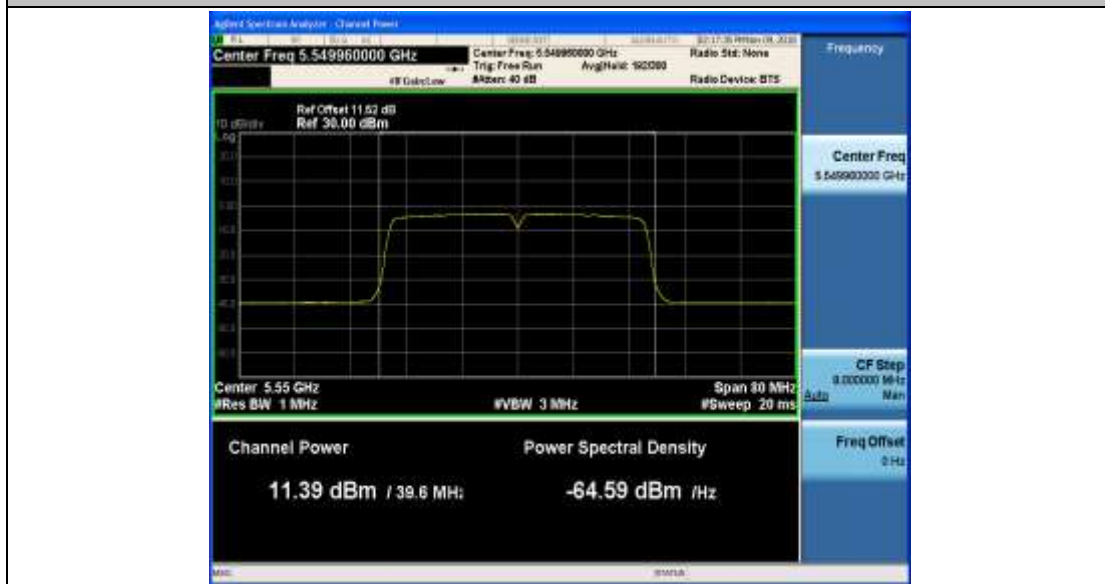
11N40SISO_Ant2_5510



11N40SISO_Ant1_5550



11N40SISO_Ant2_5550



11N40SISO_Ant1_5670



11N40SISO_Ant2_5670



11A-CDD_Ant1_5180



11A-CDD_Ant2_5180



11A-CDD_Ant1_5200



11A-CDD_Ant2_5200



11A-CDD_Ant1_5240



11A-CDD_Ant2_5240



11A-CDD_Ant1_5260



11A-CDD_Ant2_5260



11A-CDD_Ant1_5280



11A-CDD_Ant2_5280



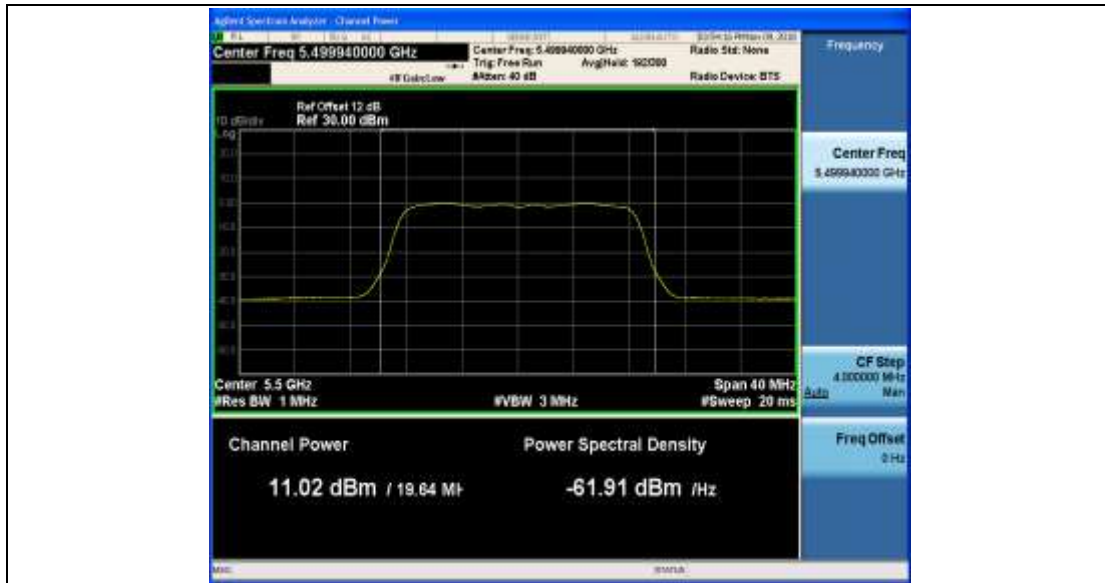
11A-CDD_Ant1_5320



11A-CDD_Ant2_5320



11A-CDD_Ant1_5500



11A-CDD_Ant2_5500



11A-CDD_Ant1_5580



11A-CDD_Ant2_5580



11A-CDD_Ant1_5700



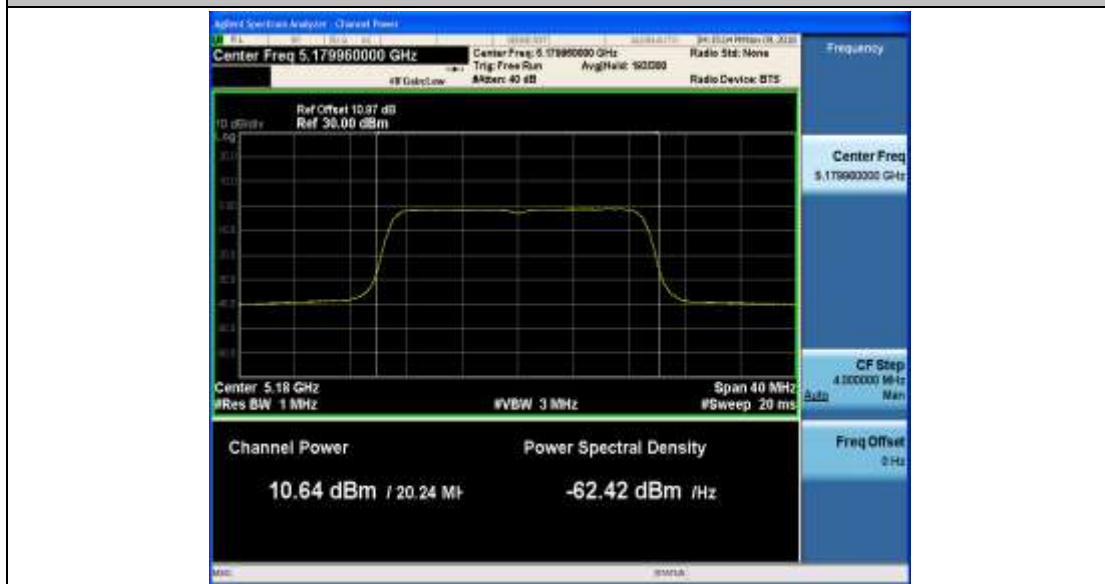
11A-CDD_Ant2_5700



11N20MIMO_Ant1_5180



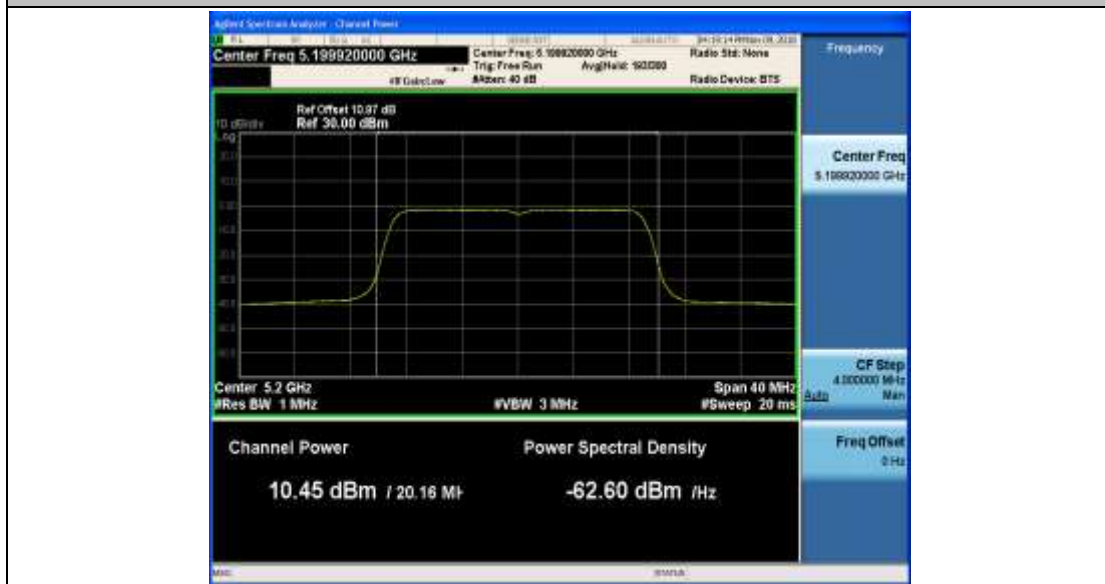
11N20MIMO_Ant2_5180



11N20MIMO_Ant1_5200



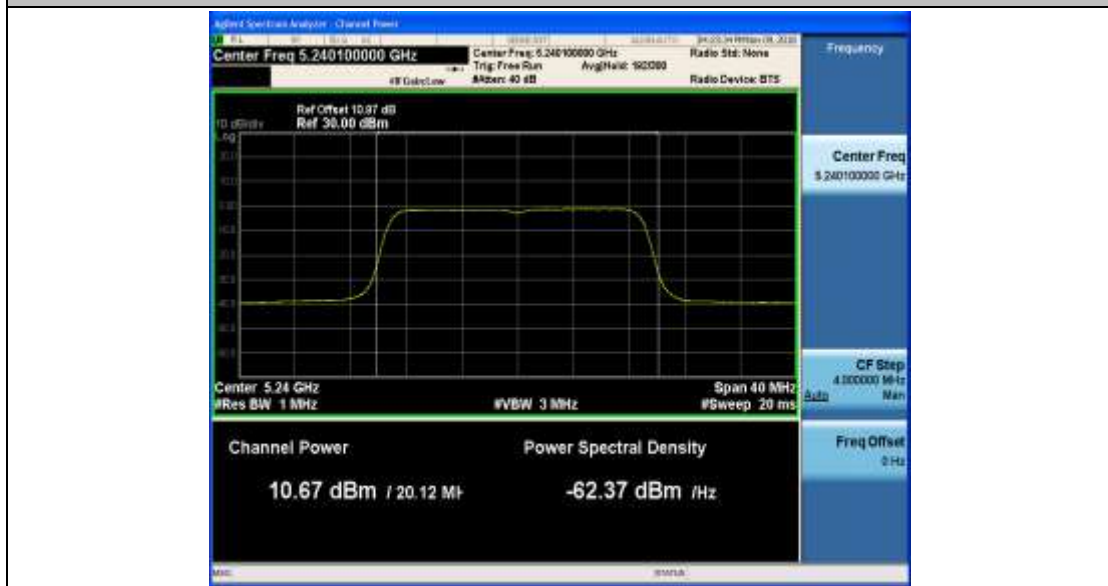
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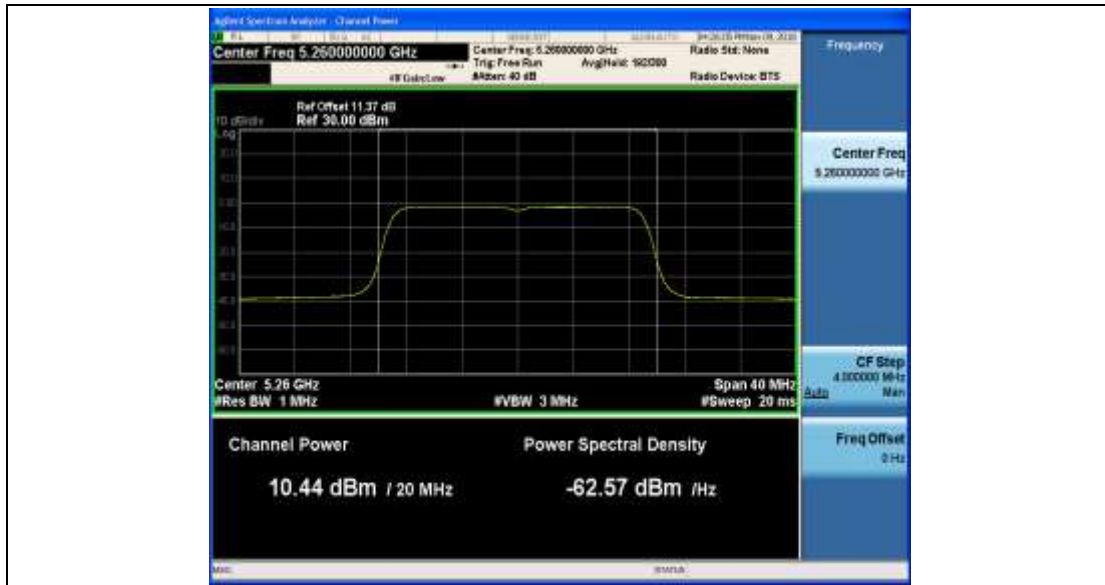
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11N20MIMO_Ant2_5240



11N20MIMO_Ant1_5260



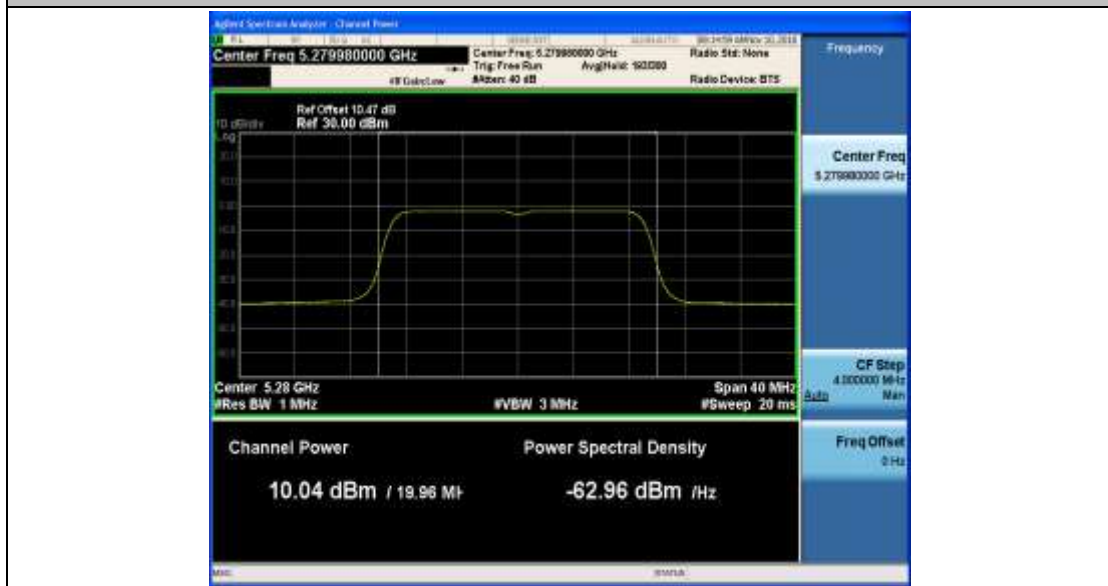
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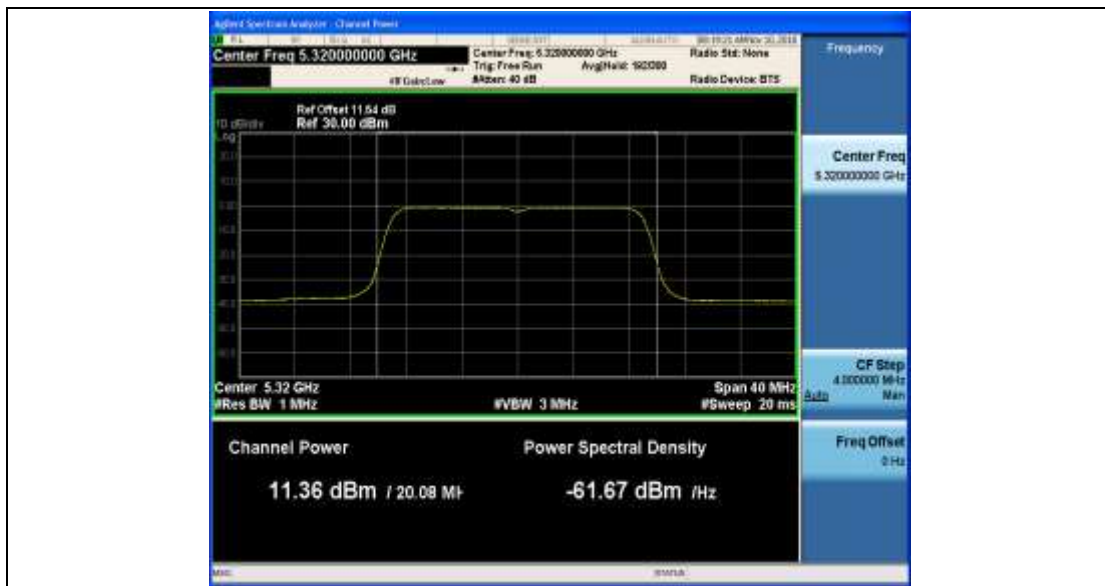
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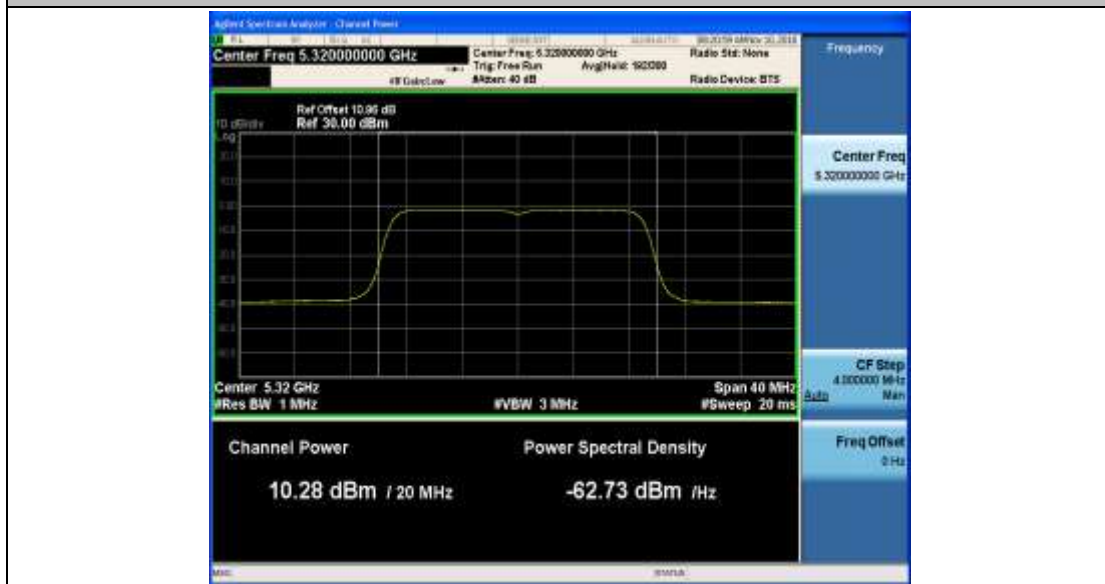
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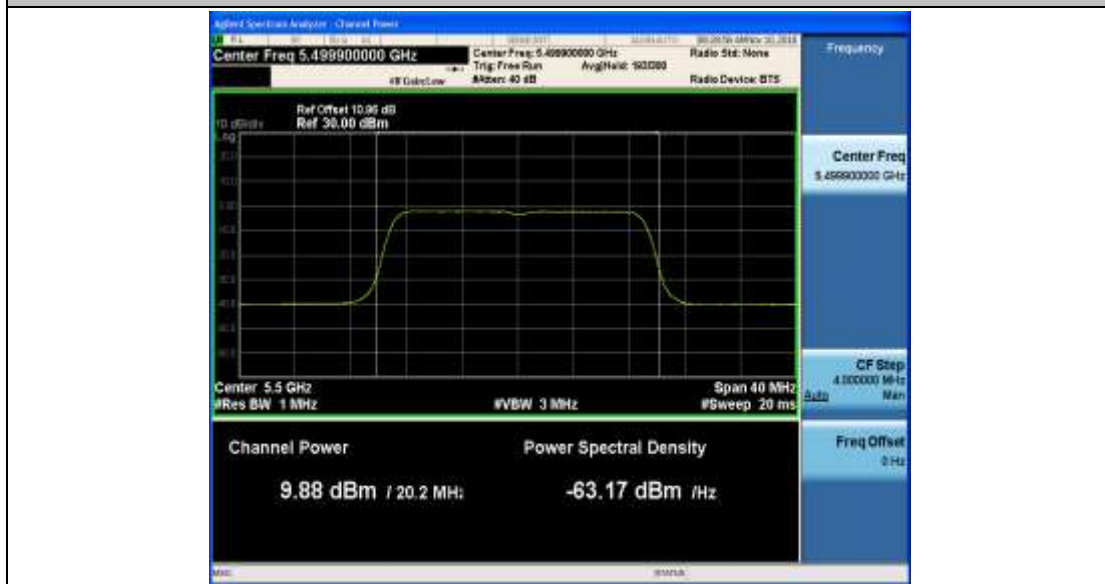
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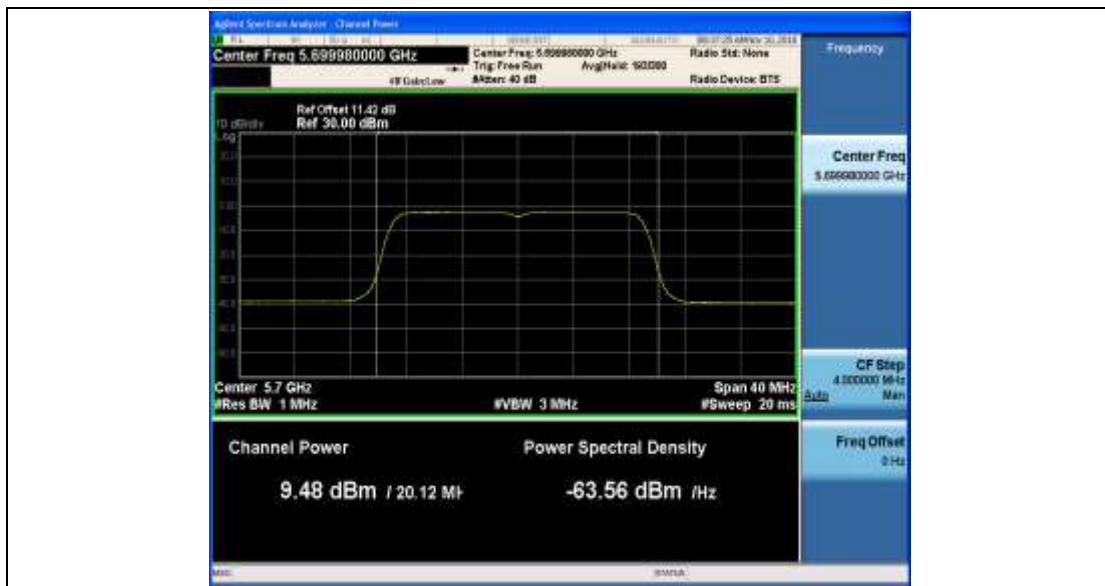
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11N20MIMO_Ant2_5580



11N20MIMO_Ant1_5700



11N20MIMO_Ant2_5700



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11N40MIMO_Ant2_5190



11N40MIMO_Ant1_5230



11N40MIMO_Ant2_5230



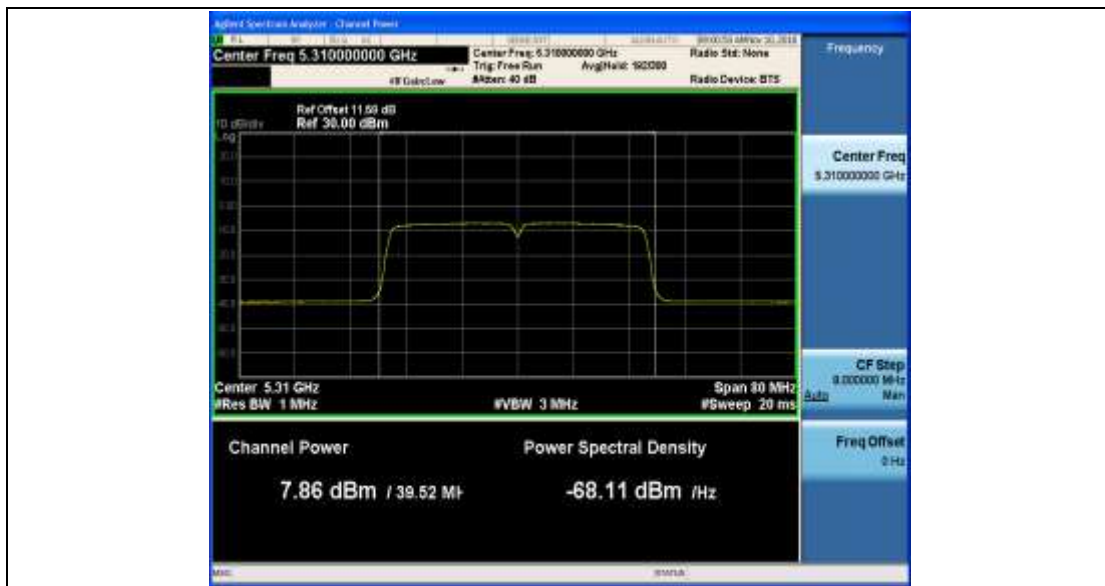
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11N40MIMO_Ant2_5550



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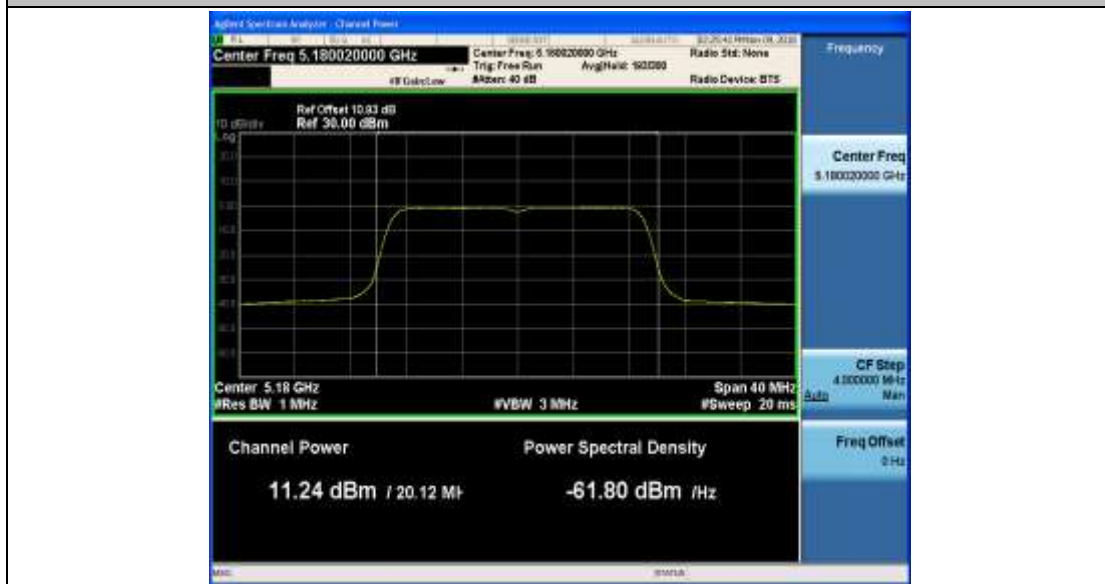
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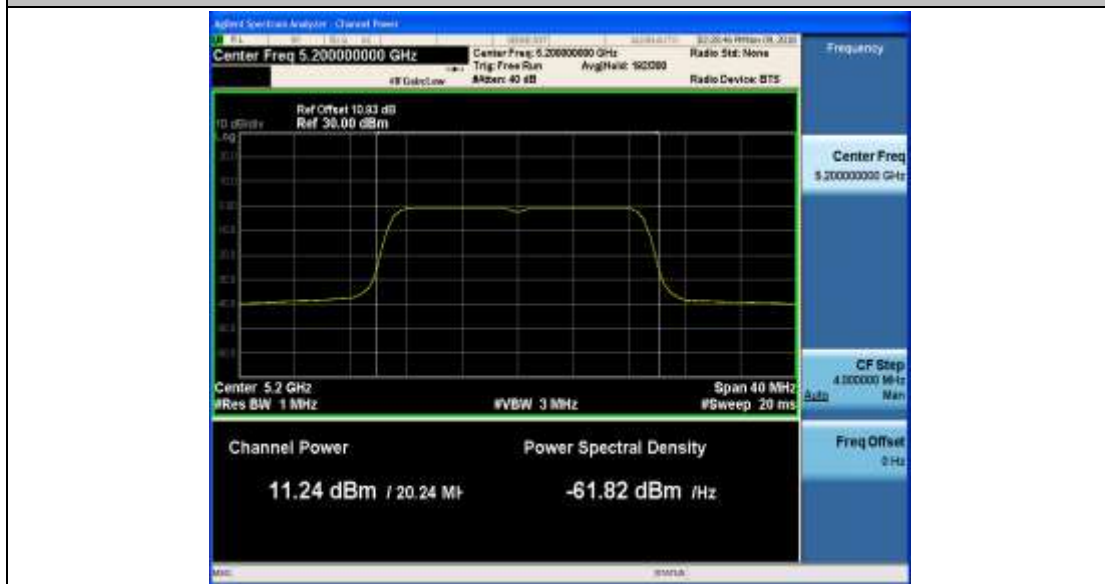
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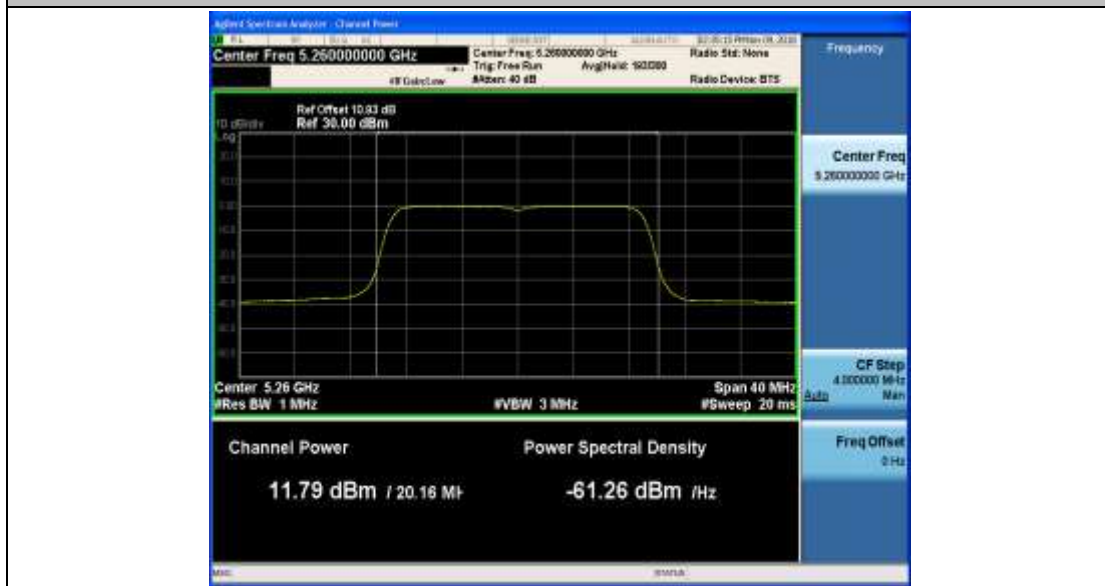
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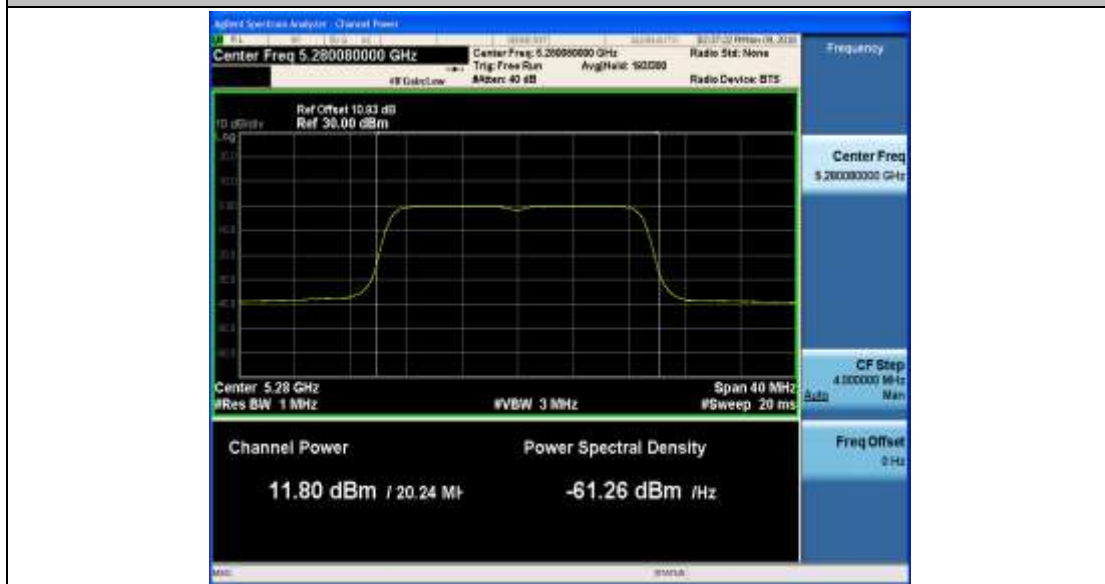
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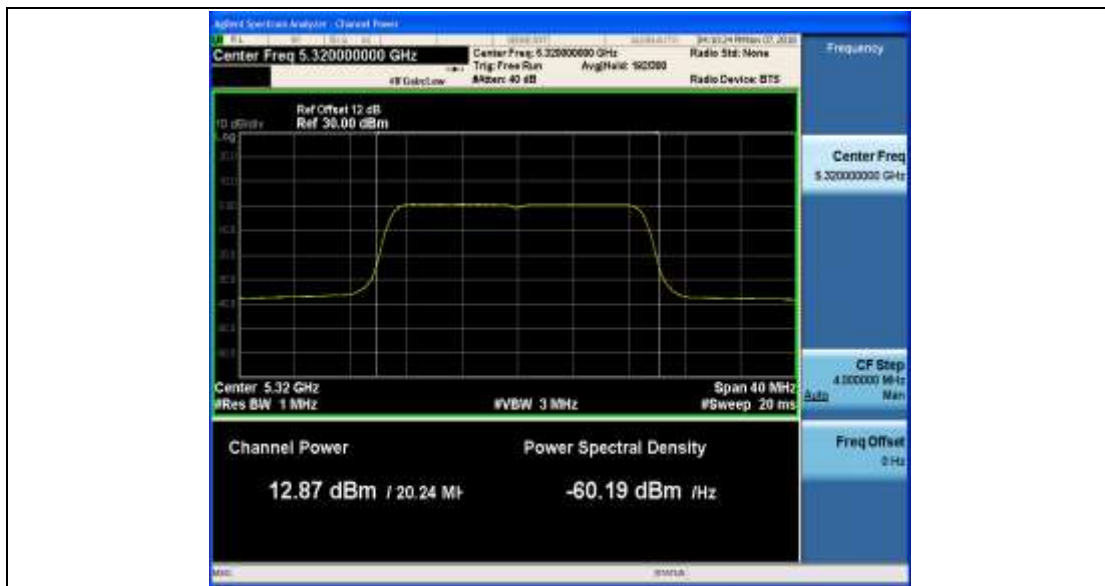
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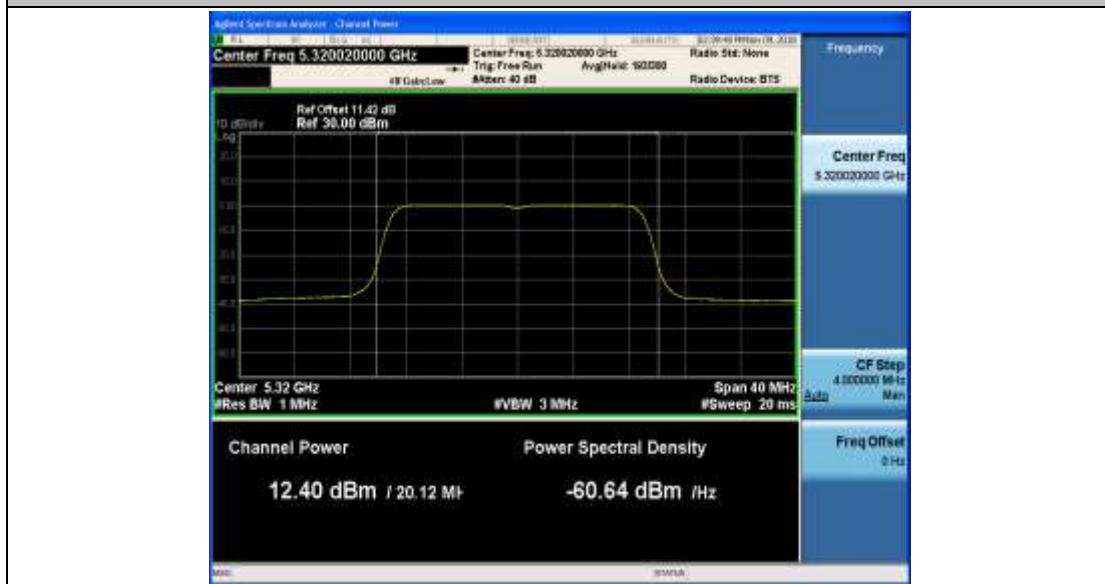
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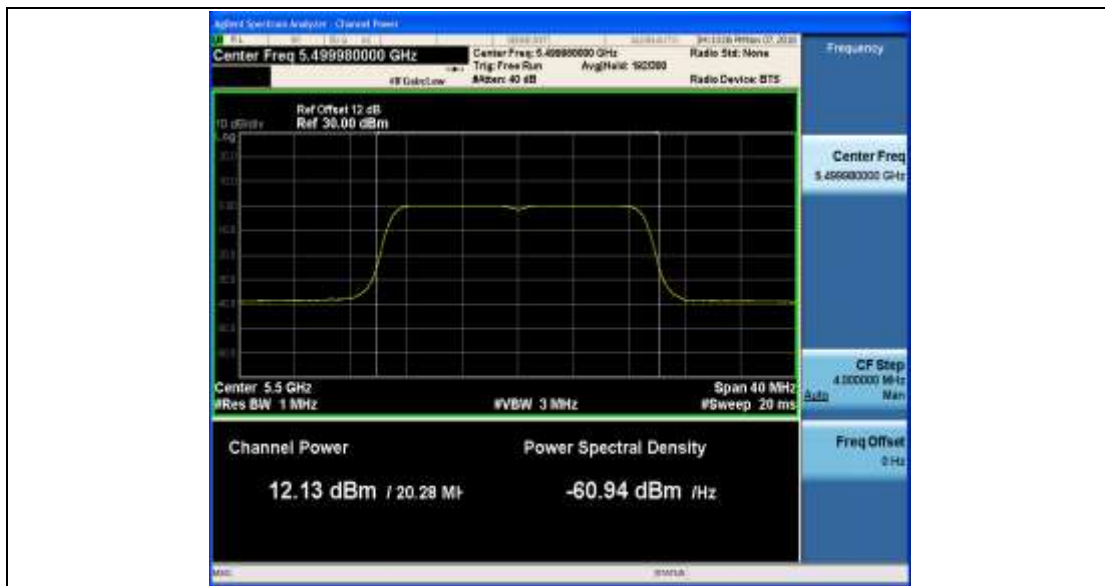
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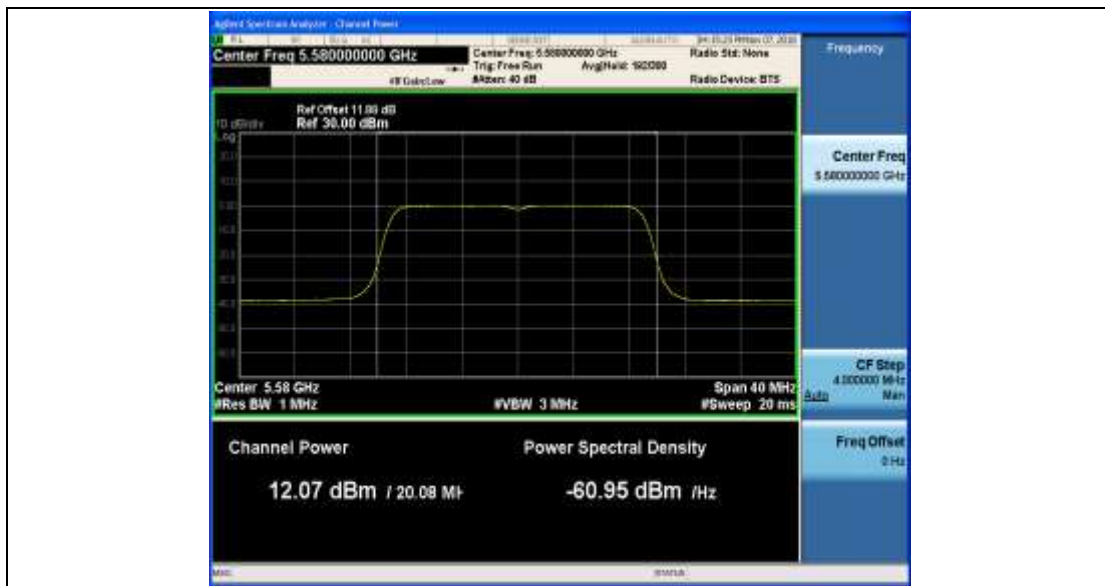
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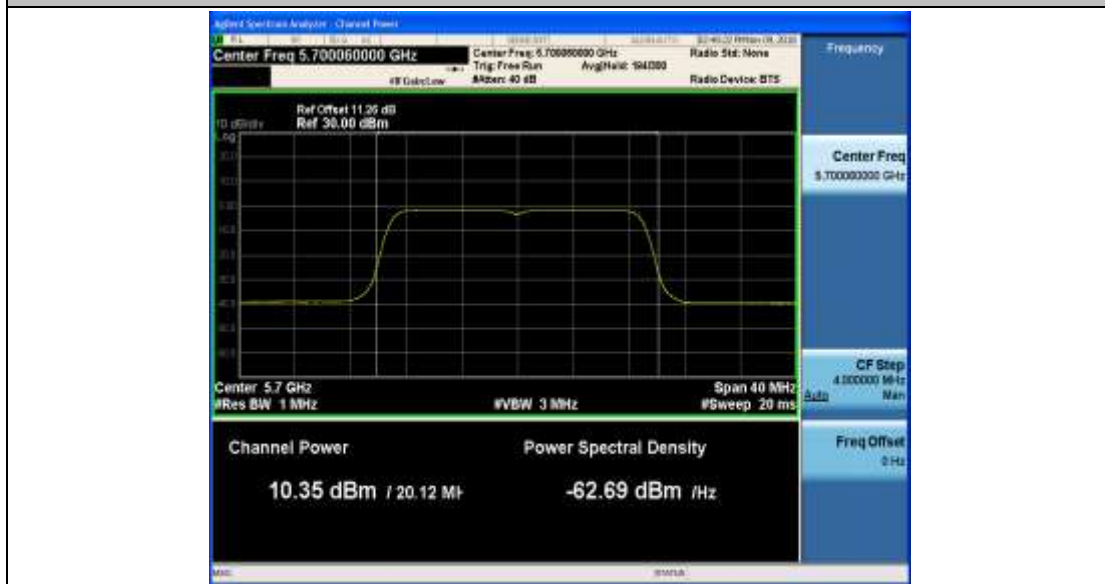
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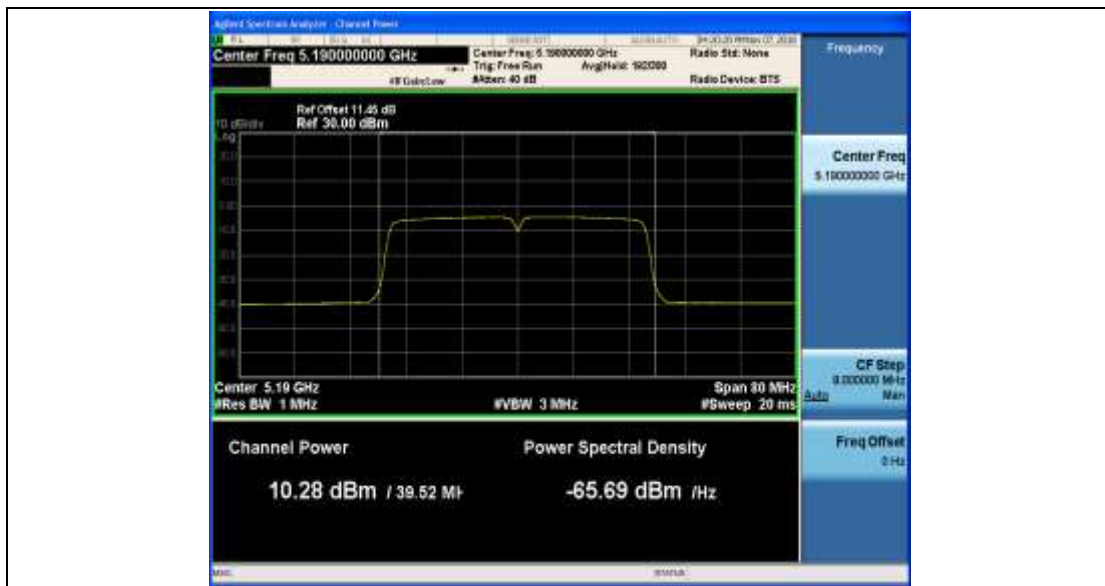
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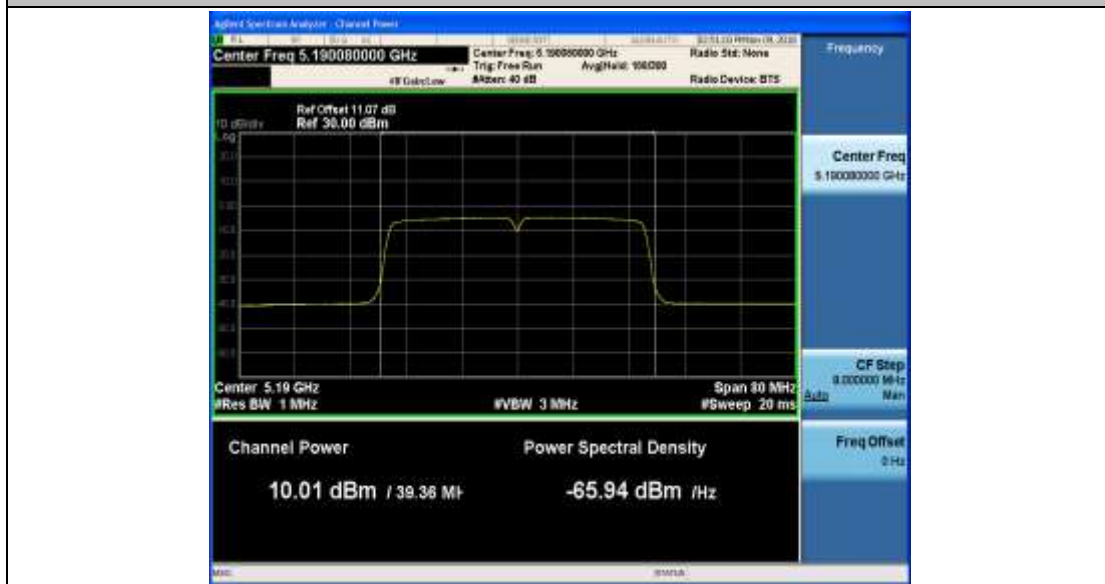
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11AC40SISO_Ant1_5190



11AC40SISO_Ant2_5190



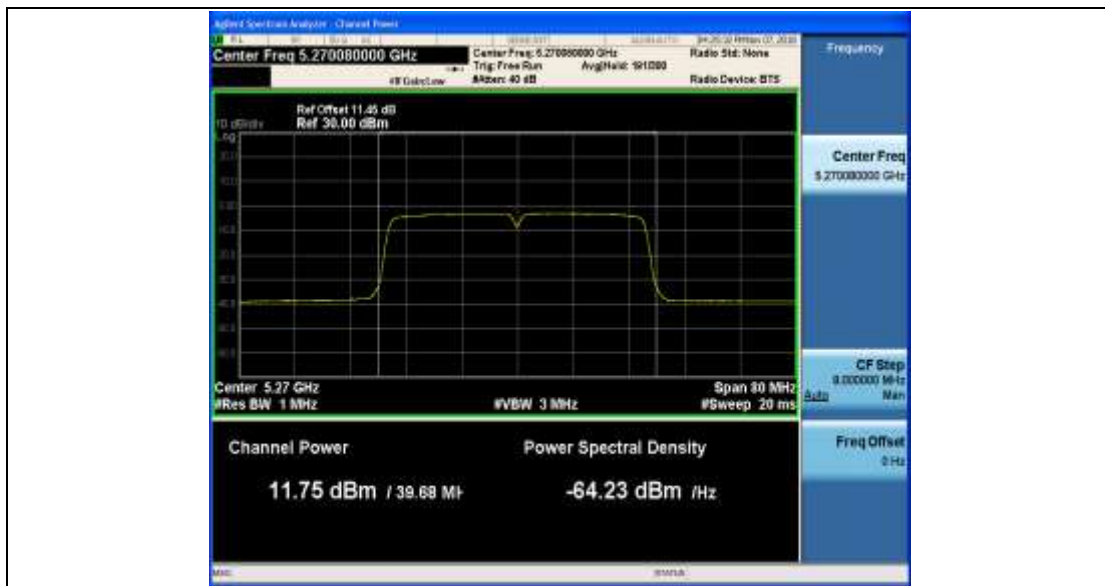
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11AC40SISO_Ant2_5230



11AC40SISO_Ant1_5270



11AC40SISO_Ant2_5270



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11AC40SISO_Ant2_5310



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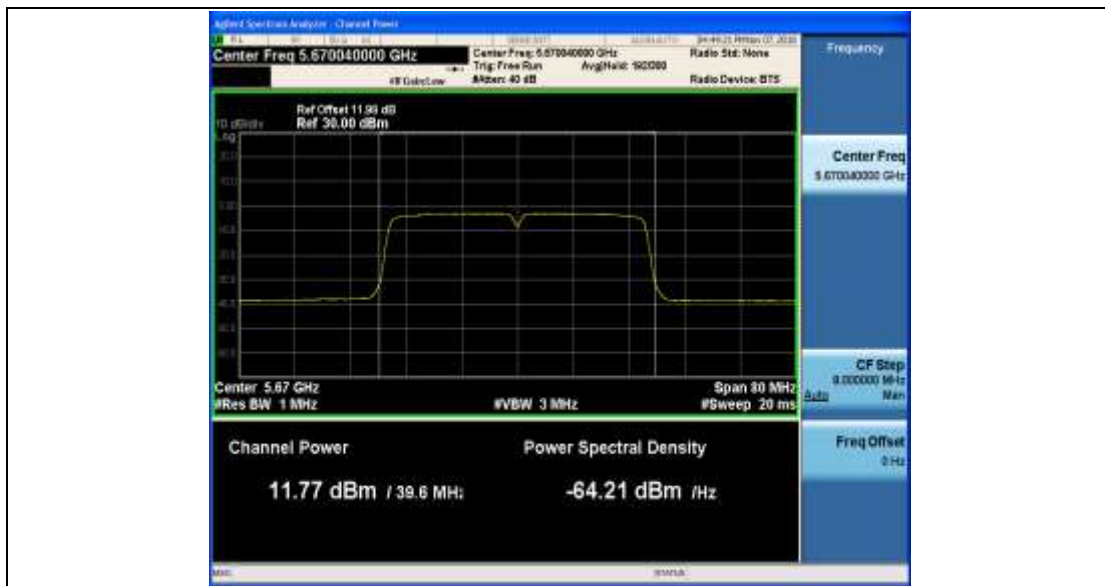
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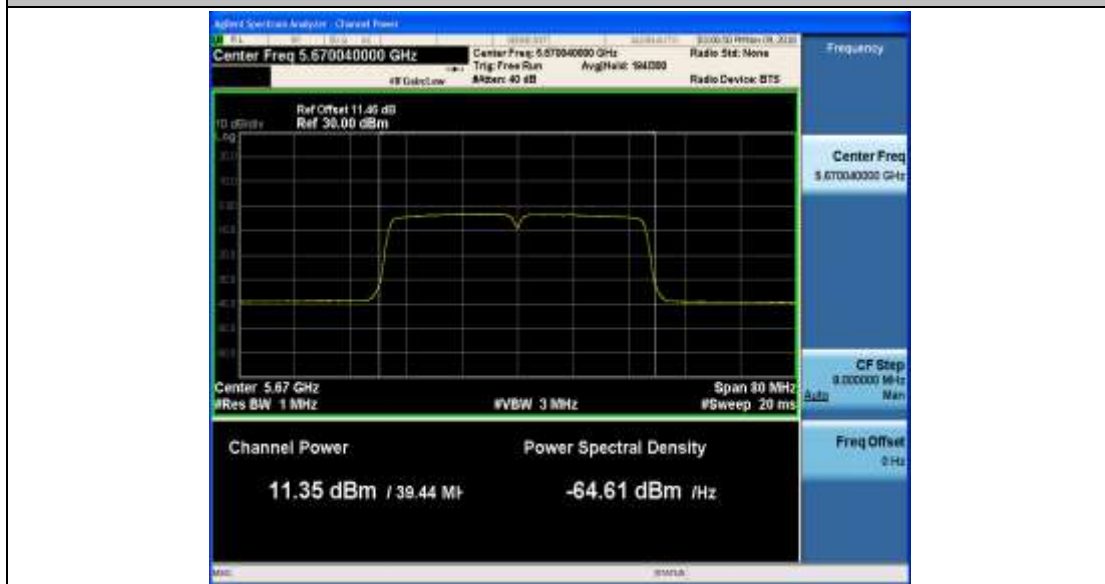
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11AC40SISO_Ant1_5670



11AC40SISO_Ant2_5670



11AC80SISO_Ant1_5210



11AC80SISO_Ant2_5210



11AC80SISO_Ant1_5290



11AC80SISO_Ant2_5290



11AC80SISO_Ant1_5530