
PROPOSED MODIFICATION DETAILS

GENERAL

This section details the modifications to the Leapfrog model Cash Register being proposed. All performance and construction deviations from the characteristics originally reported to the FCC are addressed

Leapfrog propose the following changes in the Cash Register's RFID Scanner circuit to improve manufacturing yield:

- The tank capacitance value (parallel combination of C1 and C2) of the antenna circuit will be changed from a fixed value to a determine-in-test value that achieves resonance at the 13.56 MHz carrier frequency. This change allows the factory to tune the antenna tank circuit to remove the effect of component tolerances, and obtain more consistent performance.
- A small capacitor (C2) will be added across Q3 to improve signal linearity. This reduces the level of radiated harmonics by several dB.