

# DFS PORTION OF FCC CFR47 PART 15 SUBPART E

# CERTIFICATION TEST REPORT FOR

## **BROADCOM 802,11AG WLAN MINI PCI CARD**

**MODEL NUMBER: BCM94318MPAGH** 

FCC ID: QDS-BRCM1017

REPORT NUMBER: 07U10933-1, Revision B

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Prepared for

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## DATE: MAY 2, 2007 FCC ID: QDS-BRCM1017

# **Revision History**

	Issue		
Rev.	Date	Revisions	Revised By
	4/26/07	Initial Issue	M. Heckrotte
B	5/2/07	Corrected serial number in Section 1	M Heckrotte

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# 1. ATTESTATION OF TEST RESULTS

**COMPANY NAME:** BROADCOM CORP.

190 MATHILDA PLACE

SUNNYVALE, CA 94086, USA

**EUT DESCRIPTION:** BROADCOM 802.11AG WLAN MINI PCI CARD

MODEL: BCM94318MPAGH

**SERIAL NUMBER:** DW1470

**DATE TESTED:** APRIL 9-10, 2007

#### APPLICABLE STANDARDS

STANDARD TEST RESULTS

DFS PORTION OF NO NON-COMPLIANCE NOTED

FCC PART 15 SUBPART E

Compliance Certification Services, Inc. tested the above equipment in accordance with the requirements set forth in the above standards. The test results show that the equipment tested is capable of demonstrating compliance with the requirements as documented in this report.

**Note**: The results documented in this report apply only to the tested sample, under the conditions and modes of operation as described herein. This document may not be altered or revised in any way unless done so by Compliance Certification Services and all revisions are duly noted in the revisions section. Any alteration of this document not carried out by Compliance Certification Services will constitute fraud and shall nullify the document. No part of this report may be used to claim product certification, approval, or endorsement by NVLAP, NIST, or any government agency.

Approved & Released For CCS By: Tested By:

MH

MICHAEL HECKROTTE ENGINEERING MANAGER COMPLIANCE CERTIFICATION SERVICES YOBI ZHOU EMC ENGINEER COMPLIANCE CERTIFICATION SERVICES

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## 2. TEST METHODOLOGY

The tests documented in this report were performed in accordance with FCC CFR 47 Part 15 and FCC 06-96 APPENDIX "COMPLIANCE MEASUREMENT PROCEDURES FOR UNLICENSED-NATIONAL INFORMATION INFRASTRUCTURE DEVCIES OPERATING IN THE 5250-5350 MHz AND 5470-5725 MHz BANDS INCORPORATING DYNAMIC FREQUENCY SELECTION".

## 3. FACILITIES AND ACCREDITATION

The test sites and measurement facilities used to collect data are located at 47173 Benicia Street, Fremont, California, USA. The sites are constructed in conformance with the requirements of ANSI C63.4, ANSI C63.7 and CISPR Publication 22. All receiving equipment conforms to CISPR Publication 16-1, "Radio Interference Measuring Apparatus and Measurement Methods."

CCS is accredited by NVLAP, Laboratory Code 200065-0. The full scope of accreditation can be viewed at <a href="http://www.ccsemc.com">http://www.ccsemc.com</a>.

## 4. CALIBRATION AND UNCERTAINTY

## 4.1. MEASURING INSTRUMENT CALIBRATION

The measuring equipment utilized to perform the tests documented in this report has been calibrated in accordance with the manufacturer's recommendations, and is traceable to recognized national standards.

#### 4.2. MEASUREMENT UNCERTAINTY

Where relevant, the following measurement uncertainty levels have been estimated for tests performed on the apparatus:

PARAMETER	UNCERTAINTY
Radiated Emission, 30 to 200 MHz	+/- 3.3 dB
Radiated Emission, 200 to 1000 MHz	+4.5 / -2.9 dB
Radiated Emission, 1000 to 2000 MHz	+4.5 / -2.9 dB
Power Line Conducted Emission	+/- 2.9 dB

Uncertainty figures are valid to a confidence level of 95%.

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## 4.3. DYNAMIC FREQUENCY SELECTION

## 4.3.1. LIMITS

§15.407 (h) and FCC 06-96 APPENDIX "COMPLIANCE MEASUREMENT PROCEDURES FOR UNLICENSED-NATIONAL INFORMATION INFRASTRUCTURE DEVCIES OPERATING IN THE 5250-5350 MHz AND 5470-5725 MHz BANDS INCORPORATING DYNAMIC FREQUENCY SELECTION".

Table 1: Applicability of DFS requirements prior to use of a channel

Requirement	Operational Mode				
	Master	Client (without radar detection)	Client (with radar detection)		
Non-Occupancy Period	Yes	Not required	Yes		
DFS Detection Threshold	Yes	Not required	Yes		
Channel Availability Check Time	Yes	Not required	Not required		
Uniform Spreading	Yes	Not required	Not required		

Table 2: Applicability of DFS requirements during normal operation

Requirement	Operationa	Operational Mode				
	Master Client Client		Client			
		(without DFS)	(with DFS)			
DFS Detection Threshold	Yes	Not required	Yes			
Channel Closing Transmission Time	Yes	Yes	Yes			
Channel Move Time	Yes	Yes	Yes			

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Table 3: Interference Threshold values, Master or Client incorporating In-Service Monitoring

Maximum Transmit Power	Value
	(see note)
≥ 200 milliwatt	-64 dBm
< 200 milliwatt	-62 dBm

Note 1: This is the level at the input of the receiver assuming a 0 dBi receive antenna Note 2: Throughout these test procedures an additional 1 dB has been added to the amplitude of

the test transmission waveforms to account for variations in measurement equipment. This will ensure that the test signal is at or above the detection threshold level to trigger a DFS response.

**Table 4: DFS Response requirement values** 

Parameter	Value
Non-occupancy period	30 minutes
Channel Availability Check Time	60 seconds
Channel Move Time	10 seconds
Channel Closing Transmission Time	200 milliseconds + approx. 60 milliseconds over remaining 10 second period

The instant that the Channel Move Time and the Channel Closing Transmission Time begins is as follows:

- For the Short pulse radar Test Signals this instant is the end of the *Burst*.
- For the Frequency Hopping radar Test Signal, this instant is the end of the last radar burst generated.
- For the Long Pulse radar Test Signal this instant is the end of the 12 second period defining the radar transmission.

The Channel Closing Transmission Time is comprised of 200 milliseconds starting at the beginning of the Channel Move Time plus any additional intermittent control signals required to facilitate channel changes (an aggregate of approximately 60 milliseconds) during the remainder of the 10 second period. The aggregate duration of control signals will not count quiet periods in between transmissions.

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#### Table 5 – Short Pulse Radar Test Waveforms

Radar Type	Pulse Width	PRI	Pulses	Minimum	Minimum				
	(Microseconds)	(Microseconds)		Percentage of	Trials				
				Successful					
				Detection					
1	1	1428	18	60%	30				
2	1-5	150-230	23-29	60%	30				
3	6-10	200-500	16-18	60%	30				
4	11-20	200-500	12-16	60%	30				
Aggregate (Ra	adar Types 1-4)		80%	120					

Table 6 - Long Pulse Radar Test Signal

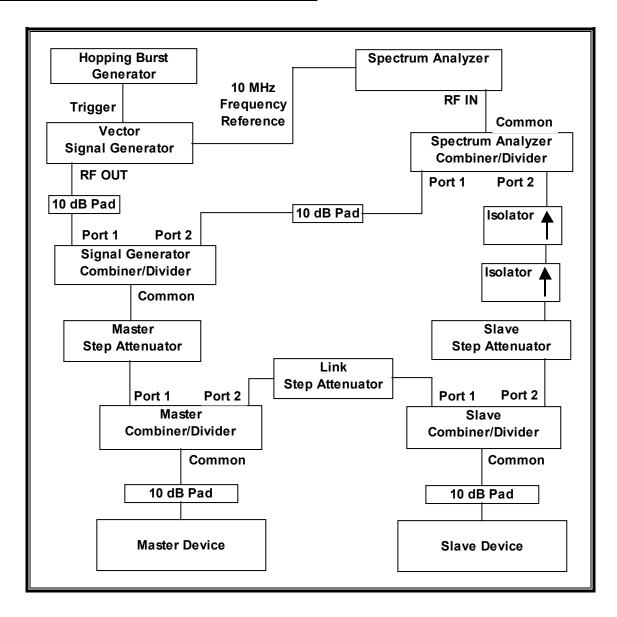
Table 0 - Long Tuise Radai Test Signai								
Radar	Bursts	Pulses	Pulse	Chirp	PRI	Minimum	Minimum	
Waveform		per	Width	Width	(µsec)	Percentage of	Trials	
		Burst	(µsec)	(MHz)		Successful		
						Detection		
5	8-20	1-3	50-100	5-20	1000-	80%	30	
					2000			

Table 7 – Frequency Hopping Radar Test Signal

Tuble / Treduciney Hopping Radual Test Signal								
Radar	Pulse	PRI	Burst	Pulses	Hopping	Minimum	Minimum	
Waveform	Width	(µsec)	Length	per	Rate	Percentage of	Trials	
	(µsec)		(ms)	Нор	(kHz)	Successful Detection		
6	1	333	300	9	.333	70%	30	

### 4.3.2. TEST AND MEASUREMENT SYSTEM

#### **CONDUCTED METHOD SYSTEM BLOCK DIAGRAM**



## **SYSTEM OVERVIEW**

The short pulse and long pulse signal generating system utilizes the NTIA software. The Vector Signal Generator has been validated by the NTIA. The hopping signal generating system utilizes the CCS simulated hopping method and system, which has been validated by the DoD, FCC and NTIA. The software selects waveform parameters from within the bounds of the signal type on a random basis using uniform distribution.

The short pulse types 2, 3 and 4, and the long pulse type 5 parameters are randomized at run-time.

The hopping type 6 pulse parameters are fixed while the hopping sequence is based on the August 2005 NTIA Hopping Frequency List. The initial starting point randomized at run-time and each subsequent starting point is incremented by 475. Each frequency in the 100-length segment is compared to the boundaries of the EUT Detection Bandwidth and the software creates a hopping burst pattern in accordance with Section 7.4.1.3 Method #2 Simulated Frequency Hopping Radar Waveform Generating Subsystem of FCC 06-96 APPENDIX. The frequency of the signal generator is incremented in 1 MHz steps from F<sub>L</sub> to F<sub>H</sub> for each successive trial. This incremental sequence is repeated as required to generate a minimum of 30 total trials and to maintain a uniform frequency distribution over the entire Detection Bandwidth.

The signal monitoring equipment consists of a spectrum analyzer set to display 8001 bins on the horizontal axis. The time-domain resolution is 2 msec / bin with a 16 second sweep time, meeting the 10 second short pulse reporting criteria. The aggregate ON time is calculated by multiplying the number of bins above a threshold during a particular observation period by the dwell time per bin, with the analyzer set to peak detection and max hold.

Should multiple RF ports be utilized for the Master and/or Slave devices (for example, for diversity or MIMO implementations), additional combiner/dividers are inserted between the Master Combiner/Divider and the pad connected to the Master Device (and/or between the Slave Combiner/Divider and the pad connected to the Slave Device). Additional pads are utilized such that there is one pad at each RF port on each EUT.

#### **SYSTEM CALIBRATION**

A 50 ohm load is connected in place of the spectrum analyzer, and the spectrum analyzer is connected in place of the master device and the signal generator is set to CW mode. The amplitude of the signal generator is adjusted to yield a level of –64 dBm as measured on the spectrum analyzer.

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Without changing any of the instrument settings, the spectrum analyer is reconnected to the Common port of the Spectrum Analyzer Combiner/Divider. Measure the amplitude and calculate the difference from – 64 dBm. Adjust the Reference Level Offset of the spectrum analyzer to this difference.

The spectrum analyzer displays the level of the signal generator as received at the antenna ports of the Master Device. The interference detection threshold may be varied from the calibrated value of –64 dBm and the spectrum analyzer will still indicate the level as received by the Master Device.

Set the signal generator to produce a radar waveform, trigger a burst manually and measure the level on the spectrum analyzer. Readjust the amplitude of the signal generator as required so that the peak level of the waveform is at a displayed level equal to the required or desired interference detection threshold. Separate signal generator amplitude settings are determined as required for each radar type.

### ADJUSTMENT OF DISPLAYED TRAFFIC LEVEL

Establish a link between the Master and Slave, adjusting the Link Step Attenuator as needed to provide a suitable received level at the Master and Slave devices. Stream the video test file to generate WLAN traffic. Confirm that the WLAN traffic level, as displayed on the spectrum analyzer, is at lower amplitude than the radar detection threshold. For Master Device testing confirm that the displayed traffic does not include Slave Device traffic. For Slave Device testing confirm that the displayed traffic does not include Master Device traffic.

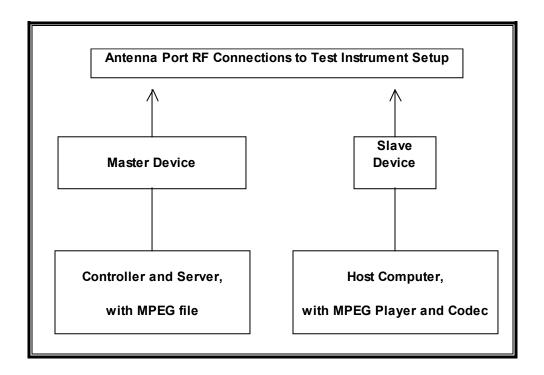
If a different setting of the Master Step Attenuator is required to meet the above conditions, perform a new System Calibration for the new Master Step Attenuator setting.

#### **TEST AND MEASUREMENT EQUIPMENT**

The following test and measurement equipment was utilized for the DFS tests documented in this report:

TEST EQUIPMENT LIST								
Description	Manufacturer	Model	Serial Number	Cal Due				
Spectrum Analyzer 3 Hz ~ 44 GHz	Agilent / HP	E4446A	US42070220	7/29/2007				
Vector Signal Generator 250kHz-								
20GHz	Agilent / HP	E8267C	US43320336	11/2/2007				
	National							
High Speed Digital I/O Card	Instruments	PCI-6534	HA1612845	1/16/2008				

## **CONDUCTED METHOD EUT TEST SETUP**



## **SUPPORT EQUIPMENT**

The following test and measurement equipment was utilized for the DFS tests documented in this report:

PERIPHERAL SUPPORT EQUIPMENT LIST								
Description	Description Manufacturer Model Serial Number							
Laptop	DELL	Dell Inspiron 4150	CN-04P449-48643-2CH-2011	DoC				
AC Adapter	DELL	ADP-70EB	TH-09364U-17971-248-8PDP	DoC				
Laptop	Compaq	Presario 3000	CNU327025L	DoC				
AC Adapter	Compaq	PA-1900-05H	3300371601	DoC				
Access Point	CISCO	AIR-AP1242AG-A-K9	FTX1042B5E0	LDK102056				
AC Adapter	Delta	ADP-18PB	PZT0628359656	DoC				

# 4.3.3. DESCRIPTION OF EUT WITH RESPECT TO FCC 06-96 REQUIREMENTS

The EUT operates over the 5250-5350 MHz range, with a carrier tuning range of 5260 to 5320 MHz.

The EUT is a Slave Device without radar detection.

The highest power level is 20.1 dBm EIRP.

The highest gain antenna assembly utilized with the EUT has a gain of 5.1 dBi. The lowest gain antenna assembly utilized with the EUT has a gain of 1.5 dBi.

All antennas are integral.

The EUT uses one transmitter connected to a 50-ohm coaxial antenna port. The antenna port is connected to the test system to perform conducted tests.

WLAN traffic is generated by streaming the video file TestFile.mp2 "6 ½ Magic Hours" from the Master to the Slave in full motion video mode using the media player with the V2.61 Codec package.

TPC is not required since the maximum EIRP is less than 500 mW (27 dBm).

The EUT utilizes the 802.11a architecture. One nominal channel bandwidth, 20 MHz, is implemented.

## OVERVIEW OF MASTER DEVICE WITH RESPECT TO FCC 06-96 REQUIREMENTS

The Master Device is a Cisco Access Point, FCC ID: LDK102056. The DFS software installed in the Master Device is revision 6.00.1. The minimum antenna gain for the Master Device is 3.5 dBi.

The rated output power of the Master unit is > 23 dBm (EIRP). Therefore the required interference threshold level is -64 dBm. After correction for antenna gain and procedural adjustments, the required conducted threshold at the antenna port is -64 + 2 + 1 = -61 dBm.

The calibrated conducted DFS Detection Threshold level is set to -64 dBm. The tested level is lower than the required level hence it provides margin to the limit.

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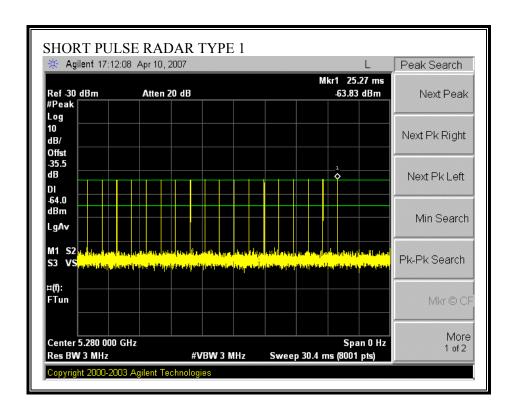
## 4.3.4. TEST CHANNEL AND METHOD

All tests were performed at a channel center frequency of 5280 MHz. Measurements were performed using conducted test methods.

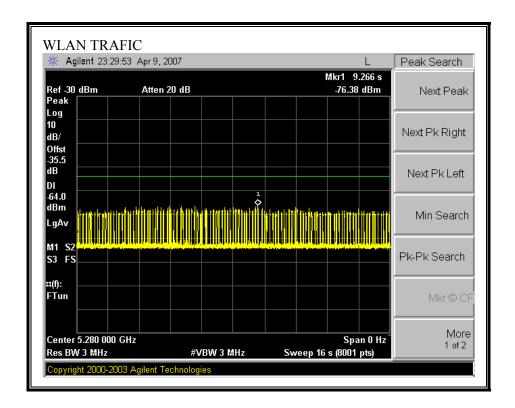
DATE: MAY 2, 2007

## 4.3.5. PLOTS OF RADAR WAVEFORM, AND WLAN TRAFFIC

### **PLOTS OF RADAR WAVEFORMS**



## PLOT OF WLAN TRAFFIC FROM SLAVE



## DATE: MAY 2, 2007 FCC ID: QDS-BRCM1017

# 4.3.6. SLAVE DEVICE CONFIGURATION - CHANNEL MOVE TIME AND CHANNEL CLOSING TRANSMISSION TIME

## **REPORTING NOTES**

The reference marker is set at the end of last radar pulse.

The delta marker is set at the end of the last WLAN transmission following the radar pulse. This delta is the channel move time.

The aggregate channel closing transmission time is calculated as follows:

Aggregate Transmission Time = (Number of analyzer bins showing transmission) \* (dwell time per bin)

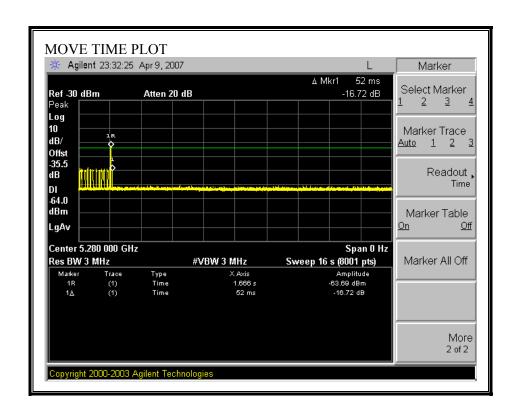
The observation period over which the aggregate time is calculated Begins at (Reference Marker + 200 msec) and Ends no earlier than (Reference Marker + 10 sec).

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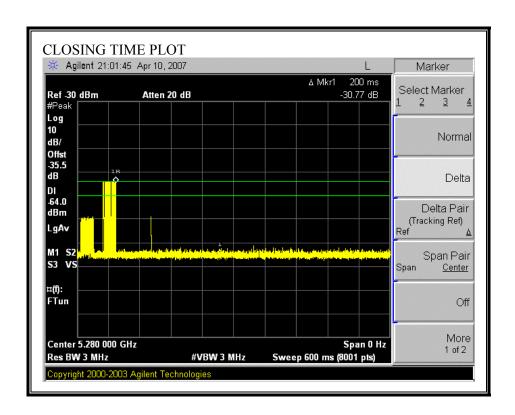
## **CHANNEL MOVE TIME RESULTS**

No non-compliance noted:

<b>Channel Move Time</b>	Limit	
(s)	(s)	
0.000	10	



## **CHANNEL CLOSING TIME RESULTS**

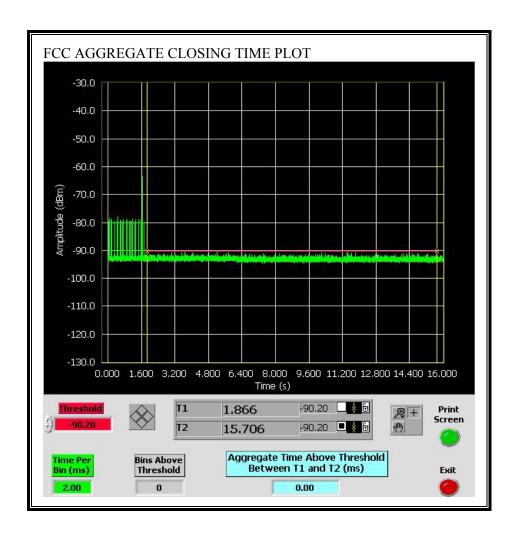


## FCC AGGREGATE CHANNEL CLOSING TRANSMISSION TIME RESULTS

No non-compliance noted:

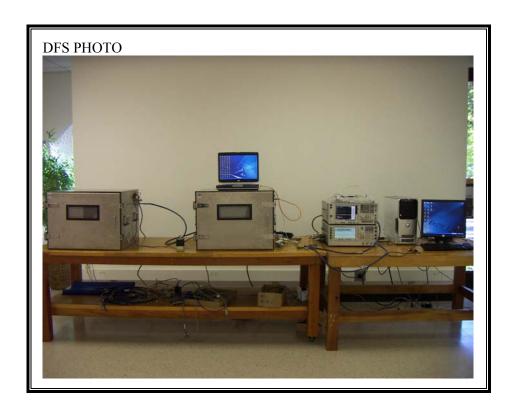
Aggregate Transmission Time	Limit	Margin
(ms)	(ms)	(ms)
0.00	60	60.00

No transmissions are observed during the aggregate monitoring period.



# 5. SETUP PHOTOS

## **DFS MEASUREMENT SETUP**



# **END OF REPORT**