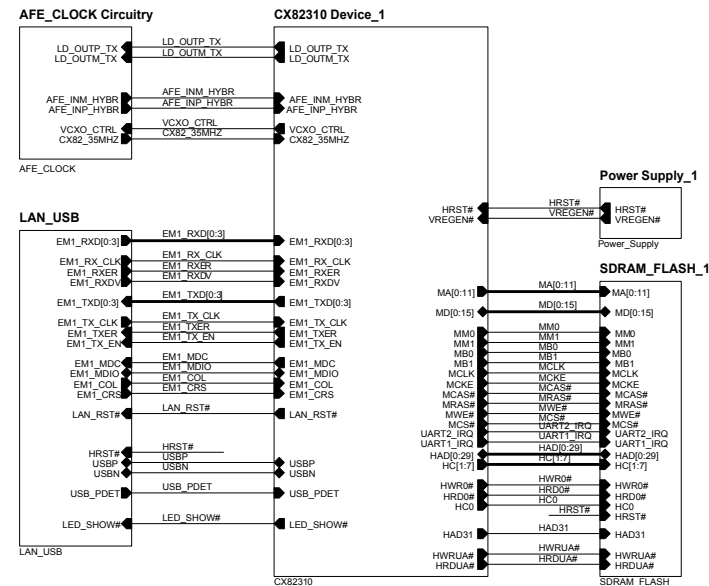


CORECESS INC.

PROJECT NAME : Corecess 3115
PROJECT CODE : C216
PRODUCT NAME : Corecess 3115
PRODUCT BOM : Corecess 3115 v2.2
CIRCUIT VER. : V2.2

SINGLE CHIP ADSL ROUTER



TOP LEVEL DESIGN

Update Notes:

V2.1

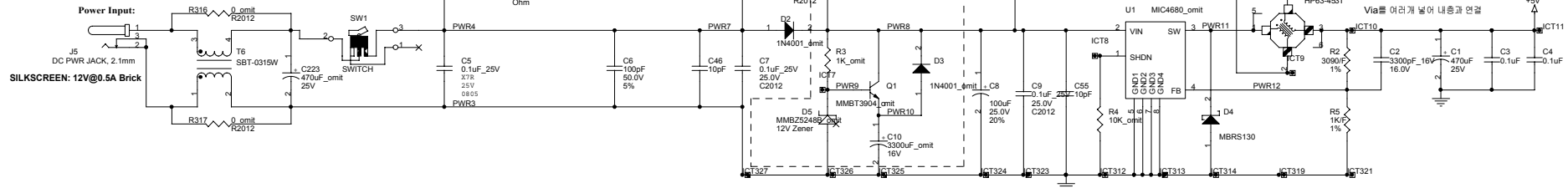
1. +5V/+12V 외부 아답터 선택적 사용
2. RS232 트랜시버를 ST3232로 교체
3. RTL8012/IP101 검출원계

V2.2

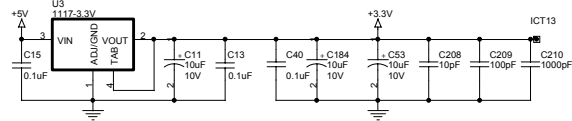
1. PS2 제거 (Option)
2. +5V -> +5VA 전환과정 CAP 삽입
3. -14, -24를 위한 TX 필터 회로 수정

Power Supply Circuitry

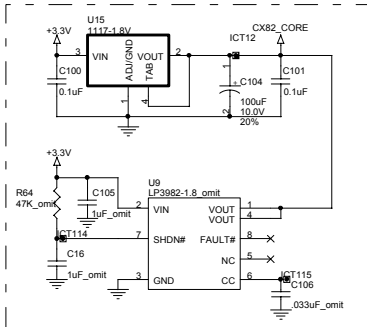
12V @ 500mA, from External XFRMR
5V @ 1000mA, from External SMPS



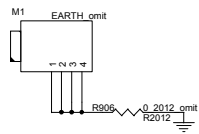
레귤레이터 VOUT 에 Copper를 놓게 넣을 것
레귤레이터 VOUT 및 GND핀에 Via를 많이 뚫을 것



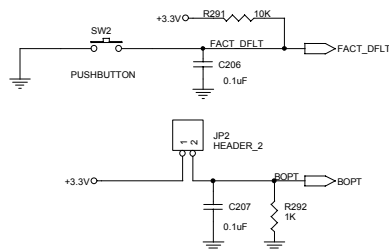
Place close to U6(CX82310)



External 1.8V Regulator



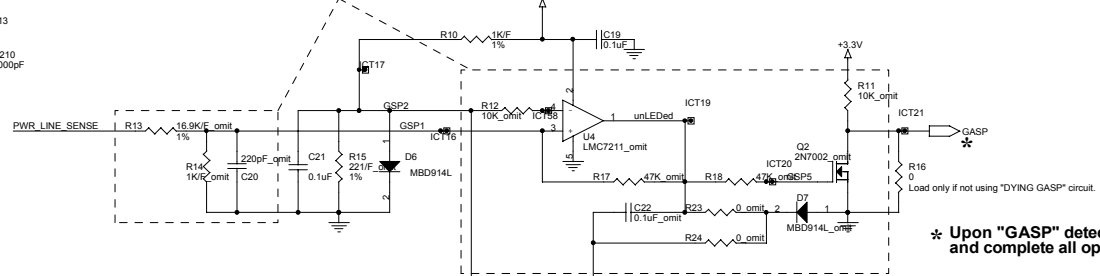
RESET to Factory Default



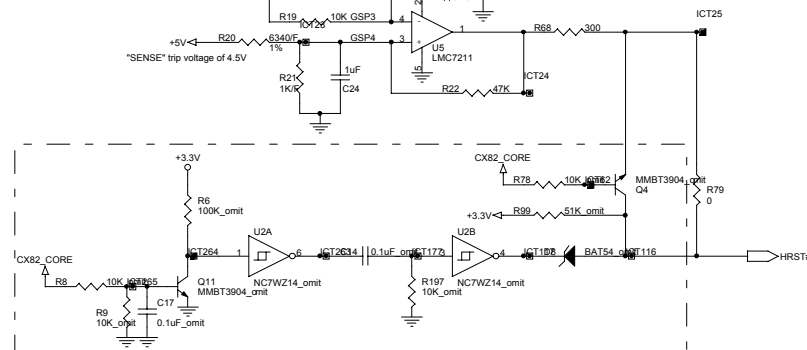
NOTE: JP2(1-2)

On : Internal Boot
Off : Boot from FLASH

LOAD FOR DYING GASP

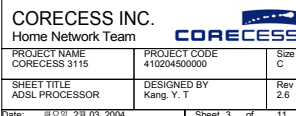


✱ Upon "GASP" detect initialize "Dying GASP Software" and complete all operations within 75msec.

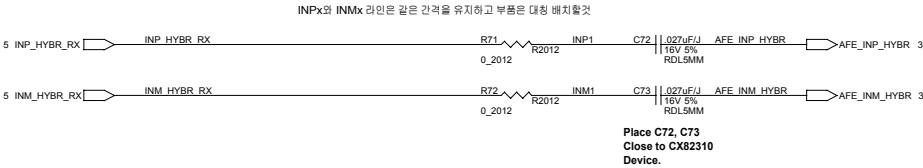


USB Delay
차후 삭제 해야됨
CX82_CORE 신호가 신호원으로 부터 너무 길게 늘어져서
Bx1에 약정함을 미치며 GND가 분리됨

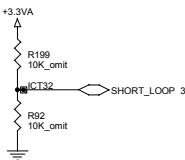
CORECESS INC. Home Network Team		
PROJECT NAME CORECESS 3115	PROJECT CODE 410204500000	Size C
SHEET TITLE POWER SUPPLY	DESIGNED BY Kang, Y. T.	Rev 2.6
Date: 2004.03.28 Sheet 2 of 11		



AFE Circuitry



SEE
NOTE



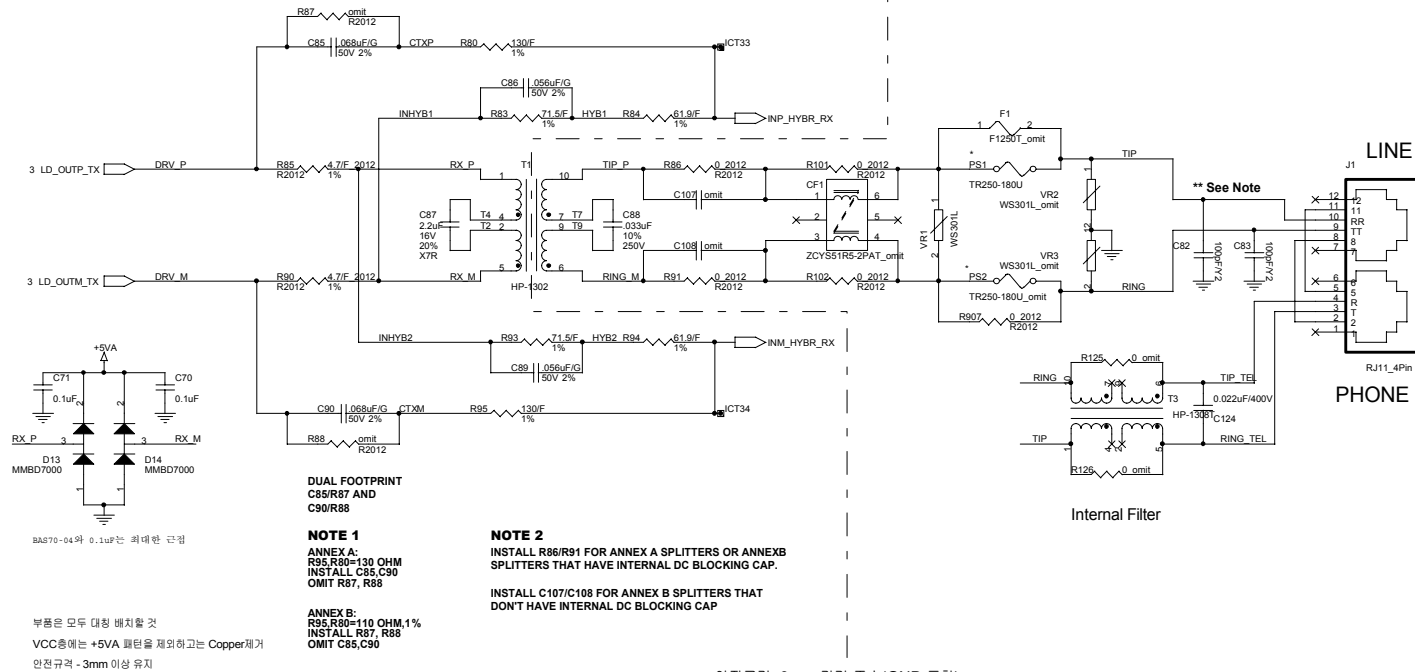
NOTE

* CX82310-24:

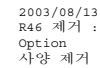
ANNEX A
INSTALL R199
OMIT R92

ANNEX B
INSTALL R92
OMIT R199

Line Driver Hybrid Circuit

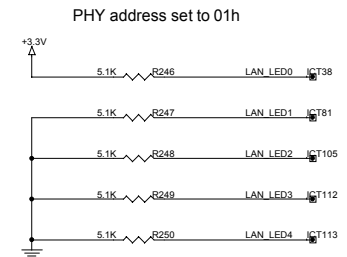
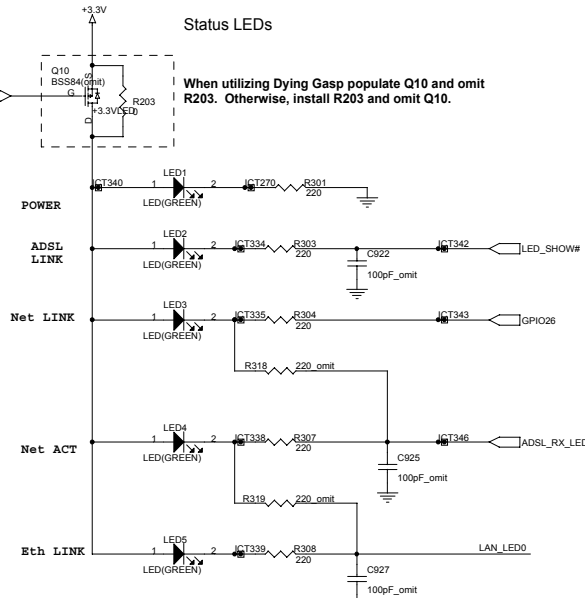
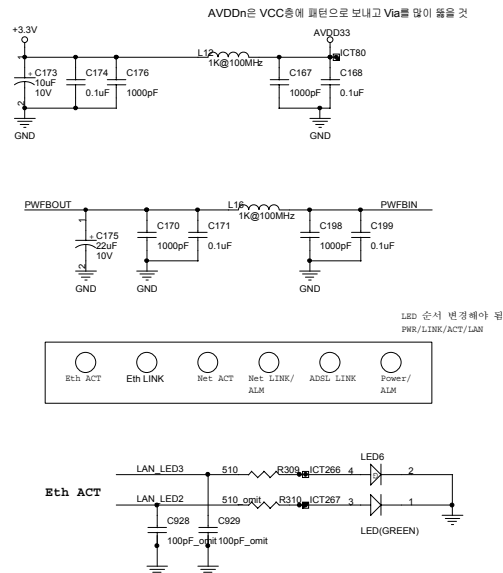
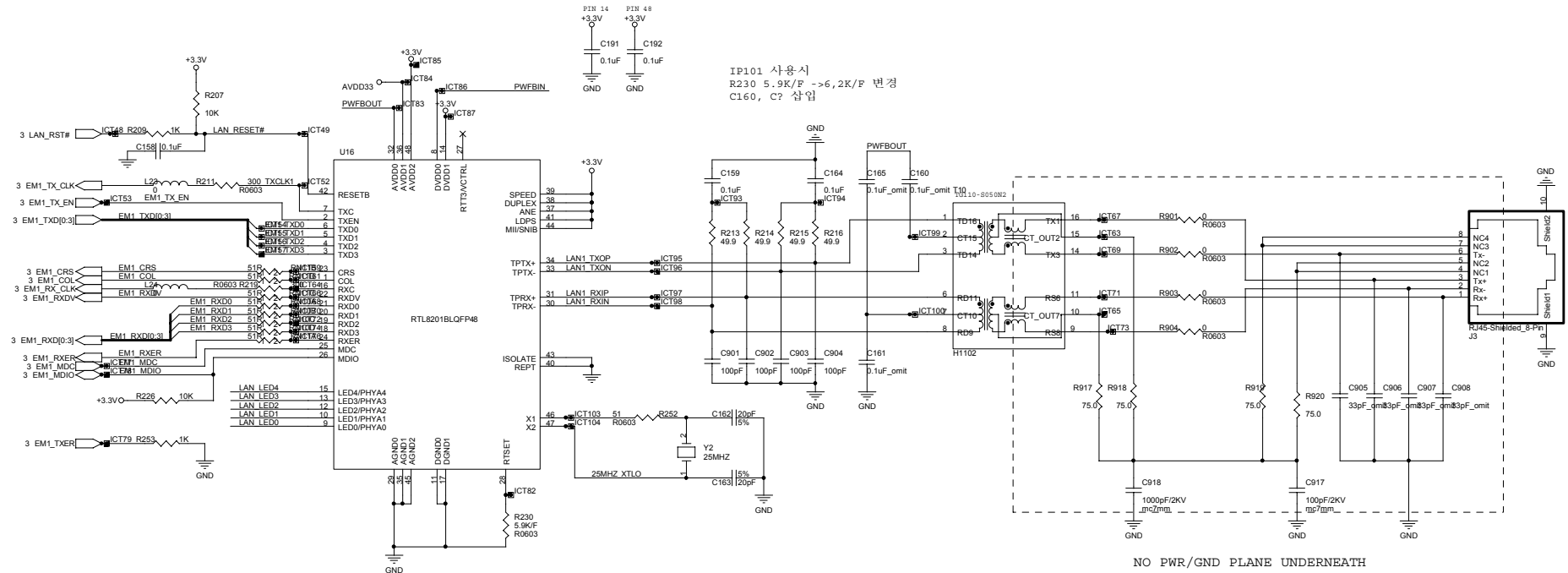


이 페이지의 모든 부품은 GND로 쌓아 다른 부품들과 격리시킬것



Ethernet PHY

EM1_TX_CLK = EM1_RX_CLK = 25MHz(25MHz)

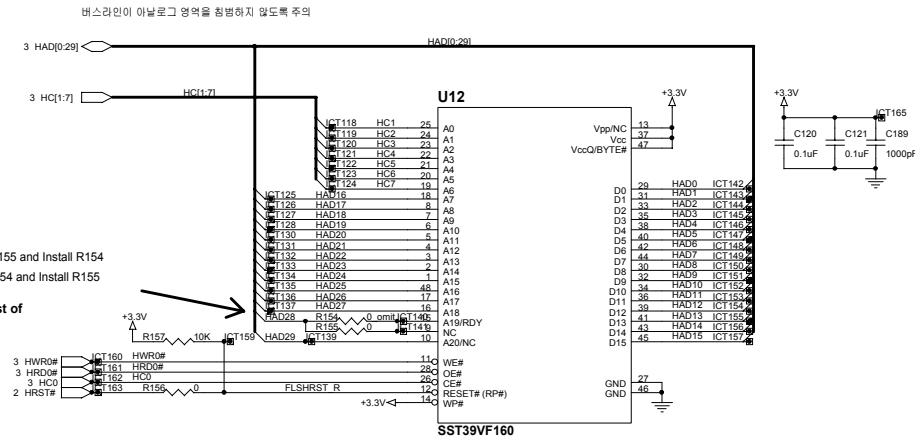


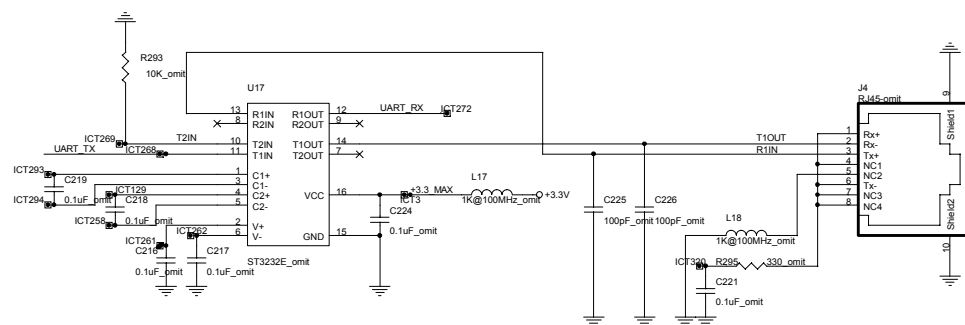
FLASH ROM Interface

NOTE for R154/R155:

When using an INTEL FLASH Device -- Remove R155 and Install R154
When using non-Intel FLASH Device -- Remove R154 and Install R155

See firmware Release Notes for complete list of supported Flash devices.

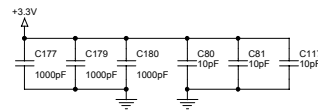
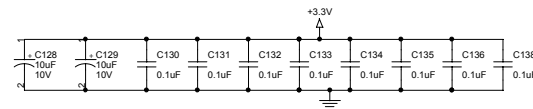
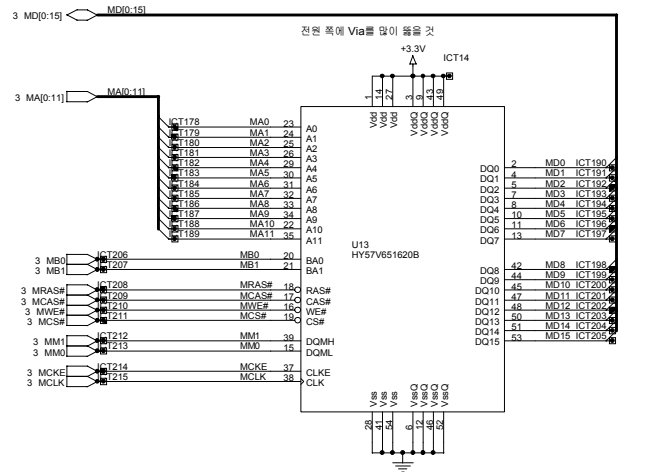




SDRAM

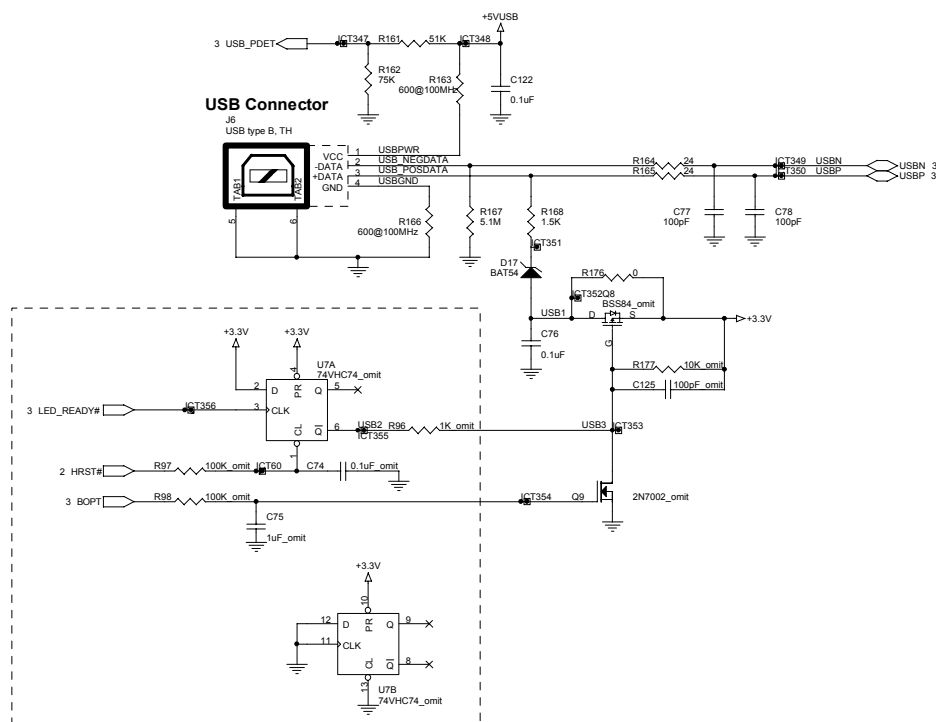
MCLK = 100MHz

최대한 메인칩 옆에 배치
버스라인은 Via를 붙이지 않것



10pF과 1000pF은 한핀에 붙이 붙이지 않것

USB Interface



USB Delay Option

The USB delay is controlled by readiness of Rushmore firmware. To be compliant to the USB 1.1 spec, this ckt is necessary.