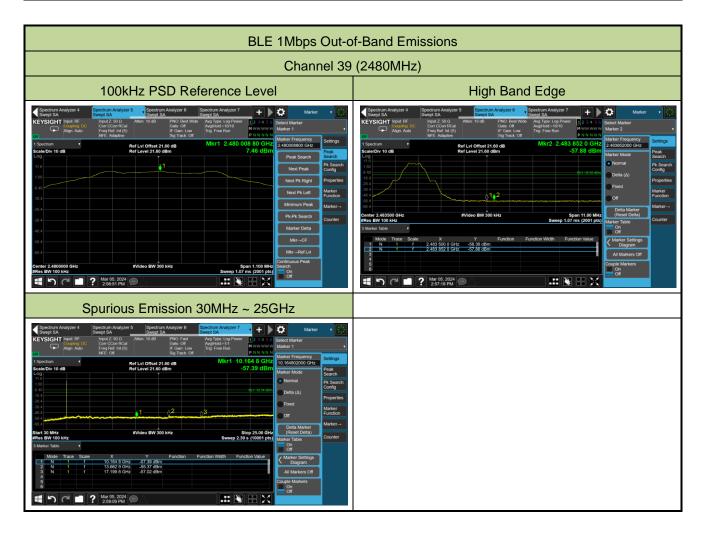
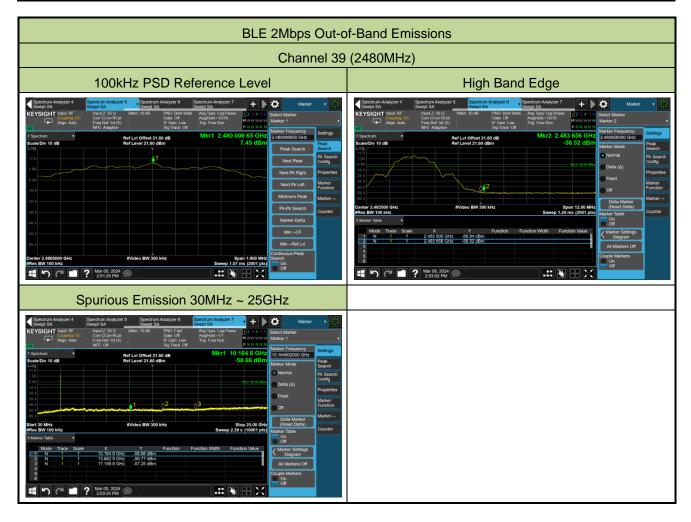


Test Site	WZ-SR5	Test Engineer	Lynn Yang
Test Date	2024-03-05	Filter	3#

Test Mode	Data Rate	Channel No.	Frequency	Limit	Result
	/ Mbps		(MHz)	(dBc)	
BLE	1	39	2480	20	Pass
BLE	2	39	2480	20	Pass





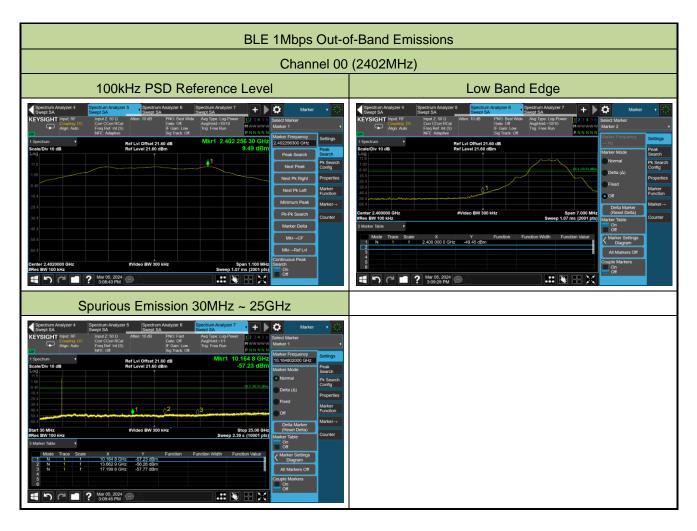




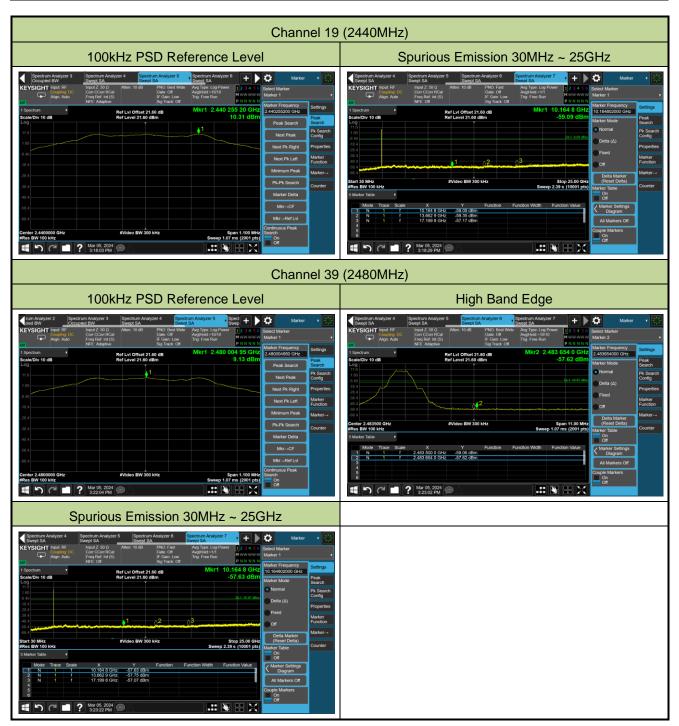
Mode 2

Test Site	WZ-SR5	Test Engineer	Lynn Yang
Test Date	2024-03-05	Filter	4#

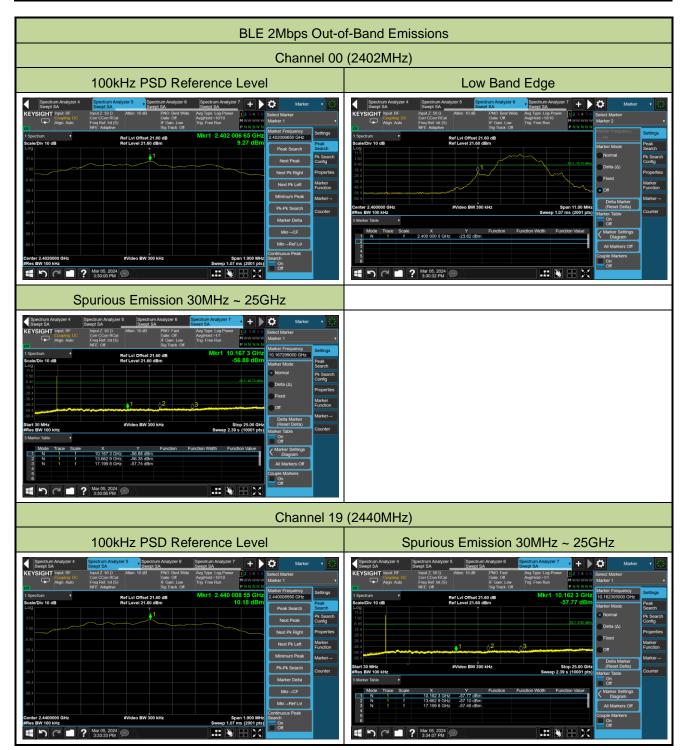
Test Mode	Data Rate / Mbps	Channel No.	Frequency (MHz)	Limit (dBc)	Result
BLE	1	00	2402	20	Pass
BLE	1	19	2440	20	Pass
BLE	1	39	2480	20	Pass
BLE	2	00	2402	20	Pass
BLE	2	19	2440	20	Pass
BLE	2	39	2480	20	Pass













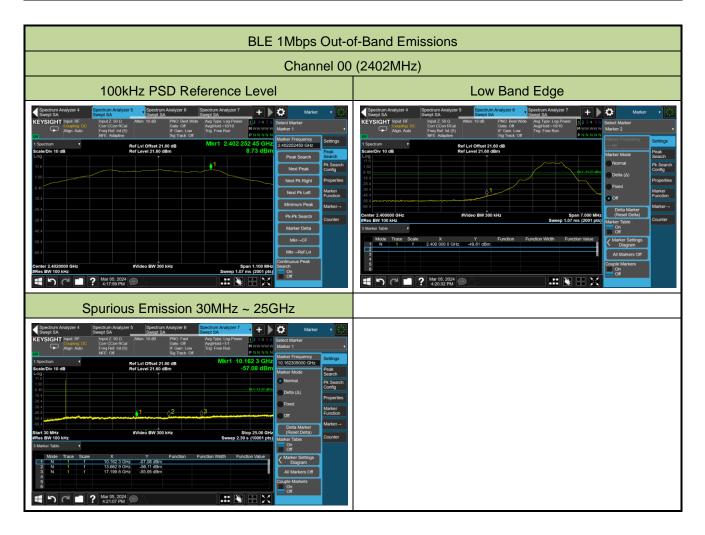


	Channel 39	9 (2480MHz)
100kHz PSD Ref	erence Level	High Band Edge
Image: Name: A constraint of the sector o	And have a series of the serie	Image: Name Description Desc
Start 30 MHz #Res BW 100 MHz S Markor Table ¥	And how to a how to h	



Test Site	WZ-SR5	Test Engineer	Lynn Yang
Test Date	2024-03-05	Filter	5#

Test Mode	Data Rate	Channel No.	Frequency	Limit	Result
	/ Mbps		(MHz)	(dBc)	
BLE	1	00	2402	20	Pass
BLE	2	00	2402	20	Pass



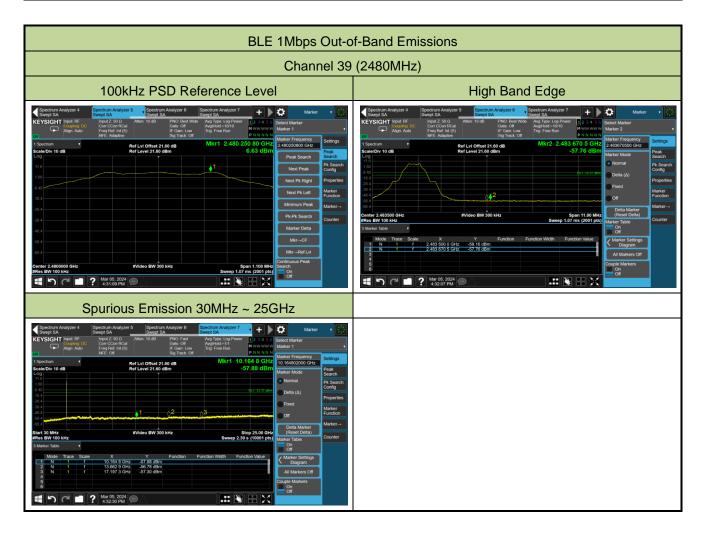






Test Site	WZ-SR5	Test Engineer	Lynn Yang
Test Date	2024-03-05	Filter	6#

Test Mode	Data Rate	Channel No.	Frequency	Limit	Result
	/ Mbps		(MHz)	(dBc)	
BLE	1	39	2480	20	Pass
BLE	2	39	2480	20	Pass





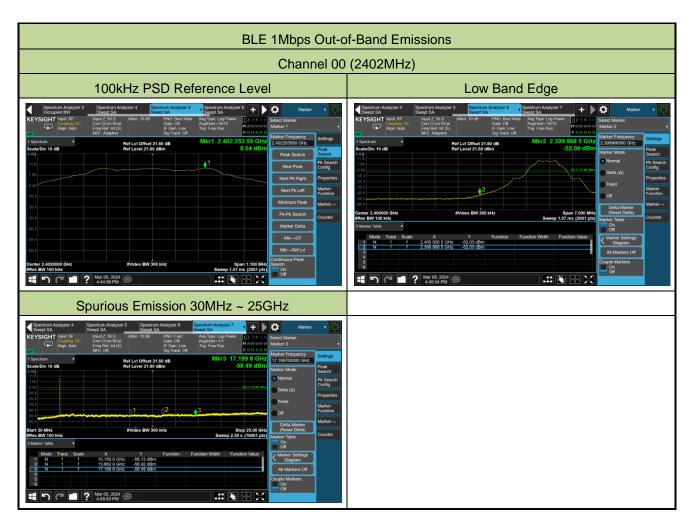




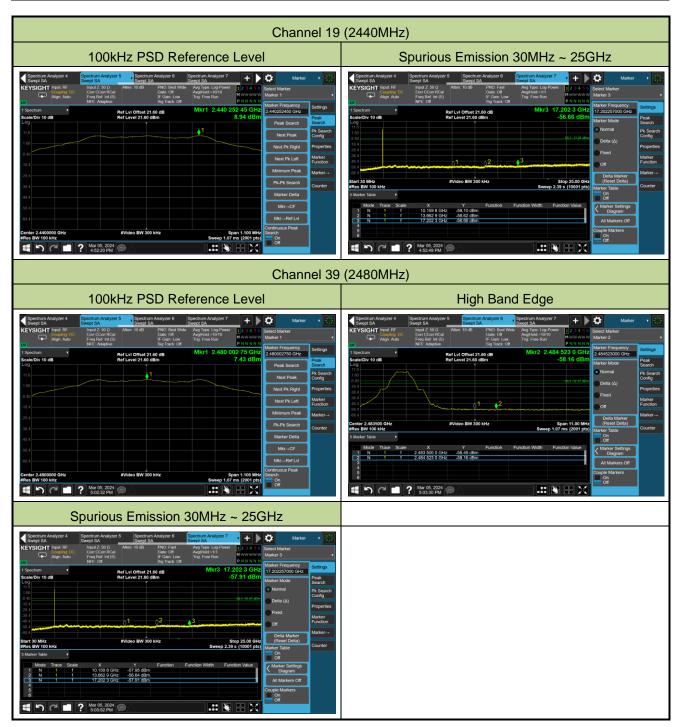
Mode 3

Test Site	WZ-SR5	Test Engineer	Lynn Yang
Test Date	2024-03-05	Filter	7#

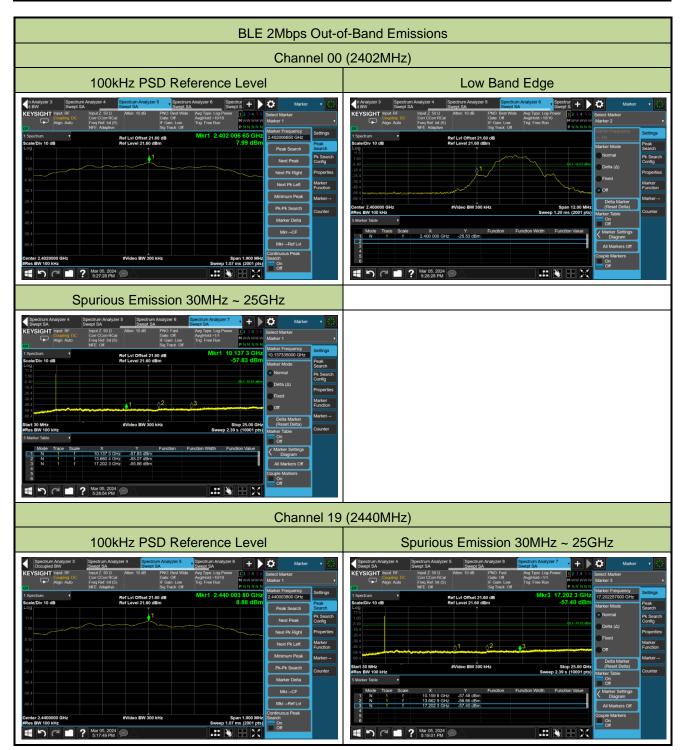
Test Mode	Data Rate / Mbps	Channel No.	Frequency (MHz)	Limit (dBc)	Result
BLE	1	00	2402	20	Pass
BLE	1	19	2440	20	Pass
BLE	1	39	2480	20	Pass
BLE	2	00	2402	20	Pass
BLE	2	19	2440	20	Pass
BLE	2	39	2480	20	Pass











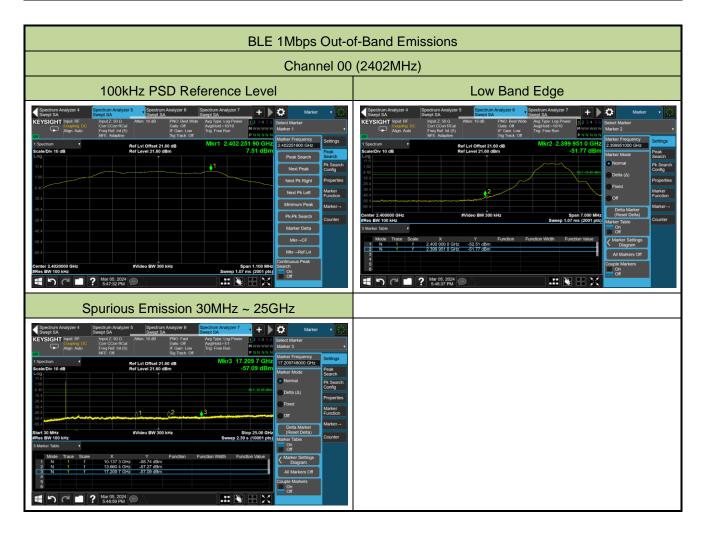


	Channel 39	(2480MHz)	
100kHz PSD Reference Level		High E	Band Edge
KEYSIGHT level life proof 25 00 register to August Add proof 25 00 register to Ref Level 210 offeet Ref Level 210 offeet	Aurker Frequency 240002030 GHz Peak Search Next Peak Next Peak Next Peak Next Peak Next Pk Left Phoperfies Next Pk Left Phoperfies Next Pk Left Marker - PiL-Pk Search Counter Marker Celta Marker Celta Marker Celta Marker Celta Conter	Selection Table 1	Control of the c
Specifium Analyzer 4 (Stepsifium Analyzer 4) (Stepsifium Analyzer 4) (Stepsifi	Marker - elect Marker Marker Sterngen Arker Face Prad		



Test Site	WZ-SR5	Test Engineer	Lynn Yang
Test Date	2024-03-05	Filter	8#

Test Mode	Data Rate	Channel No.	Frequency	Limit	Result
	/ Mbps		(MHz)	(dBc)	
BLE	1	00	2402	20	Pass
BLE	2	00	2402	20	Pass



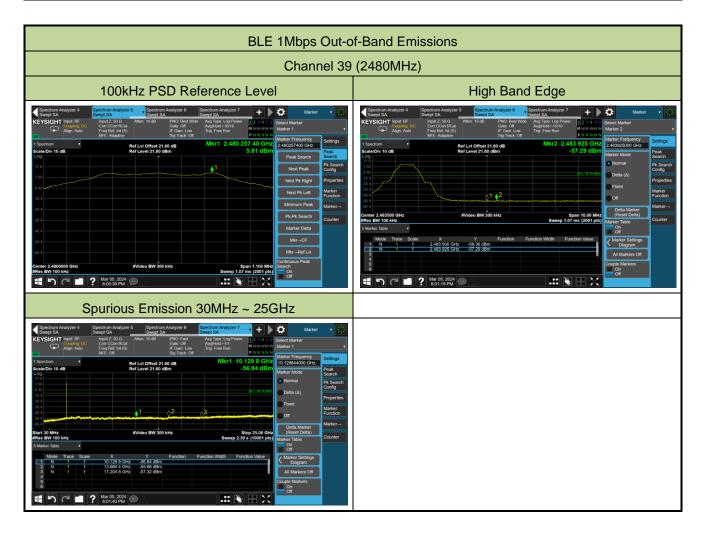




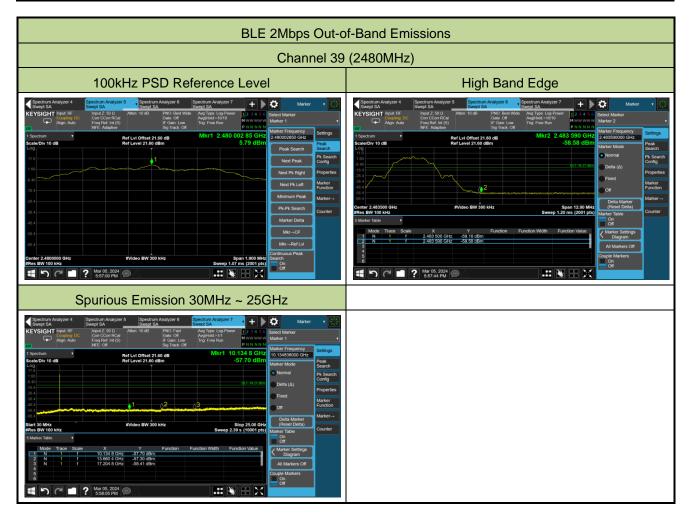


Test Site	WZ-SR5	Test Engineer	Lynn Yang
Test Date	2024-03-05	Filter	9#

Test Mode	Data Rate	Channel No.	Frequency	Limit	Result
	/ Mbps		(MHz)	(dBc)	
BLE	1	39	2480	20	Pass
BLE	2	39	2480	20	Pass









Mode 4

Test Site	WZ-SR5	Test Engineer	Lynn Yang
Test Date	2024-06-16		

Ant 2 Frequency	Ant 4 Frequency	Limit	Result
(MHz)	(MHz)	(dBc)	
2402	2404	20	Pass
2404	2402	20	Pass
2478	2480	20	Pass
2480	2478	20	Pass

