

**Block Diagram**

The block diagram of the WPN824 product includes an Atheros AR2313 SoC and memory system, with one 10/100M Ethernet WAN interface and one 4-port 10/100 Ethernet switch. All Ethernet ports support Auto-negotiation as well as Auto-MDI/MDIX. An on board Atheros AR2112 radio supports the wireless interface. Status LEDs are driven by the CPU. A power subsystem including one switching regulator and two linear regulators take 12VDC in to create the voltage levels required by the CPU, memory, wireless interface, and switch.

Following is a block diagram of the circuit board:

