

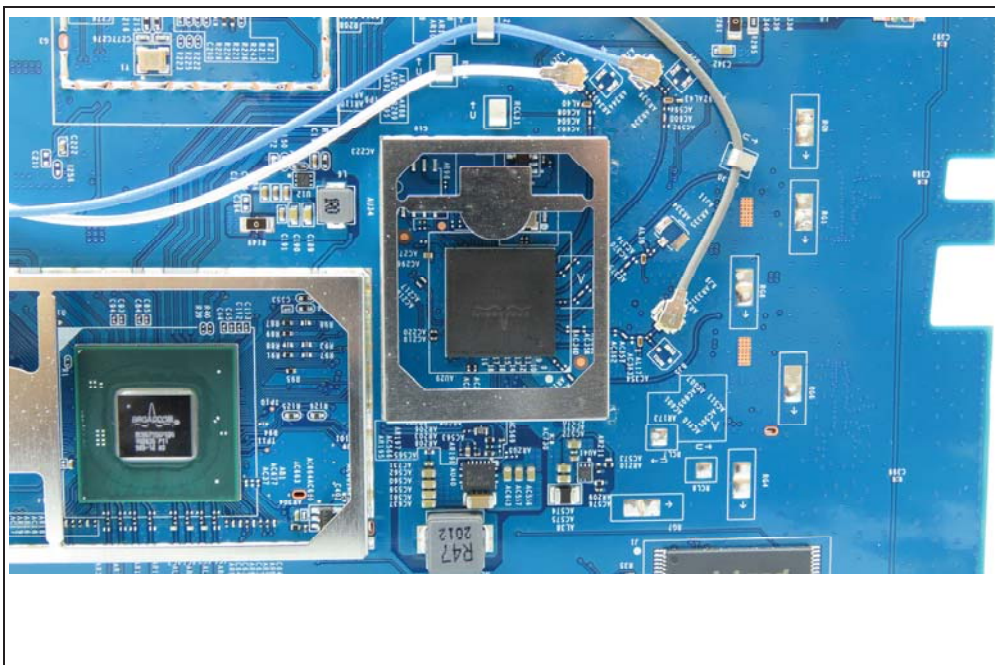
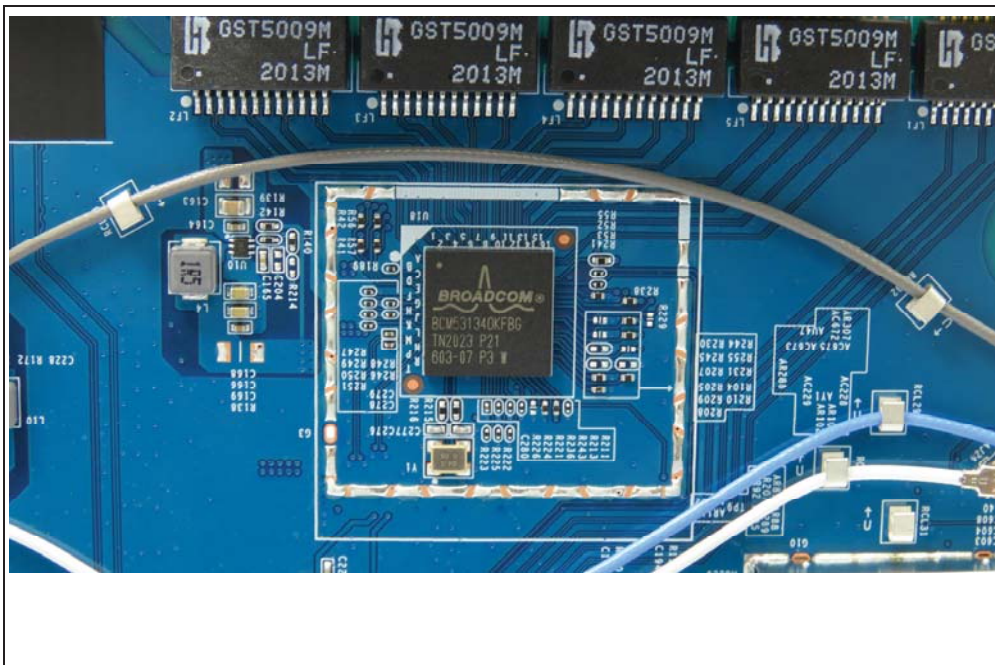


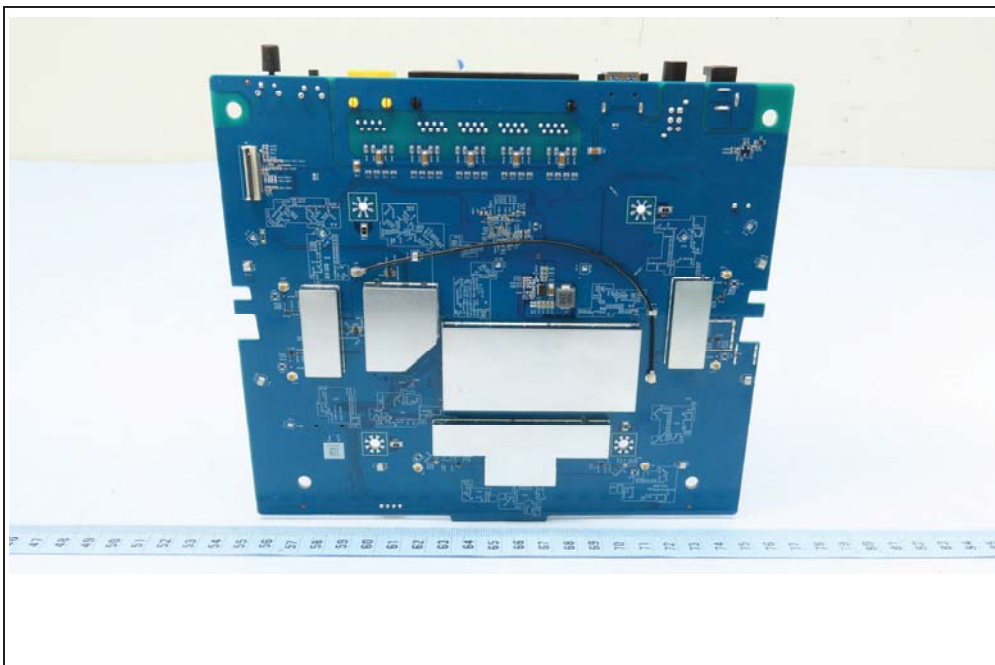
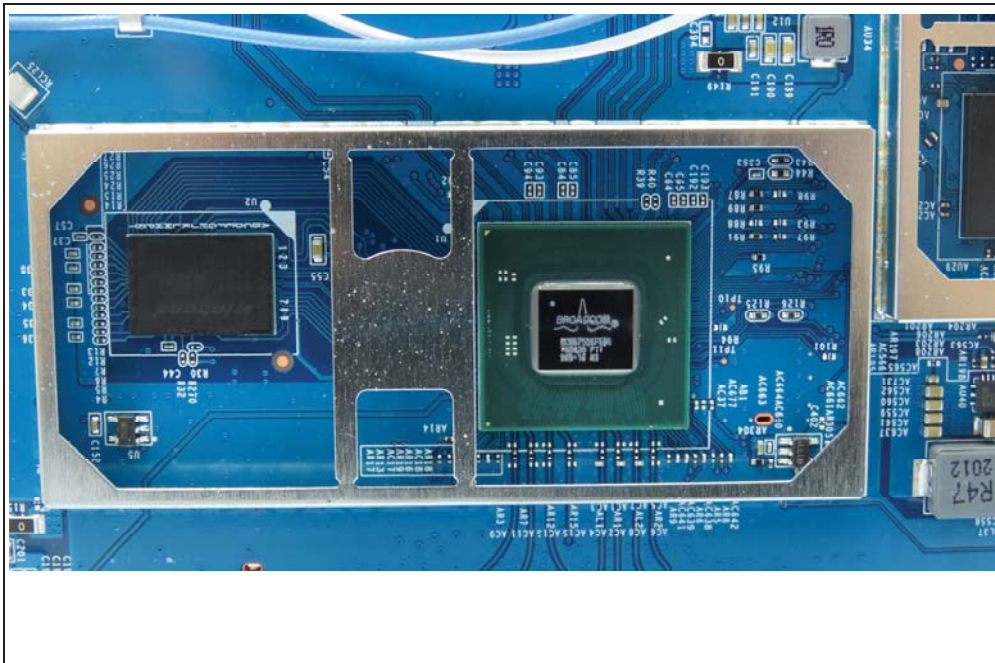
**BUREAU
VERITAS**

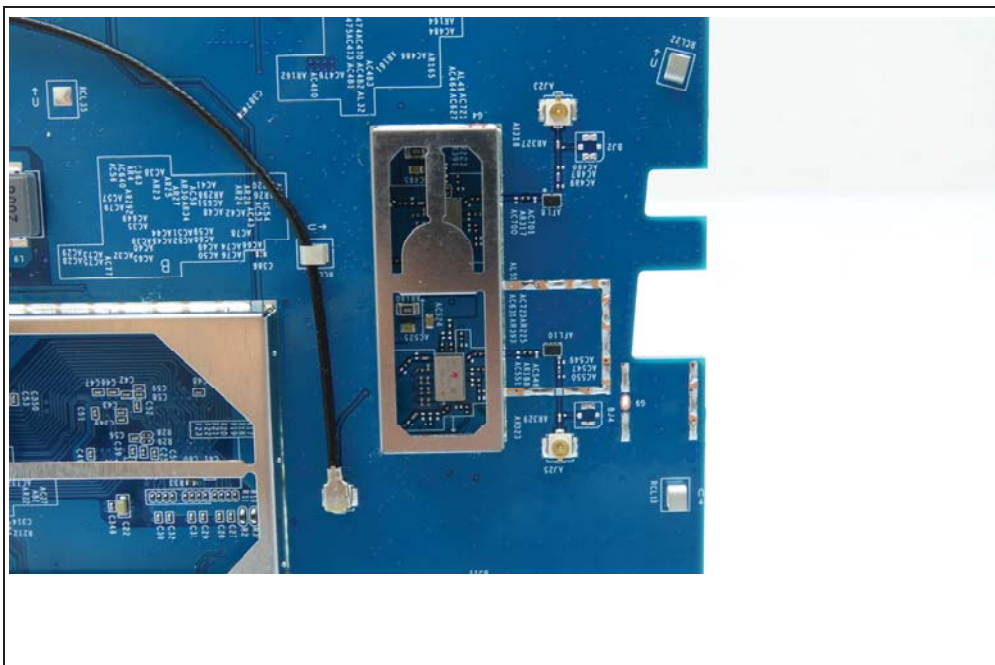
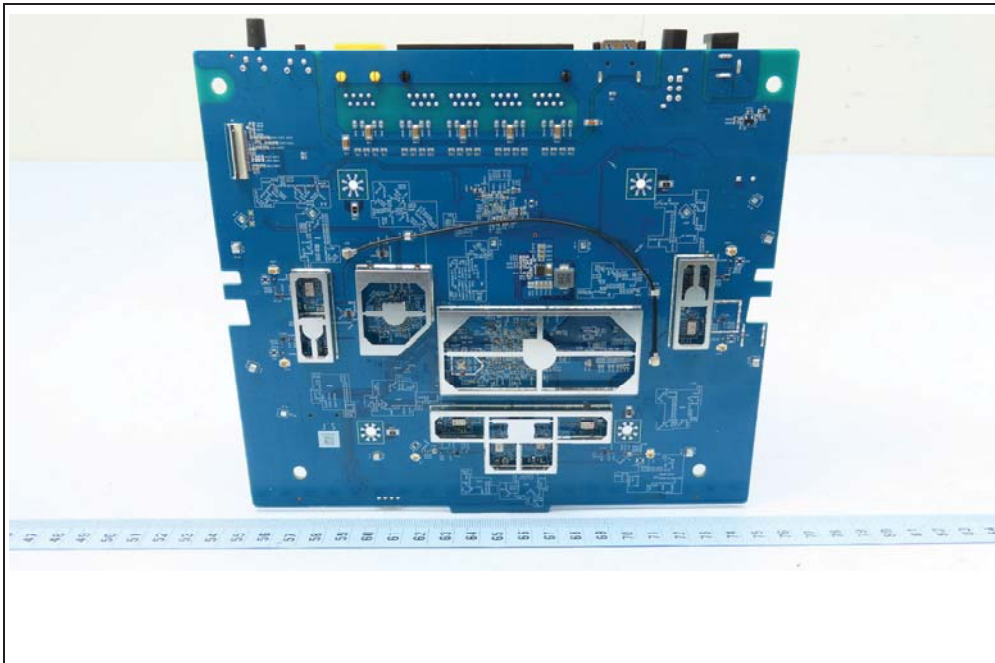
CONSTRUCTION PHOTOS OF EUT

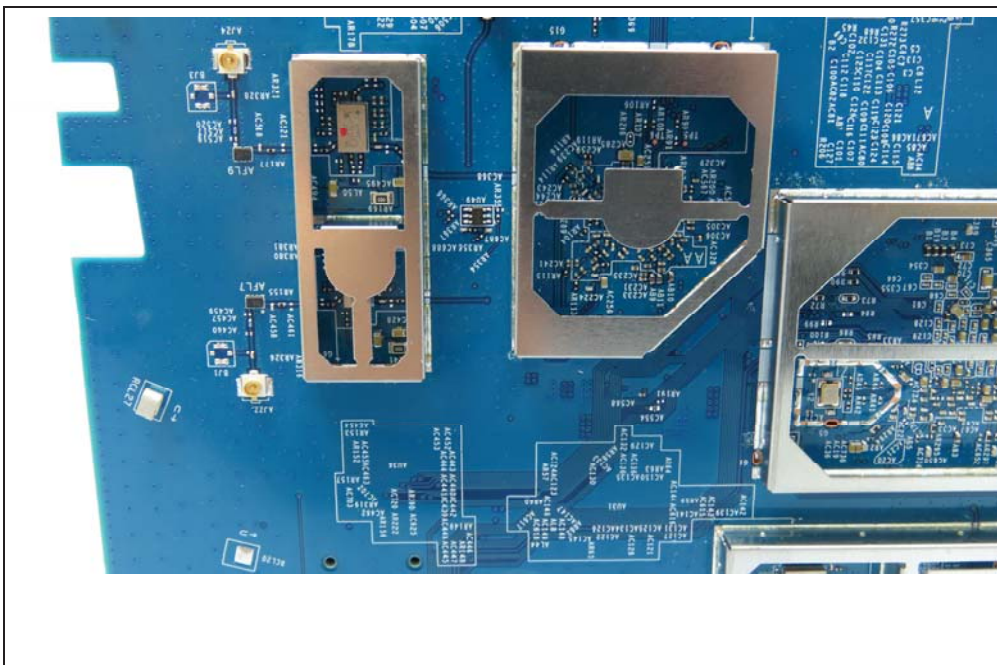
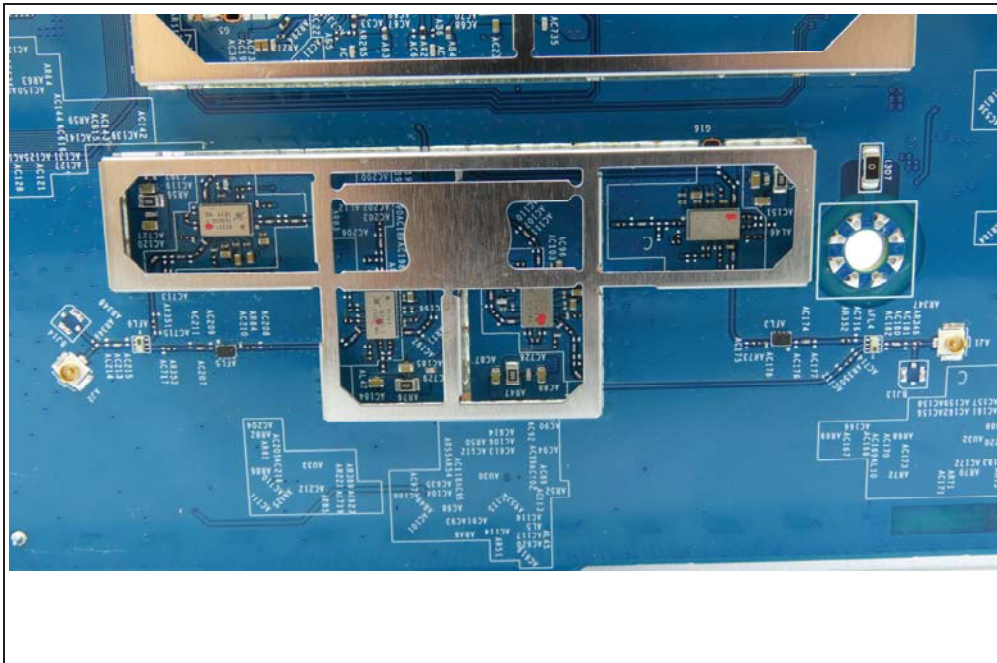




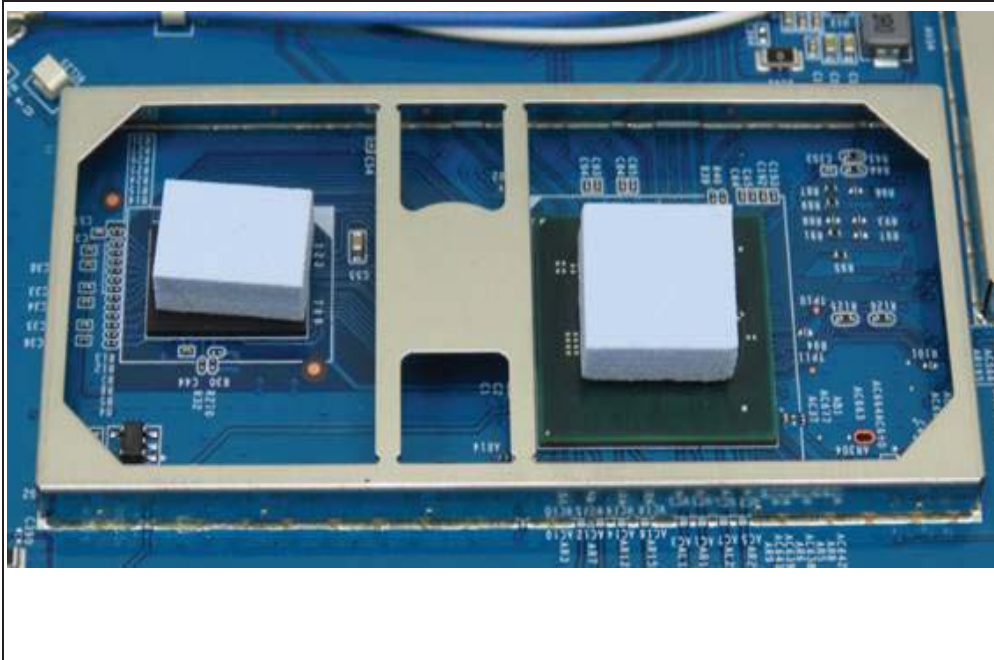




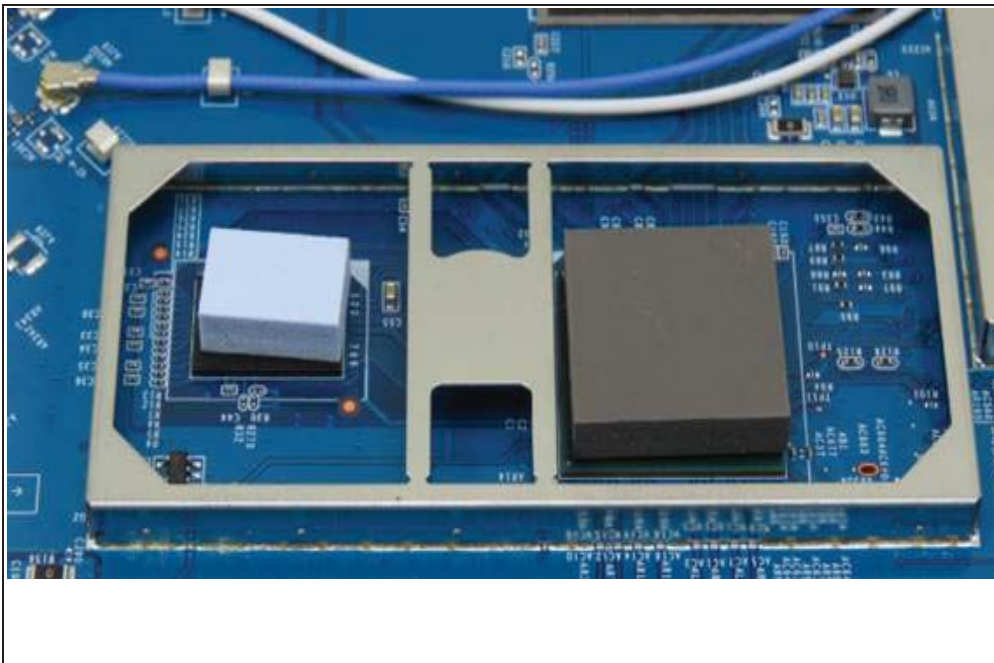


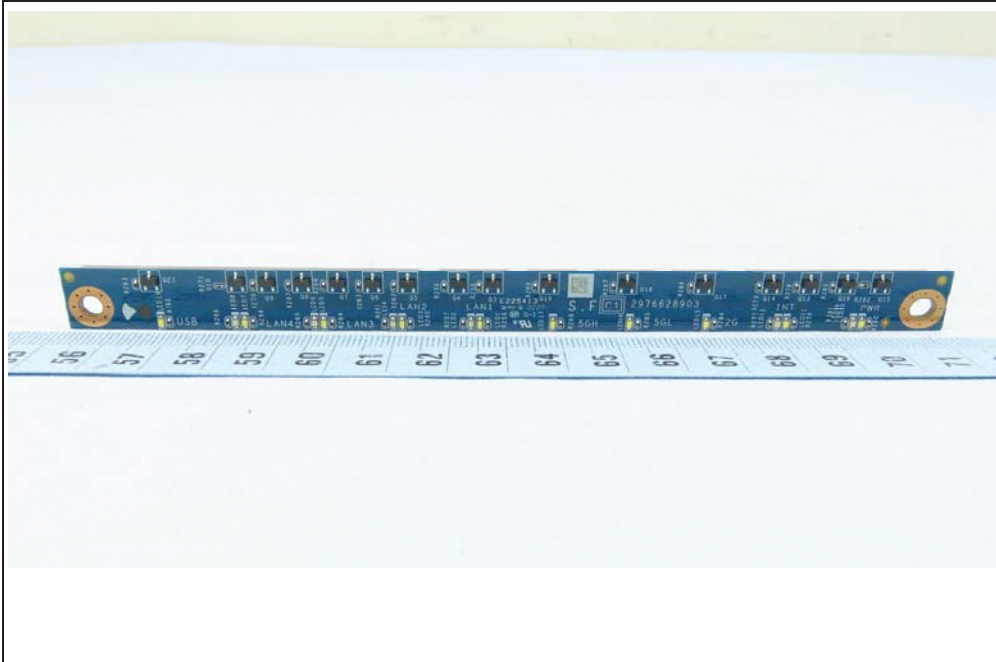


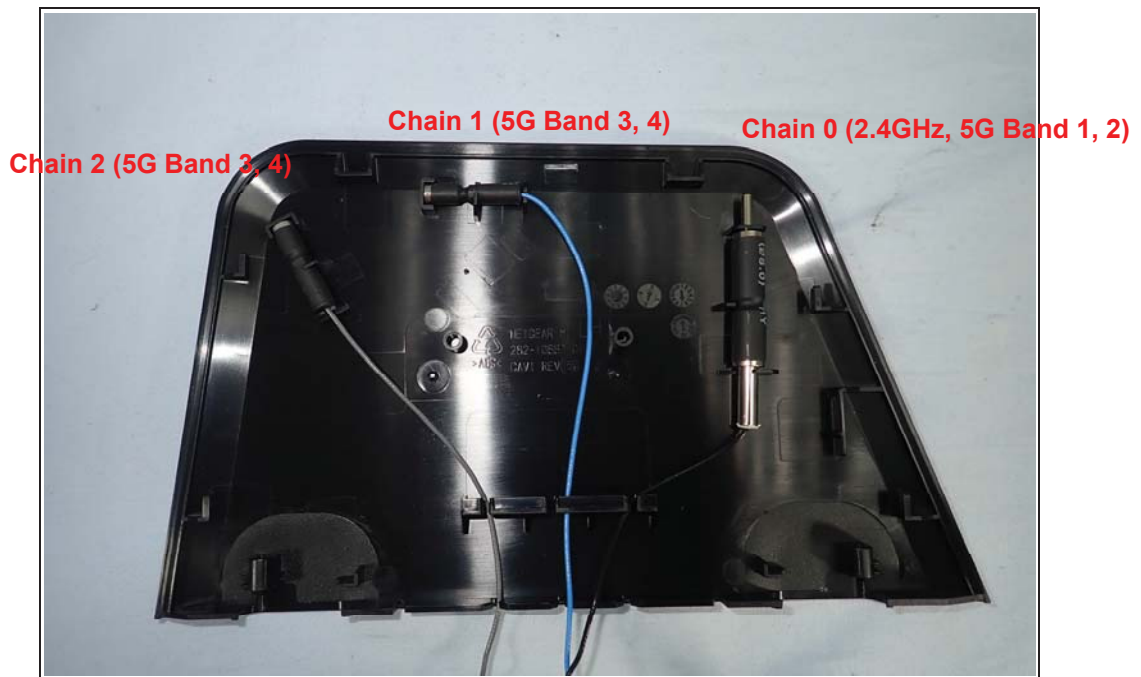
Option A: PCB with thermal pad



Option B: PCB with thermal pad







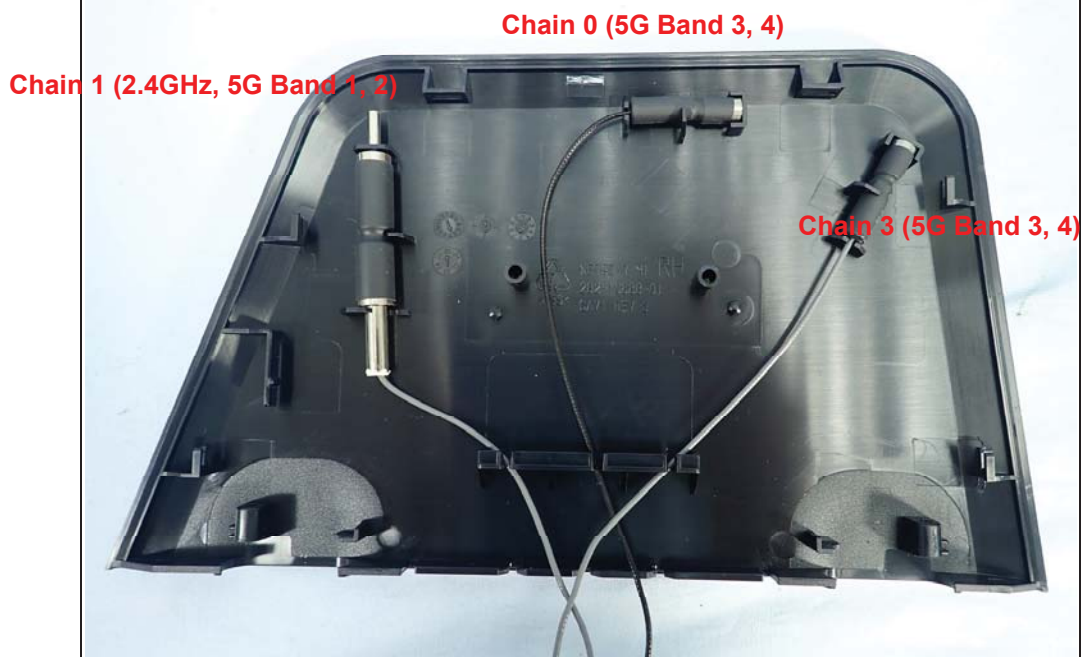


BUREAU
VERITAS





**BUREAU
VERITAS**





BUREAU
VERITAS

