

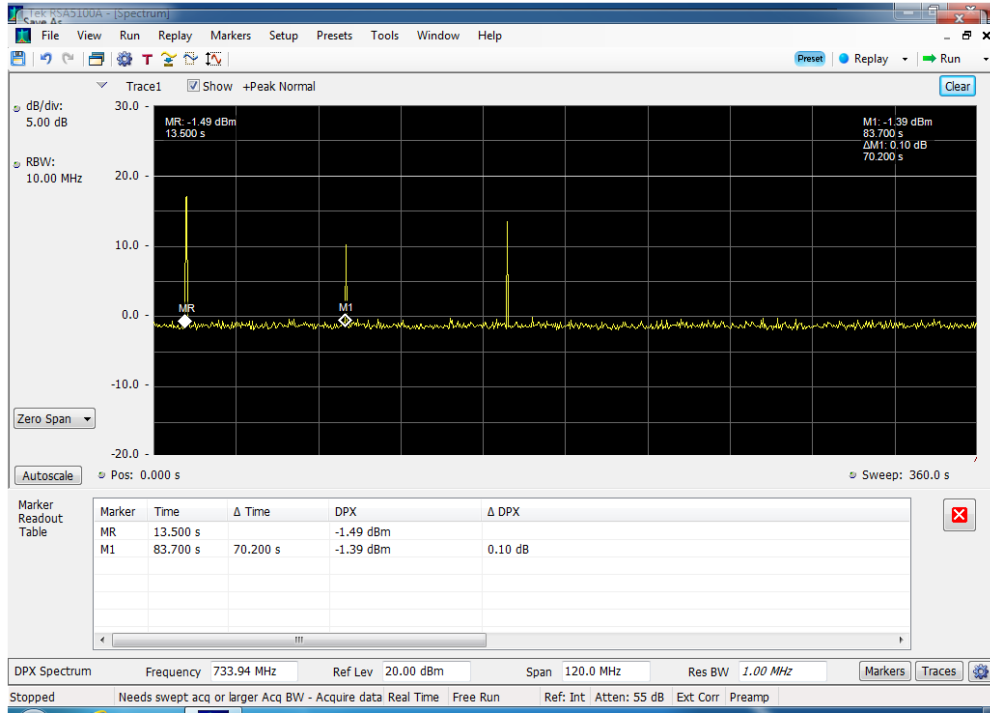


Annex I

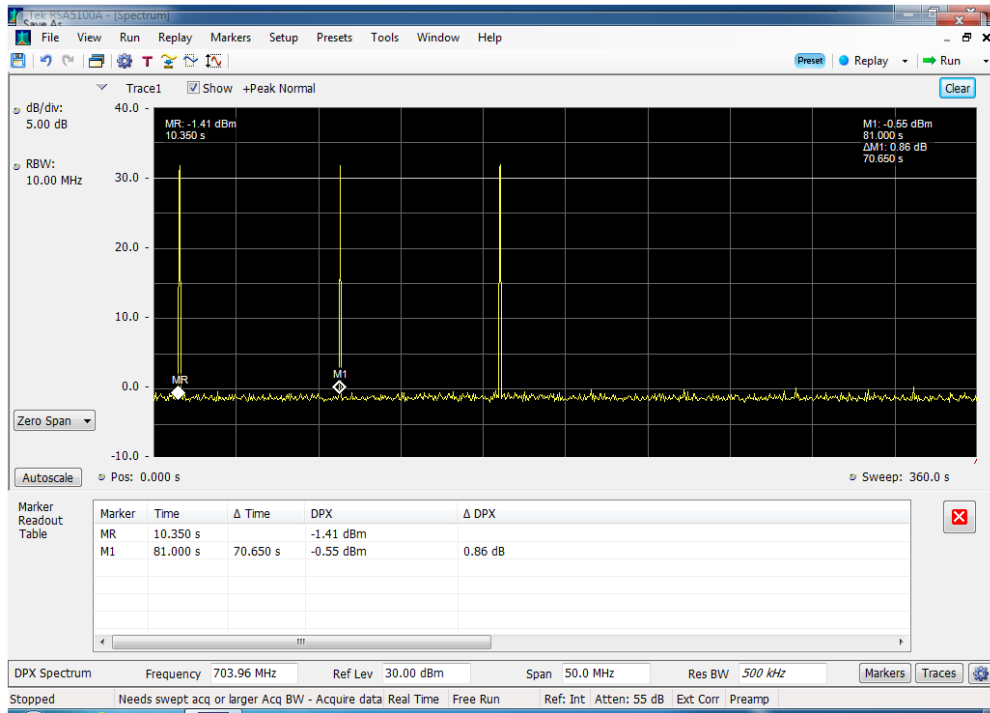
Anti-Oscillation



of Restarts_B12 DL_728 - 746 MHz_p2040009

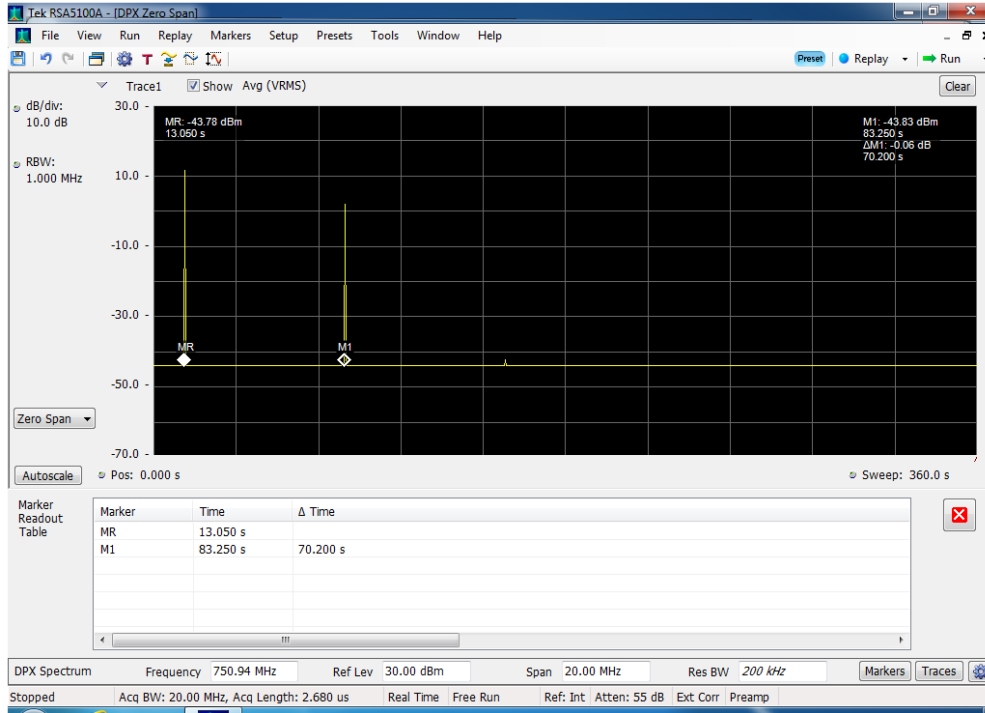


of Restarts_B12 UL_698 - 716 MHz_p2040009

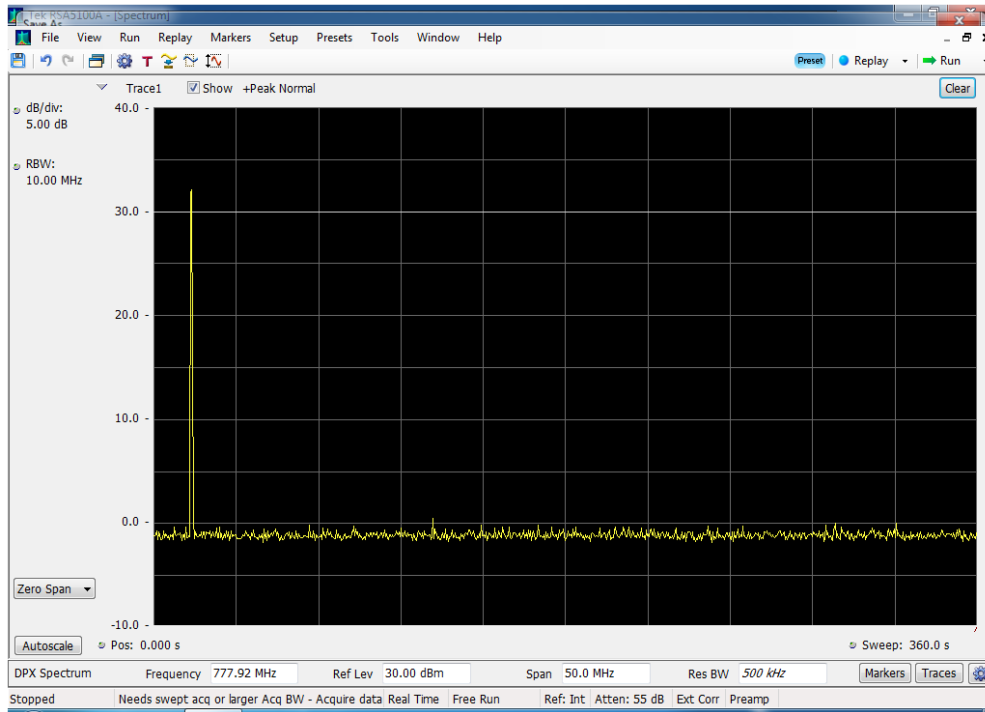




of Restarts_B13 DL_746 - 757 MHz_p2040009

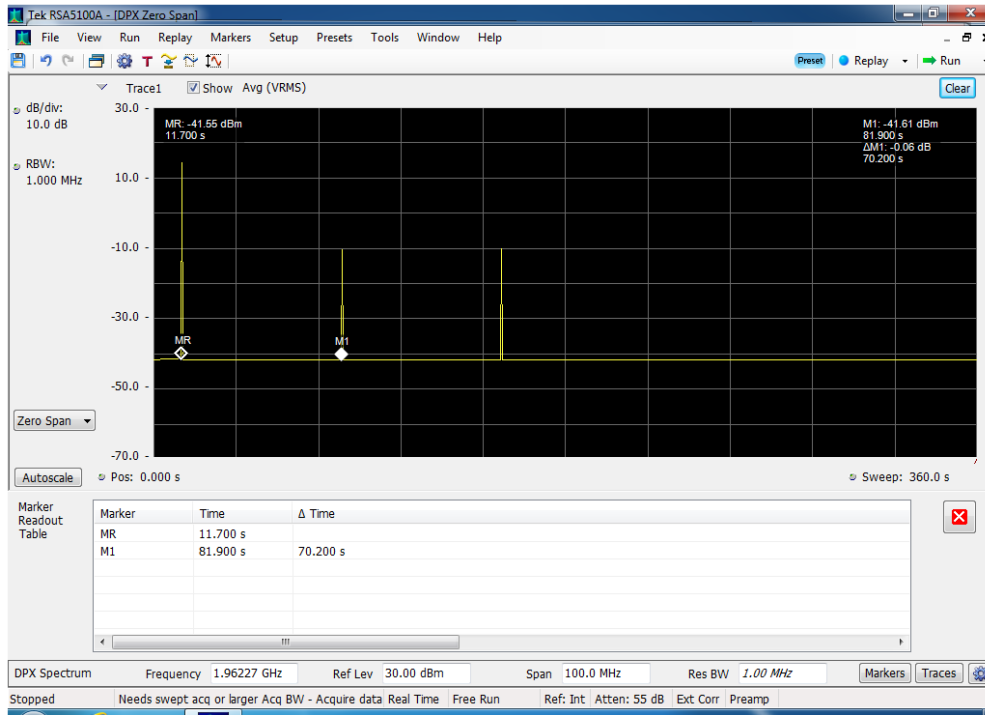


of Restarts_B13 UL_776 - 787 MHz_p2040009

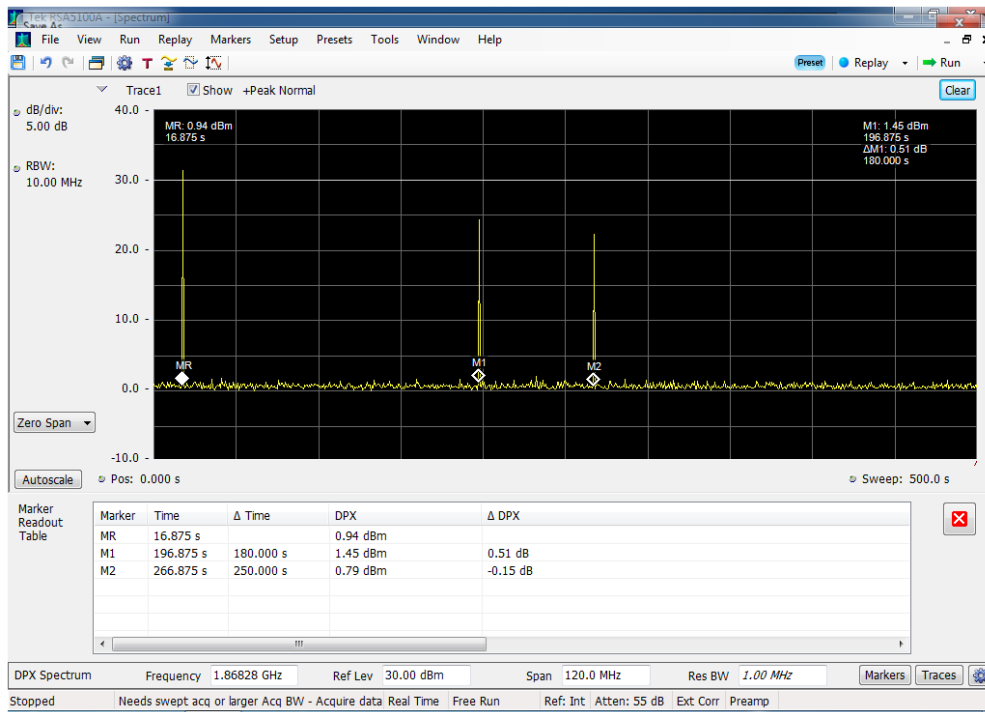




of Restarts_B2 DL_1930 - 1995 MHz_p2040009

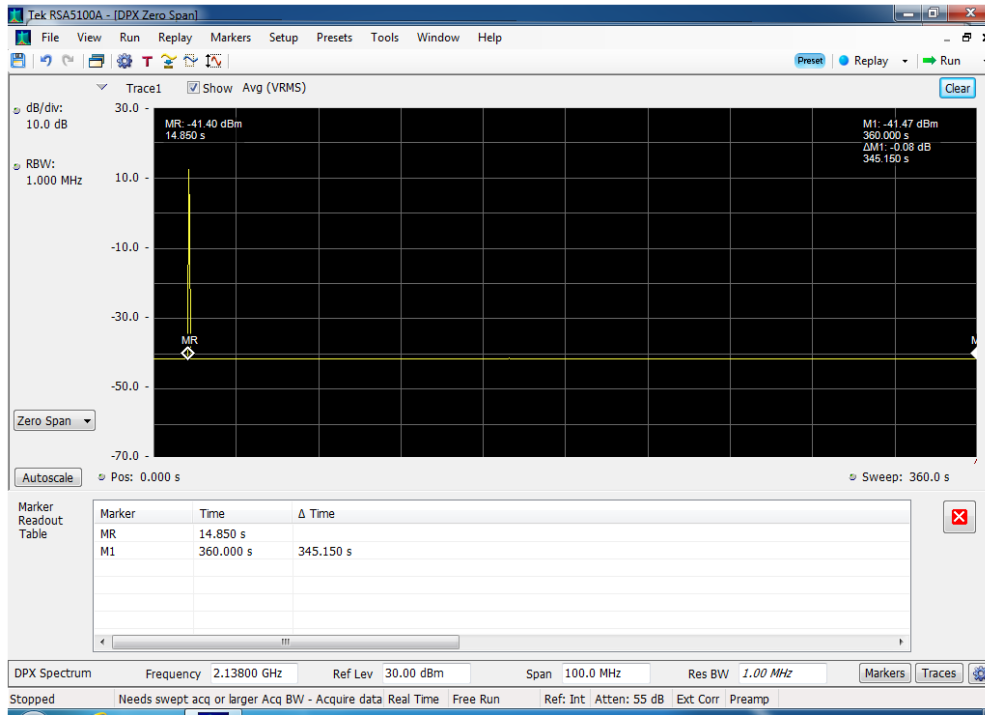


of Restarts_B2 UL_1850 - 1915 MHz_p2040009

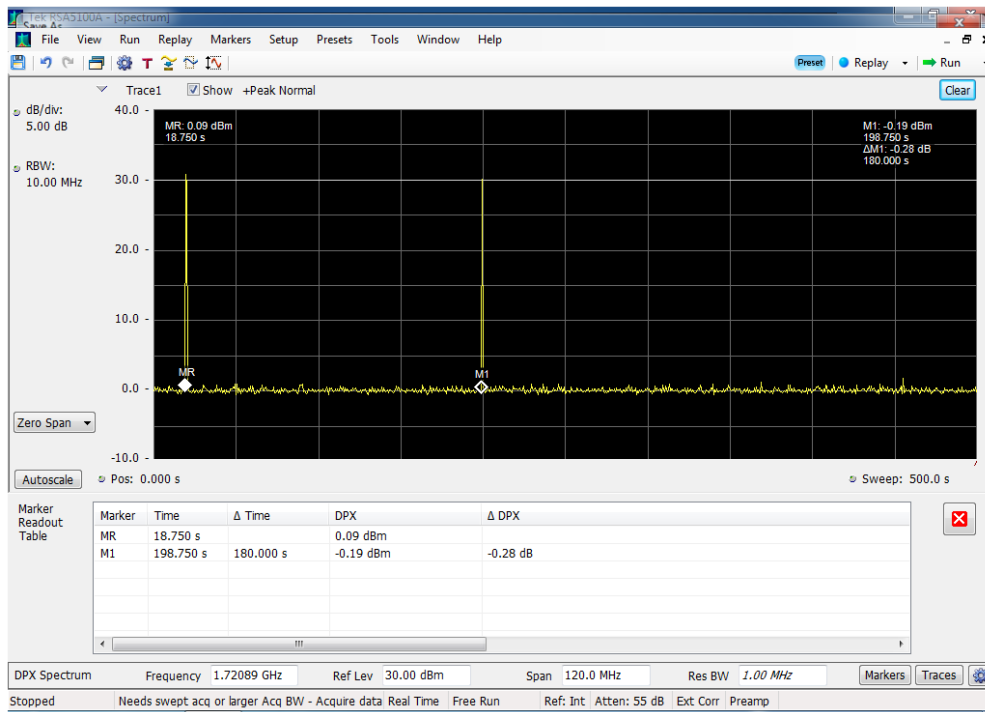




of Restarts_B4 DL_2110 - 2155 MHz_p2040009

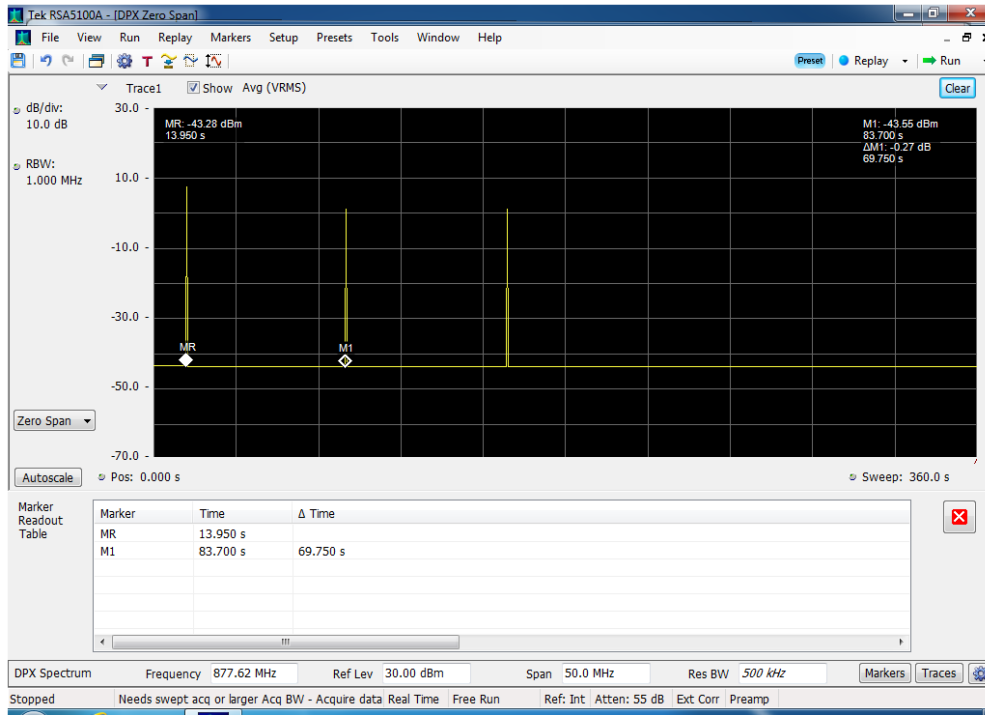


of Restarts_B4 UL_1710 - 1755 MHz_p2040009

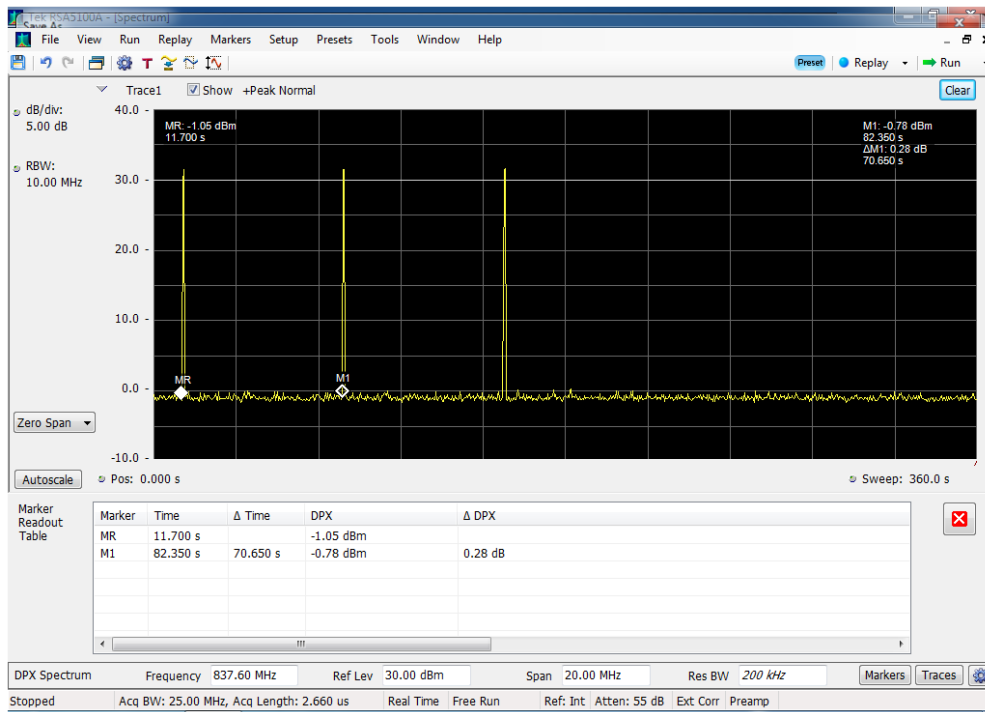




of Restarts_B5 DL_869 - 894 MHz_p2040009

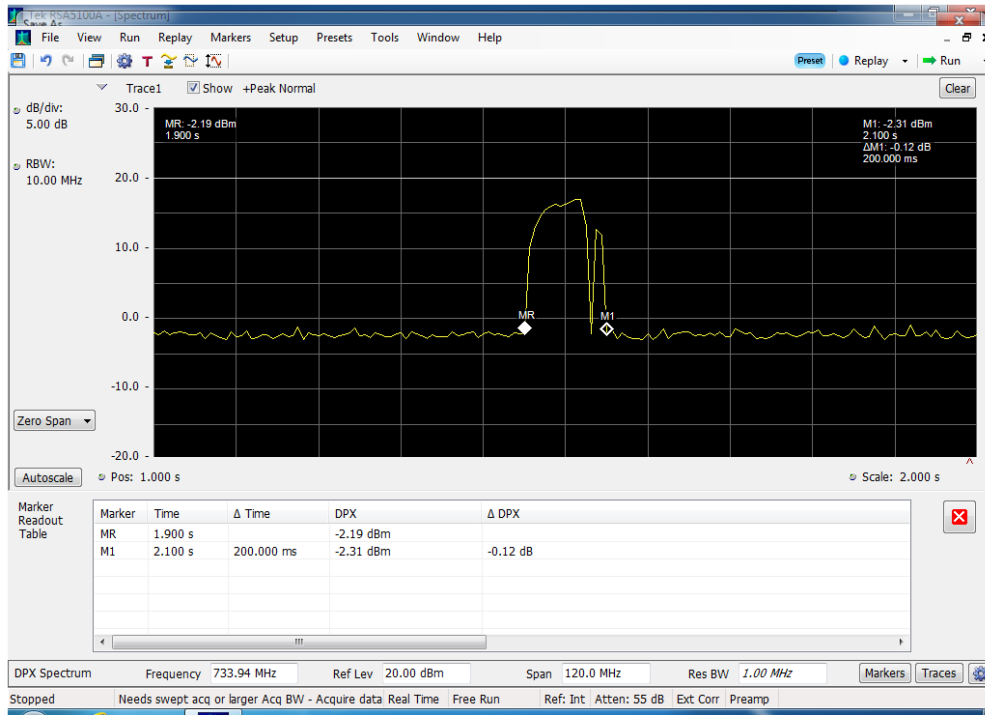


of Restarts_B5 UL_824 - 849 MHz_p2040009

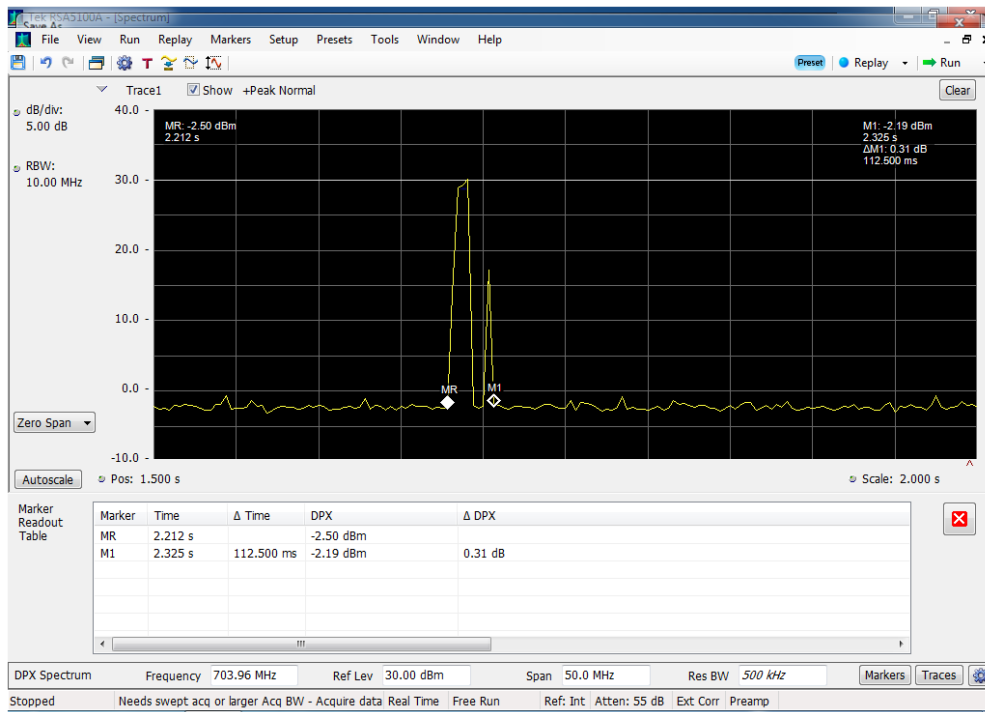




Detection Time_B12 DL_728 - 746 MHz_p2040009

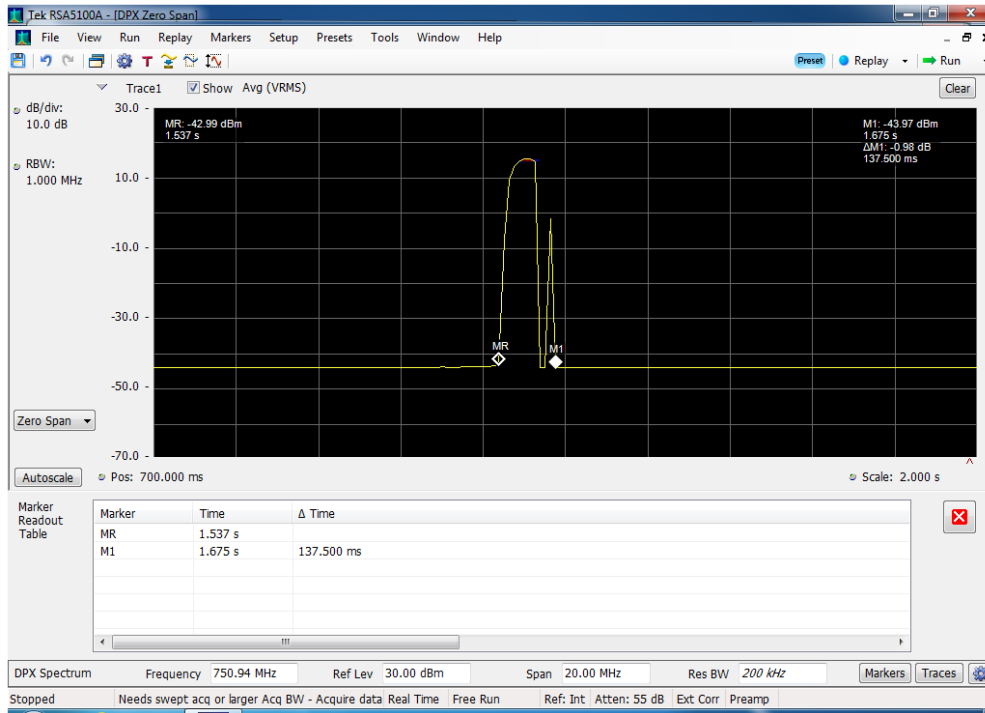


Detection Time_B12 UL_698 - 716 MHz_p2040009

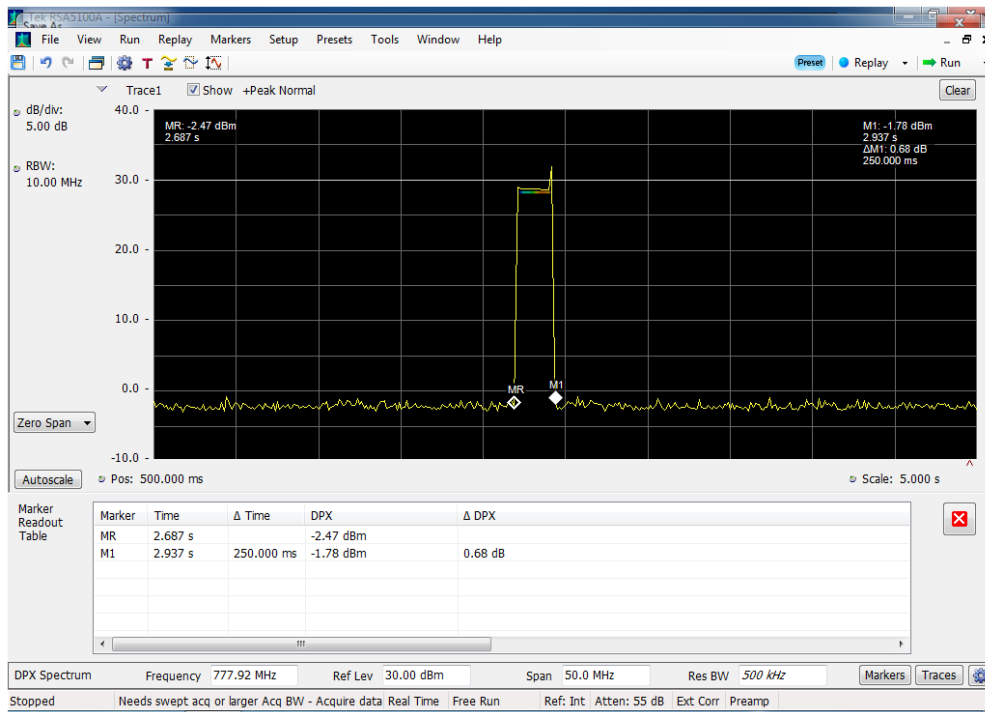




Detection Time_B13 DL_746 - 757 MHz_p2040009

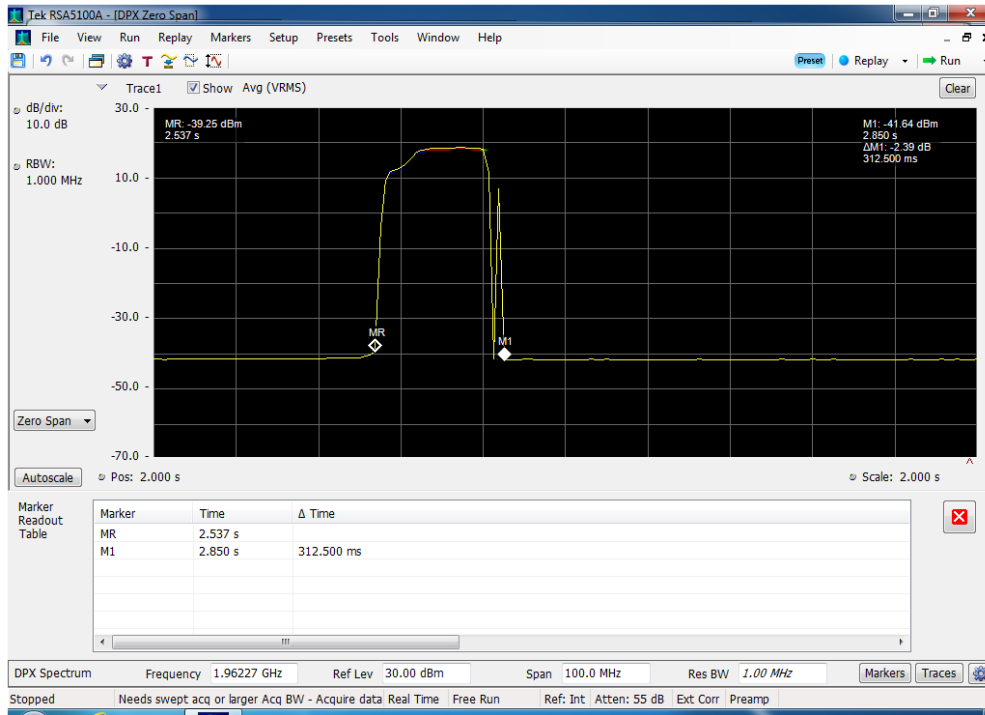


Detection Time_B13 UL_776 - 787 MHz_p2040009

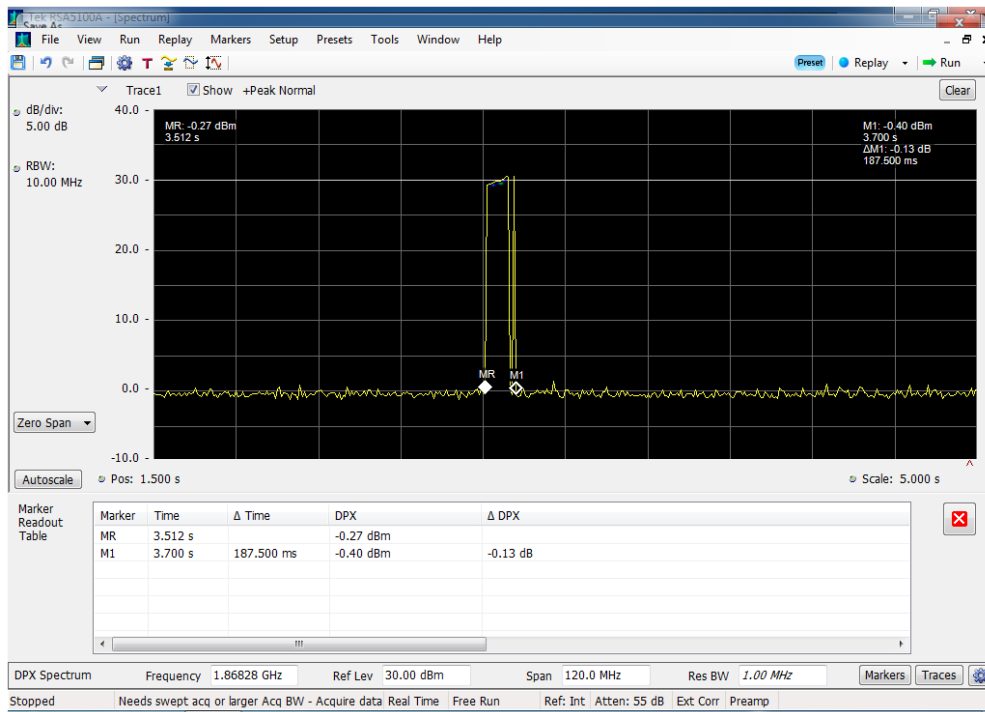




Detection Time_B2 DL_1930 - 1995 MHz_p2040009

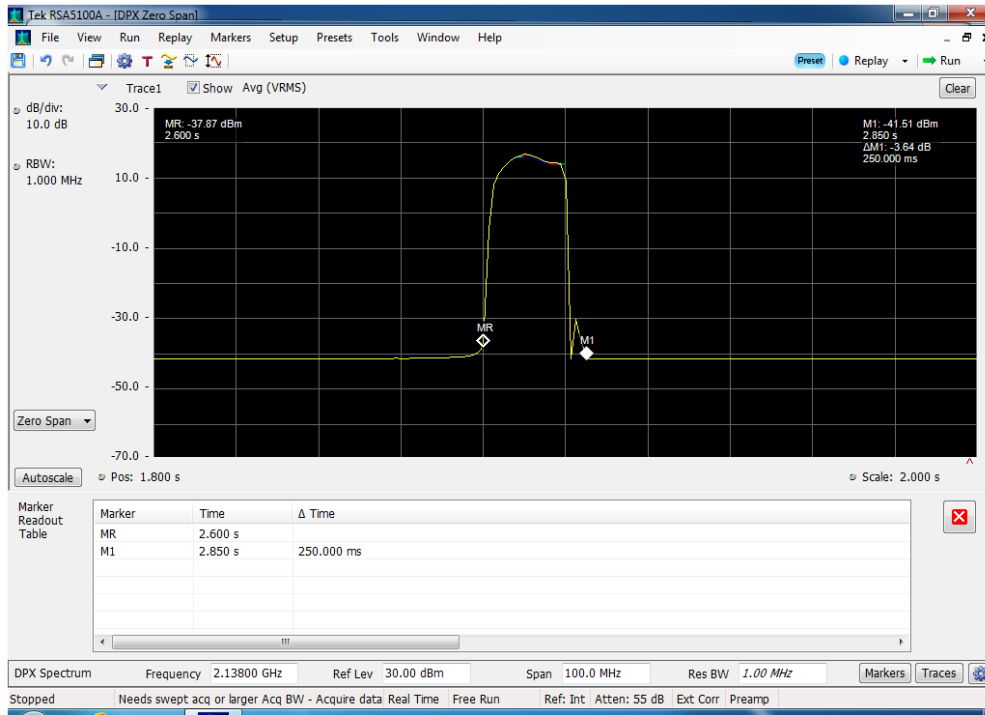


Detection Time_B2 UL_1850 - 1915 MHz_p2040009

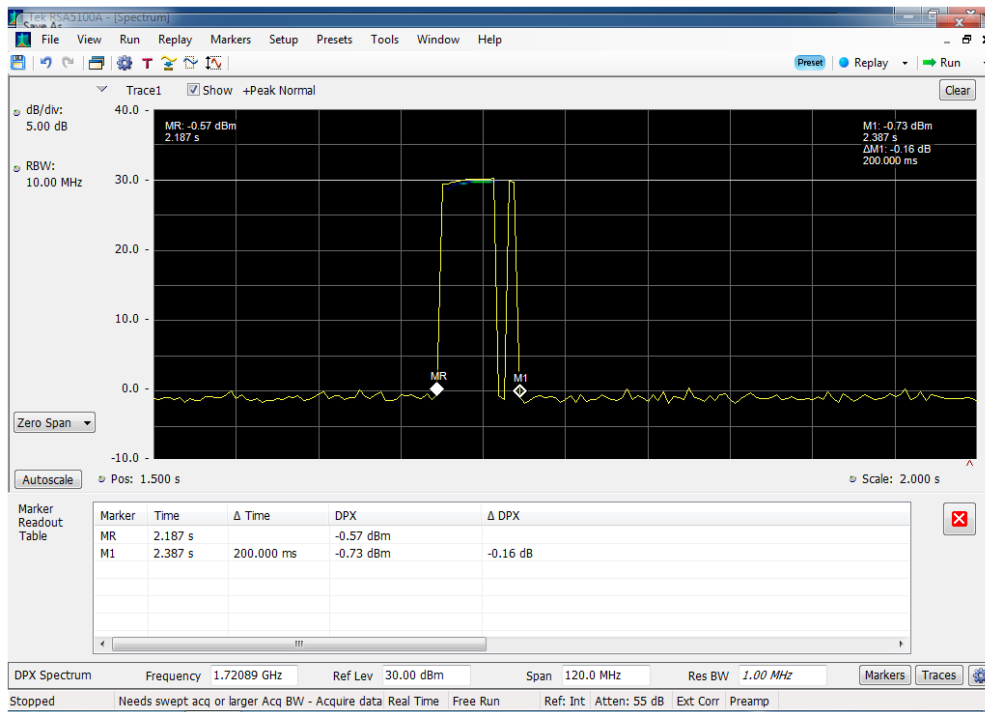




Detection Time_B4 DL_2110 - 2155 MHz_p2040009

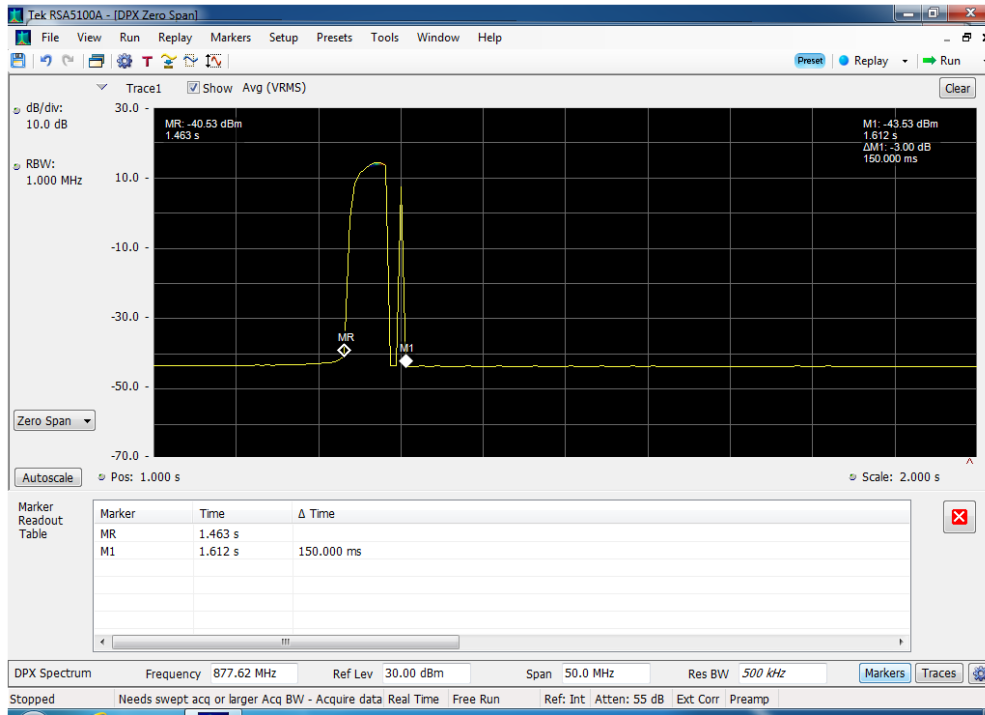


Detection Time_B4 UL_1710 - 1755 MHz_p2040009

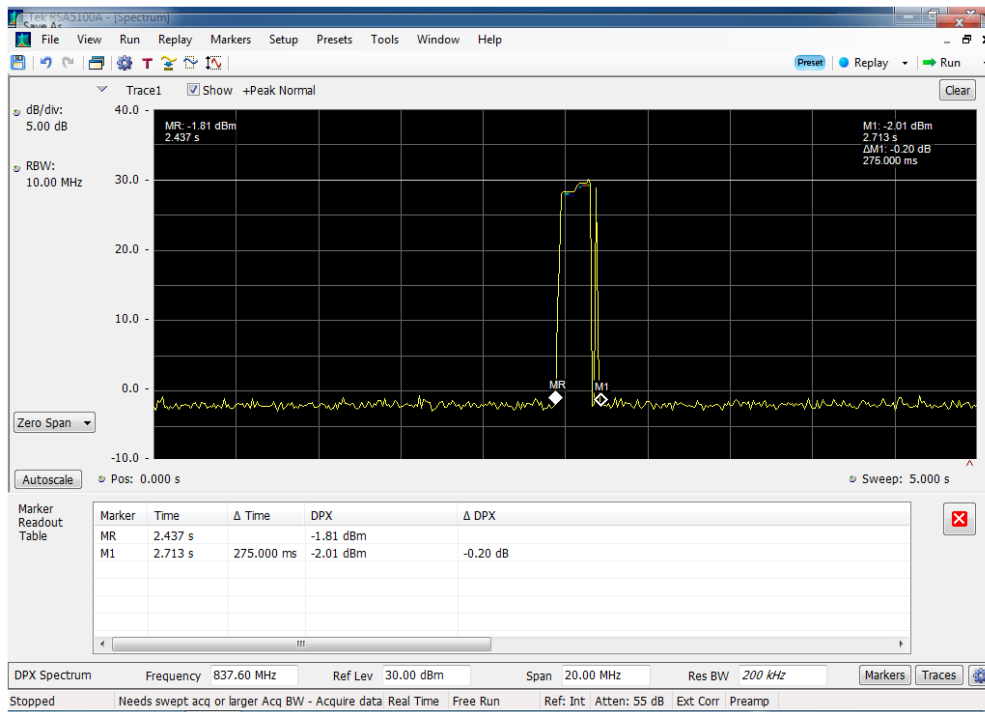




Detection Time_B5 DL_869 - 894 MHz_p2040009

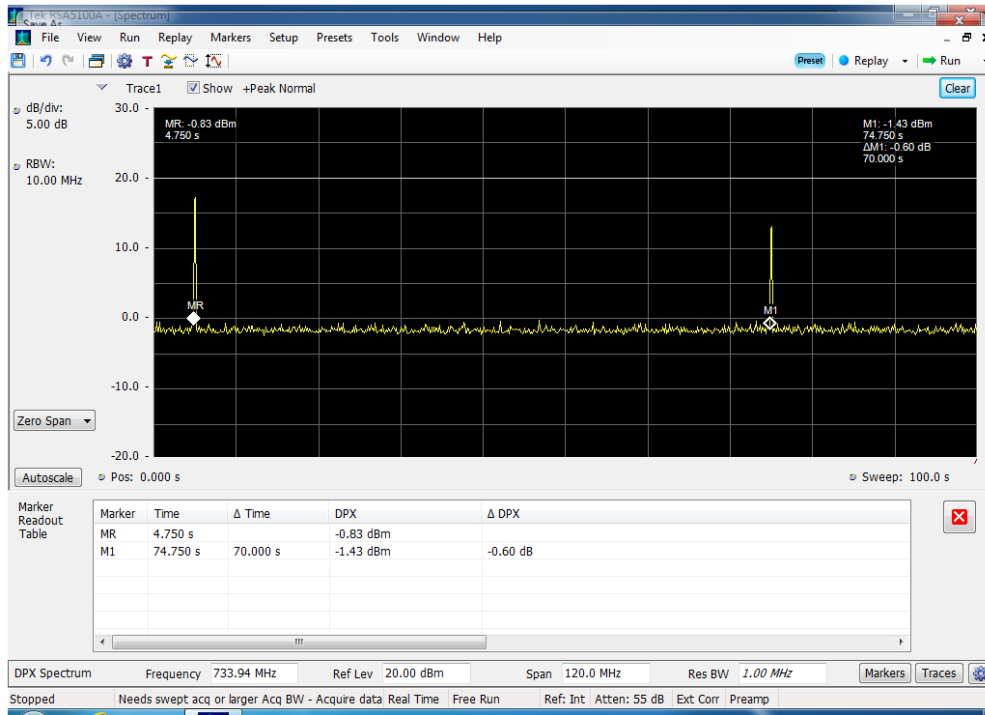


Detection Time_B5 UL_824 - 849 MHz_p2040009





Restart Time_B12 DL_728 - 746 MHz_p2040009

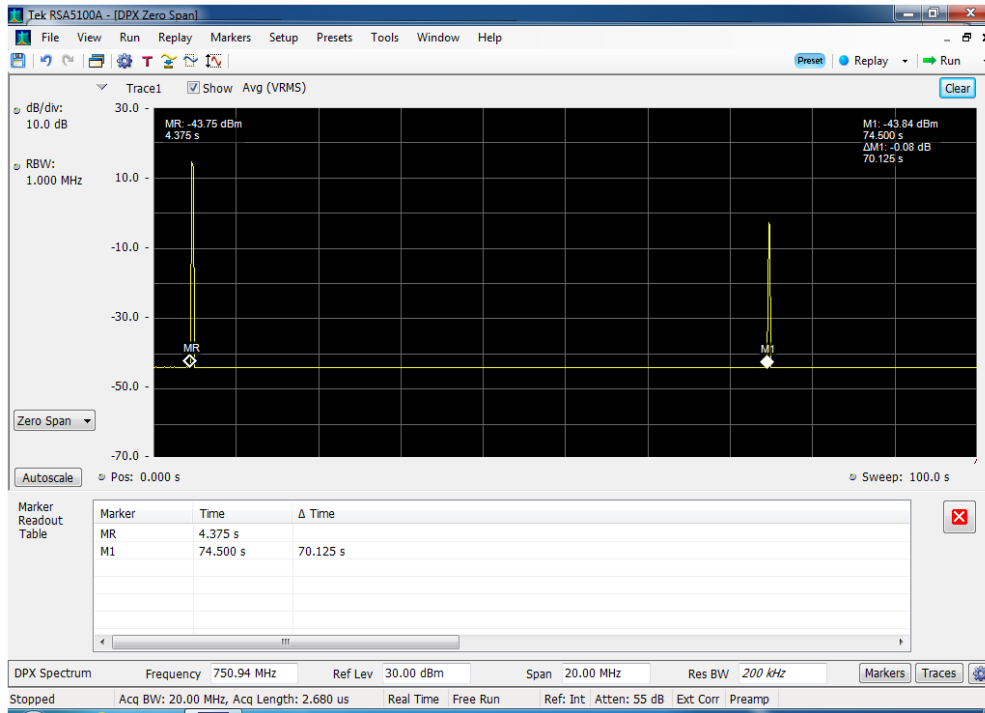


Restart Time_B12 UL_698 - 716 MHz_p2040009

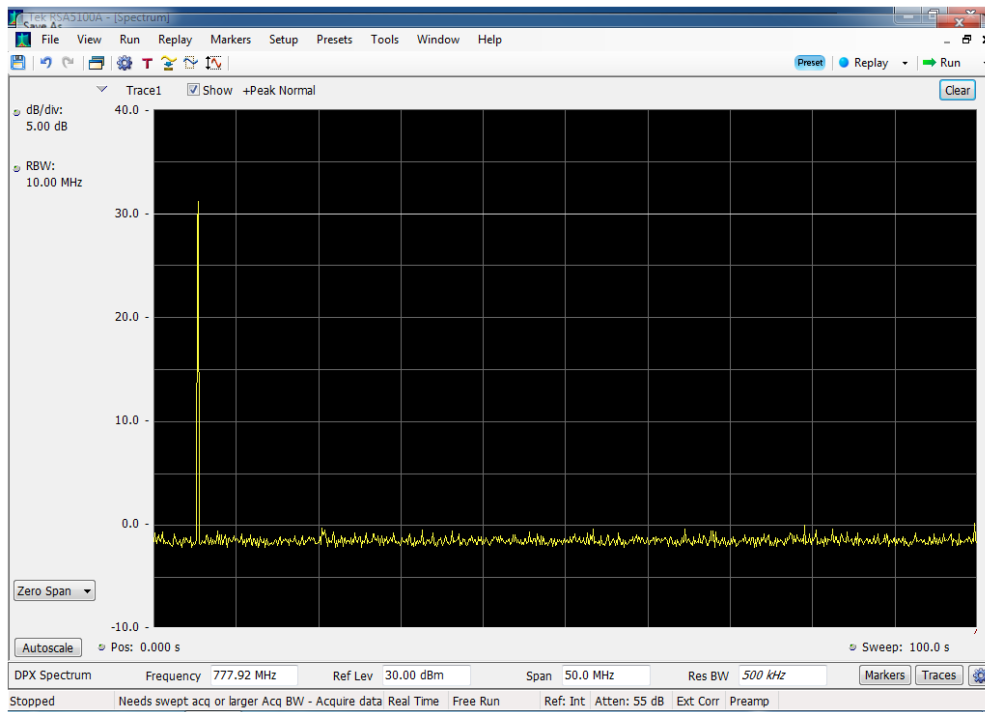




Restart Time_B13 DL_746 - 757 MHz_p2040009

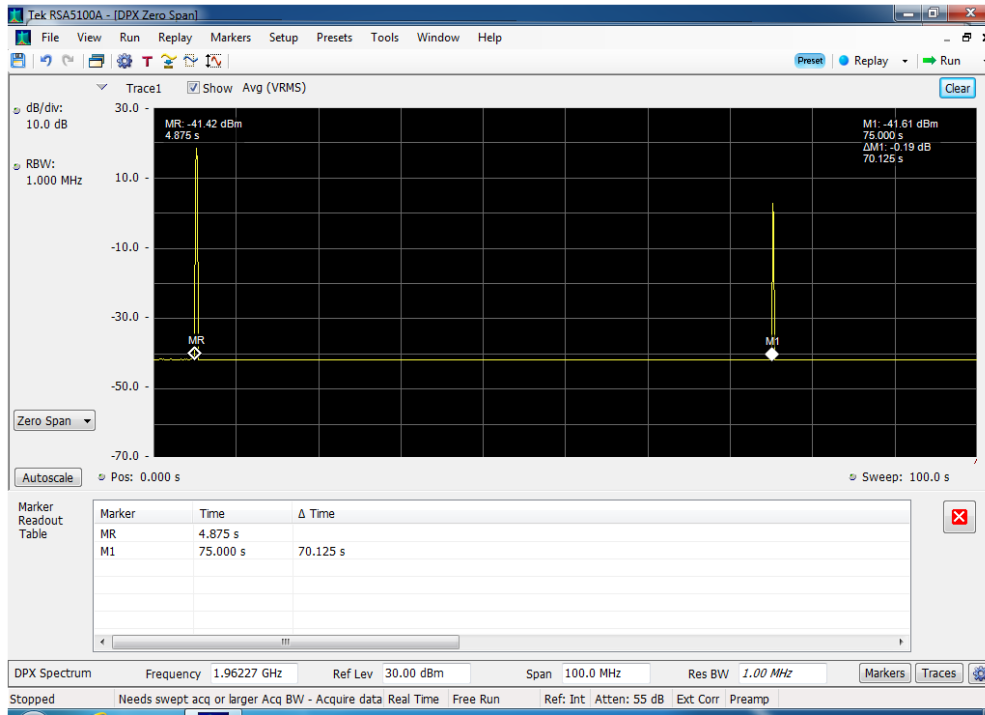


Restart Time_B13 UL_776 - 787 MHz_p2040009

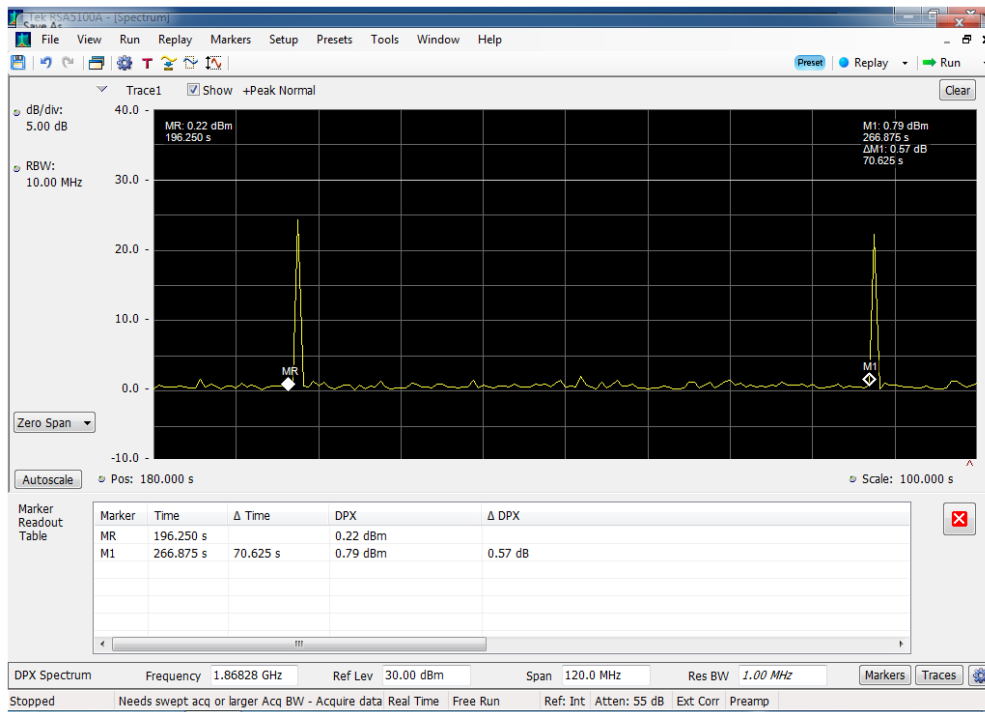




Restart Time_B2 DL_1930 - 1995 MHz_p2040009

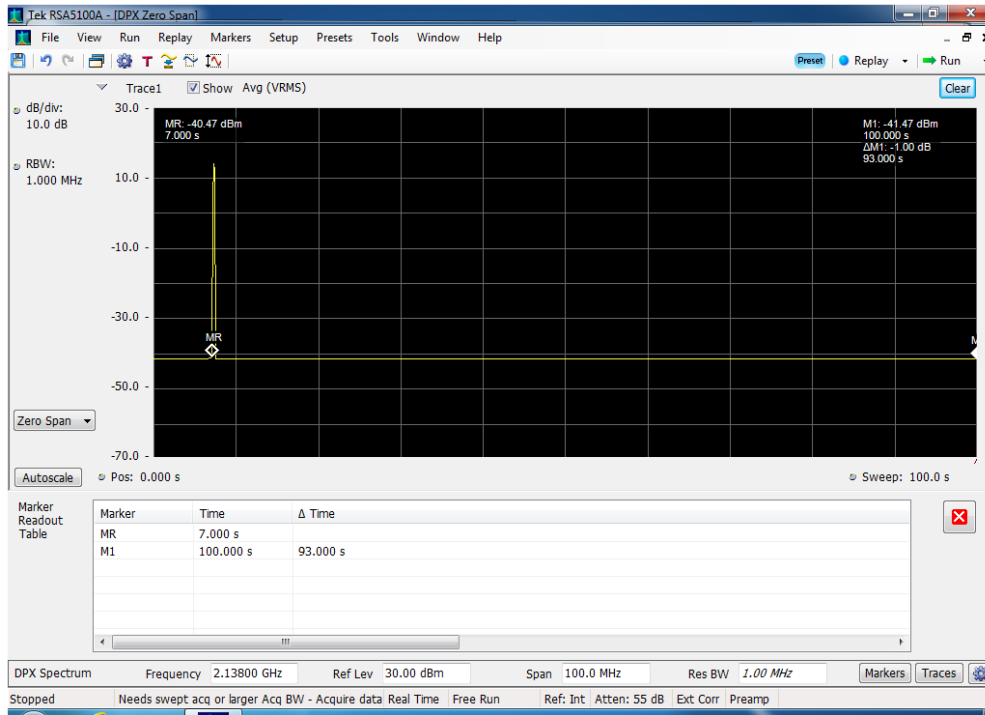


Restart Time_B2 UL_1850 - 1915 MHz_p2040009

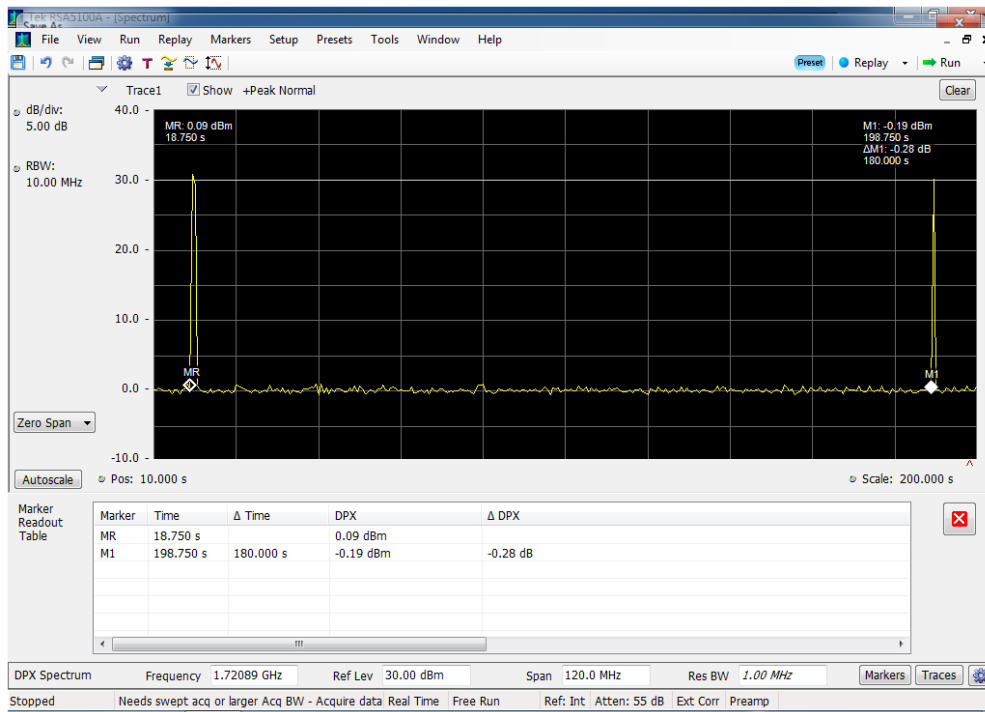




Restart Time_B4 DL_2110 - 2155 MHz_p2040009

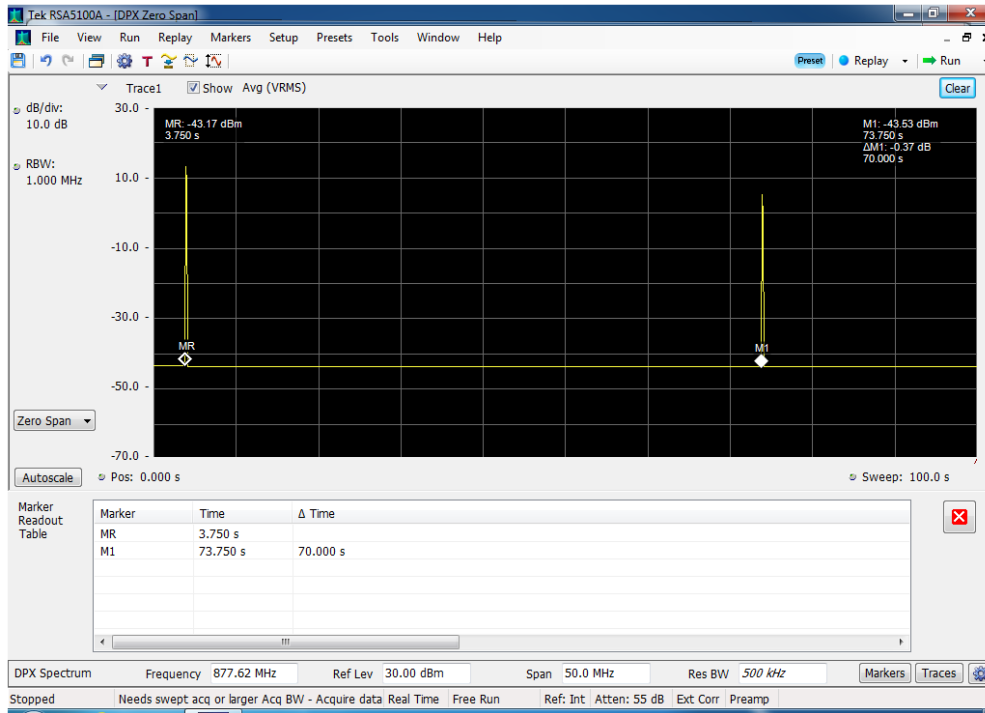


Restart Time_B4 UL_1710 - 1755 MHz_p2040009





Restart Time_B5 DL_869 - 894 MHz_p2040009



Restart Time_B5 UL_824 - 849 MHz_p2040009

