



## **Annex I**

### **Anti - Oscillation**



### Osc Det\_DL\_1930 - 1995 MHz

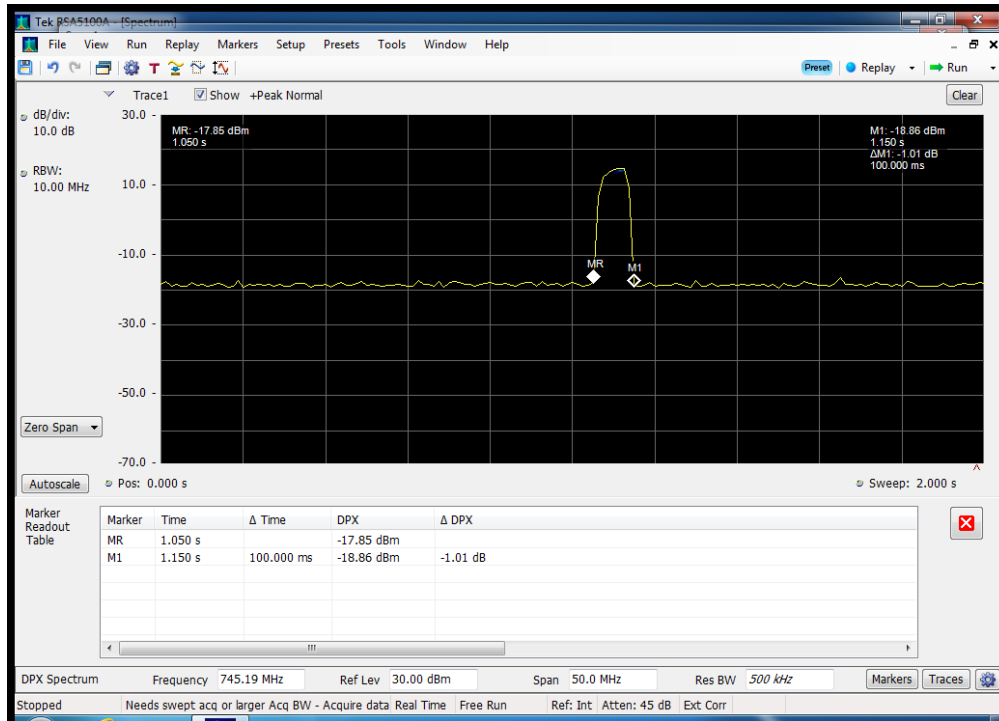


### Osc Det\_DL\_2110 - 2155 MHz

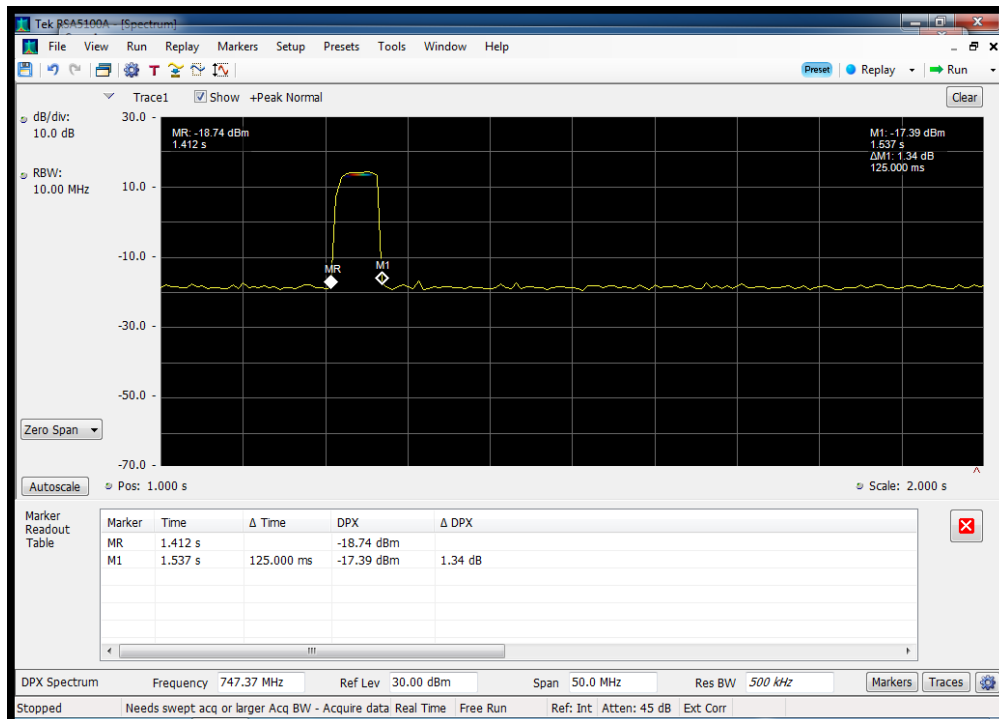




### Osc Det\_DL\_728 - 746 MHz

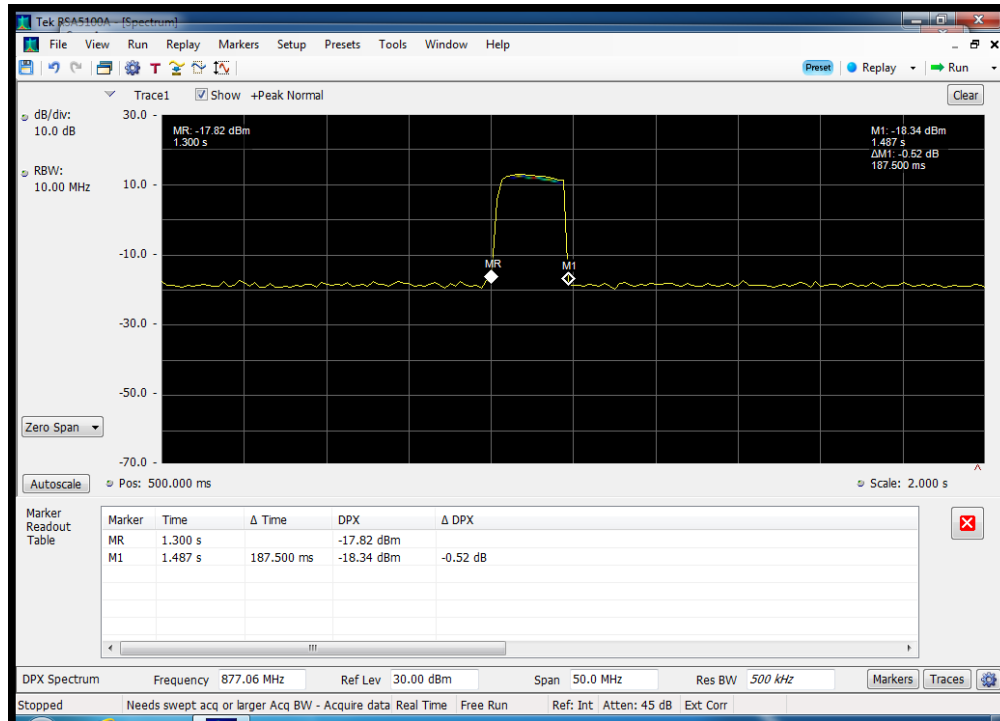


### Osc Det\_DL\_746 - 757 MHz

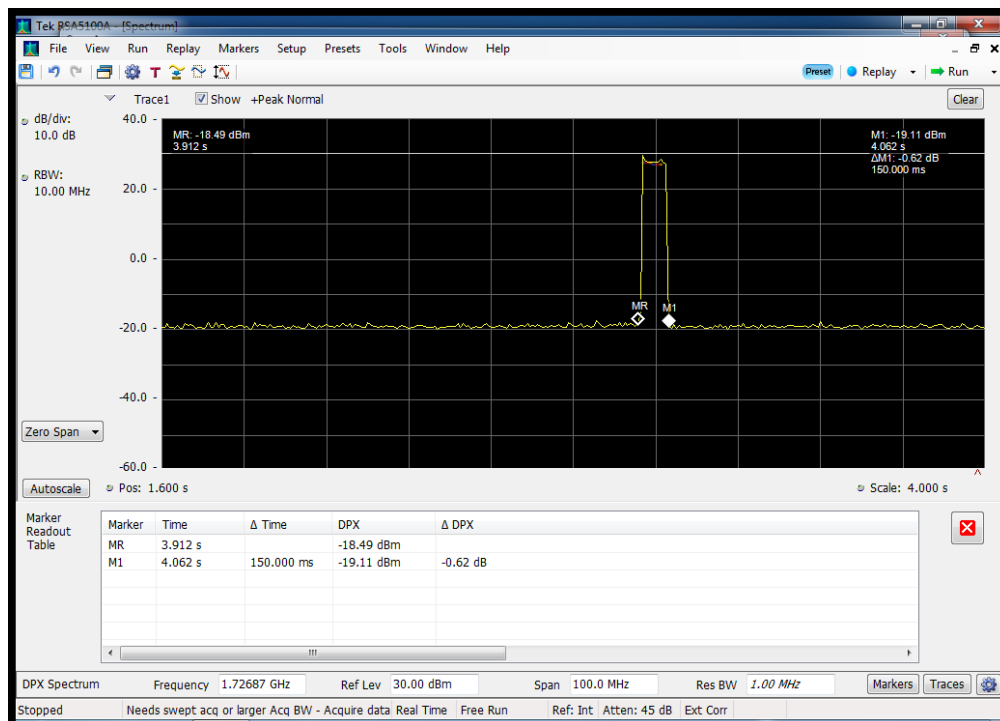




### Osc Det\_DL\_869 - 894 MHz

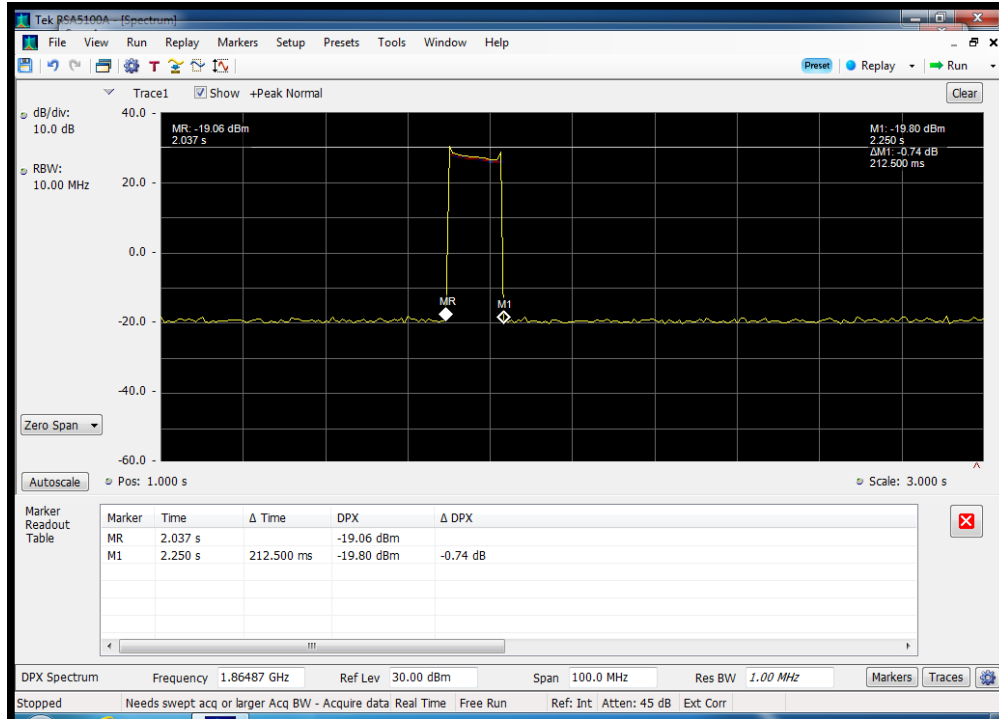


### Osc Det\_UL\_1710 - 1755 MHz





### Osc Det\_UL\_1850 - 1915 MHz

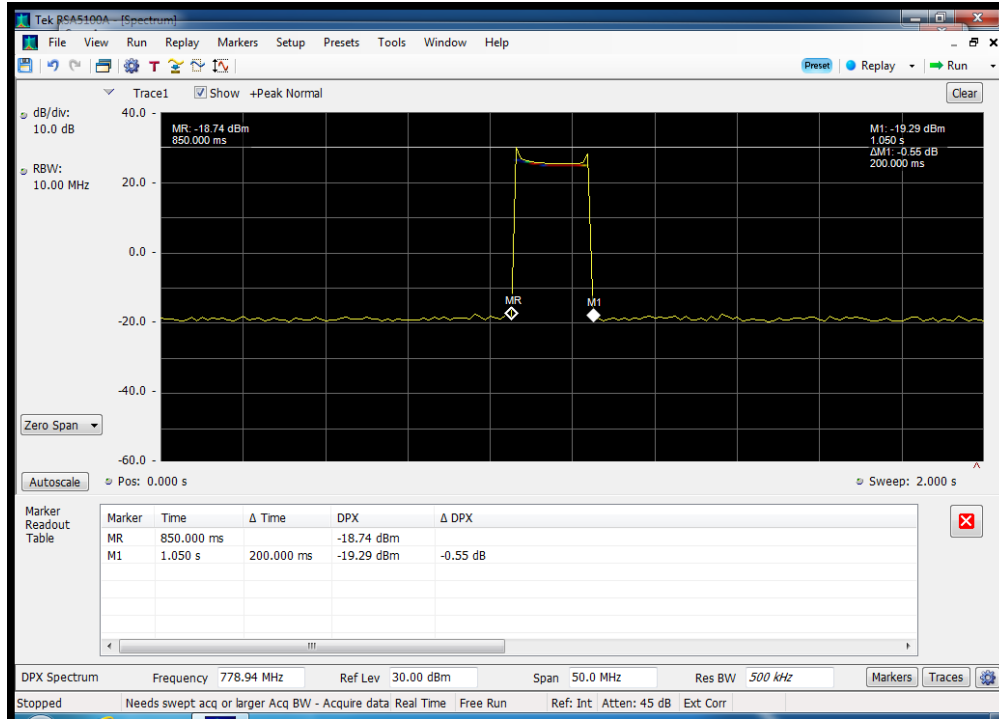


### Osc Det\_UL\_698 - 716 MHz





### Osc Det\_UL\_776 - 787 MHz

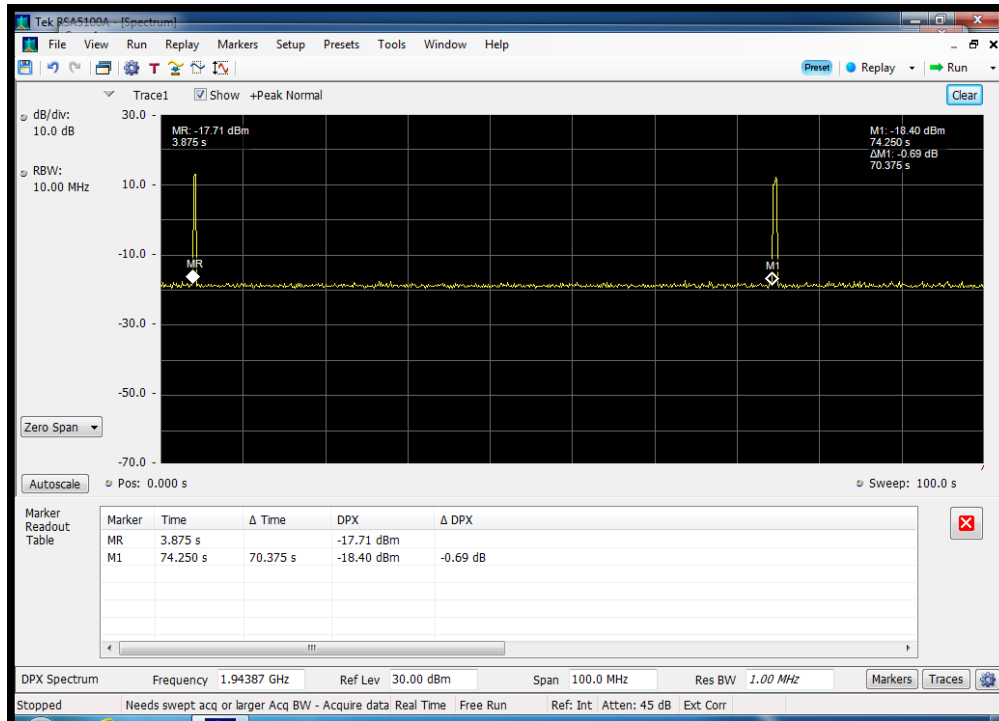


### Osc Det\_UL\_824 - 849 MHz

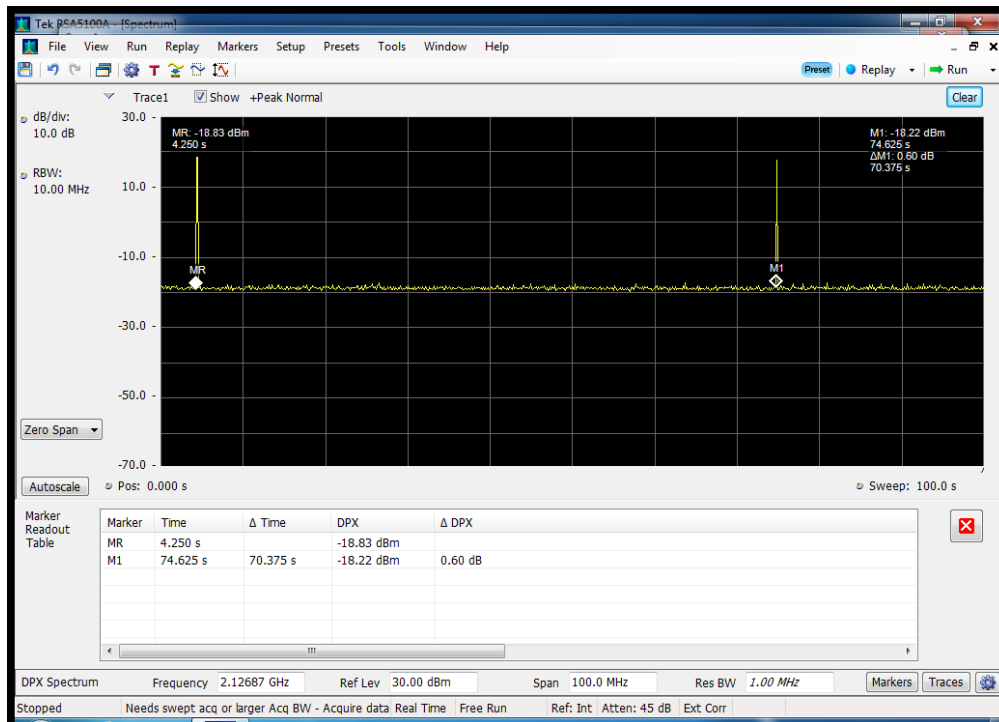




### Restart Time\_DL\_1930 - 1995 MHz

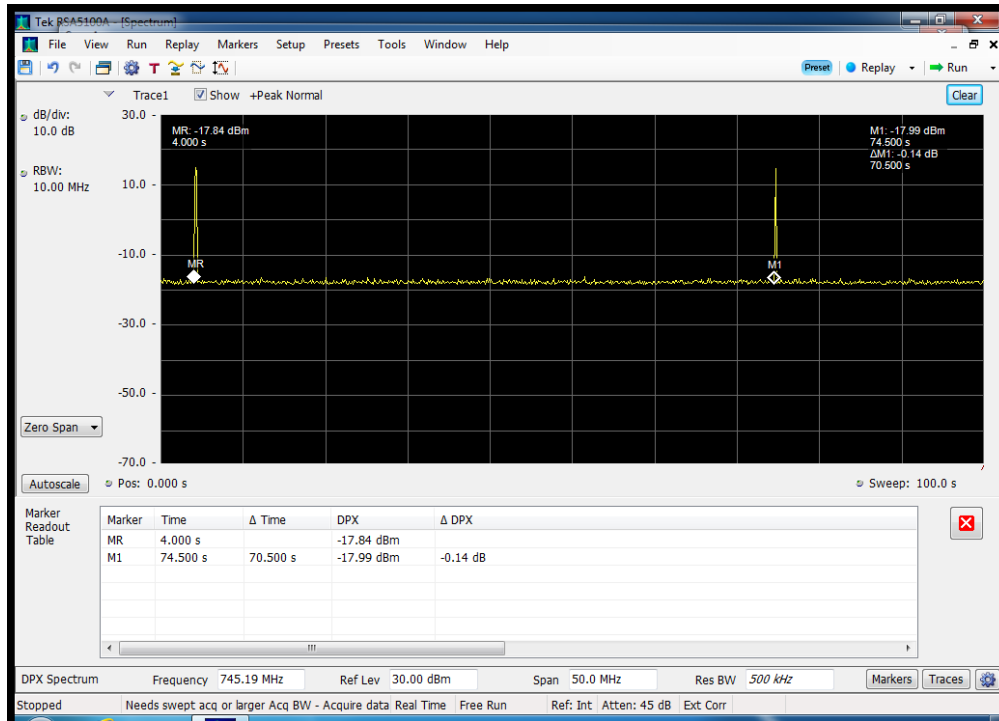


### Restart Time\_DL\_2110 - 2155 MHz

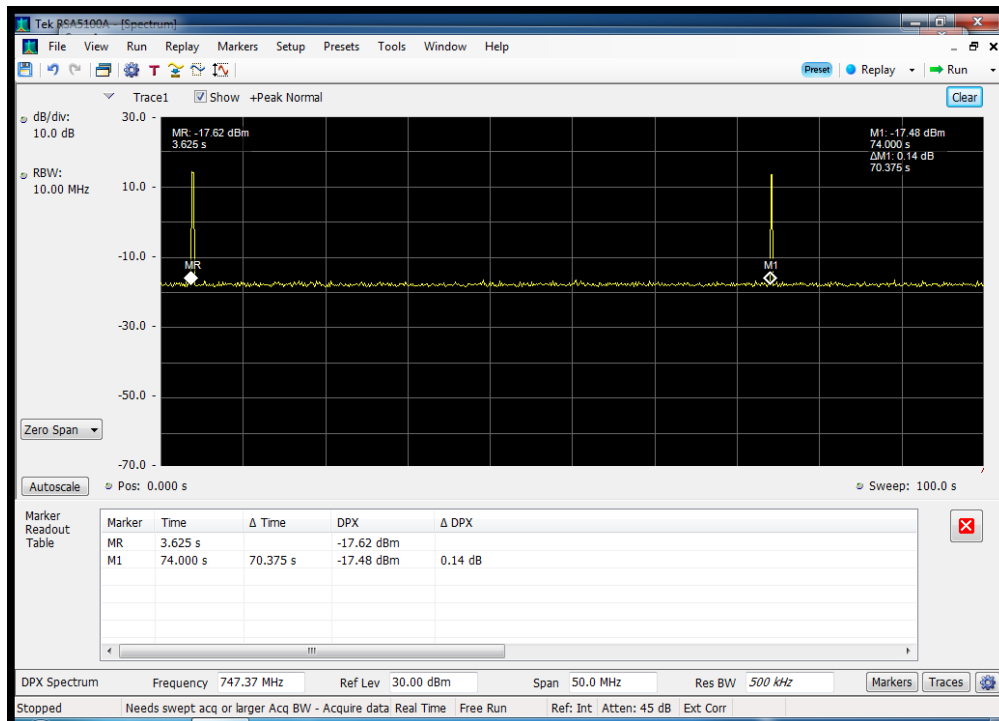




### Restart Time\_DL\_728 - 746 MHz



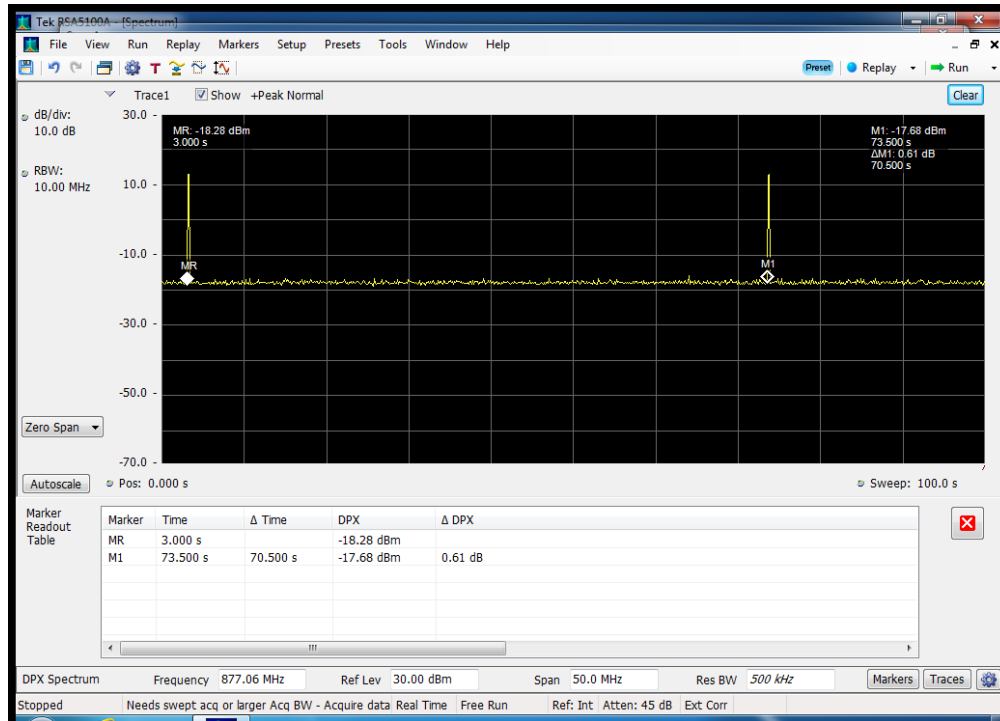
### Restart Time\_DL\_746 - 757 MHz



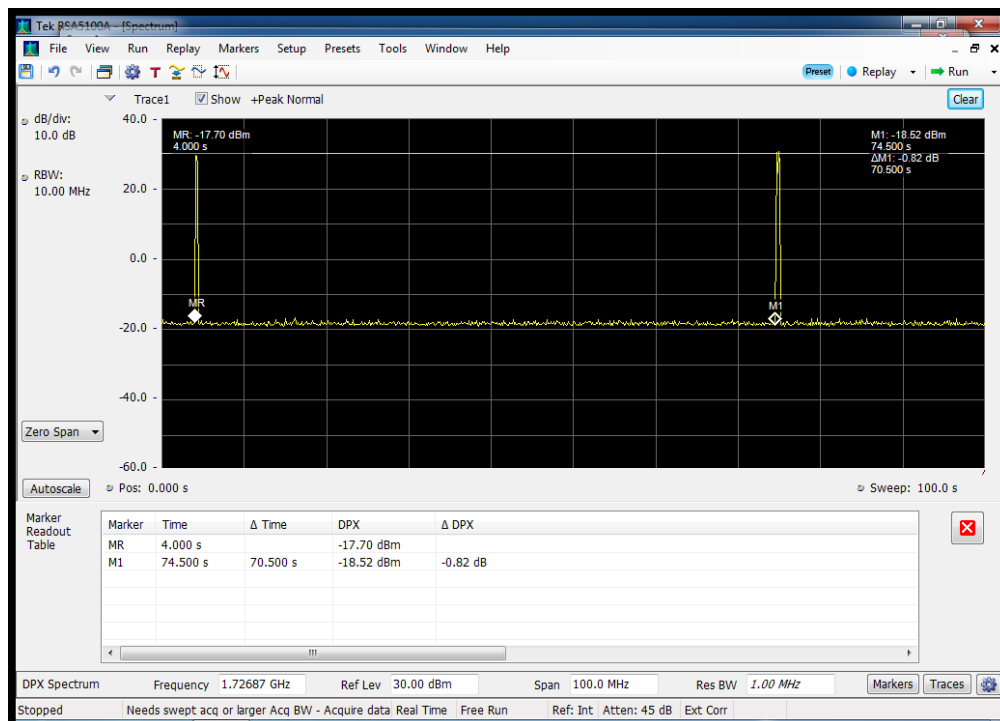




### Restart Time\_DL\_869 - 894 MHz

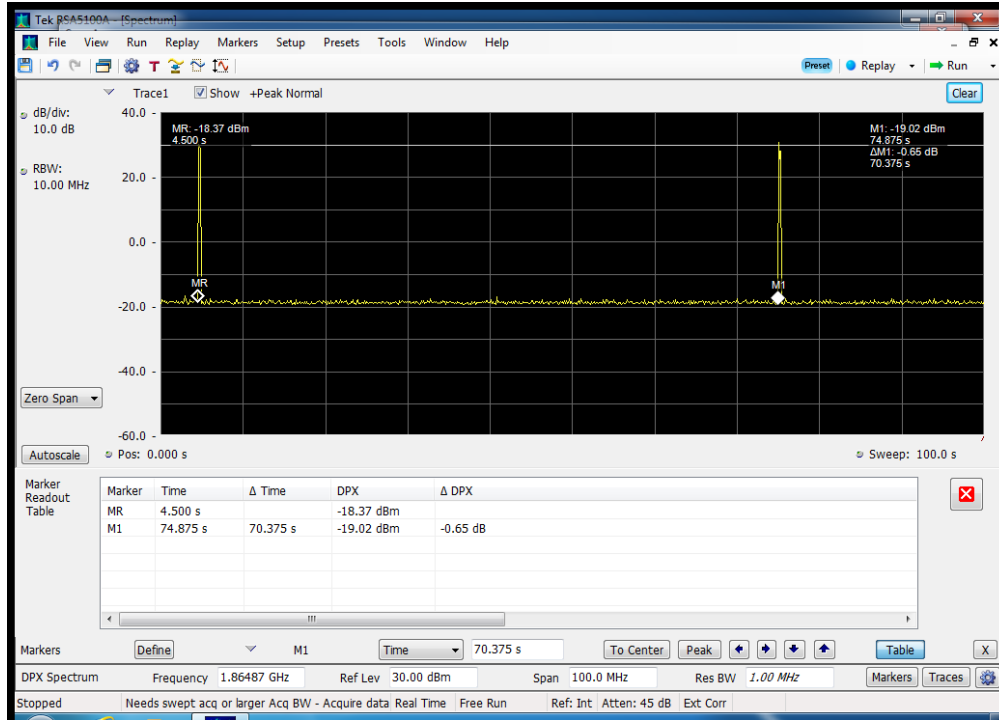


### Restart Time\_UL\_1710 - 1755 MHz

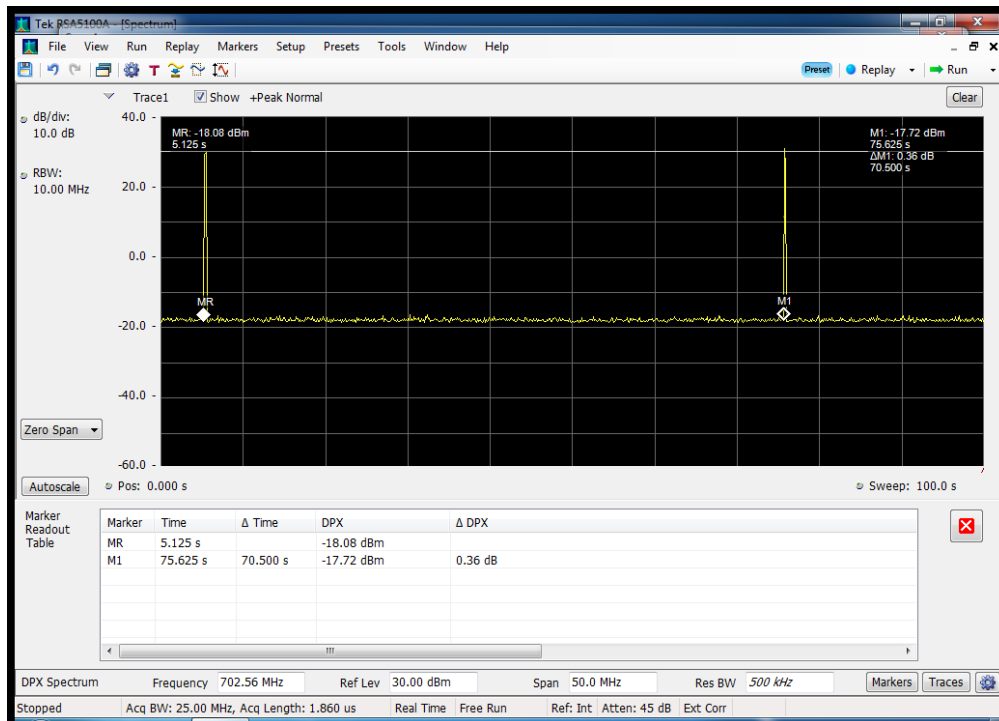




### Restart Time\_UL\_1850 - 1915 MHz

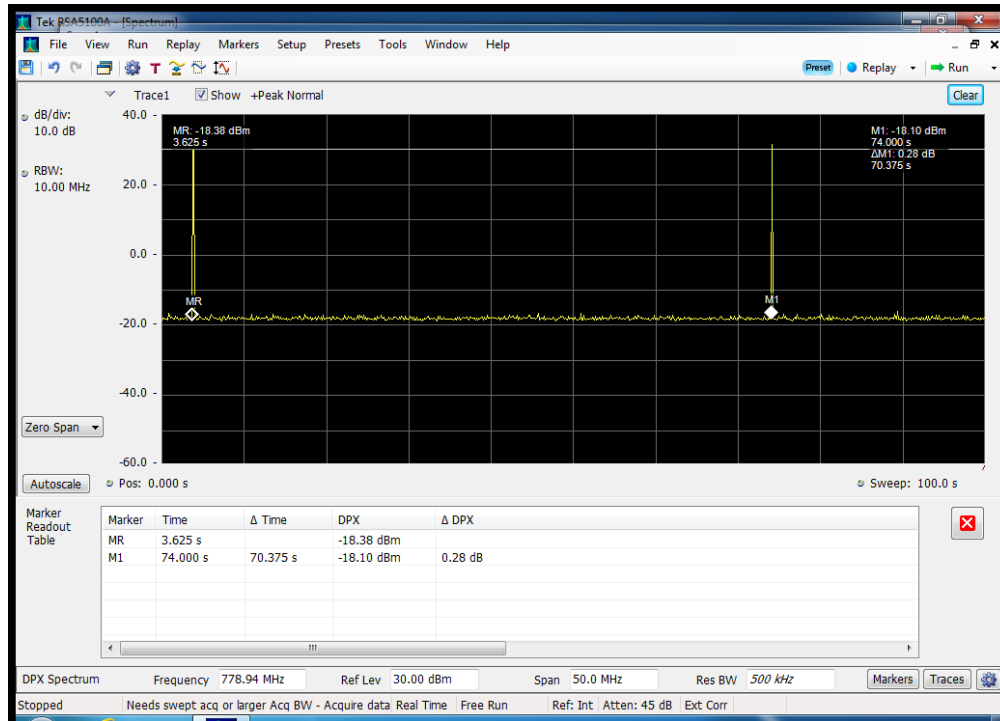


### Restart Time\_UL\_698 - 716 MHz

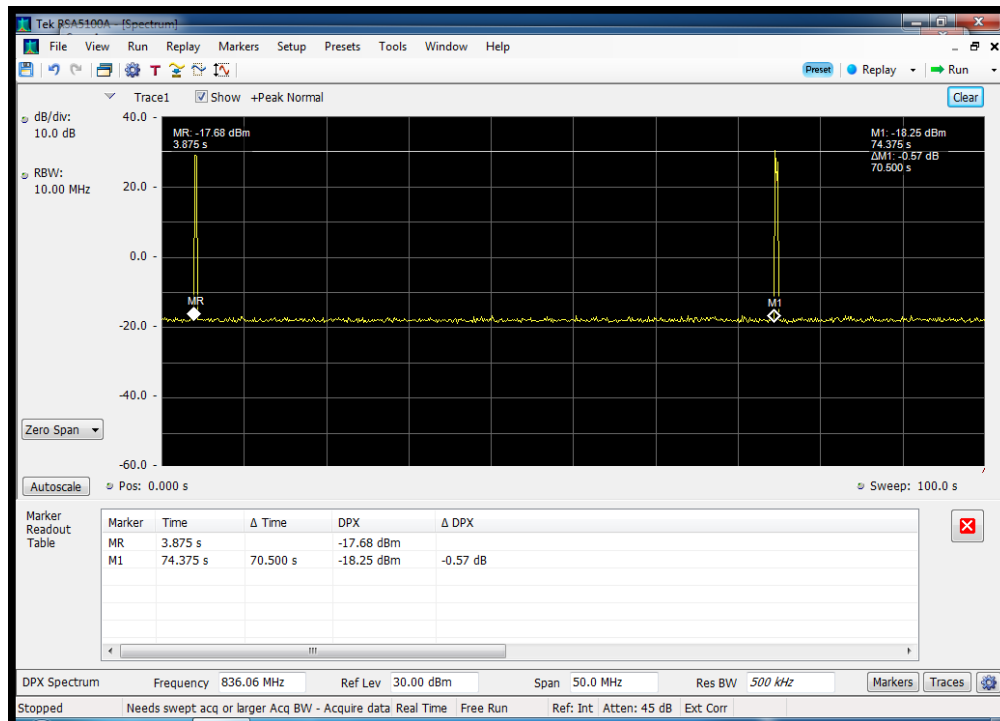




### Restart Time\_UL\_776 - 787 MHz

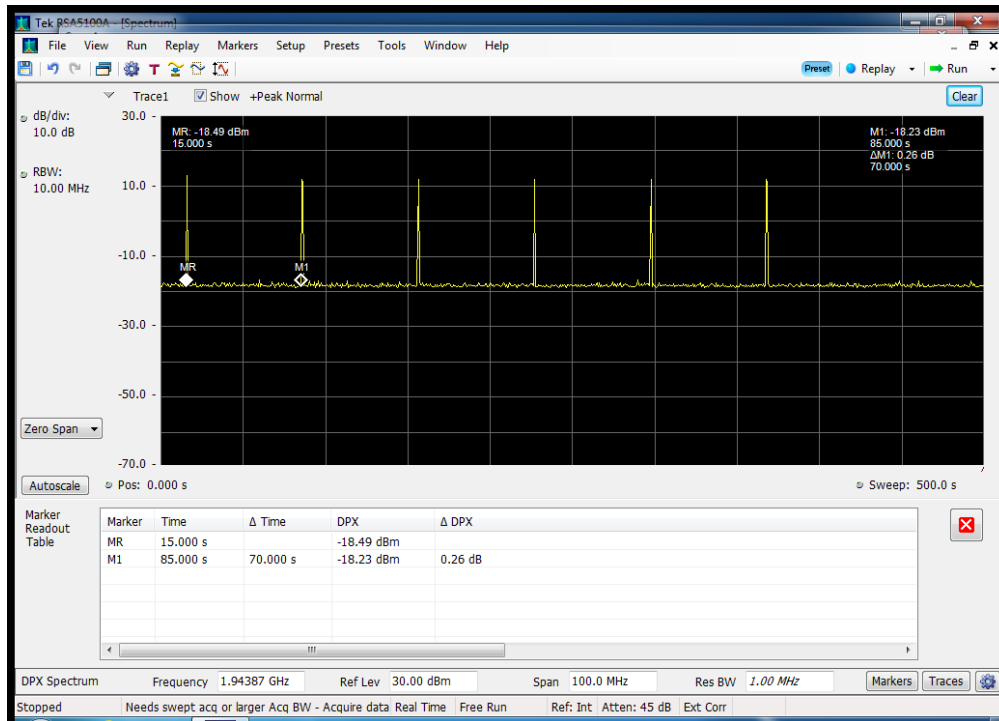


### Restart Time\_UL\_824 - 849 MHz

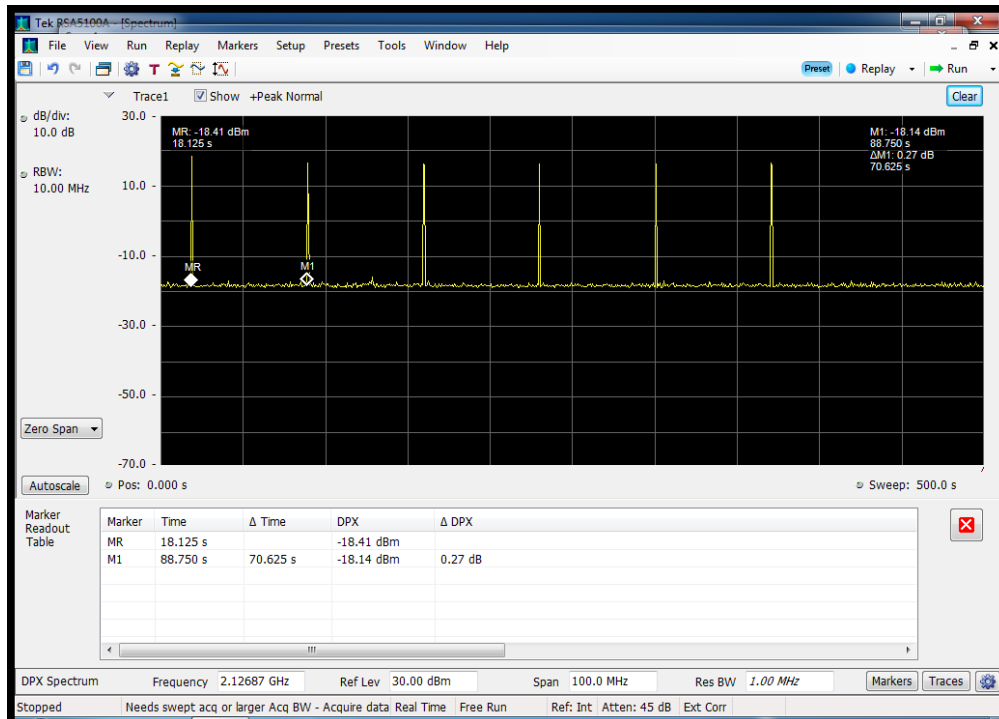




### # of Restarts\_DL\_1930 - 1995 MHz

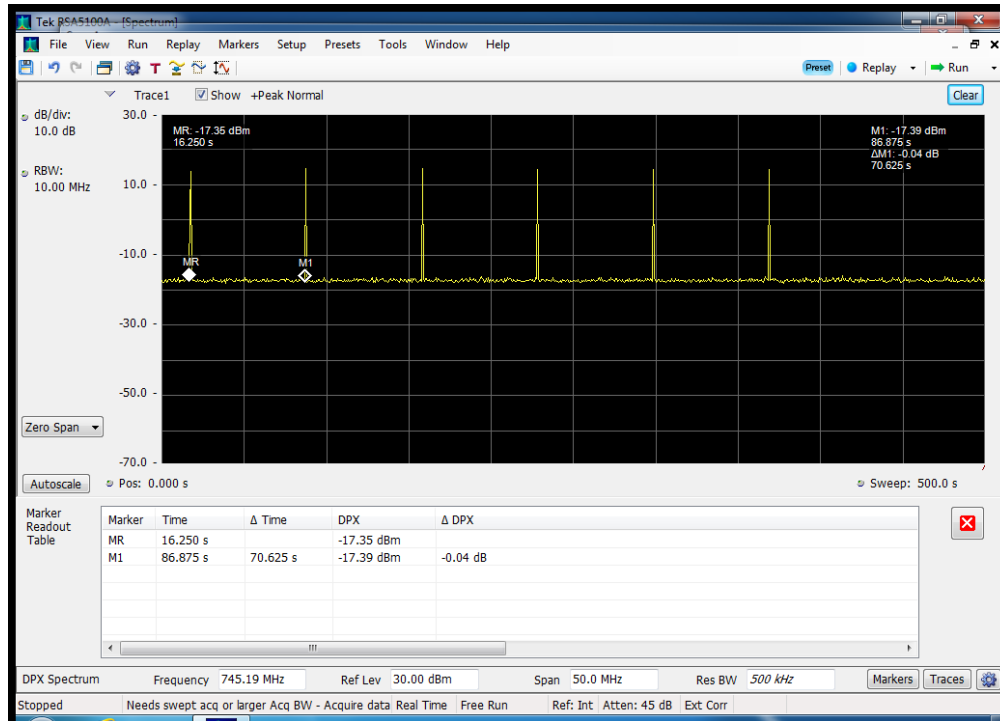


### # of Restarts\_DL\_2110 - 2155 MHz

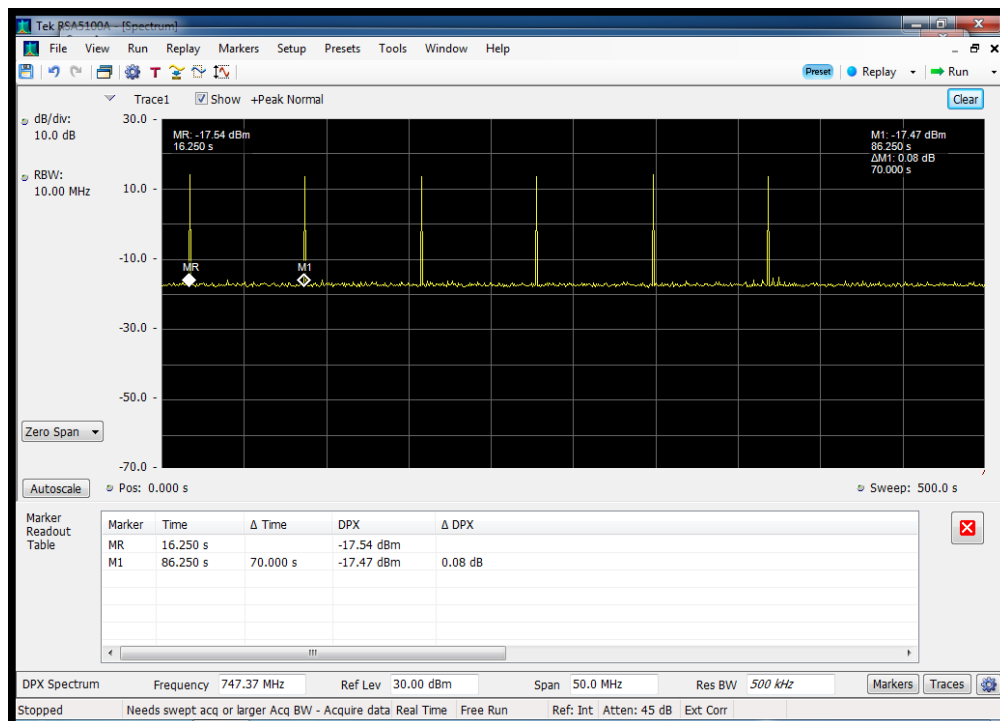




### # of Restarts\_DL\_728 - 746 MHz

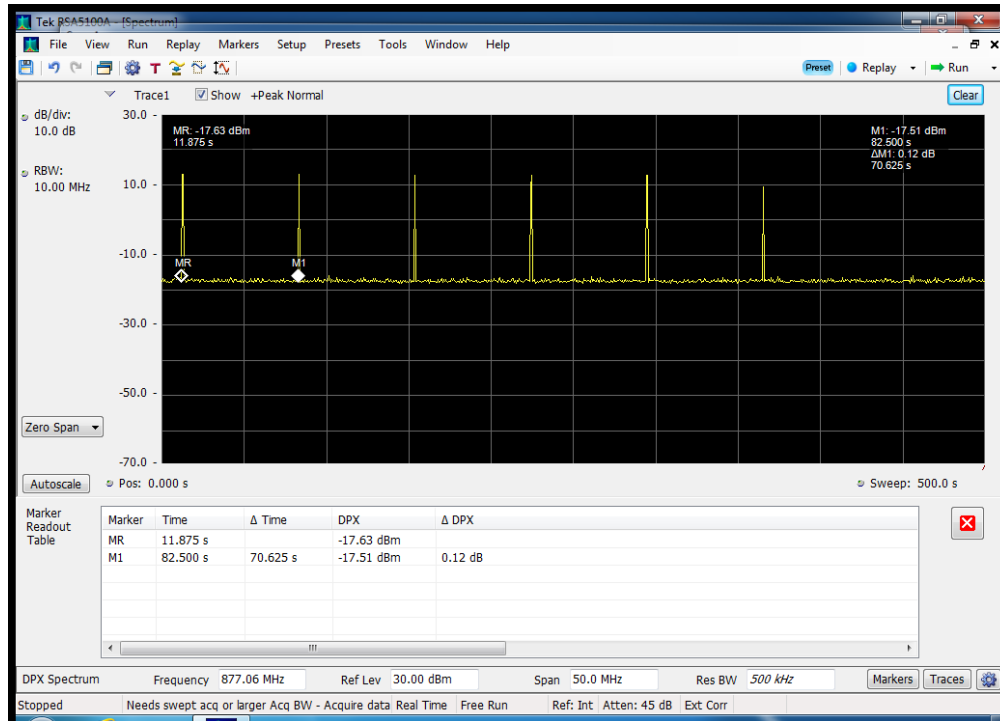


### # of Restarts\_DL\_746 - 757 MHz





### # of Restarts\_DL\_869 - 894 MHz

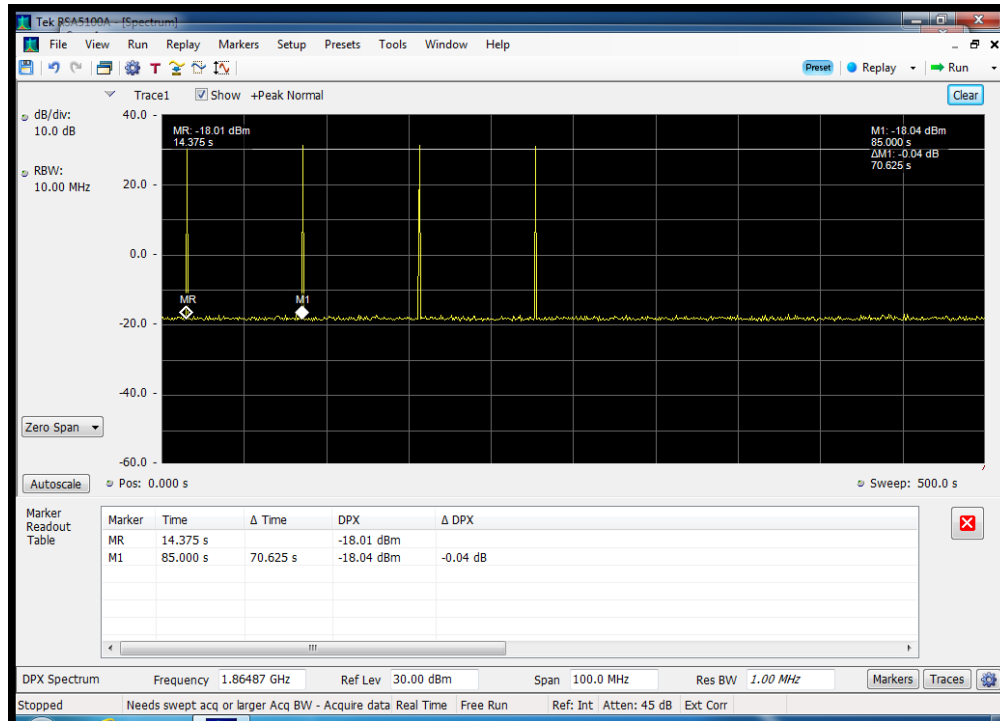


### # of Restarts\_UL\_1710 - 1755 MHz

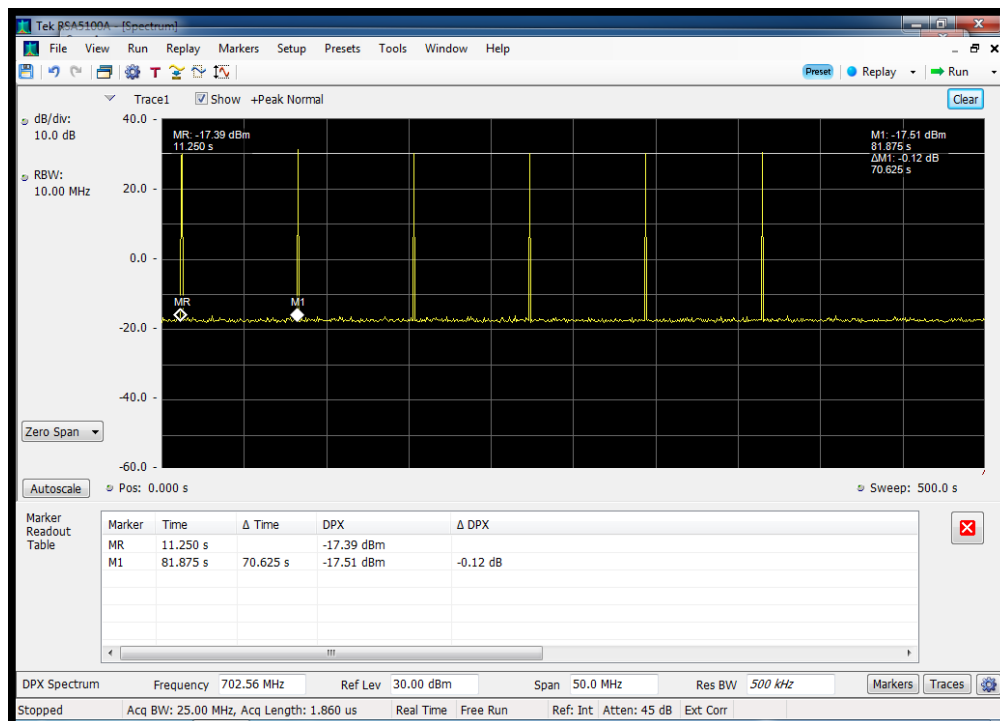




### # of Restarts\_UL\_1850 - 1915 MHz

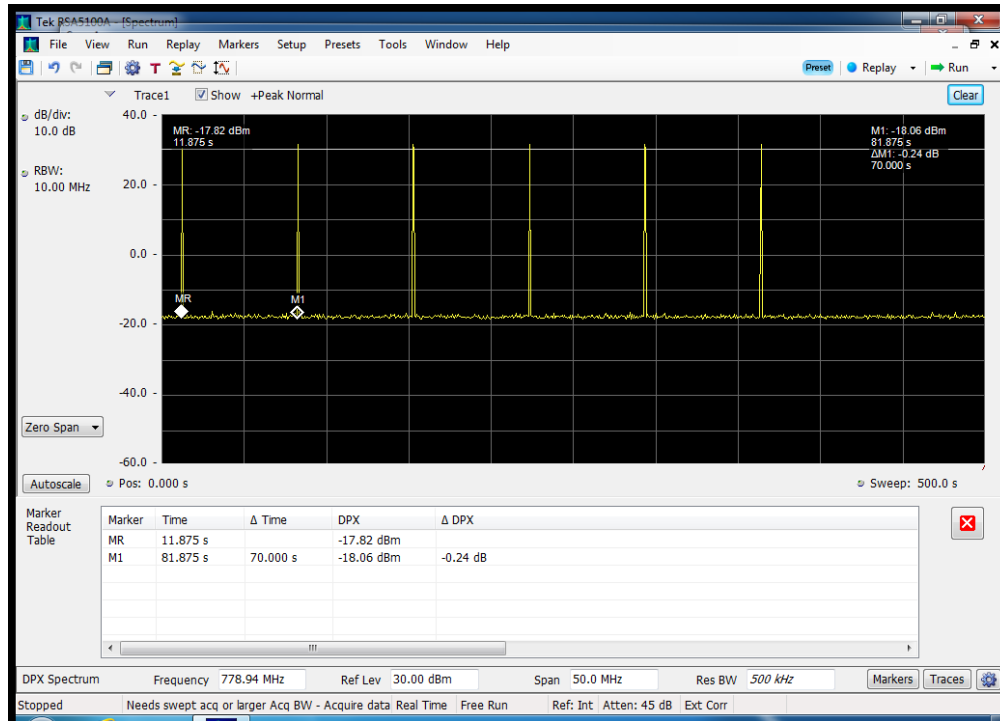


### # of Restarts\_UL\_698 - 716 MHz





### # of Restarts\_UL\_776 - 787 MHz



### # of Restarts\_UL\_824 - 849 MHz

