

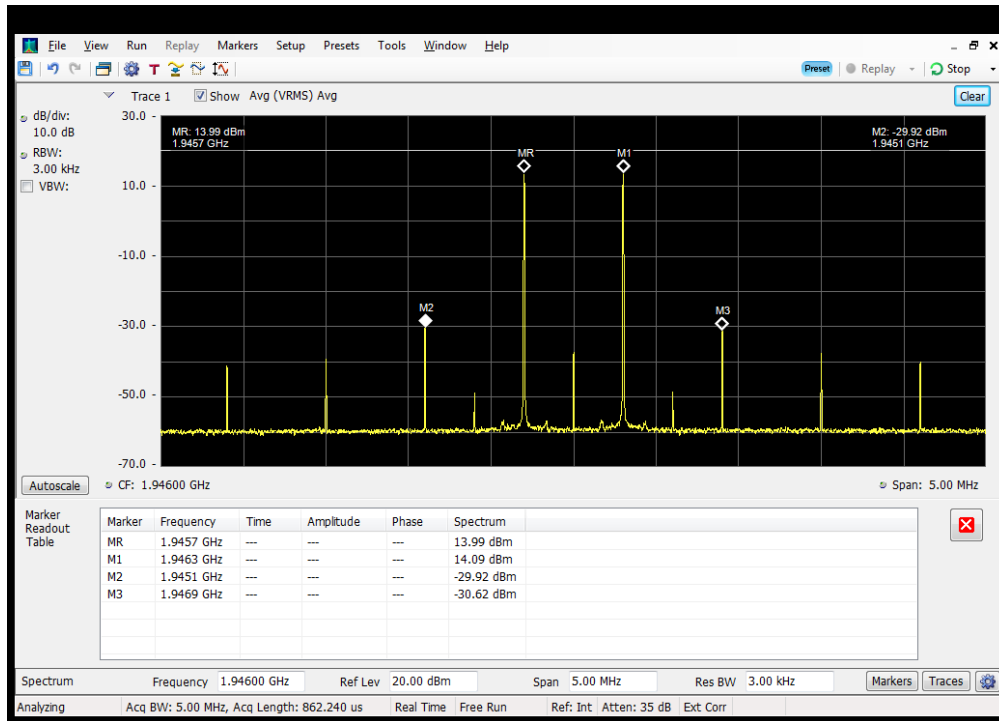


## **Annex B**

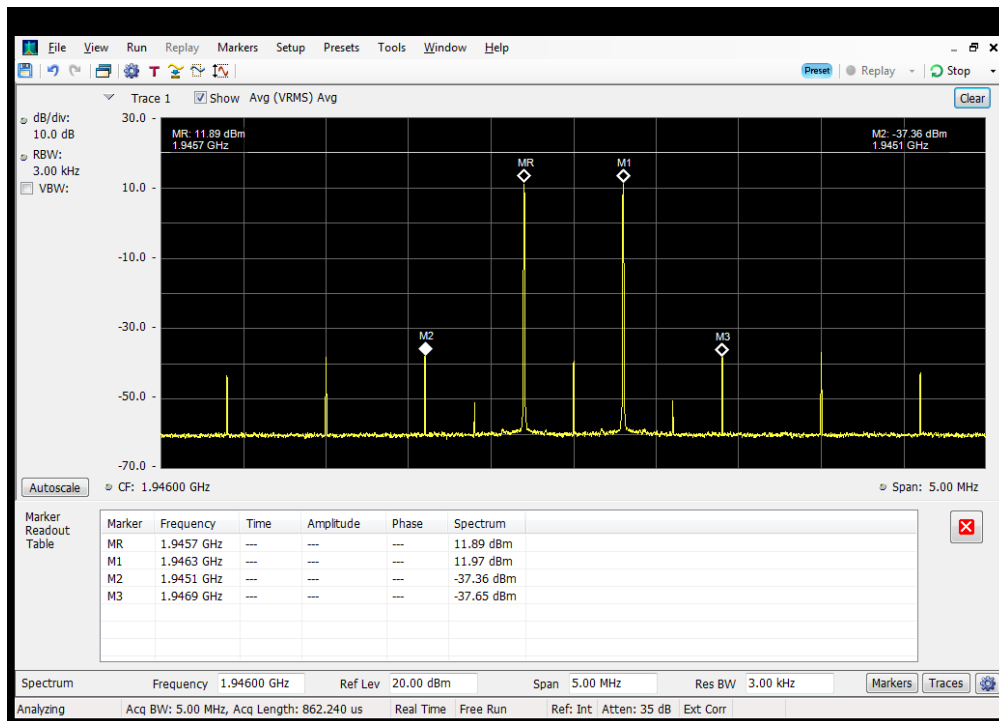
# **Intermodulation**



### Intermodulation\_DownLink\_1930 - 1995 MHz\_Dedicated Port\_Donor Port 1 to Server Port 2\_B25

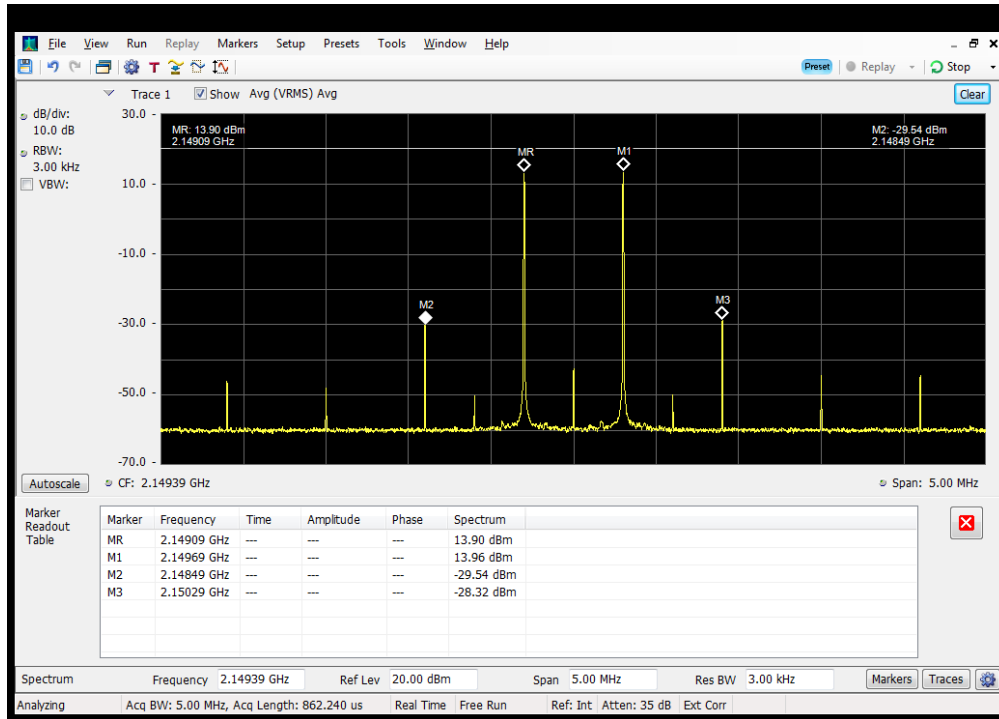


### Intermodulation\_DownLink\_1930 - 1995 MHz\_Dedicated Port\_Donor Port 1 to Server Port 4\_B25

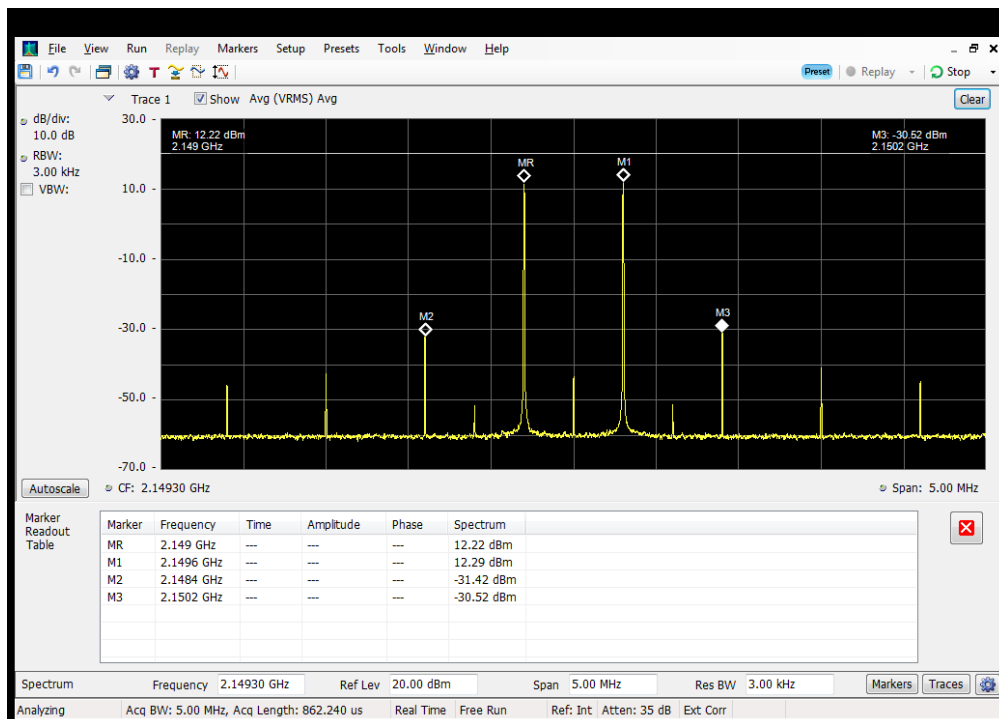




### Intermodulation\_DownLink\_2110 - 2155 MHz\_Dedicated Port\_Donor Port 1 to Server Port 2\_B4

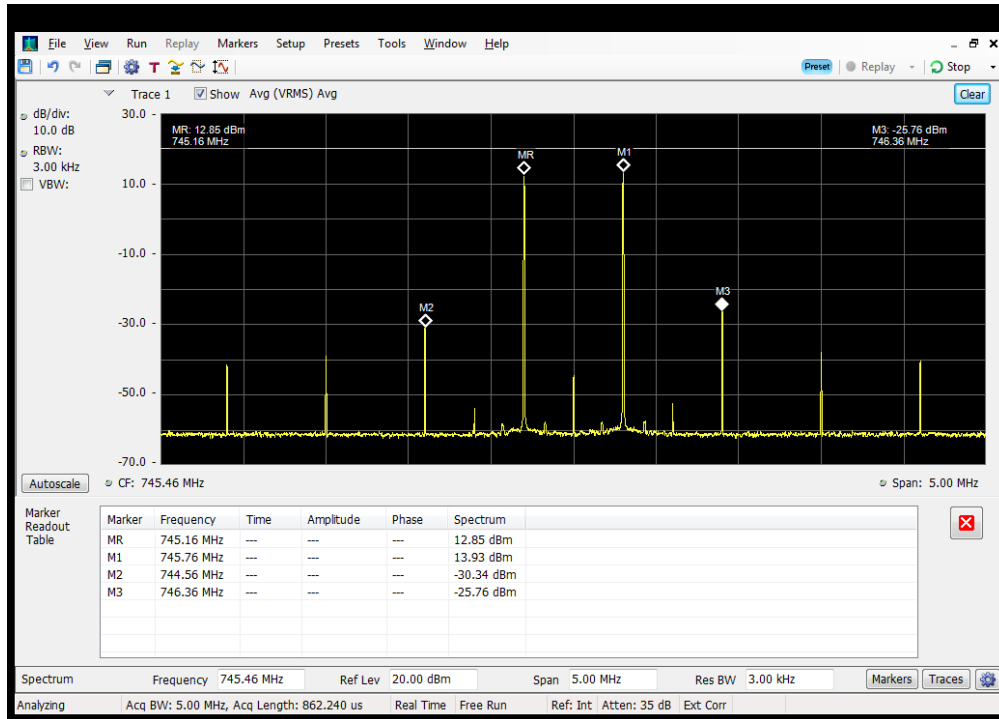


### Intermodulation\_DownLink\_2110 - 2155 MHz\_Dedicated Port\_Donor Port 1 to Server Port 4\_B4

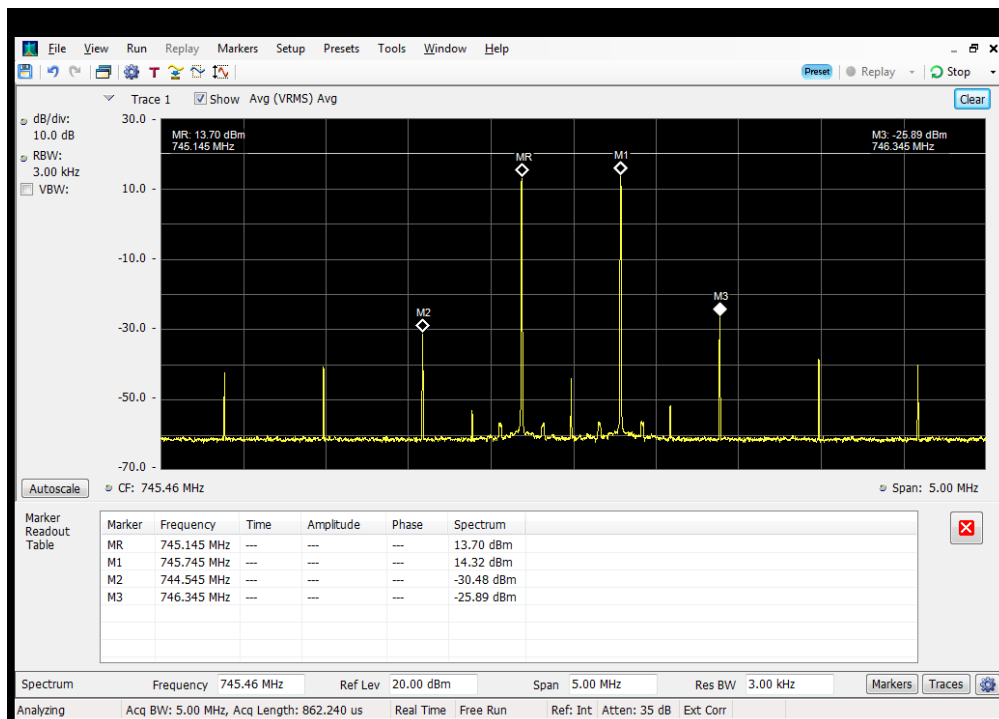




### Intermodulation\_DownLink\_728 - 746 MHz\_Common Port\_Donor Port 1 to Server Port 1\_B12

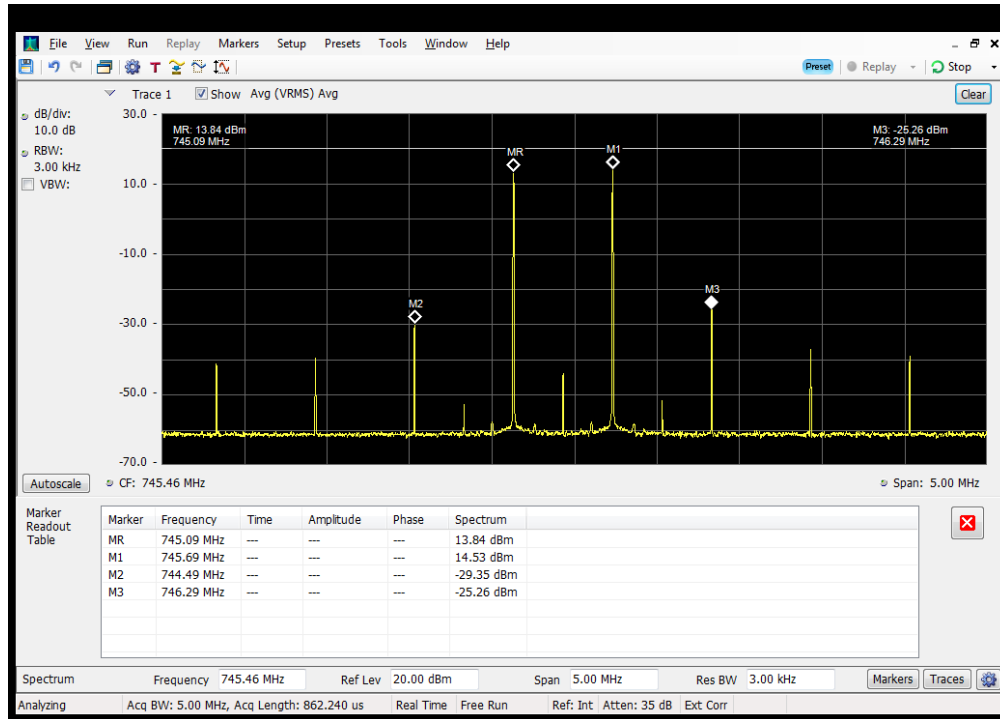


### Intermodulation\_DownLink\_728 - 746 MHz\_Common Port\_Donor Port 1 to Server Port 2\_B12

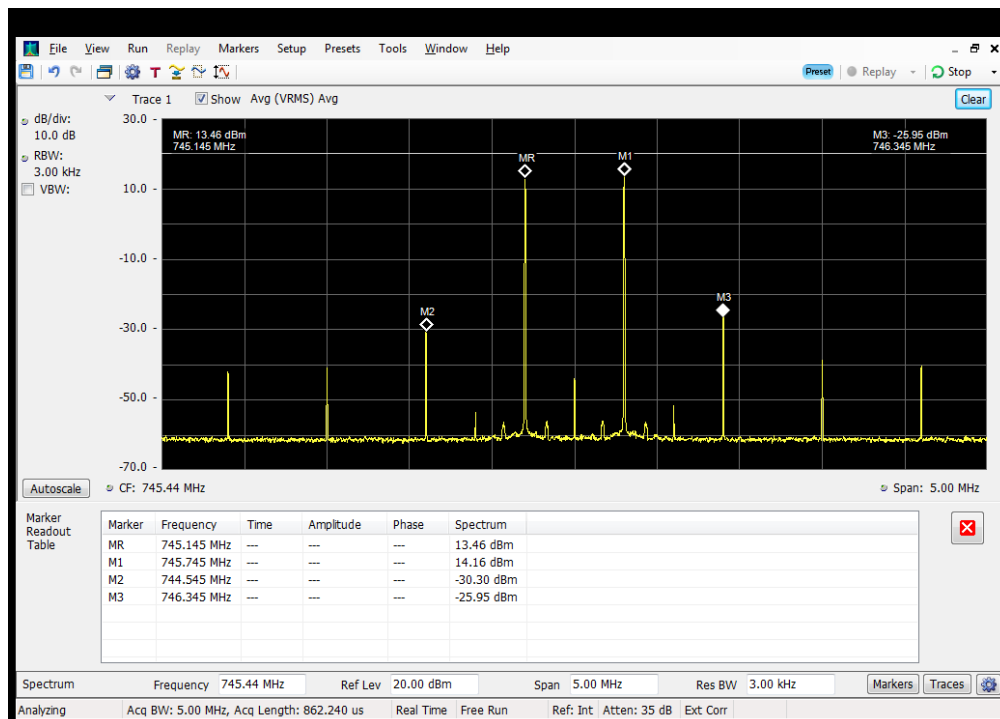




### Intermodulation\_DownLink\_728 - 746 MHz\_Common Port\_Donor Port 1 to Server Port 3\_B12

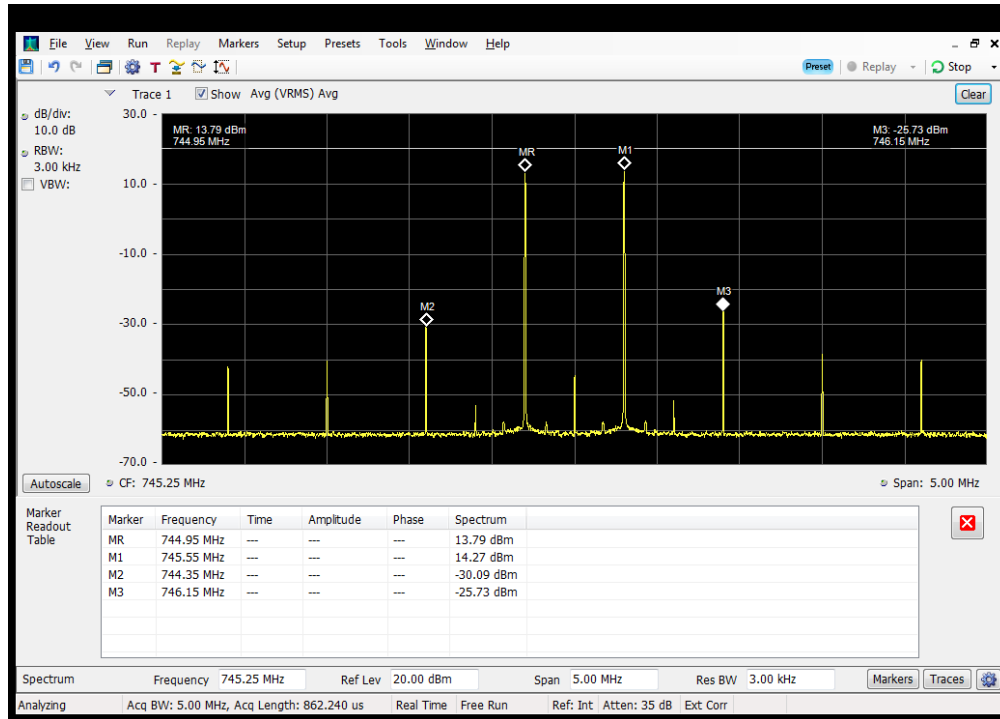


### Intermodulation\_DownLink\_728 - 746 MHz\_Common Port\_Donor Port 1 to Server Port 4\_B12

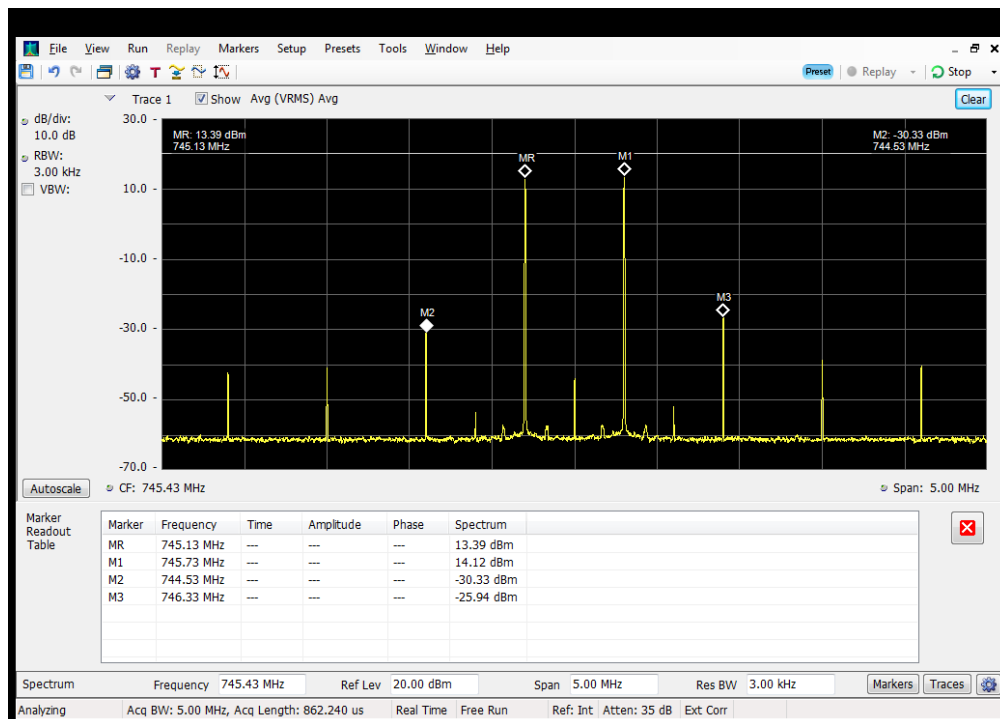




### Intermodulation\_DownLink\_728 - 746 MHz\_Dedicated Port\_Donor Port 3 to Server Port 2\_B12

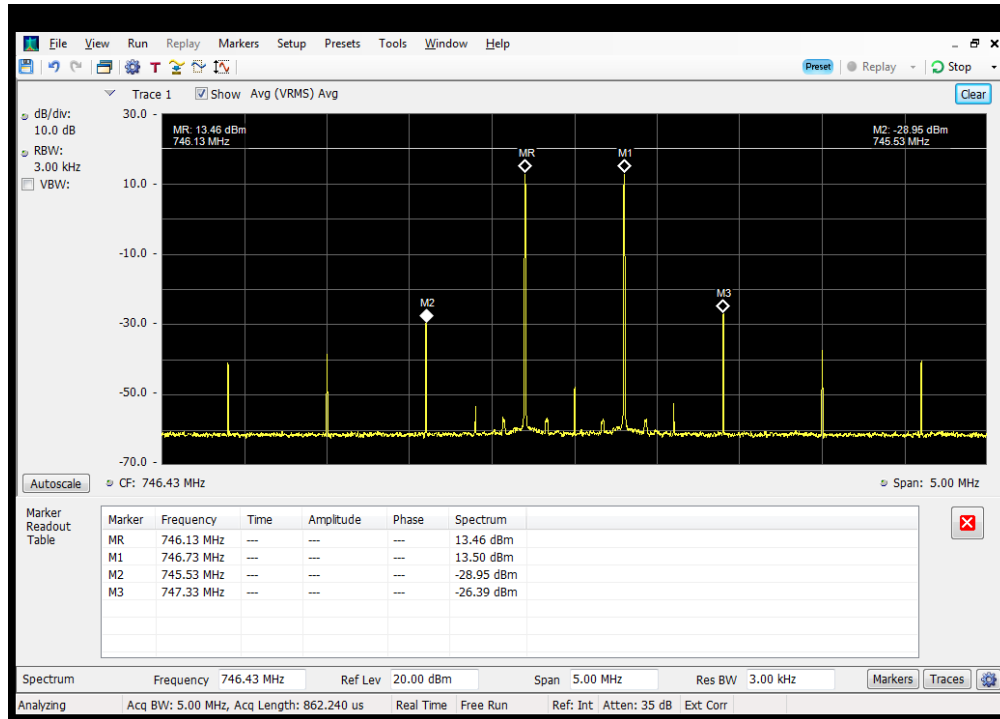


### Intermodulation\_DownLink\_728 - 746 MHz\_Dedicated Port\_Donor Port 3 to Server Port 4\_B12

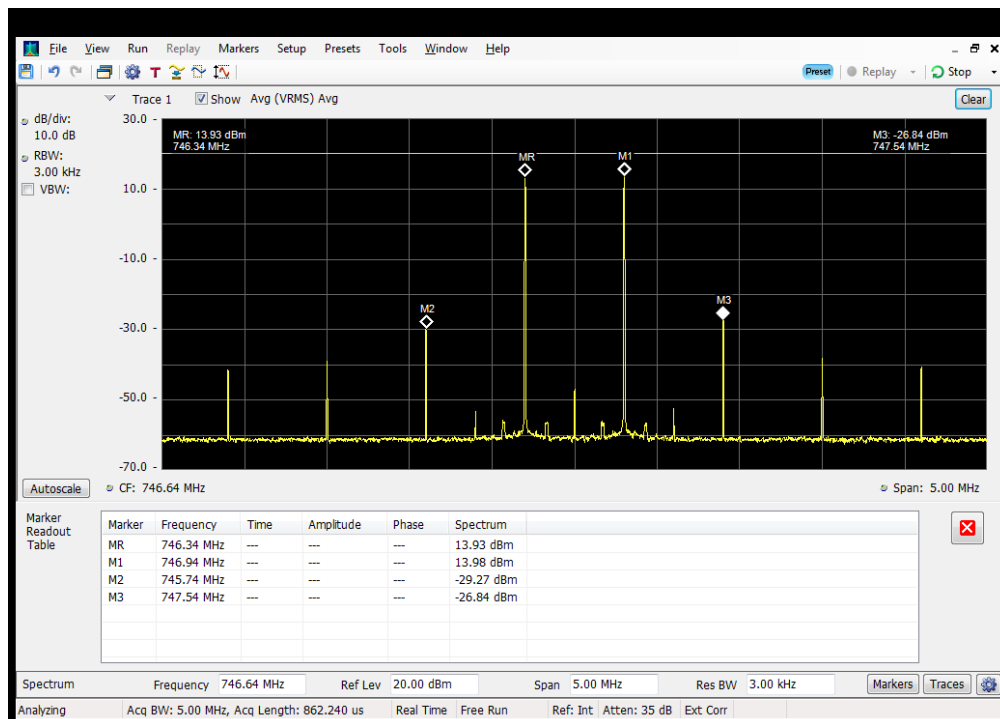




### Intermodulation\_DownLink\_746 - 757 MHz\_Common Port\_Donor Port 1 to Server Port 1\_B13

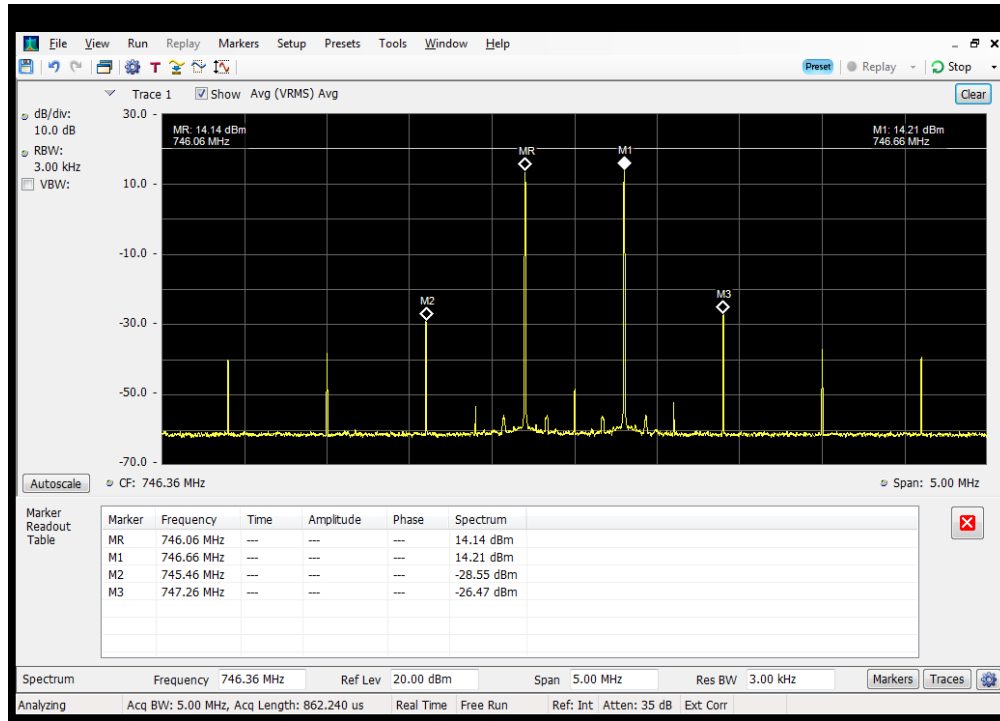


### Intermodulation\_DownLink\_746 - 757 MHz\_Common Port\_Donor Port 1 to Server Port 2\_B13

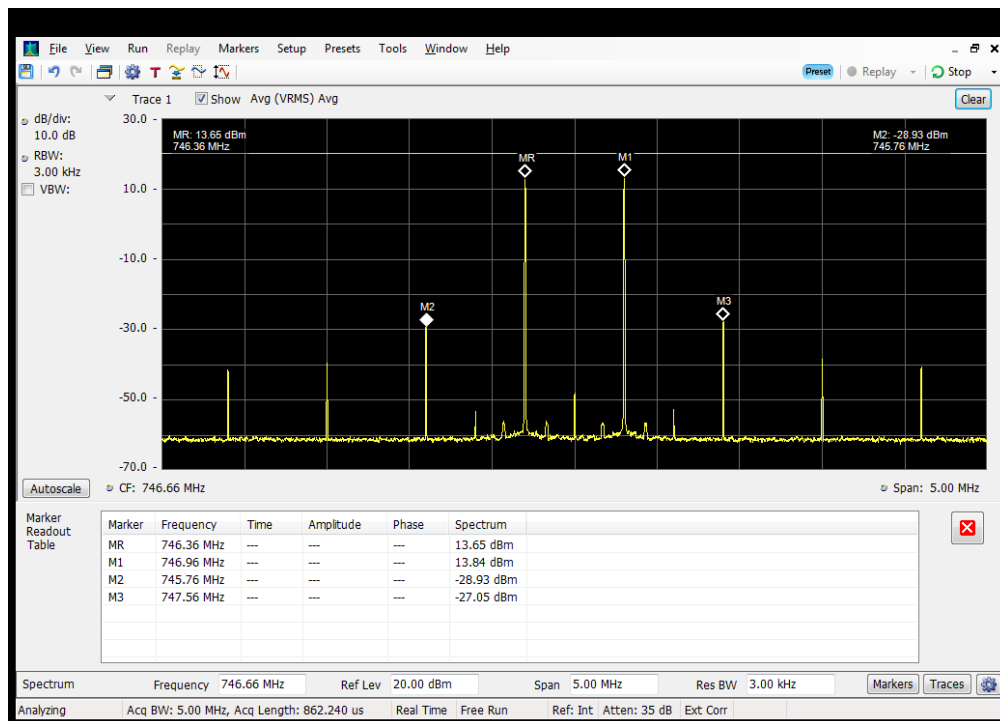




### Intermodulation\_DownLink\_746 - 757 MHz\_Common Port\_Donor Port 1 to Server Port 3\_B13



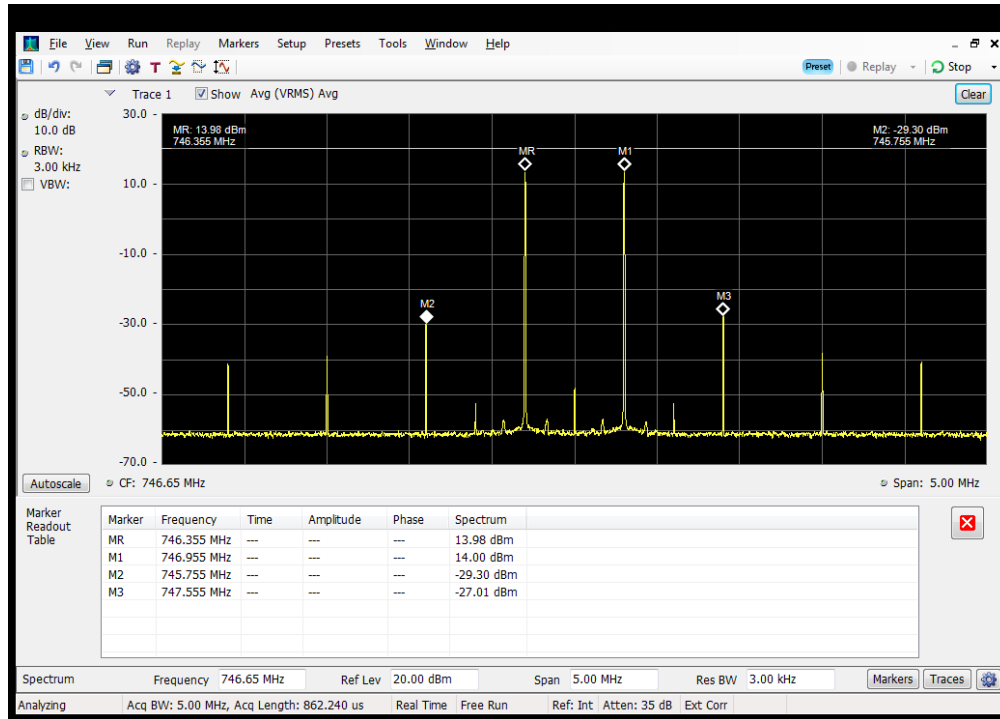
### Intermodulation\_DownLink\_746 - 757 MHz\_Common Port\_Donor Port 1 to Server Port 4\_B13



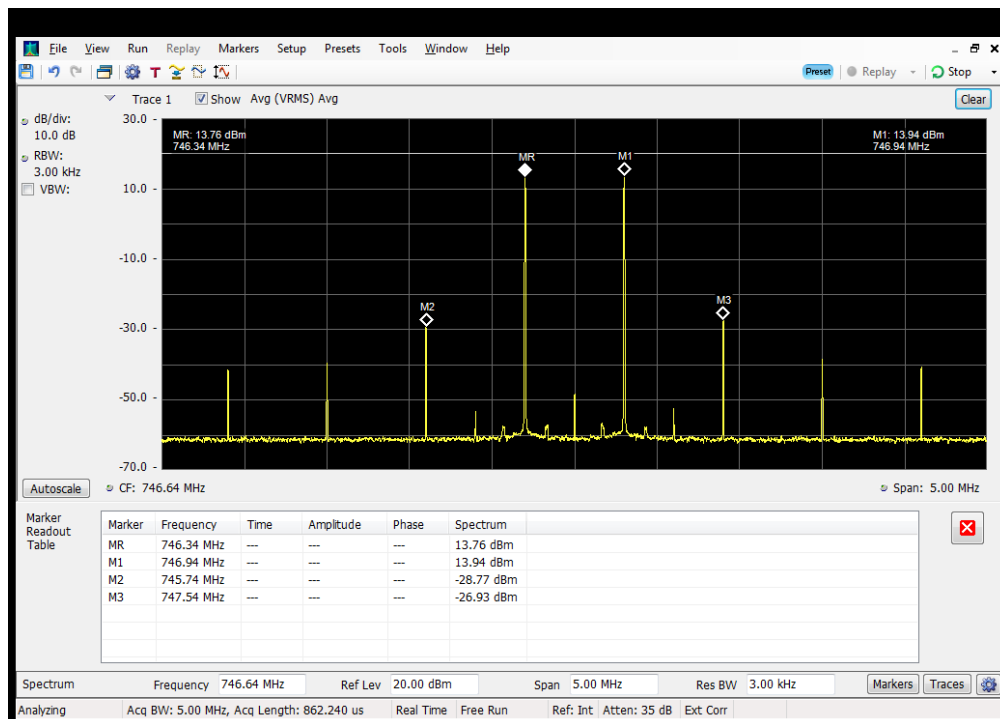




### Intermodulation\_DownLink\_746 - 757 MHz\_Dedicated Port\_Donor Port 3 to Server Port 2\_B13

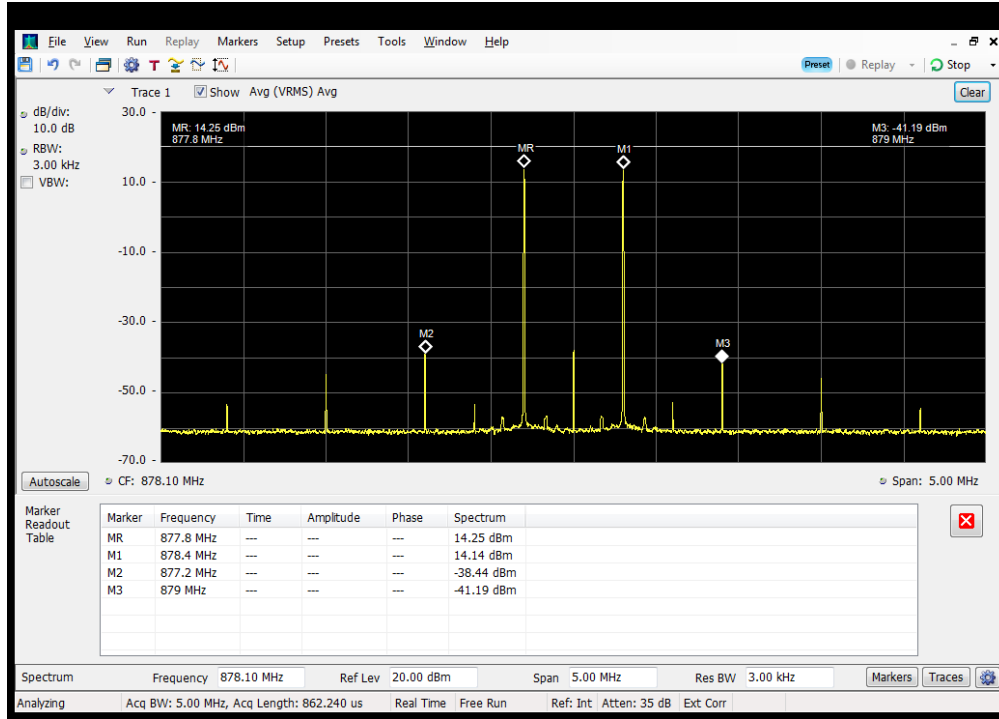


### Intermodulation\_DownLink\_746 - 757 MHz\_Dedicated Port\_Donor Port 3 to Server Port 4\_B13

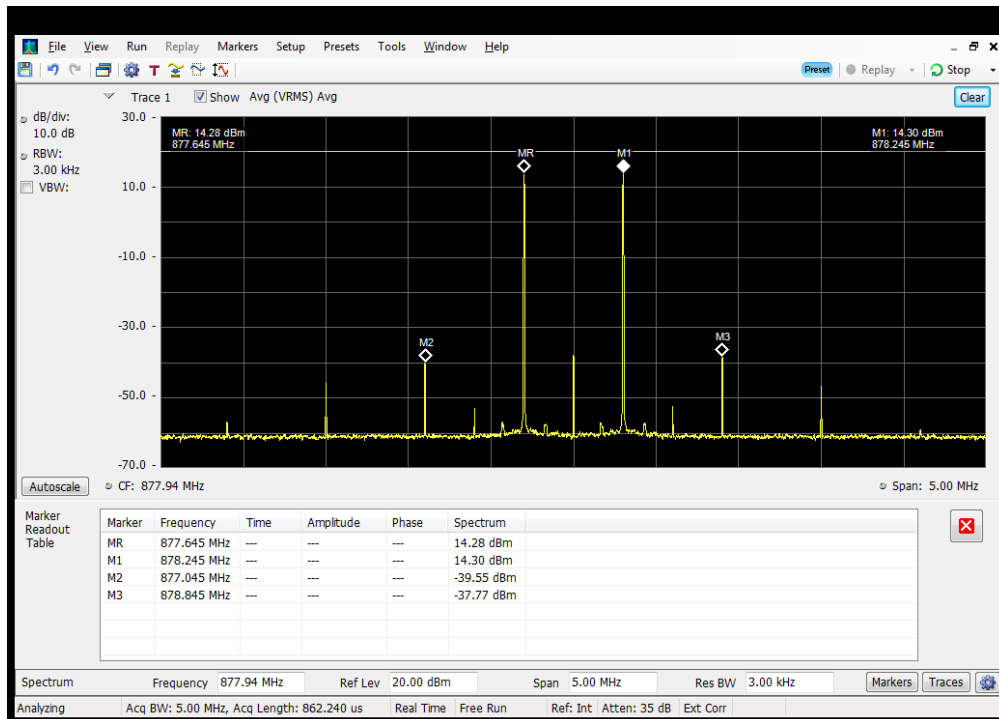




### Intermodulation\_DownLink\_869 - 894 MHz\_Common Port\_Donor Port 1 to Server Port 1\_B5

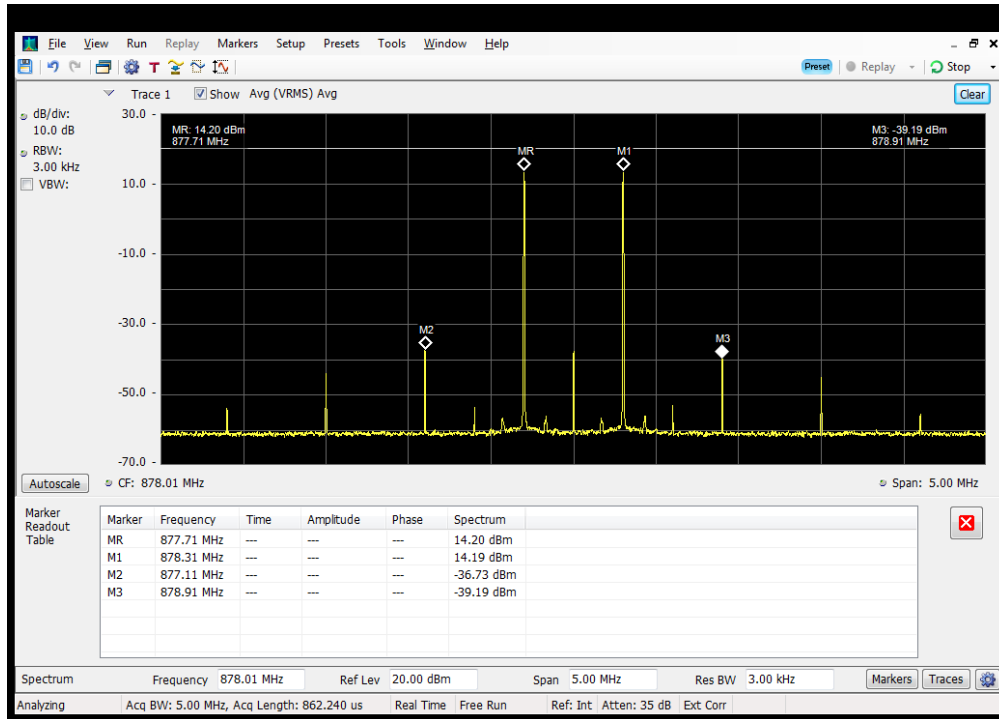


### Intermodulation\_DownLink\_869 - 894 MHz\_Common Port\_Donor Port 1 to Server Port 2\_B5

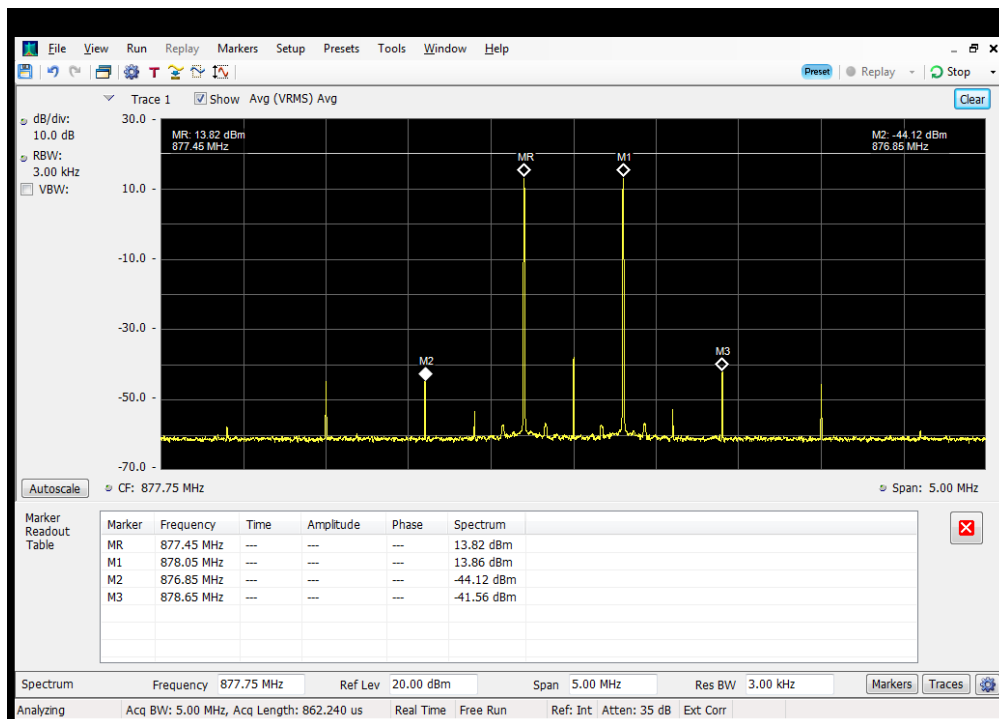




### Intermodulation\_DownLink\_869 - 894 MHz\_Common Port\_Donor Port 1 to Server Port 3\_B5

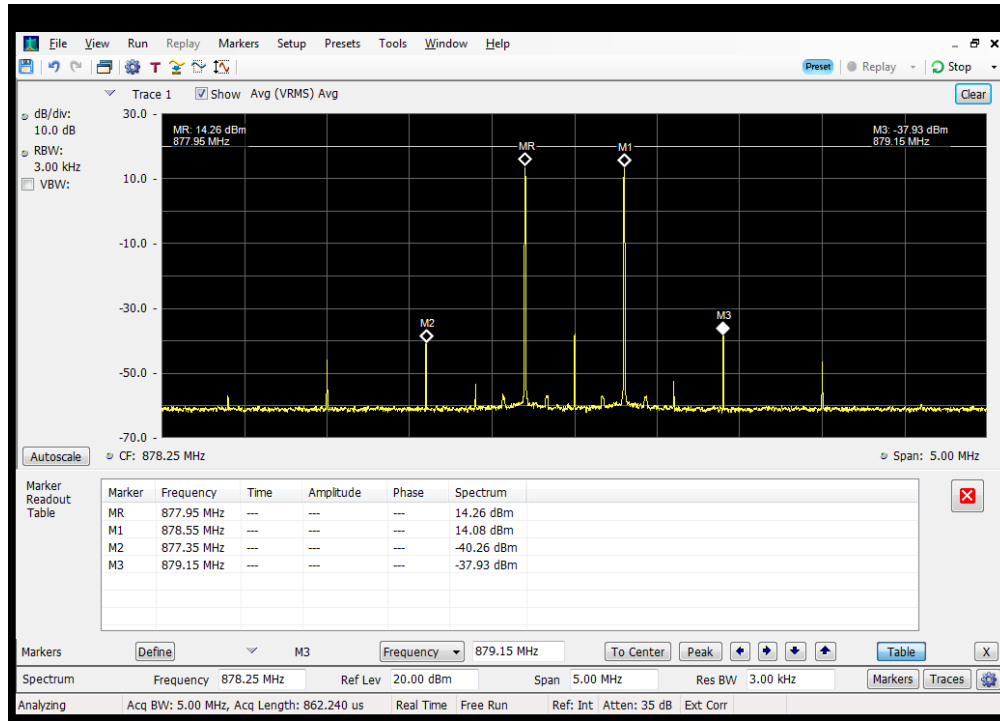


### Intermodulation\_DownLink\_869 - 894 MHz\_Common Port\_Donor Port 1 to Server Port 4\_B5

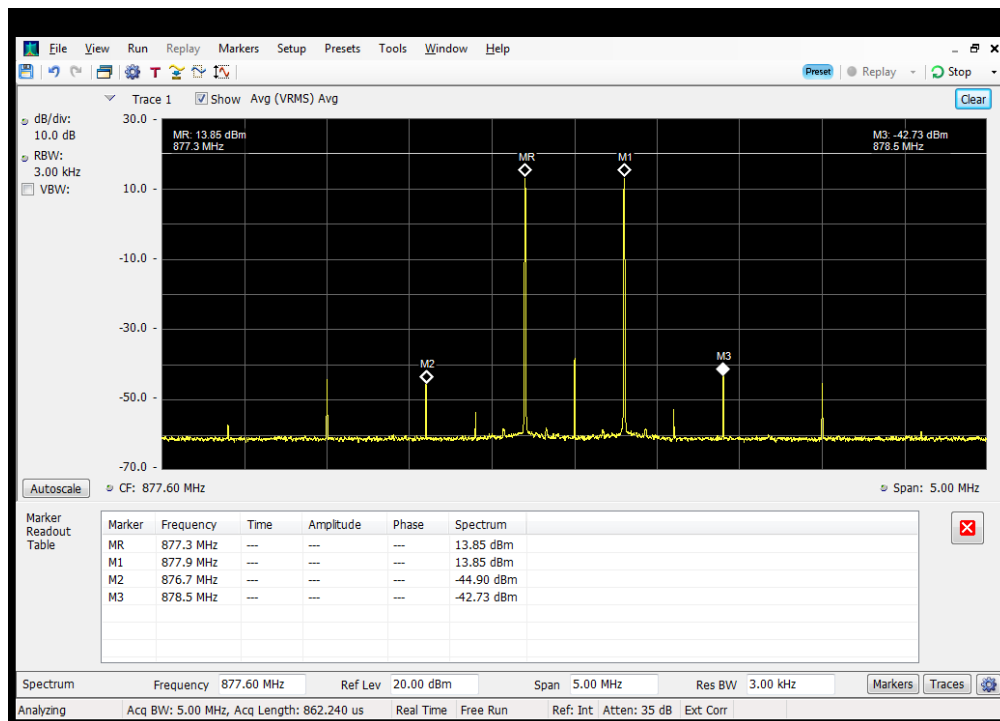




### Intermodulation\_DownLink\_869 - 894 MHz\_Dedicated Port\_Donor Port 2 to Server Port 2\_B5

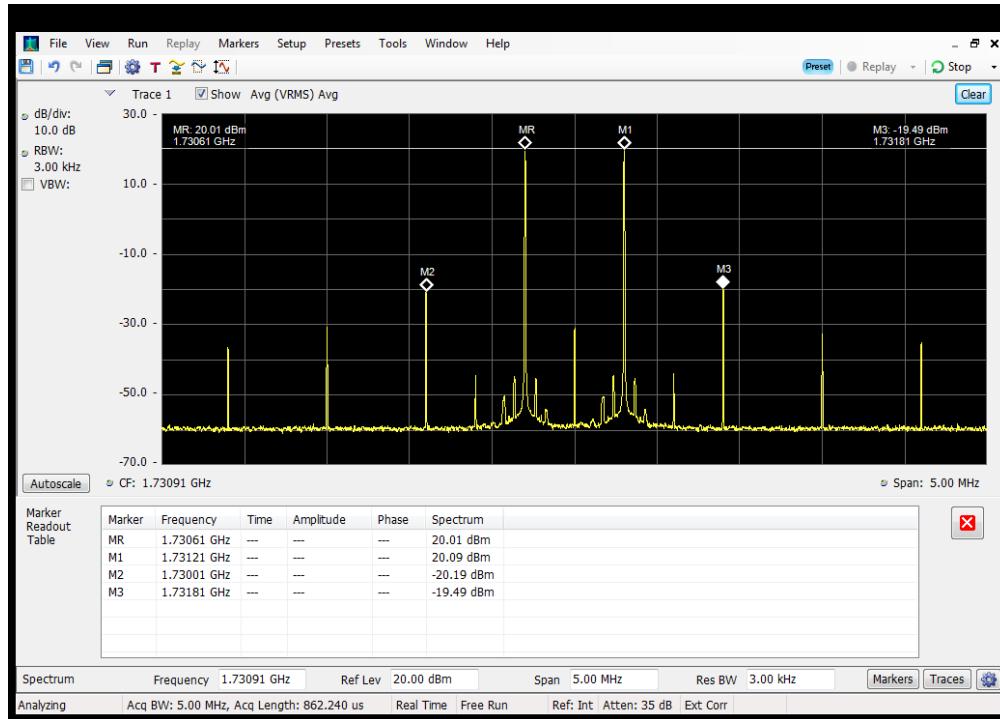


### Intermodulation\_DownLink\_869 - 894 MHz\_Dedicated Port\_Donor Port 2 to Server Port 4\_B5

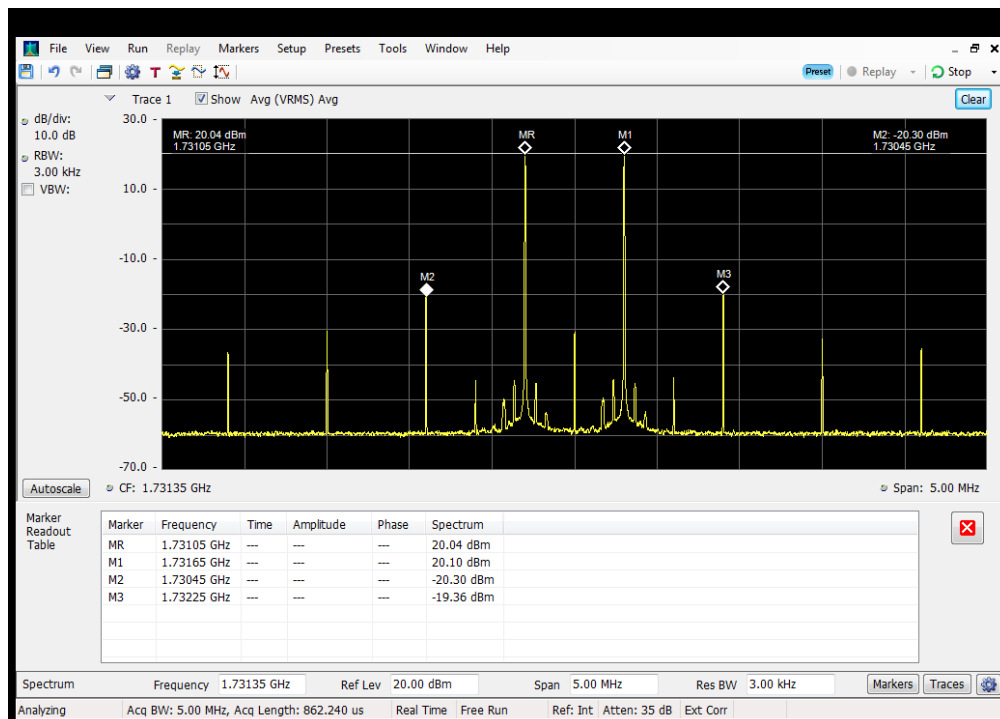




### Intermodulation\_UpLink\_1710 - 1755 MHz\_Dedicated Port\_Server Port 1 to Donor Port 1\_B4

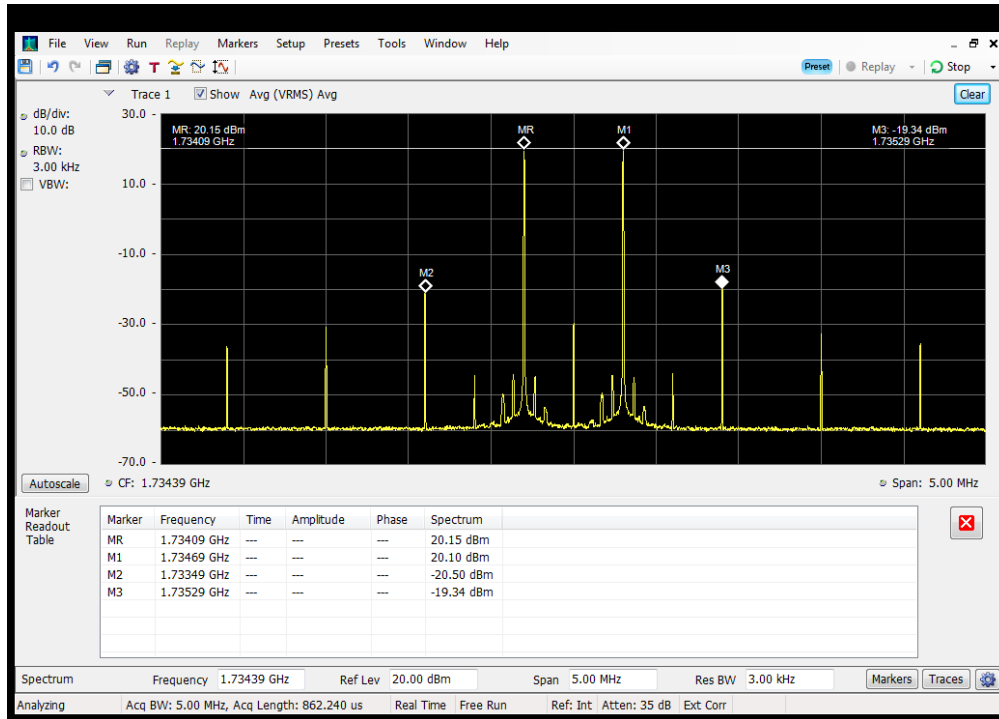


### Intermodulation\_UpLink\_1710 - 1755 MHz\_Dedicated Port\_Server Port 2 to Donor Port 1\_B4

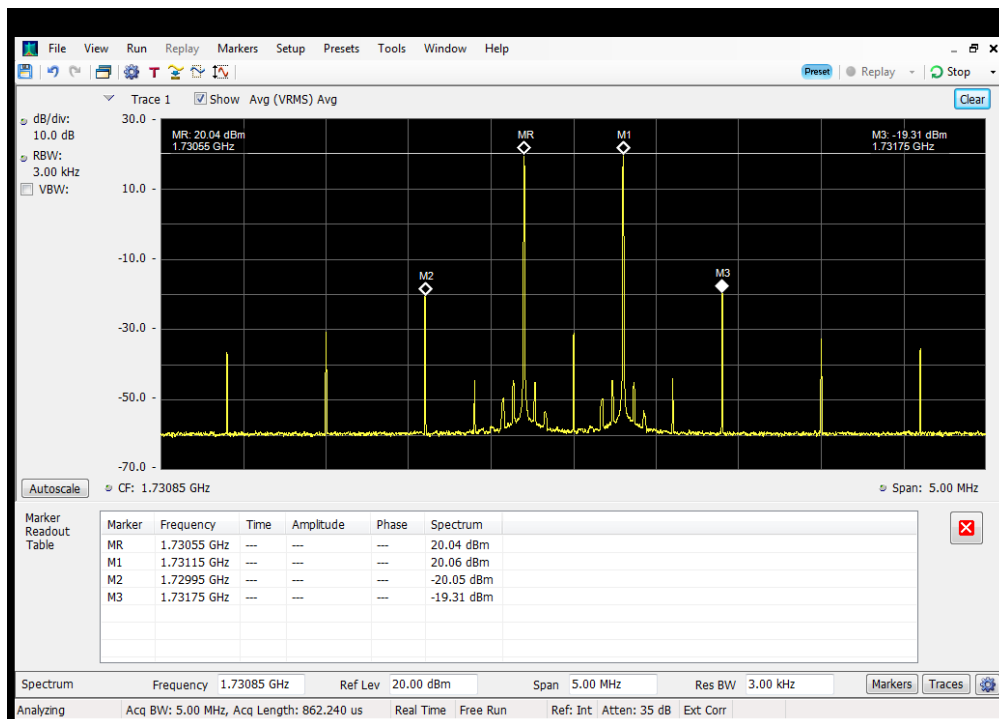




### Intermodulation\_UpLink\_1710 - 1755 MHz\_Dedicated Port\_Server Port 3 to Donor Port 1\_B4

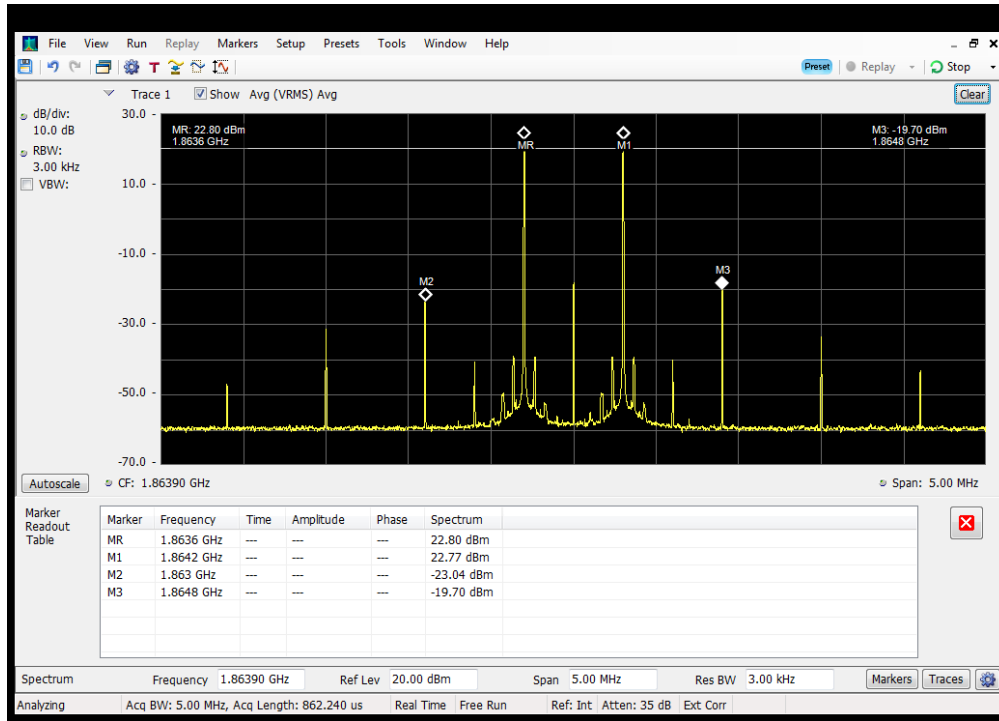


### Intermodulation\_UpLink\_1710 - 1755 MHz\_Dedicated Port\_Server Port 4 to Donor Port 1\_B4

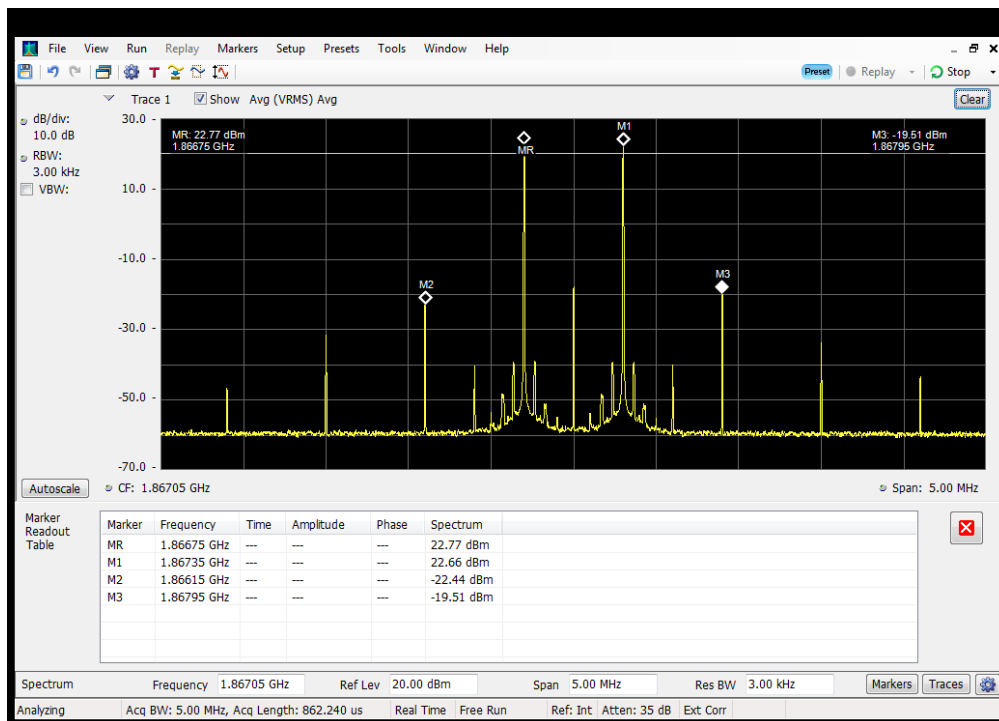




### Intermodulation\_UpLink\_1850 - 1915 MHz\_Dedicated Port\_Server Port 1 to Donor Port 1\_B25

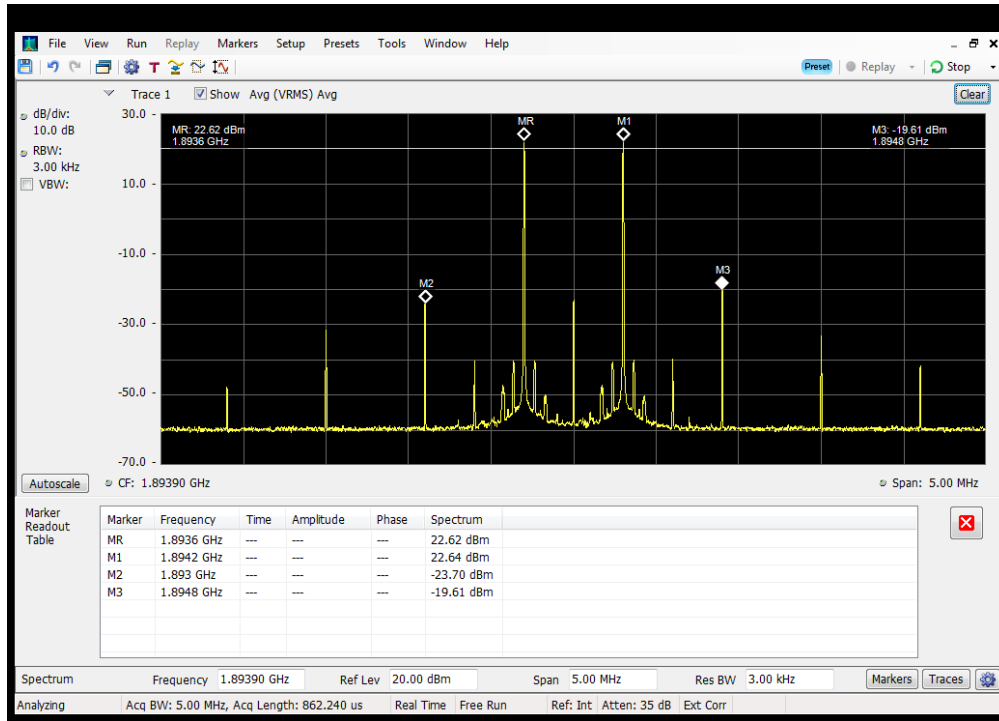


### Intermodulation\_UpLink\_1850 - 1915 MHz\_Dedicated Port\_Server Port 2 to Donor Port 1\_B25

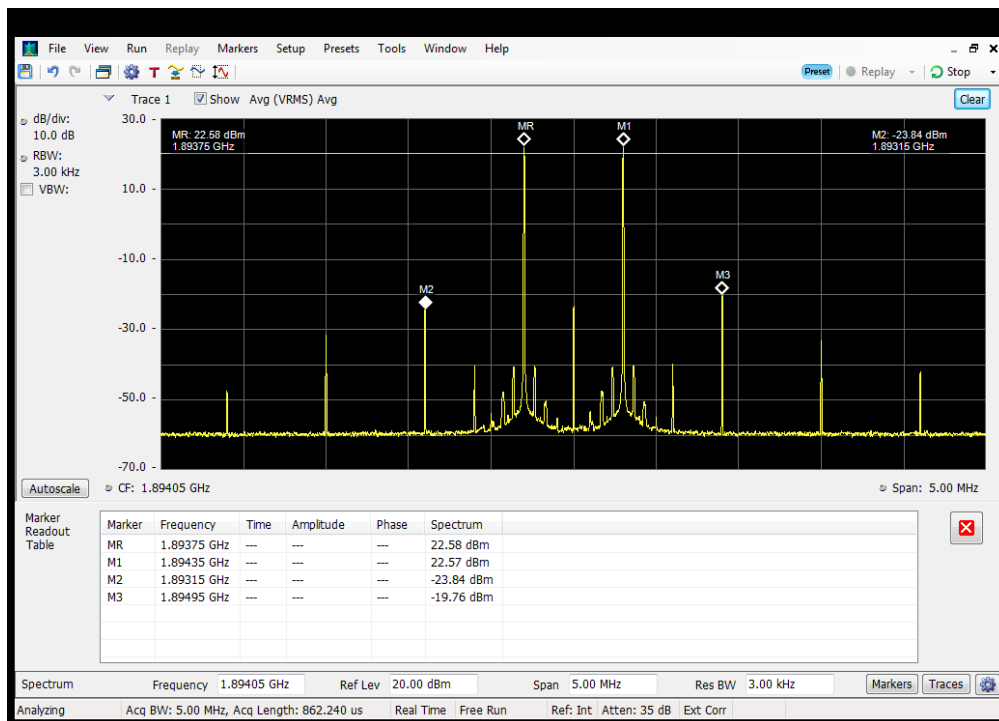




### Intermodulation\_UpLink\_1850 - 1915 MHz\_Dedicated Port\_Server Port 3 to Donor Port 1\_B25



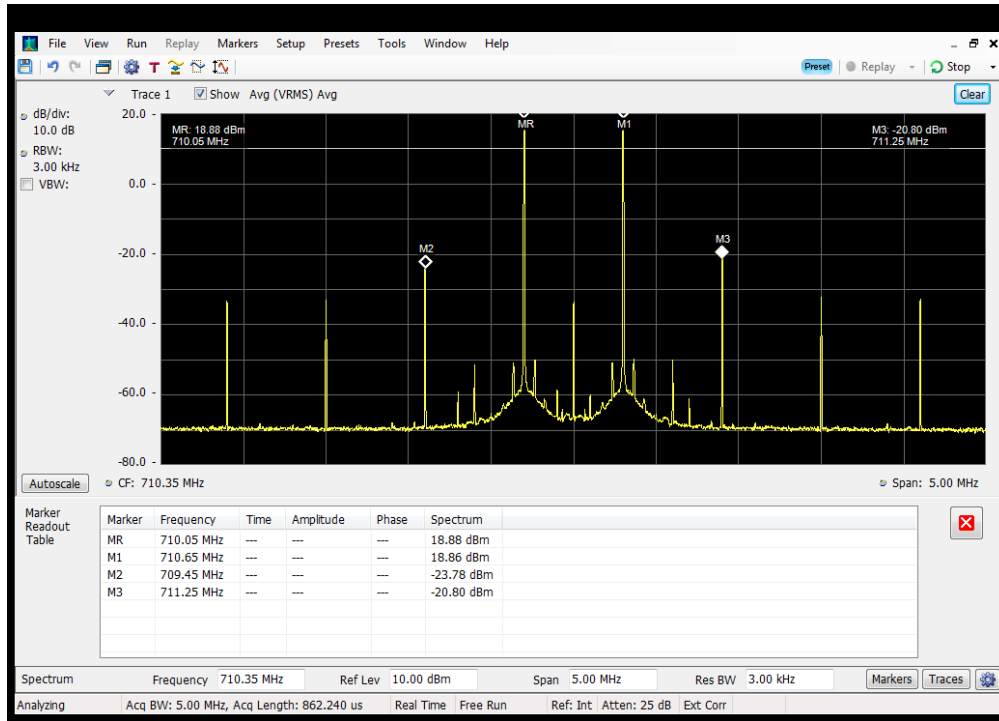
### Intermodulation\_UpLink\_1850 - 1915 MHz\_Dedicated Port\_Server Port 4 to Donor Port 1\_B25



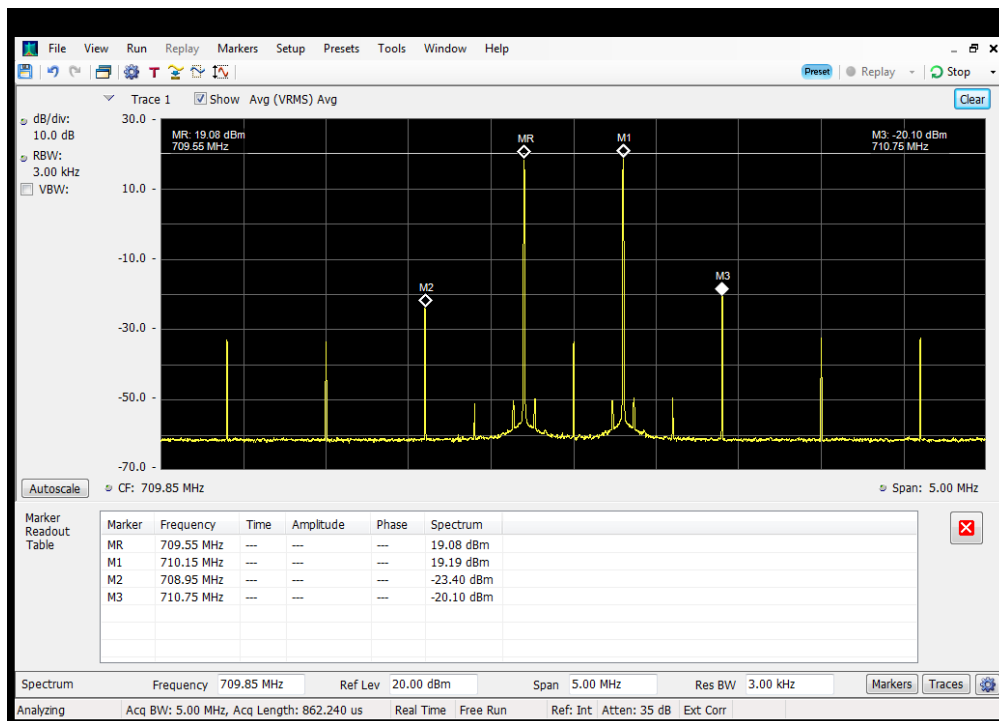




### Intermodulation\_UpLink\_698 - 716 MHz\_Common Port\_Server Port 1 to Donor Port 1\_B12

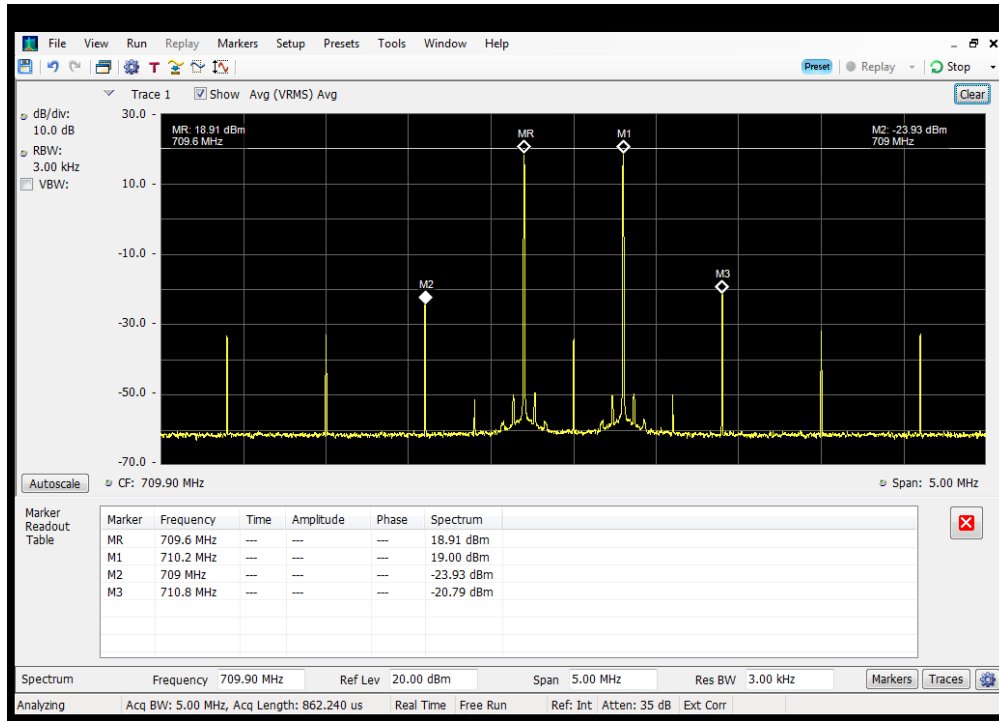


### Intermodulation\_UpLink\_698 - 716 MHz\_Common Port\_Server Port 2 to Donor Port 1\_B12

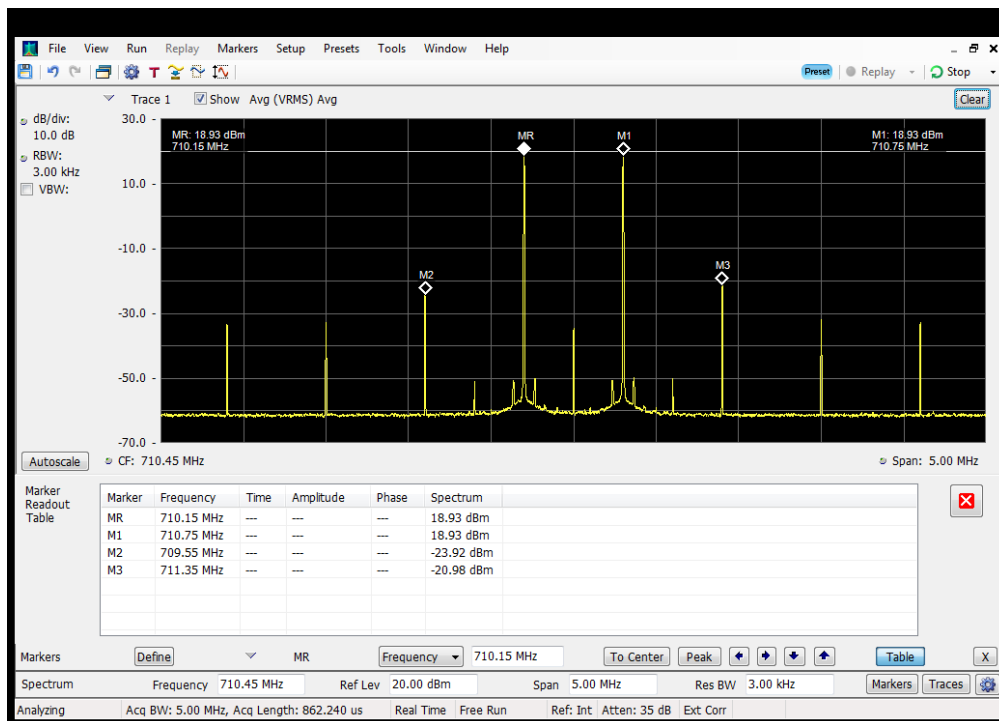




### Intermodulation\_UpLink\_698 - 716 MHz\_Common Port\_Server Port 3 to Donor Port 1\_B12

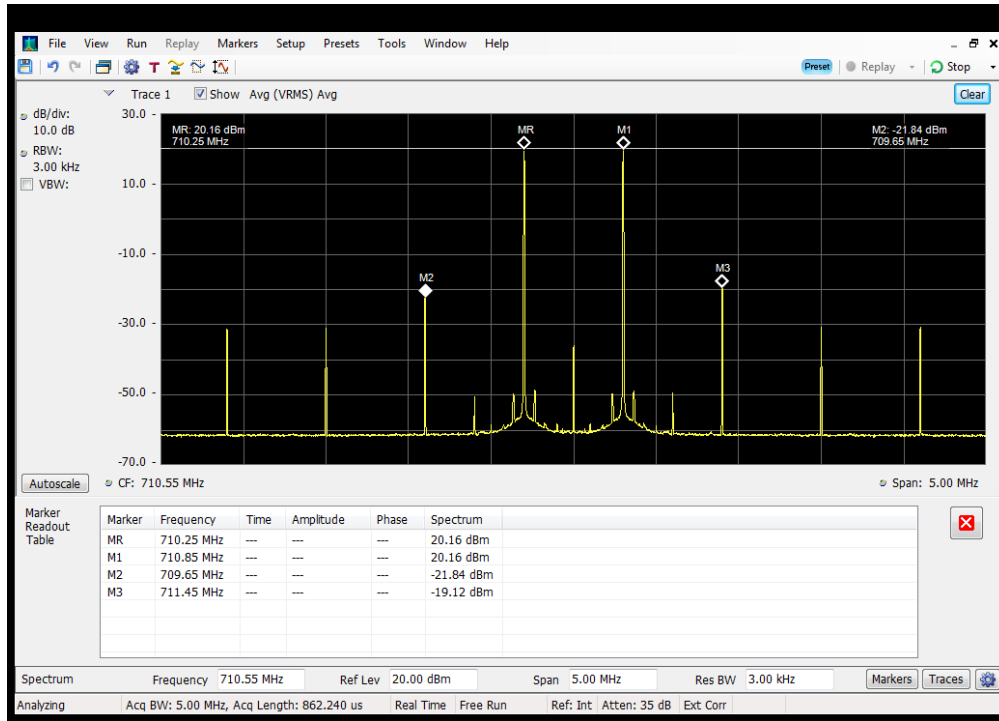


### Intermodulation\_UpLink\_698 - 716 MHz\_Common Port\_Server Port 4 to Donor Port 1\_B12

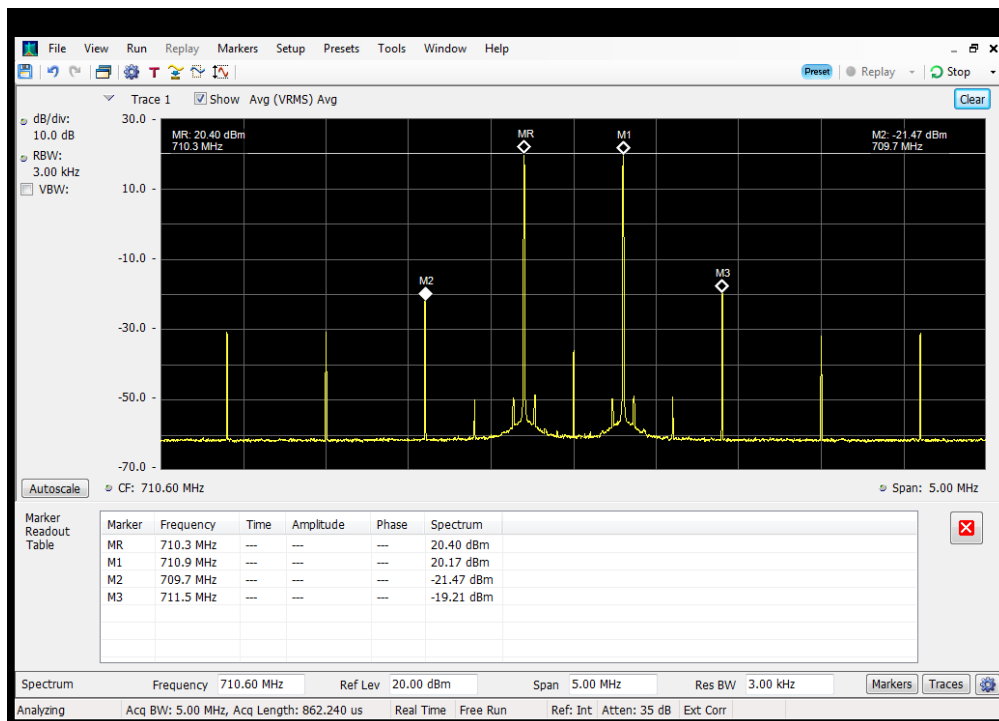




### Intermodulation\_UpLink\_698 - 716 MHz\_Dedicated Port\_Server Port 1 to Donor Port 3\_B12

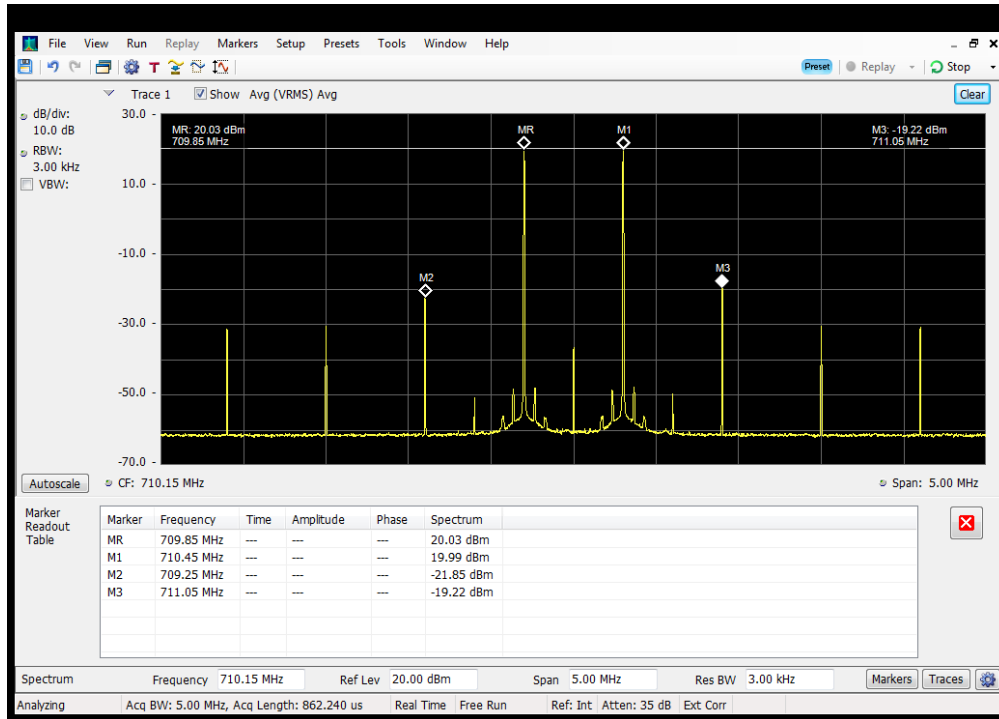


### Intermodulation\_UpLink\_698 - 716 MHz\_Dedicated Port\_Server Port 2 to Donor Port 3\_B12

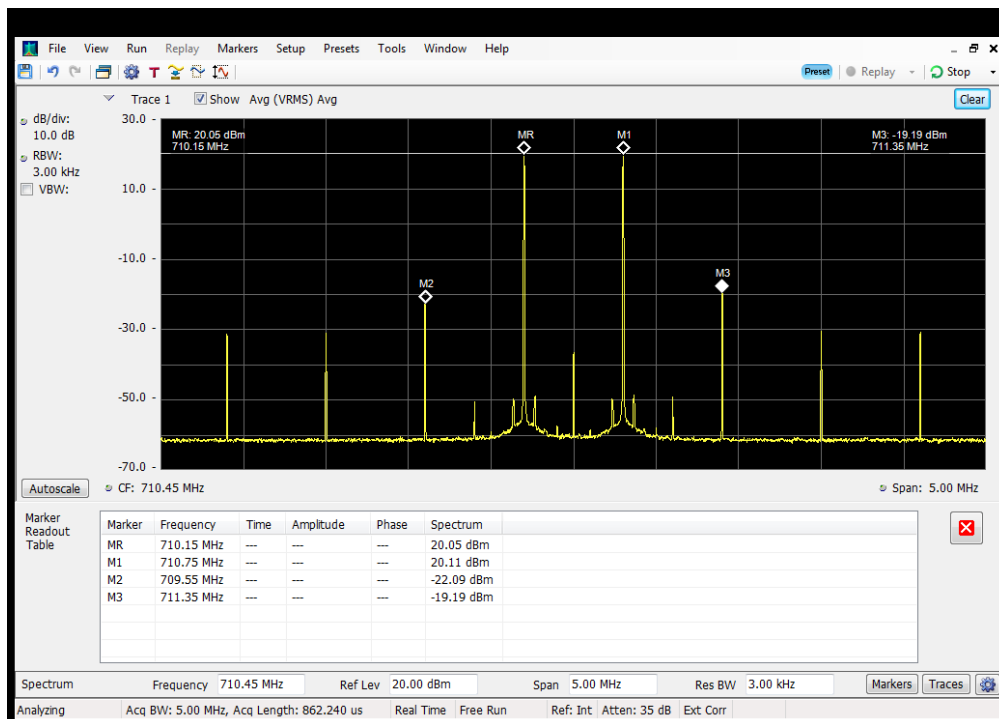




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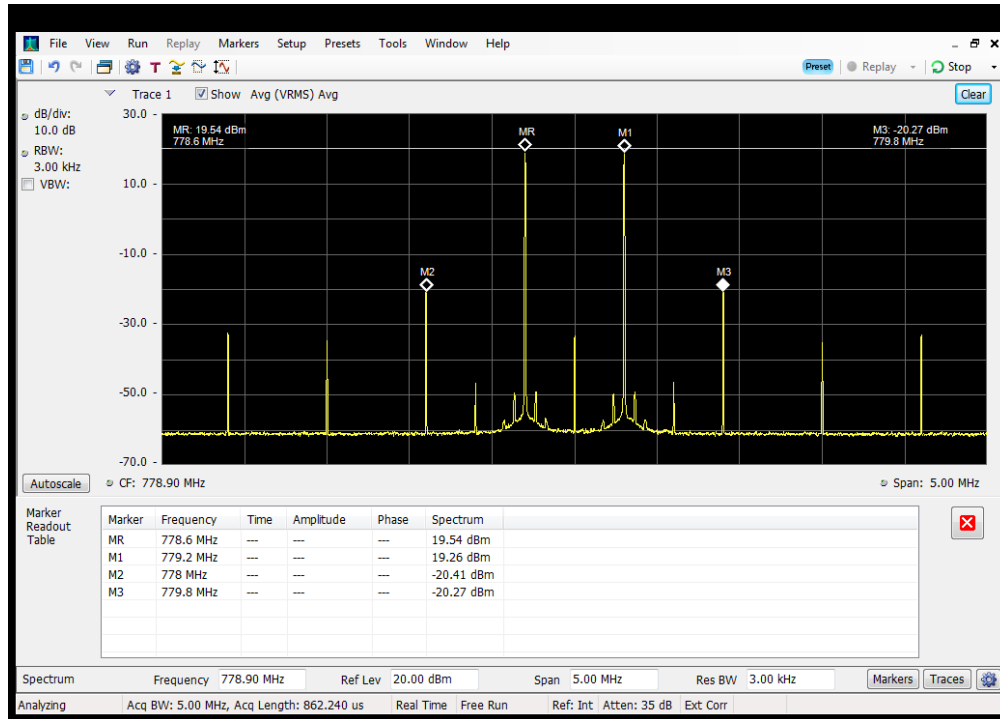


### Intermodulation\_UpLink\_698 - 716 MHz\_Dedicated Port\_Server Port 4 to Donor Port 3\_B12

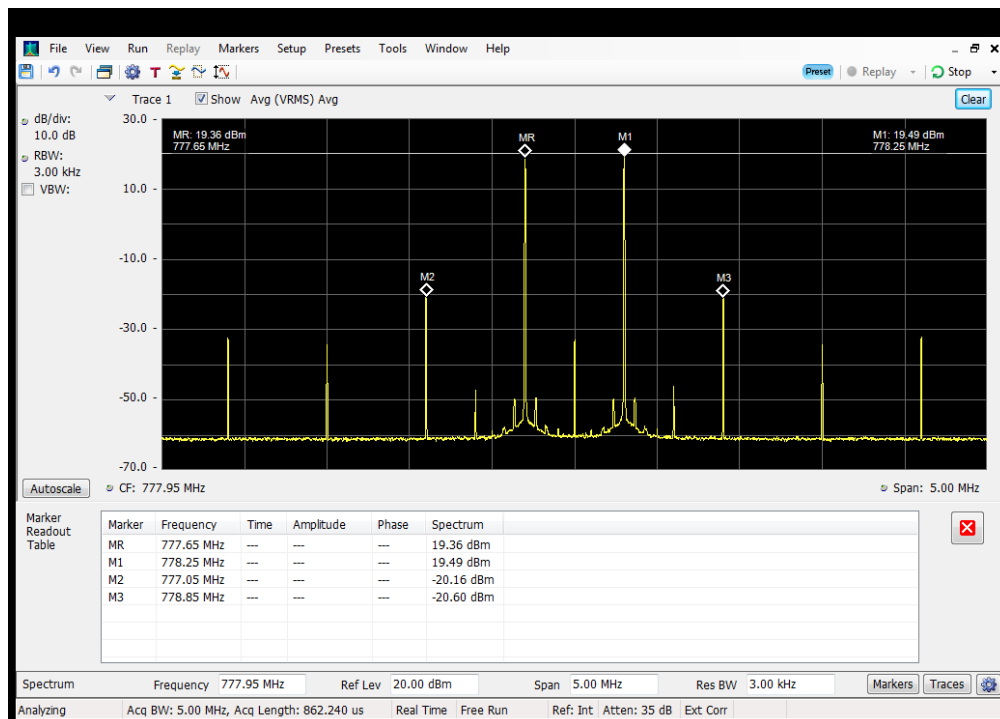




### Intermodulation\_UpLink\_776 - 787 MHz\_Common Port\_Server Port 1 to Donor Port 1\_B13

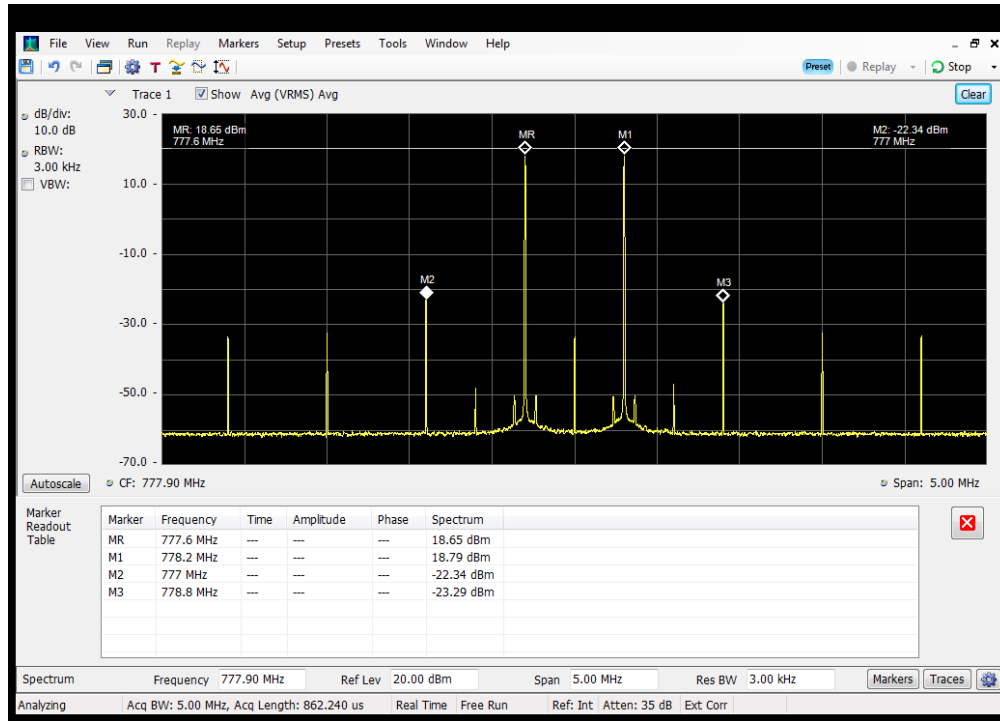


### Intermodulation\_UpLink\_776 - 787 MHz\_Common Port\_Server Port 2 to Donor Port 1\_B13

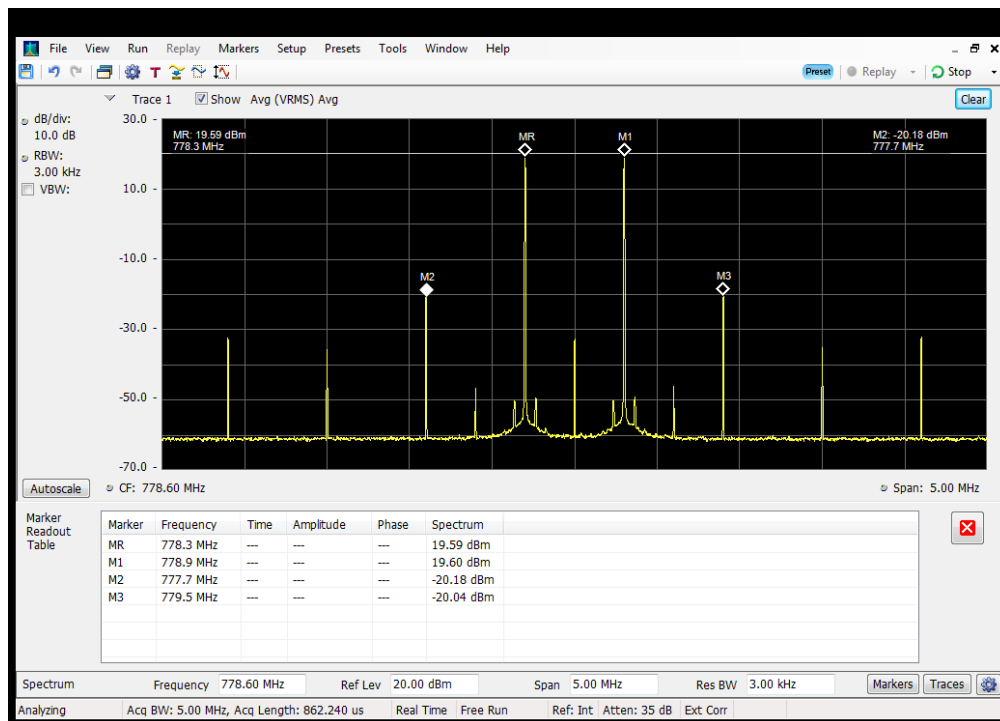




### Intermodulation\_UpLink\_776 - 787 MHz\_Common Port\_Server Port 3 to Donor Port 1\_B13

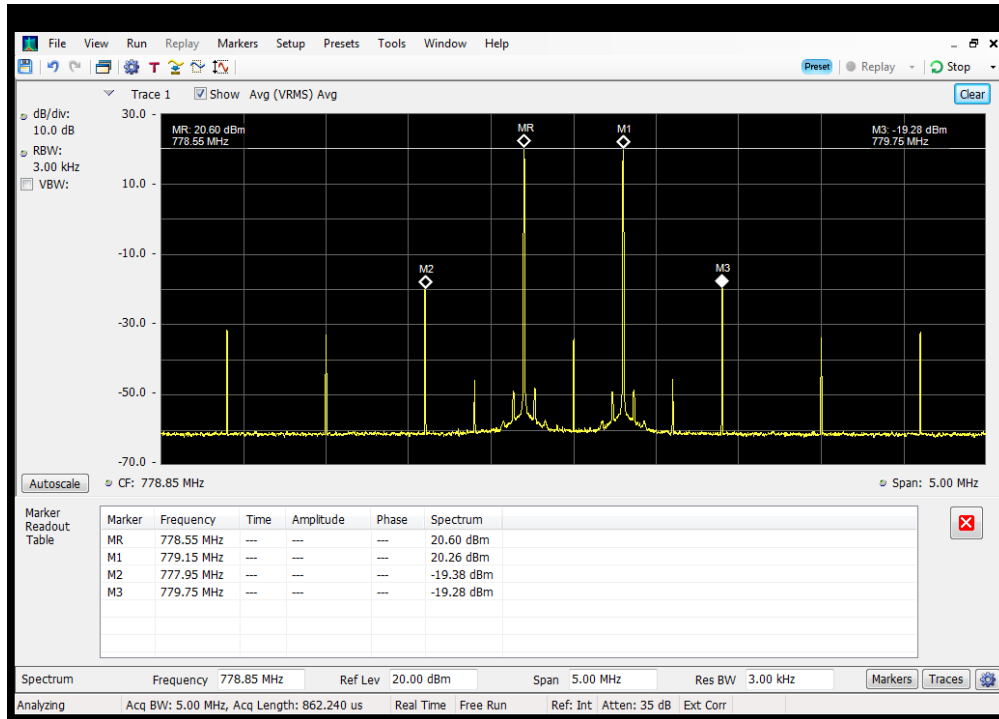


### Intermodulation\_UpLink\_776 - 787 MHz\_Common Port\_Server Port 4 to Donor Port 1\_B13

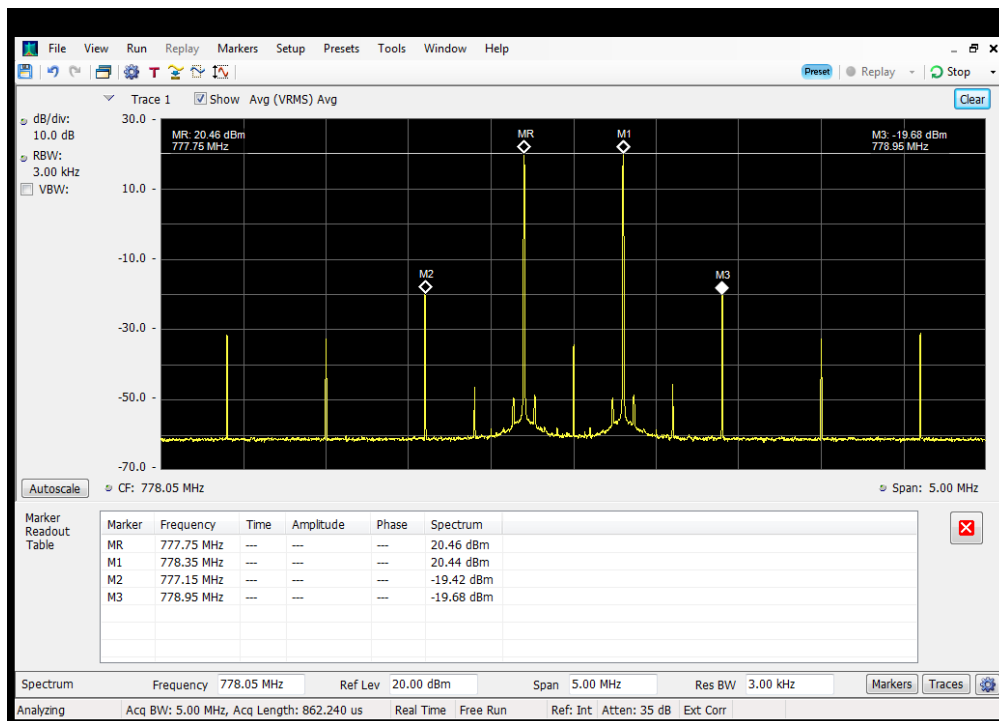




### Intermodulation\_UpLink\_776 - 787 MHz\_Dedicated Port\_Server Port 1 to Donor Port 3\_B13

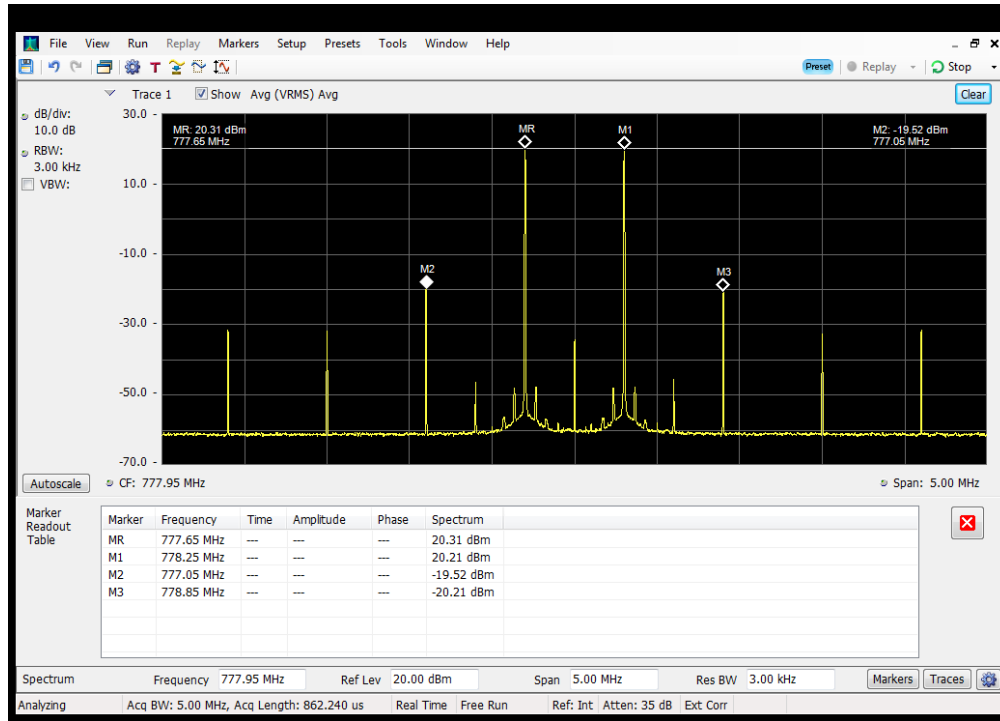


### Intermodulation\_UpLink\_776 - 787 MHz\_Dedicated Port\_Server Port 2 to Donor Port 3\_B13

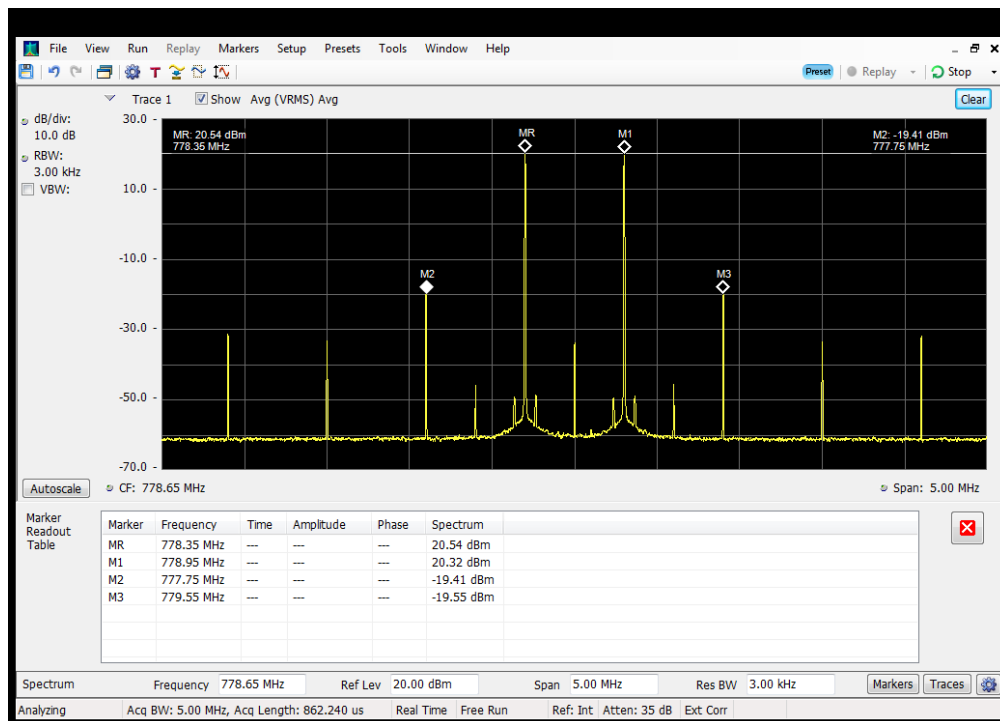




### Intermodulation\_UpLink\_776 - 787 MHz\_Dedicated Port\_Server Port 3 to Donor Port 3\_B13



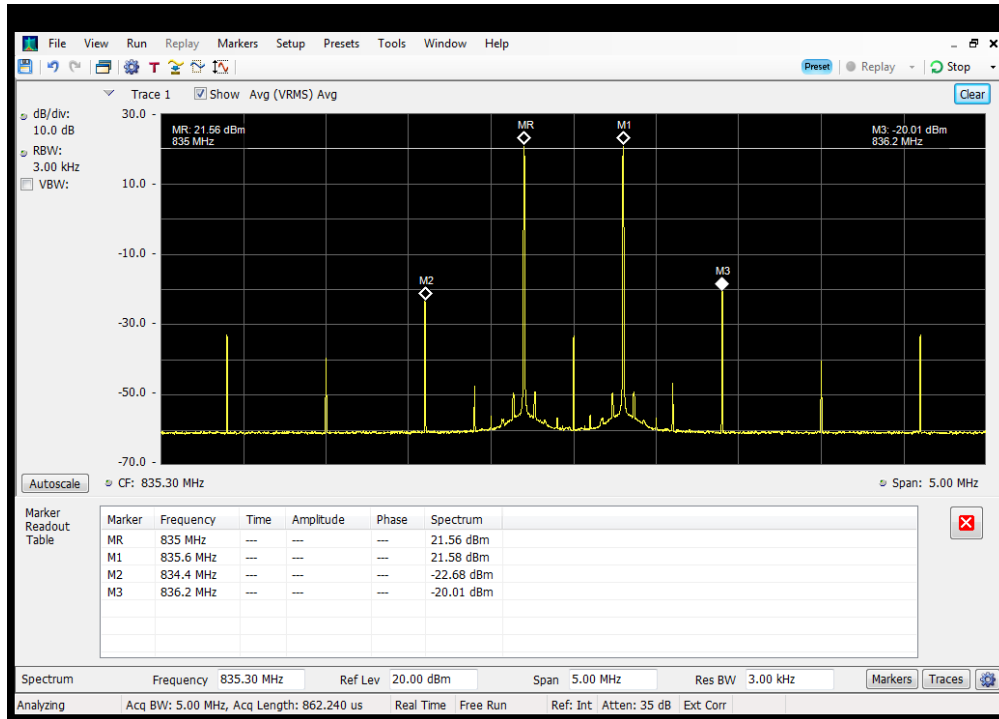
### Intermodulation\_UpLink\_776 - 787 MHz\_Dedicated Port\_Server Port 4 to Donor Port 3\_B13



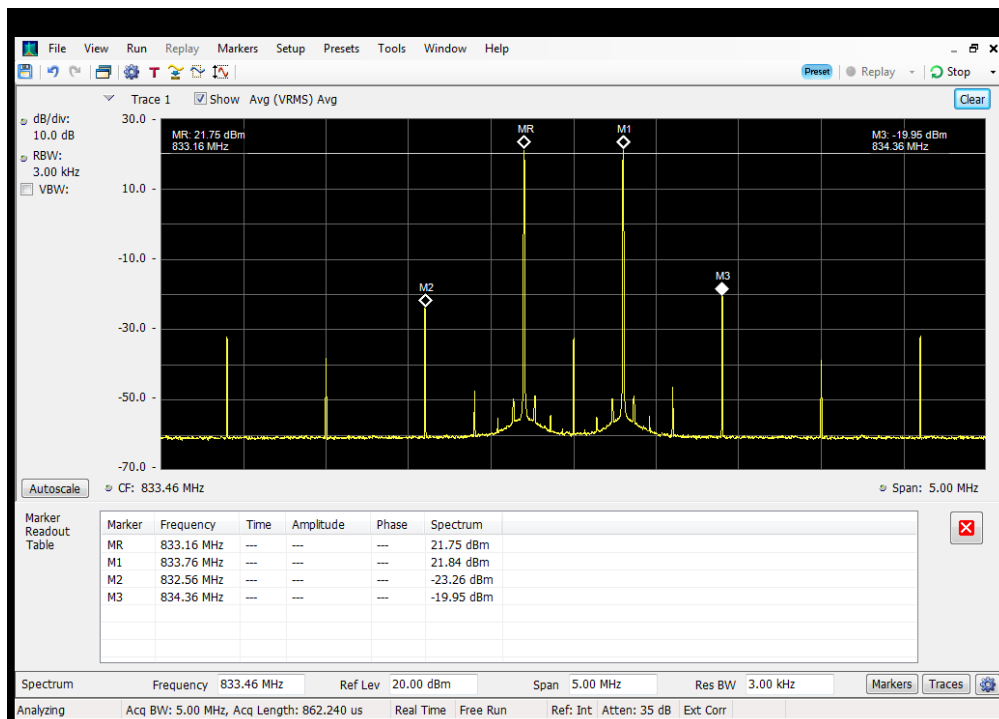




### Intermodulation\_UpLink\_824 - 849 MHz\_Common Port\_Server Port 1 to Donor Port 1\_B5

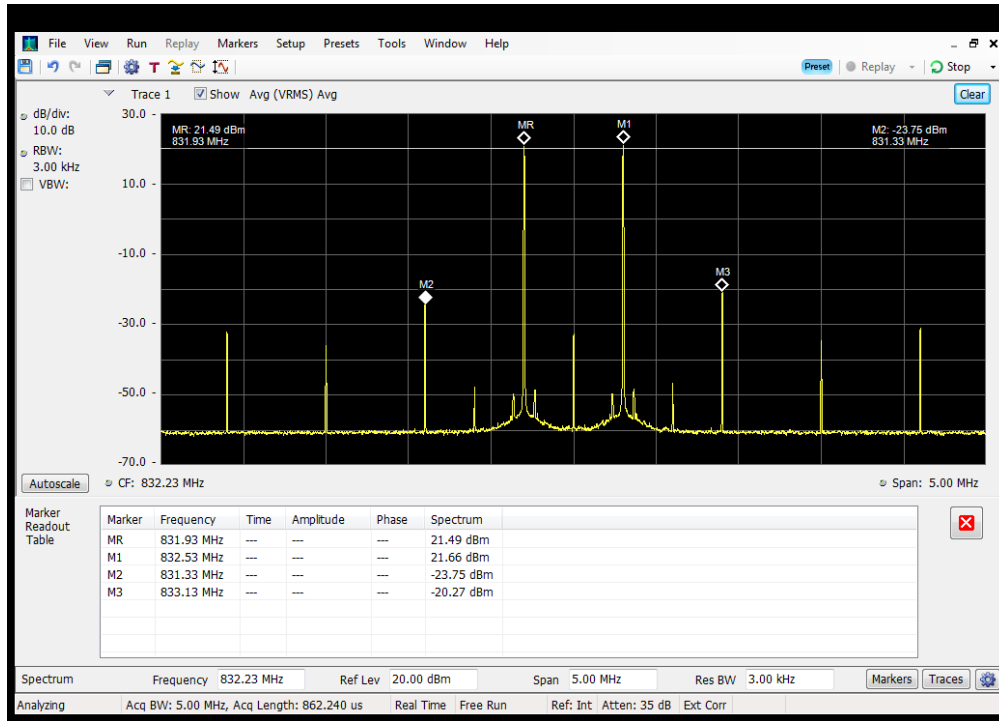


### Intermodulation\_UpLink\_824 - 849 MHz\_Common Port\_Server Port 2 to Donor Port 1\_B5

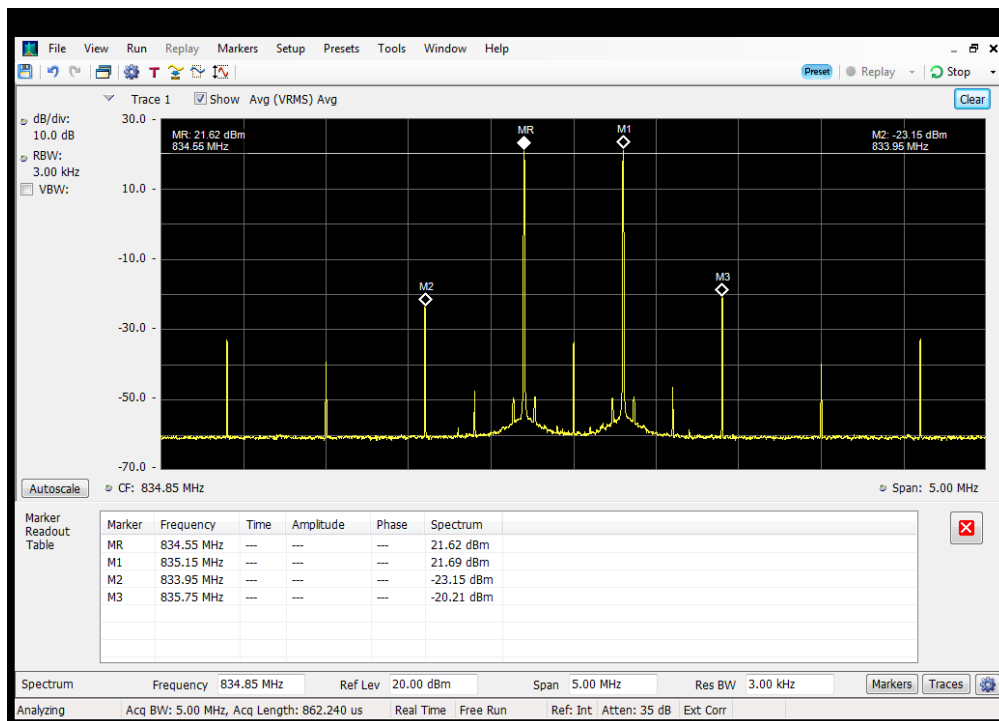




### Intermodulation\_UpLink\_824 - 849 MHz\_Common Port\_Server Port 3 to Donor Port 1\_B5

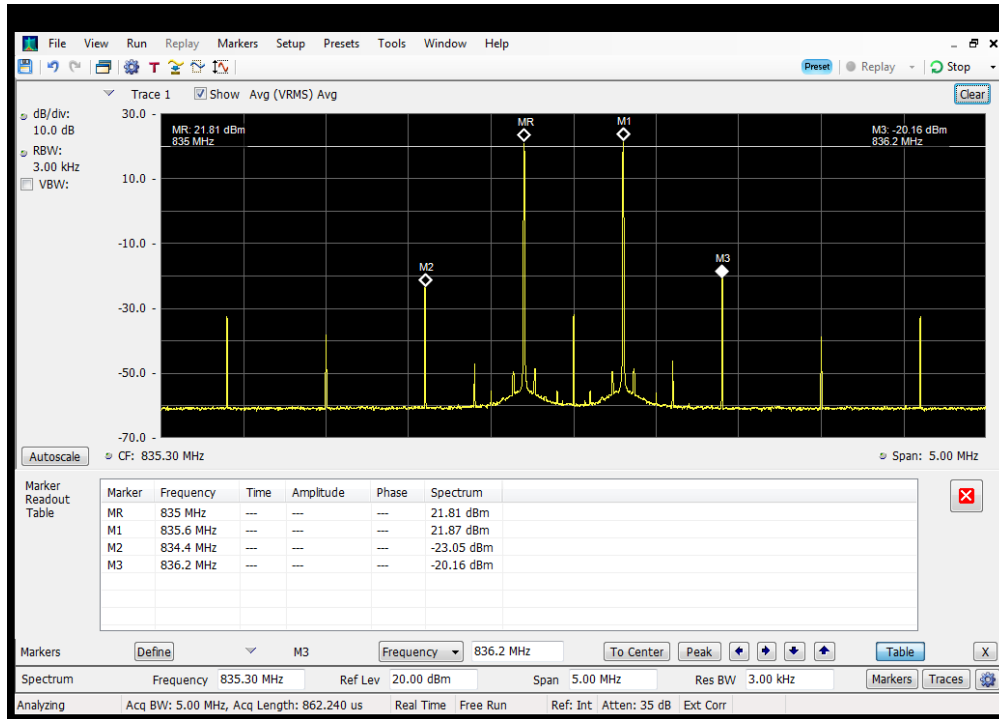


### Intermodulation\_UpLink\_824 - 849 MHz\_Common Port\_Server Port 4 to Donor Port 1\_B5

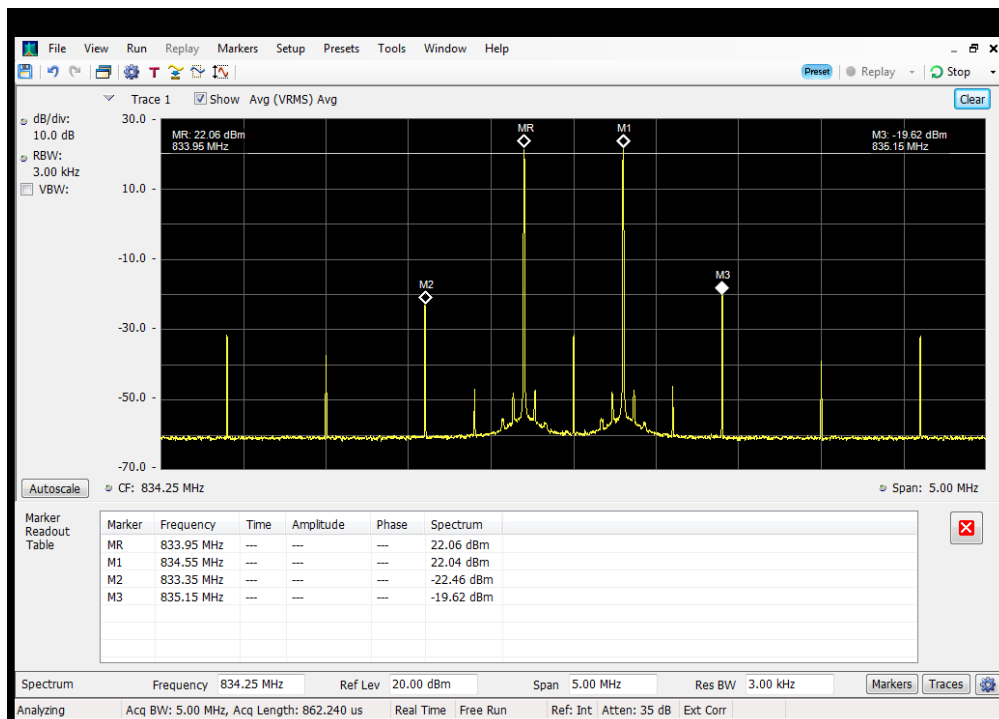




### Intermodulation\_UpLink\_824 - 849 MHz\_Dedicated Port\_Server Port 1 to Donor Port 2\_B5

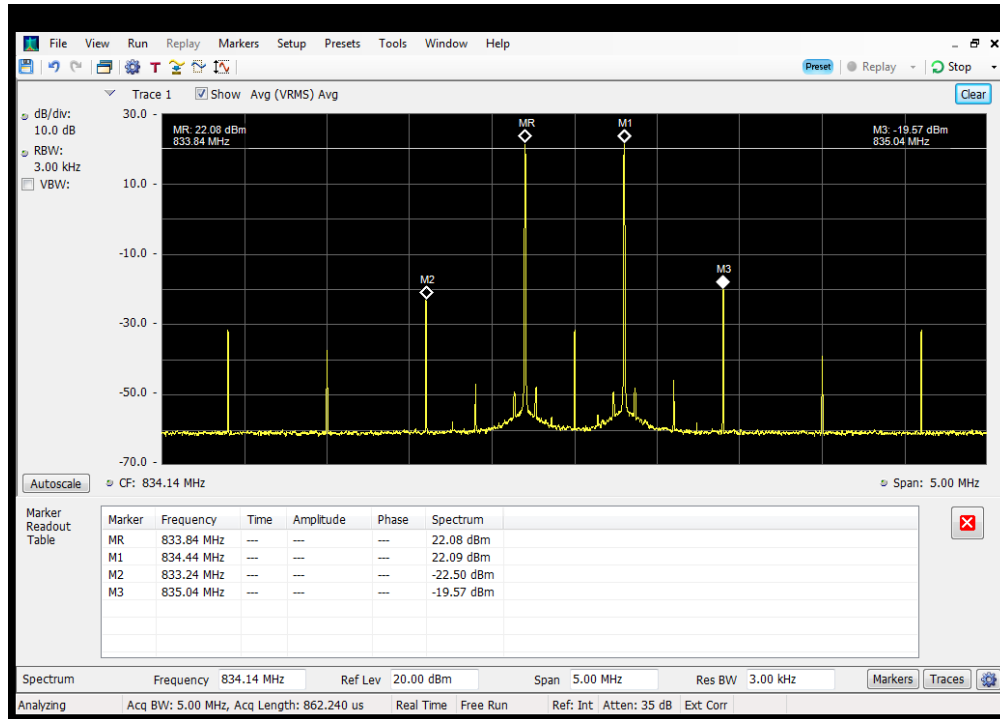


### Intermodulation\_UpLink\_824 - 849 MHz\_Dedicated Port\_Server Port 2 to Donor Port 2\_B5





### Intermodulation\_UpLink\_824 - 849 MHz\_Dedicated Port\_Server Port 3 to Donor Port 2\_B5



### Intermodulation\_UpLink\_824 - 849 MHz\_Dedicated Port\_Server Port 4 to Donor Port 2\_B5

