

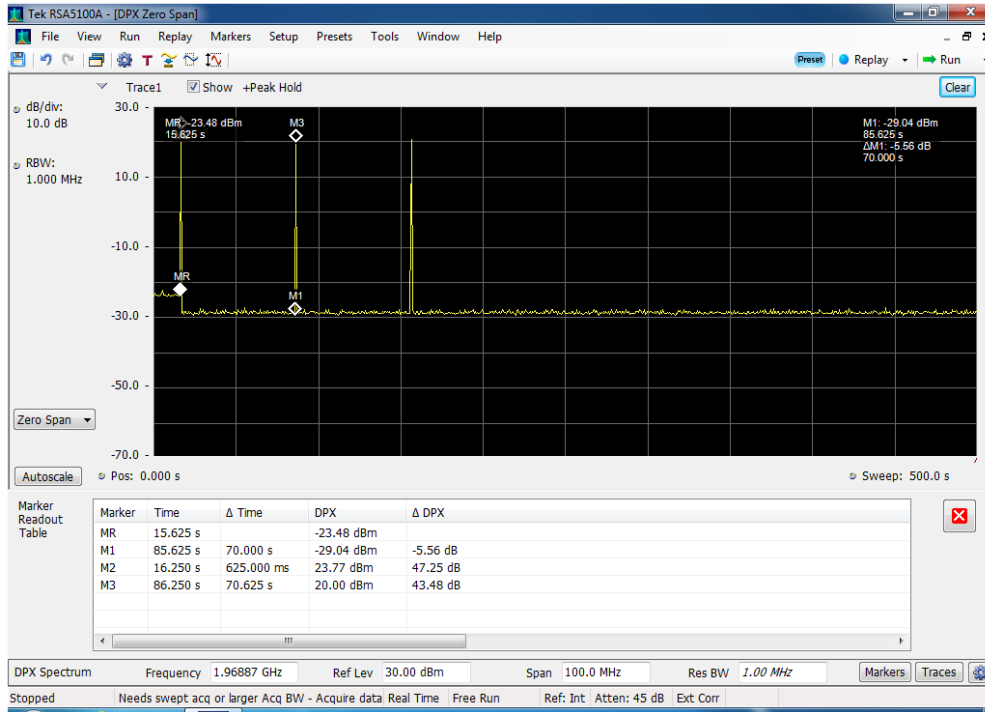


Annex I

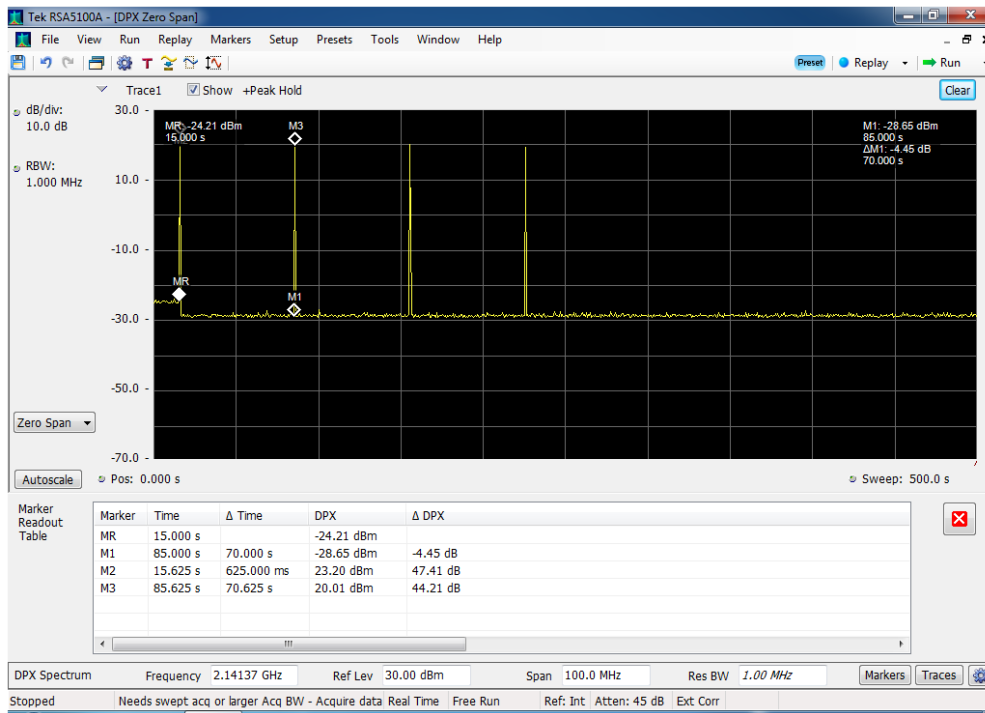
Anti-Oscillation



Anti-Oscillation_# of Restarts_DL_1960 MHz

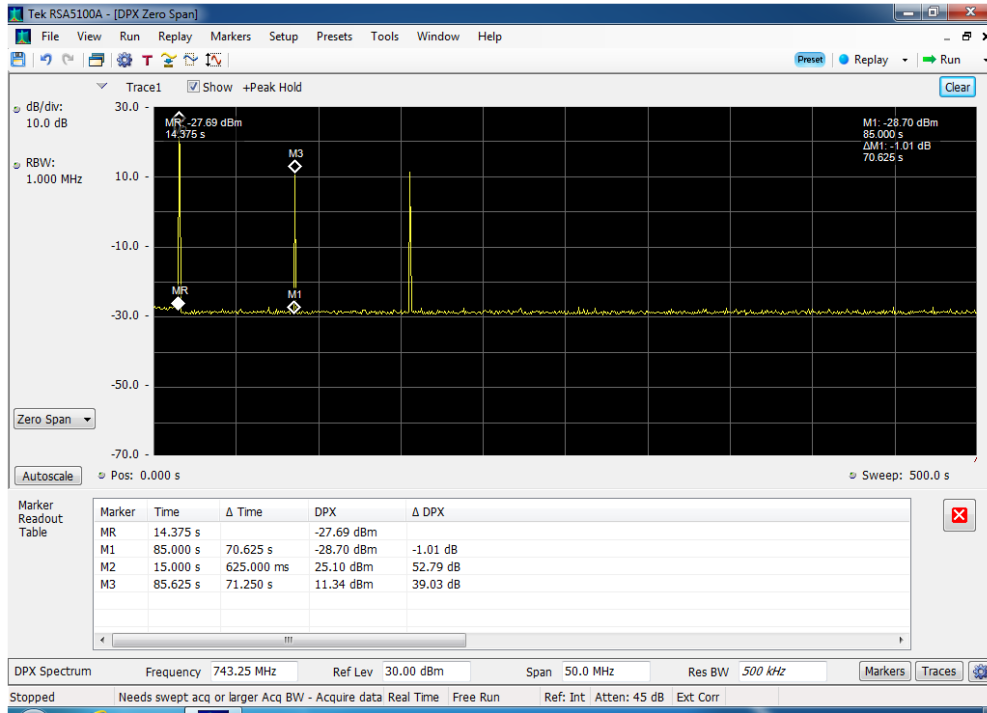


Anti-Oscillation_# of Restarts_DL_2132 MHz

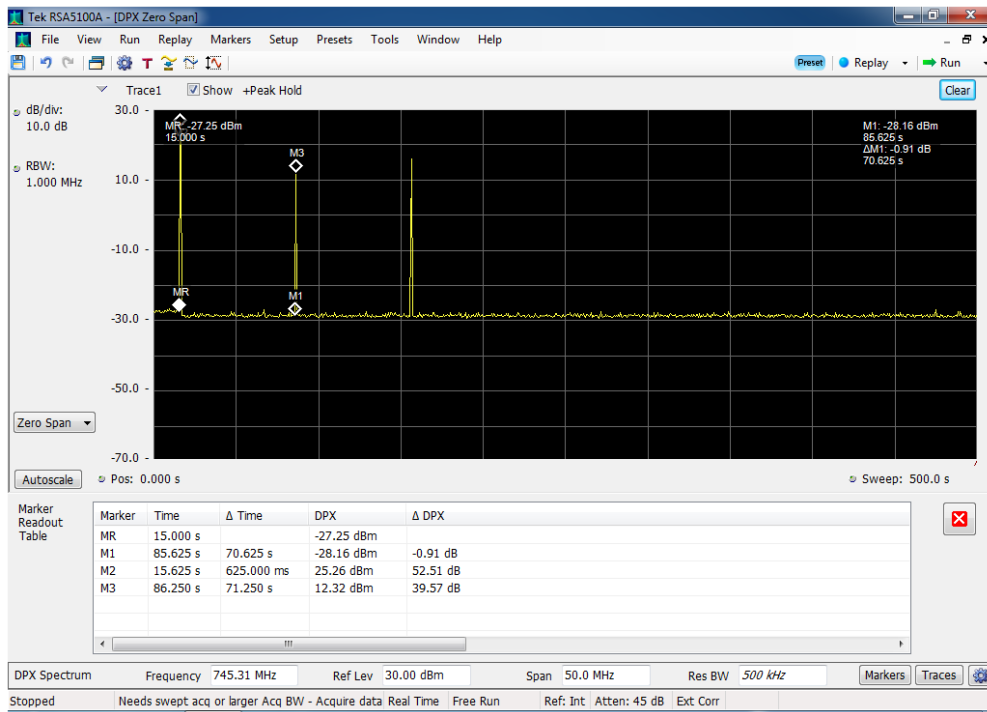




Anti-Oscillation_# of Restarts_DL_737 MHz

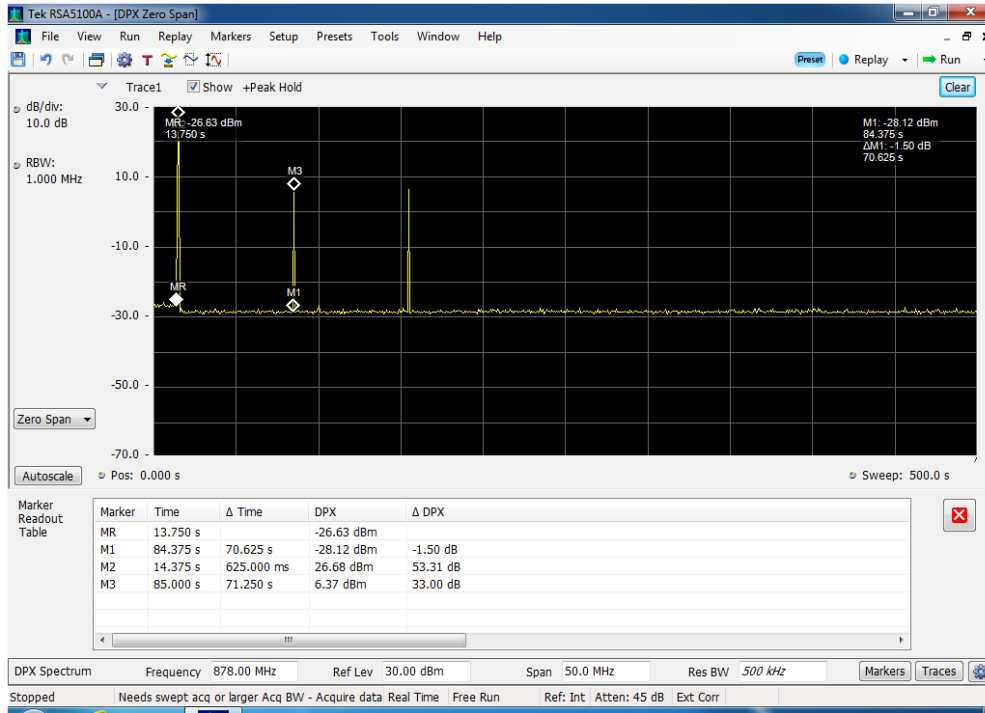


Anti-Oscillation_# of Restarts_DL_751 MHz

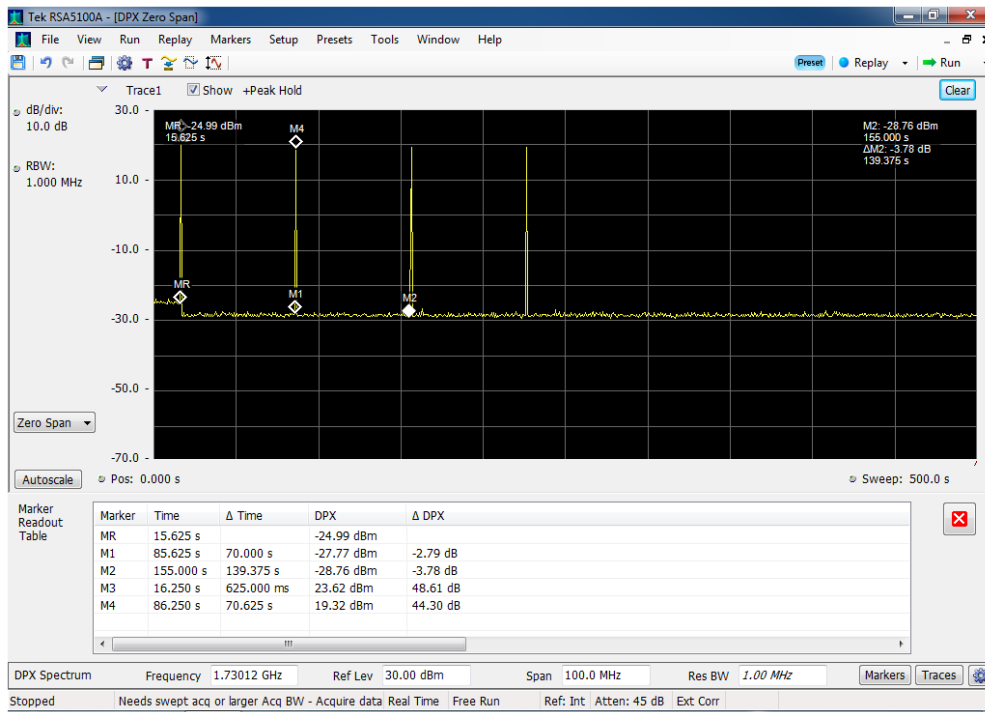




Anti-Oscillation_# of Restarts_DL_881 MHz

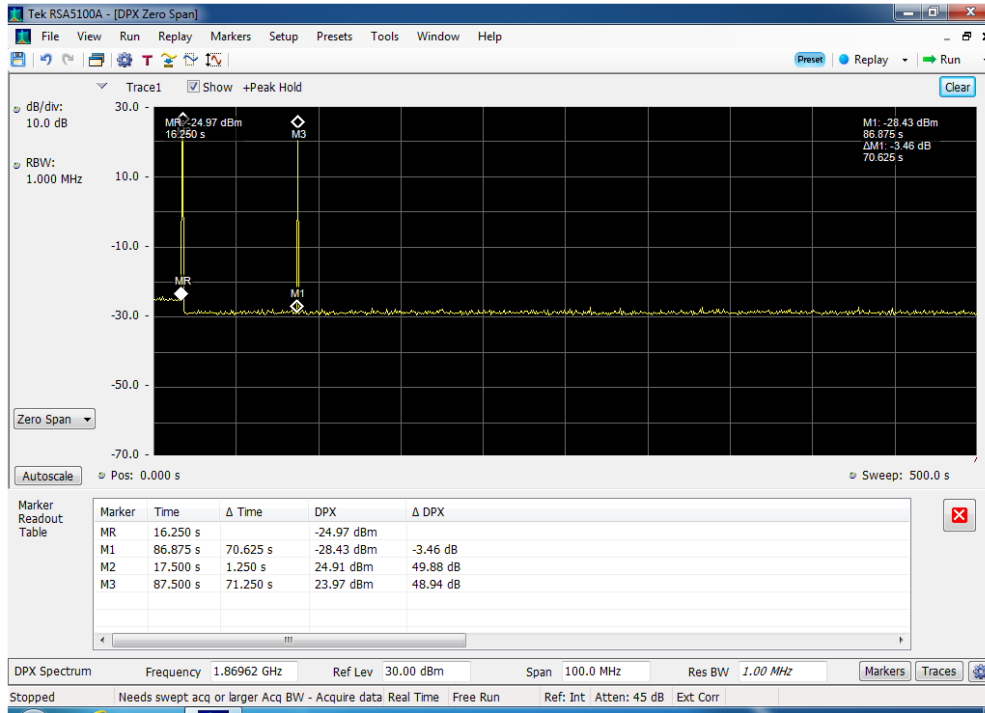


Anti-Oscillation_# of Restarts_UL_1732 MHz

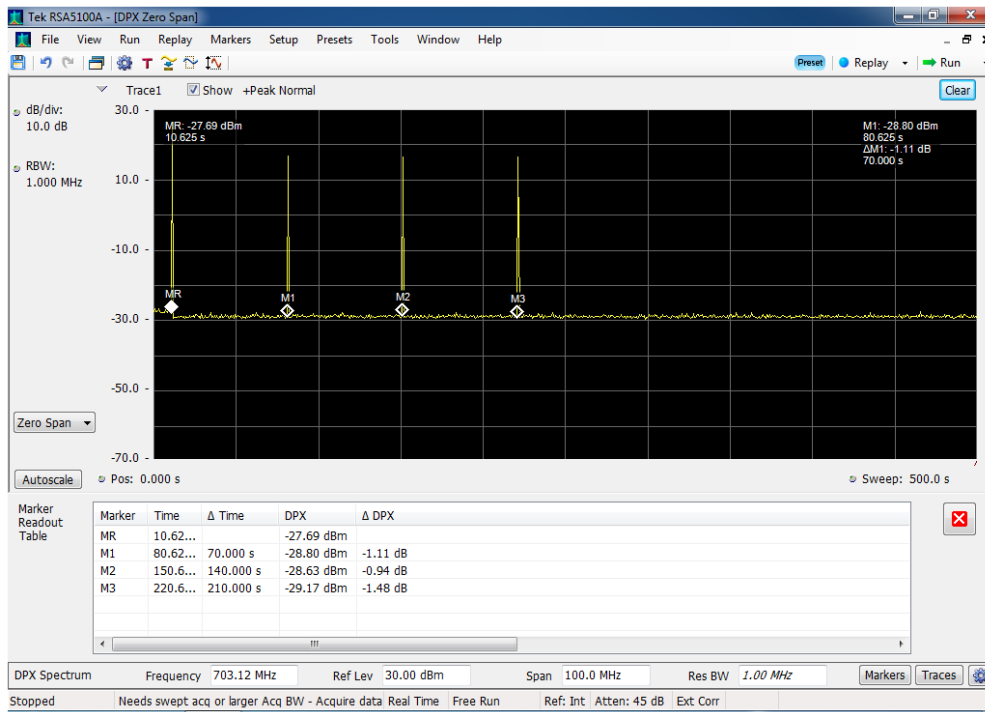




Anti-Oscillation_# of Restarts_UL_1880 MHz

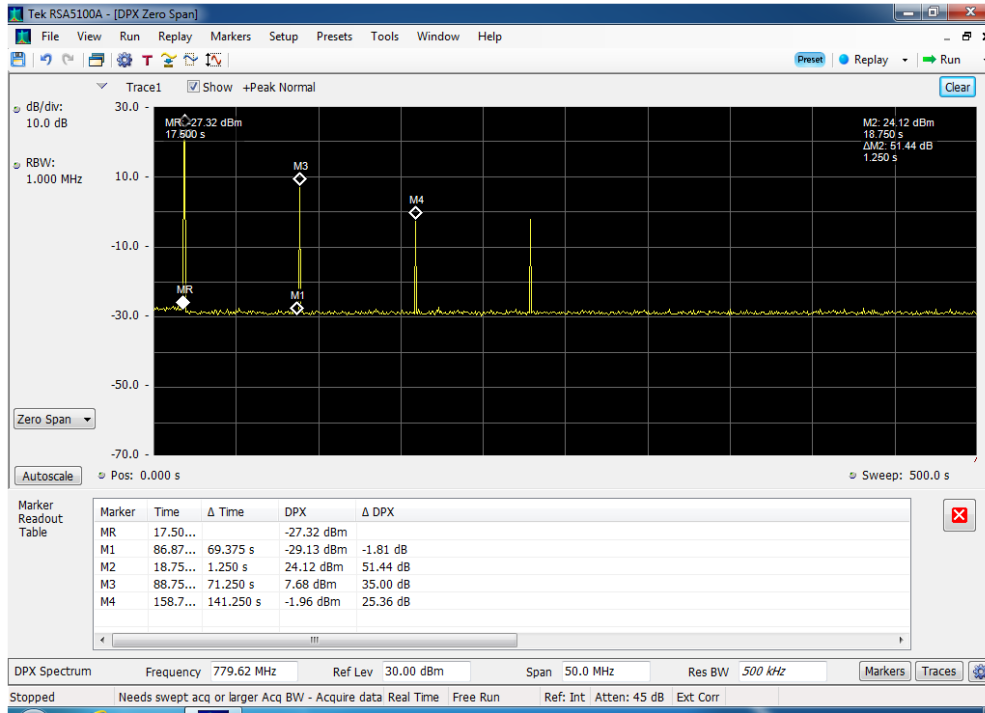


Anti-Oscillation_# of Restarts_UL_707 MHz

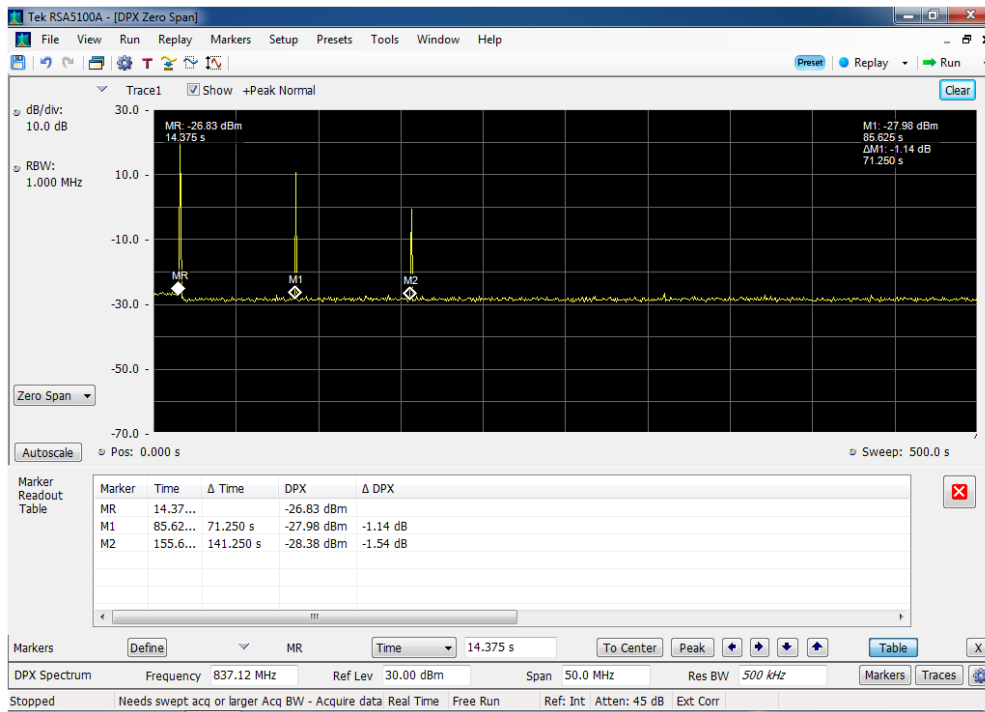




Anti-Oscillation_# of Restarts_UL_781 MHz

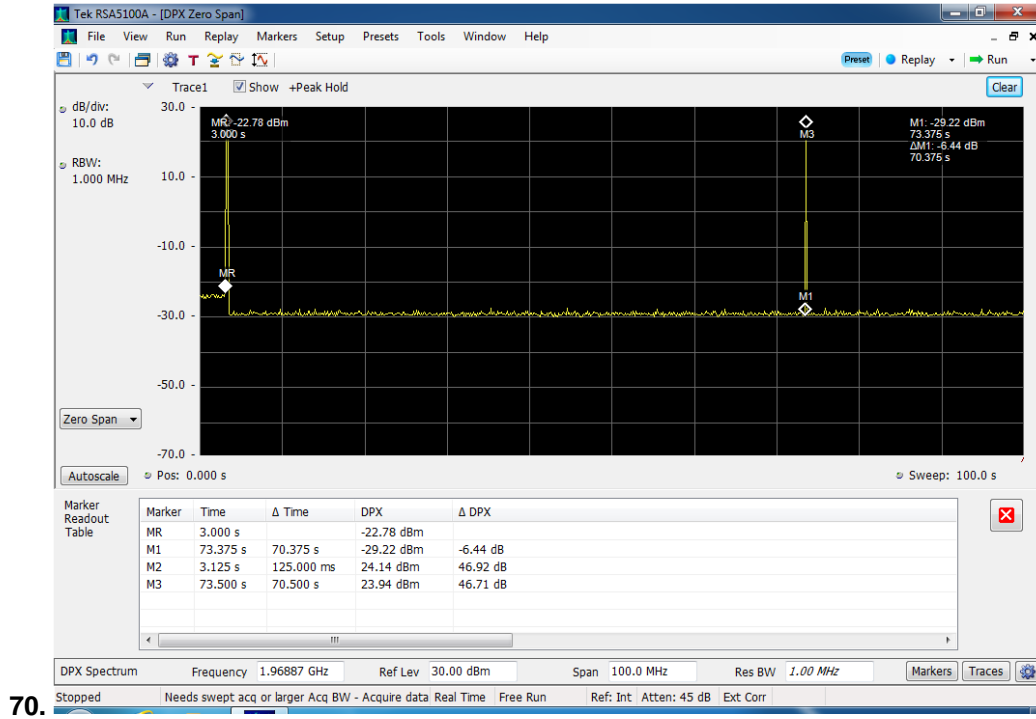


Anti-Oscillation_# of Restarts_UL_836 MHz

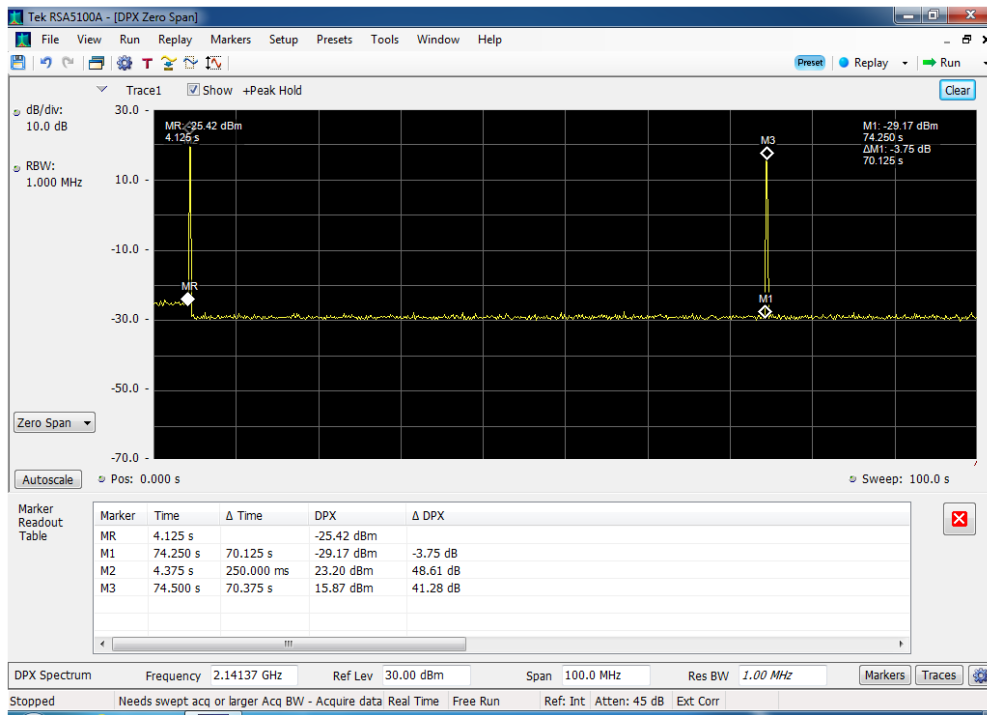




Anti-Oscillation_Restart Time_DL_1960 MHz

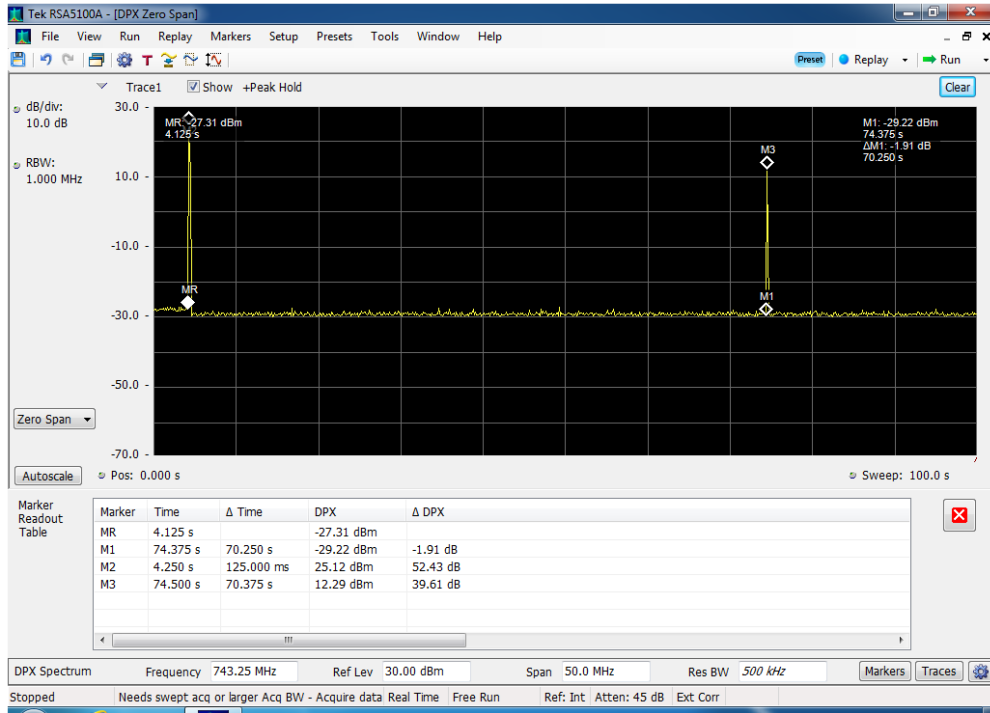


Anti-Oscillation_Restart Time_DL_2132 MHz

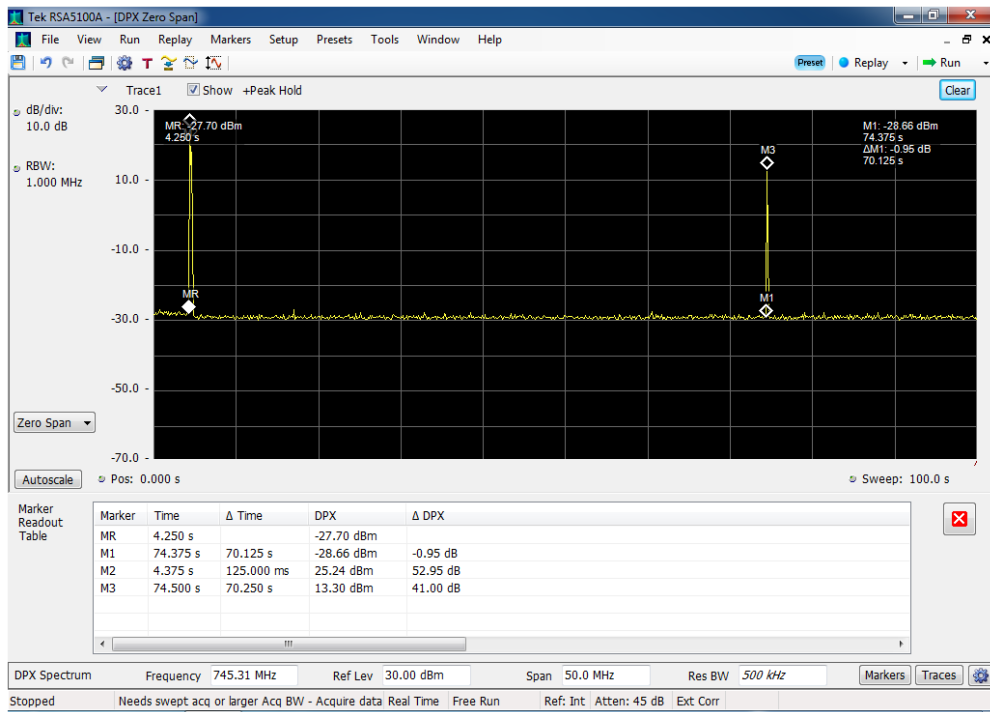




Anti-Oscillation_Restart Time_DL_737 MHz

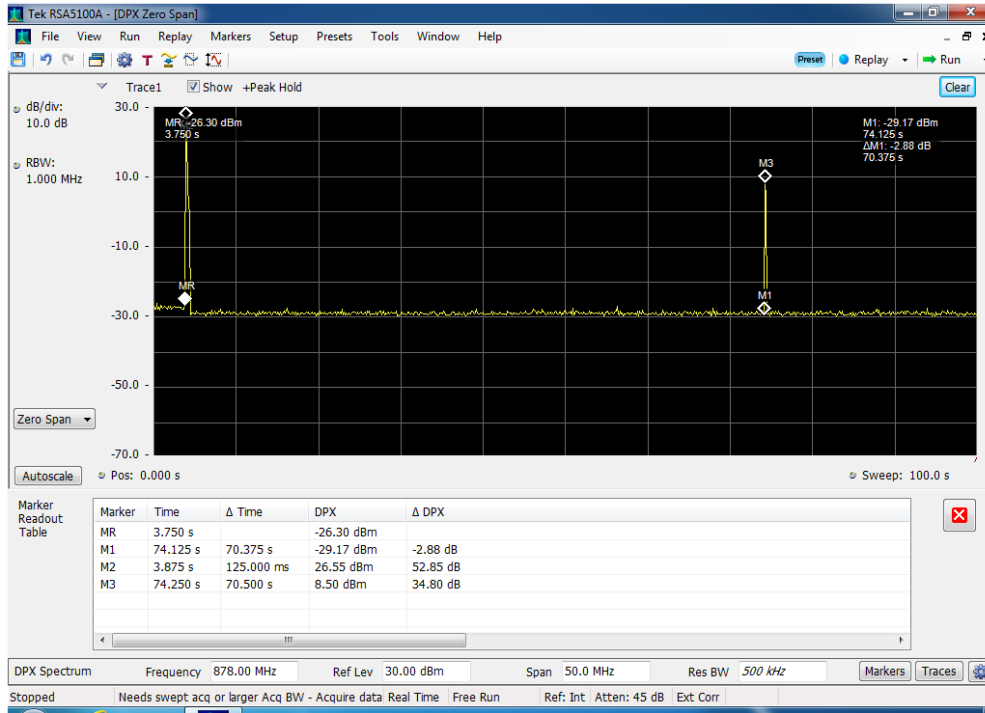


Anti-Oscillation_Restart Time_DL_751 MHz

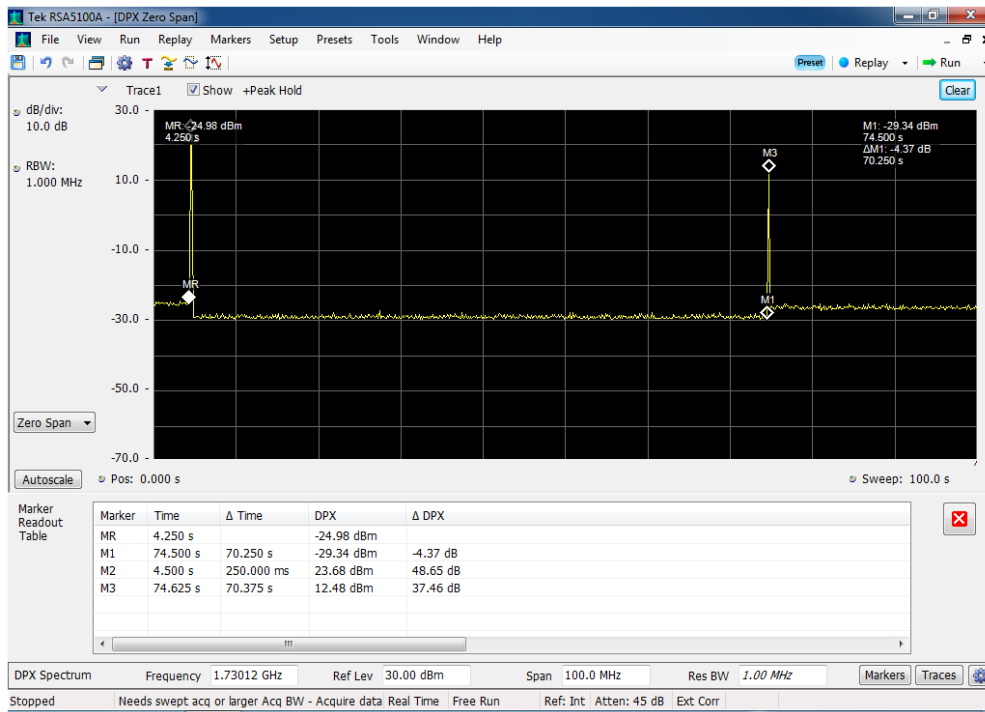




Anti-Oscillation_Restart Time_DL_881 MHz

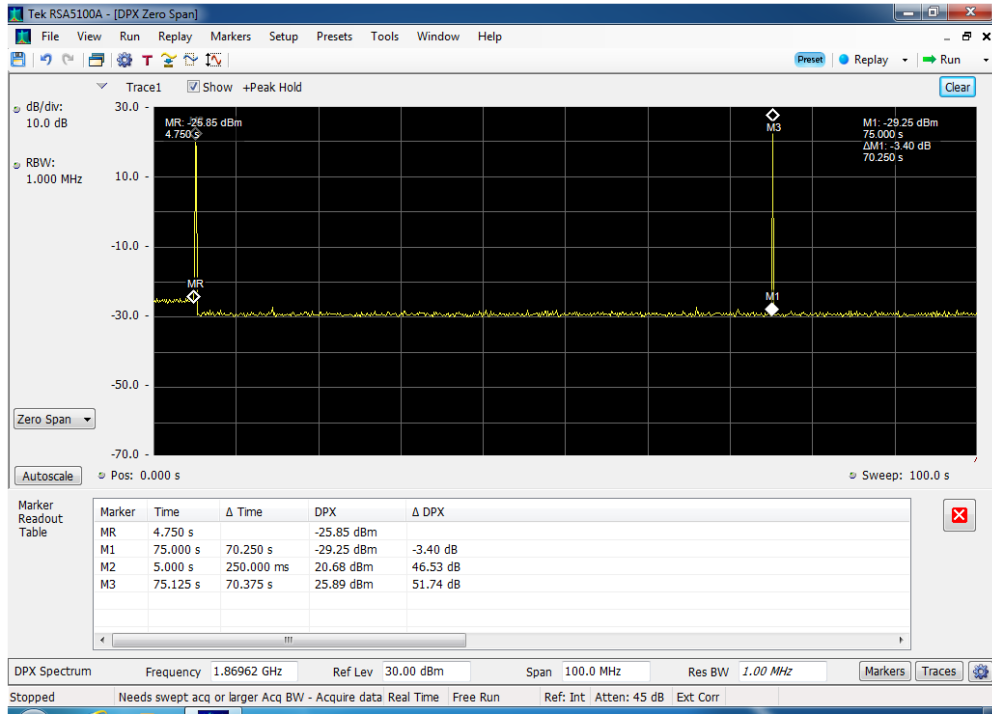


Anti-Oscillation_Restart Time_UL_1732 MHz

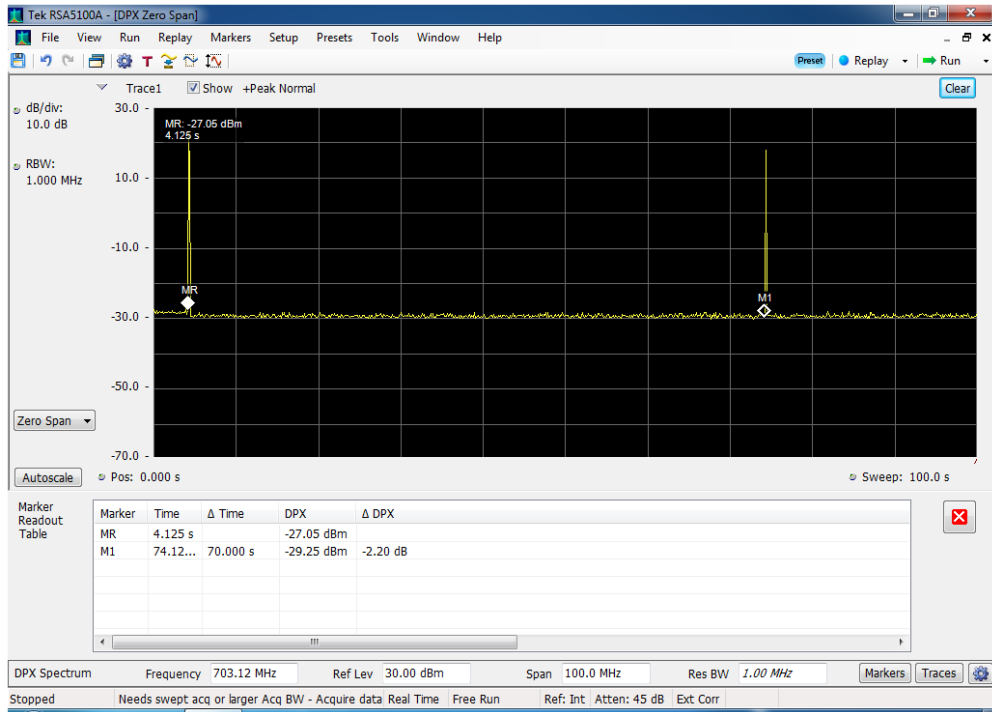




Anti-Oscillation_Restart Time_UL_1880 MHz

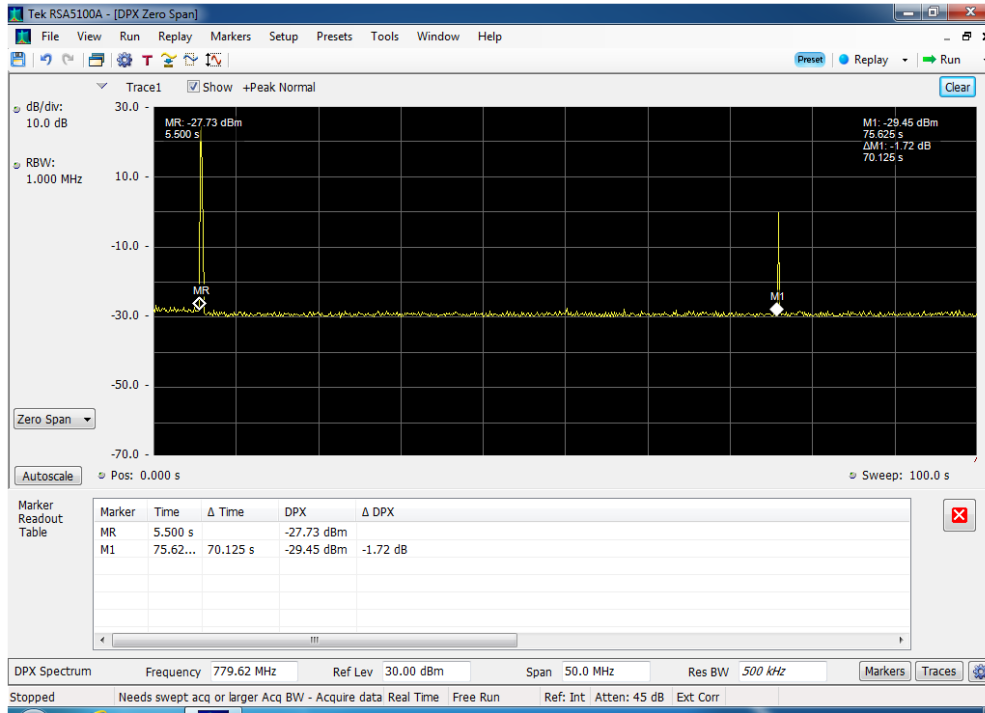


Anti-Oscillation_Restart Time_UL_707 MHz

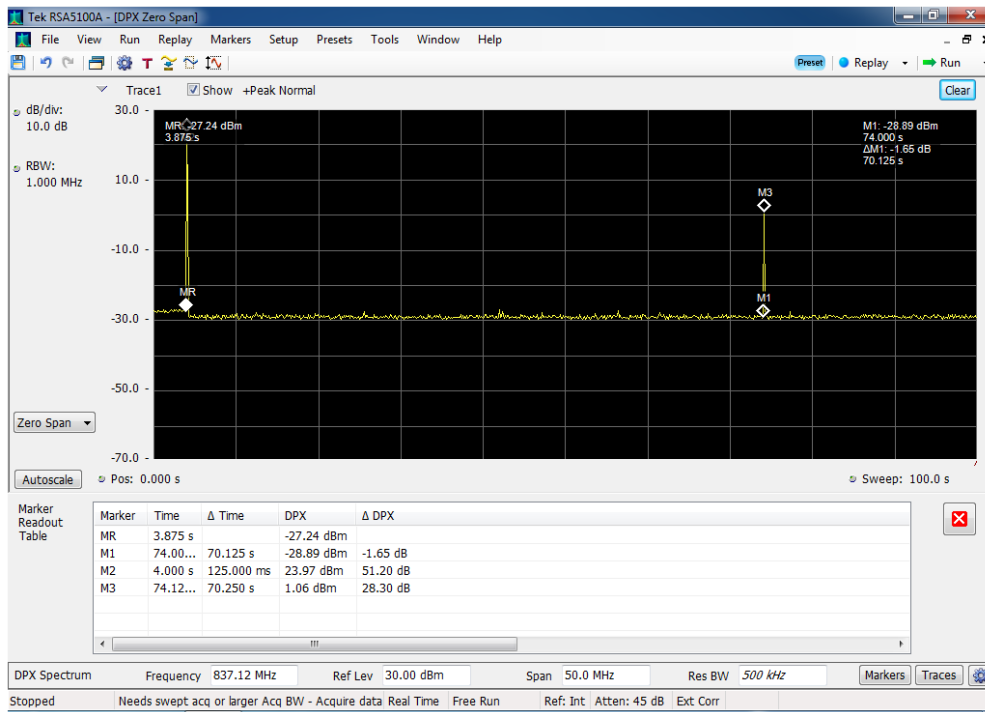




Anti-Oscillation_Restart Time_UL_781 MHz

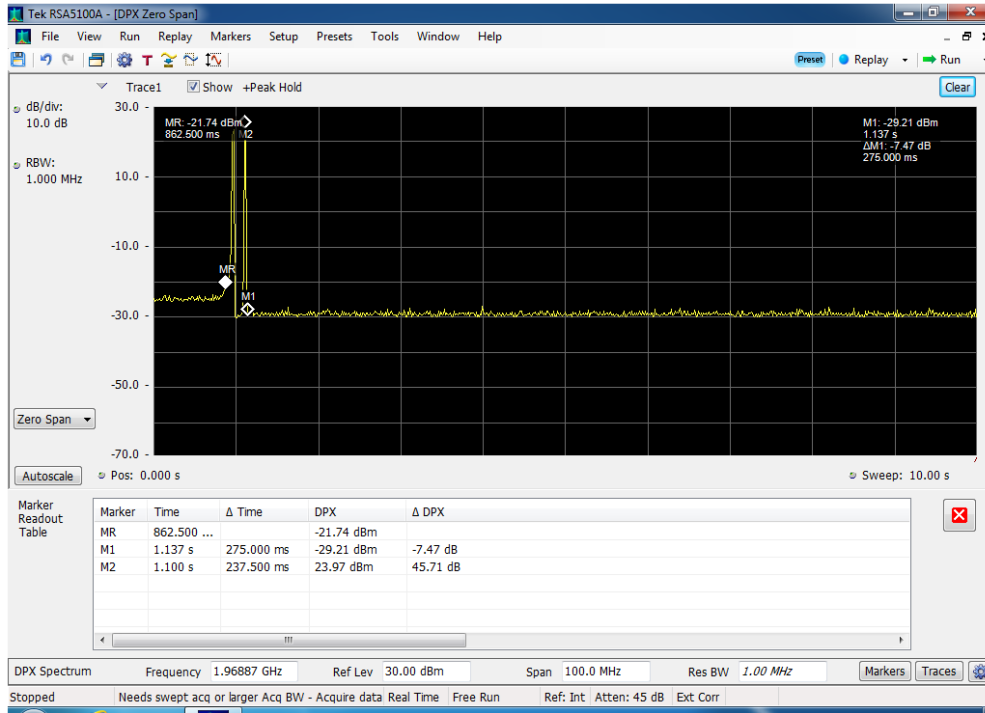


Anti-Oscillation_Restart Time_UL_836 MHz

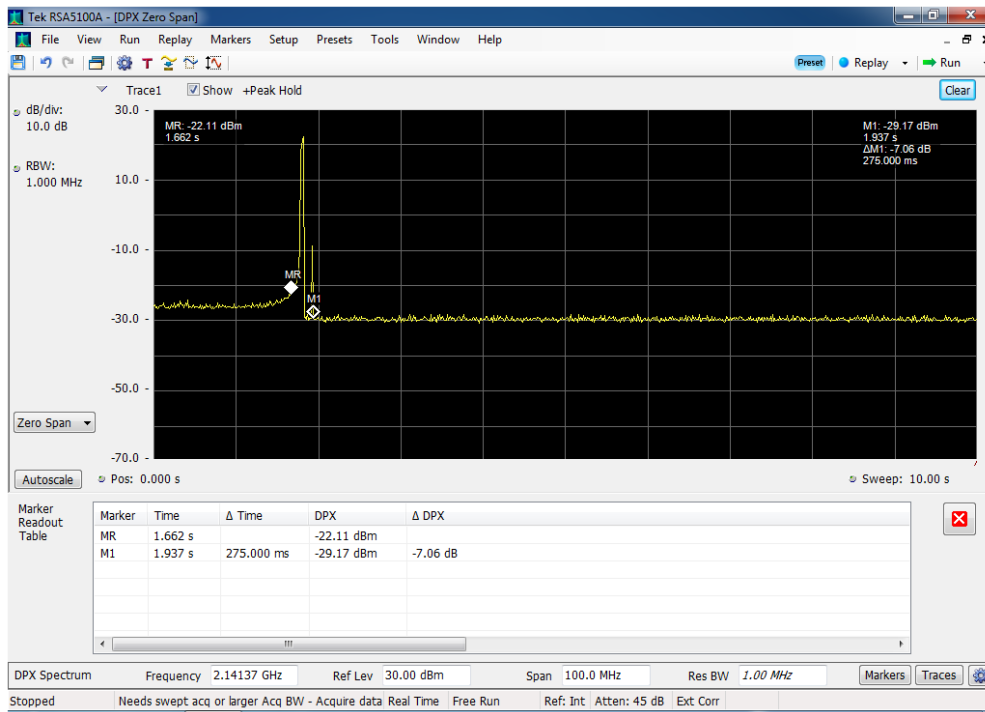




Oscillation Detection _DL_1960 MHz_1

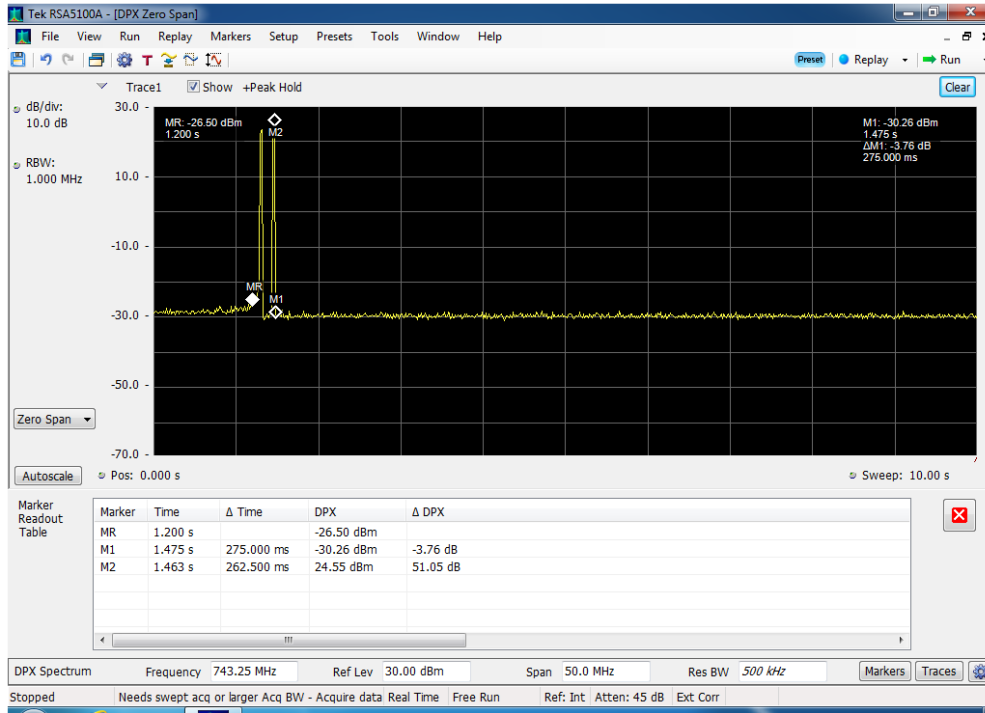


Oscillation Detection _DL_2132 MHz_1

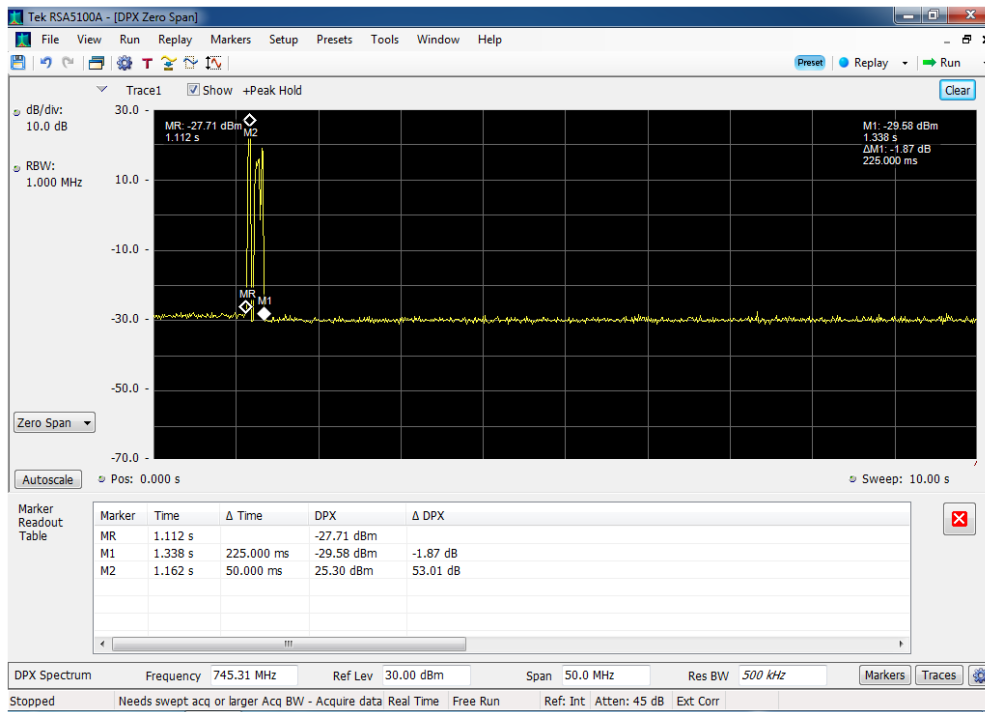




Oscillation Detection _DL_737 MHz_1

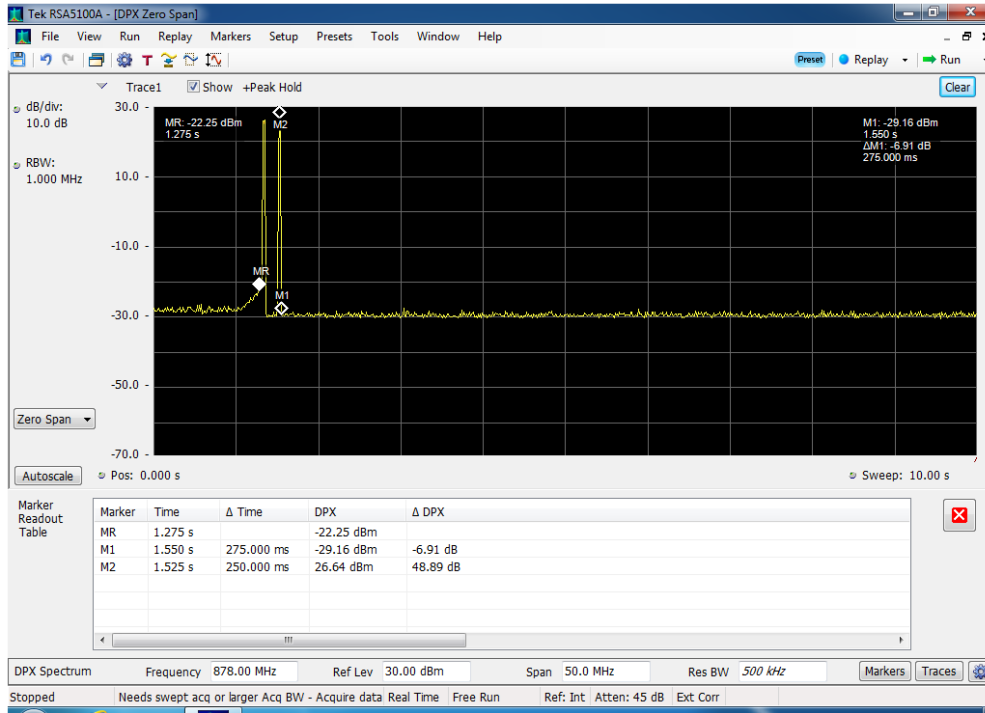


Oscillation Detection _DL_751 MHz

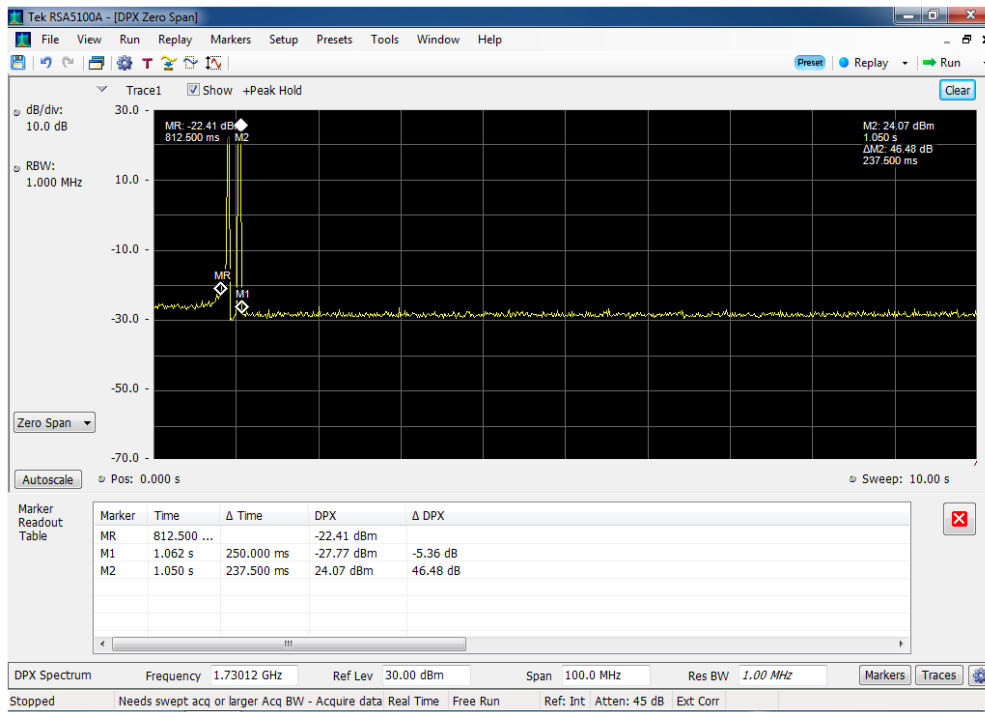




Oscillation Detection _DL_881 MHz_1

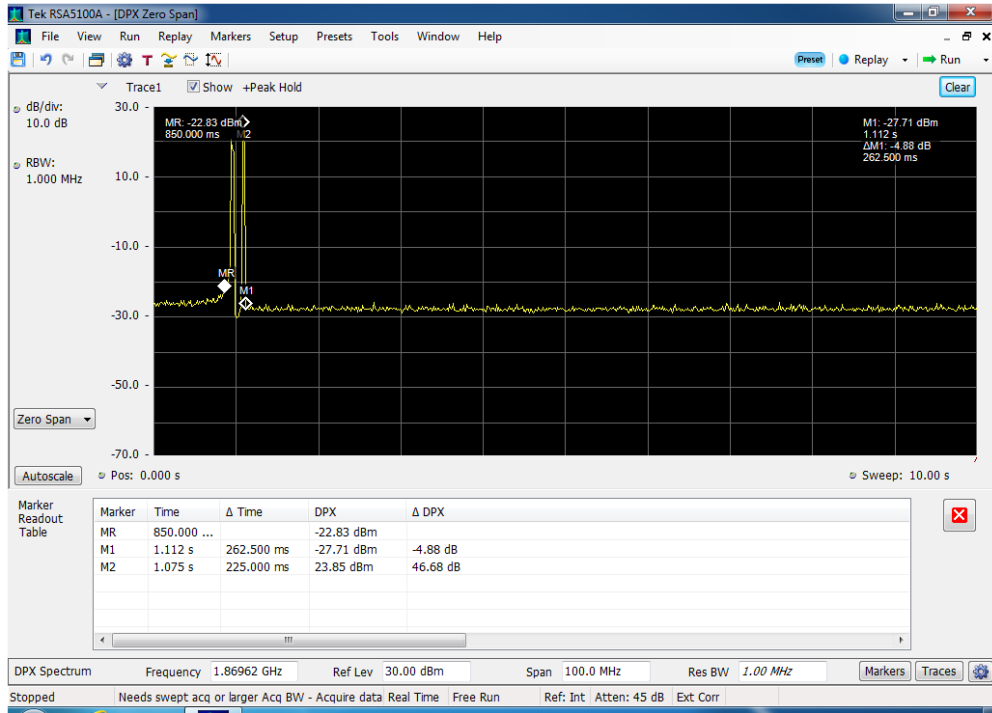


Oscillation Detection _UL_1732 MHz

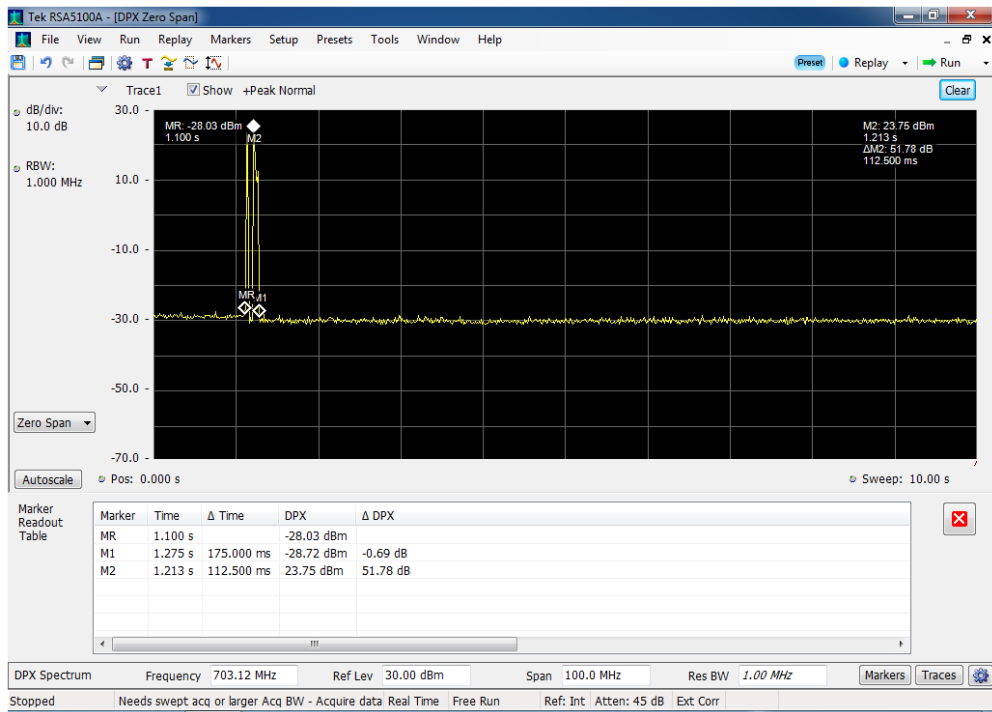




Oscillation Detection _UL_1880 MHz

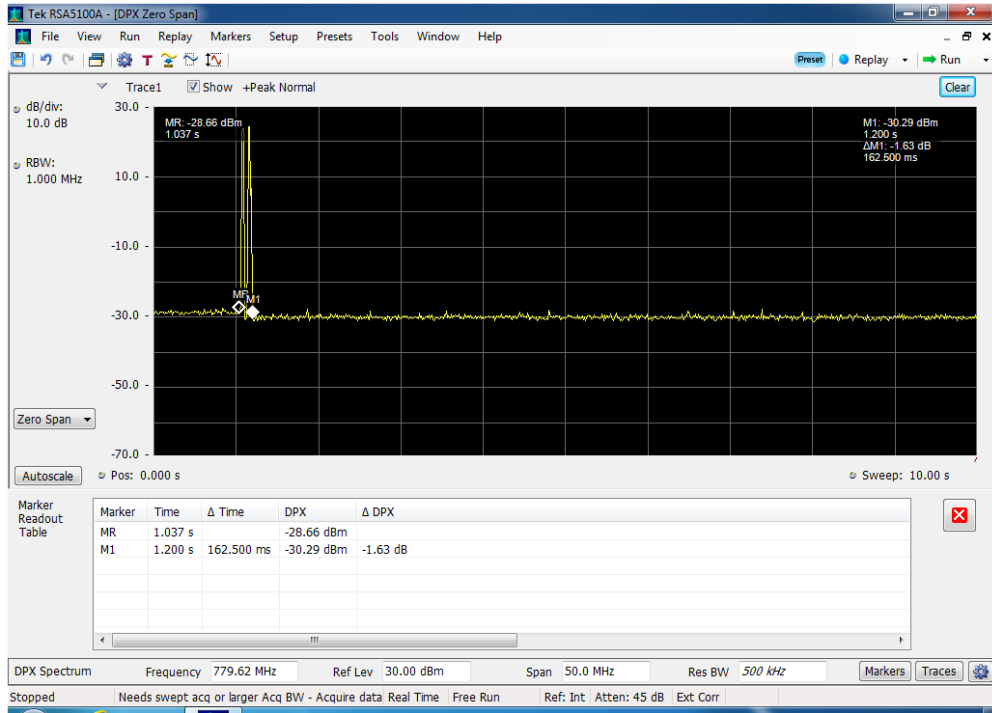


Oscillation Detection _UL_707 MHz





Oscillation Detection _UL_781 MHz



Oscillation Detection _UL_836 MHz

