



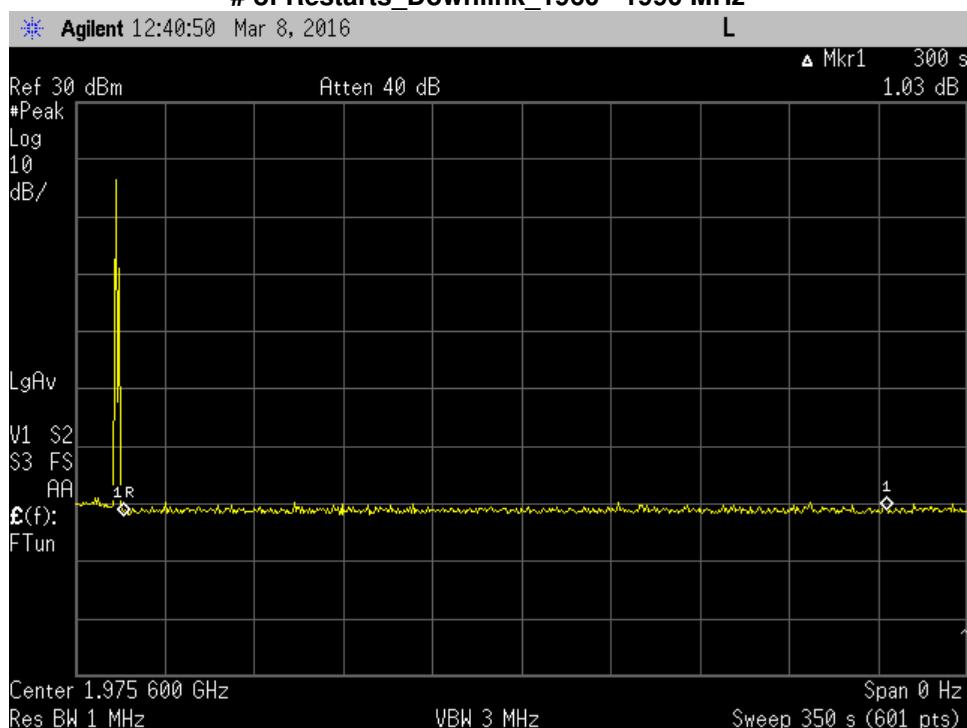
Compliance Testing, LLC
Testing since 1963

Annex I

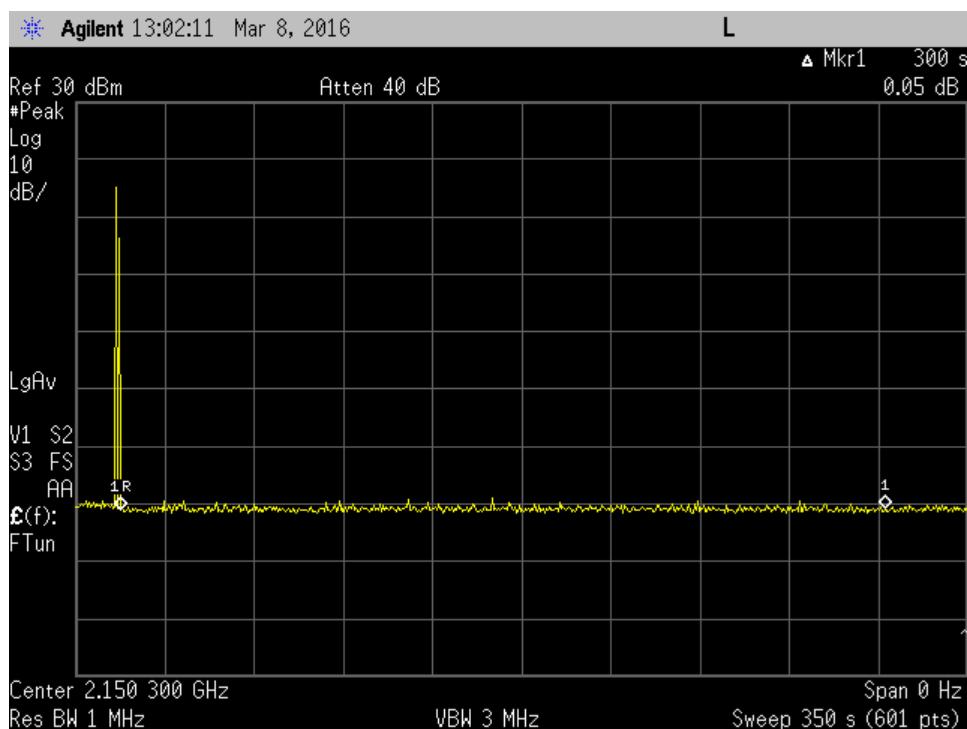
Anti-Oscillation



of Restarts_Downlink_1960 - 1990 MHz

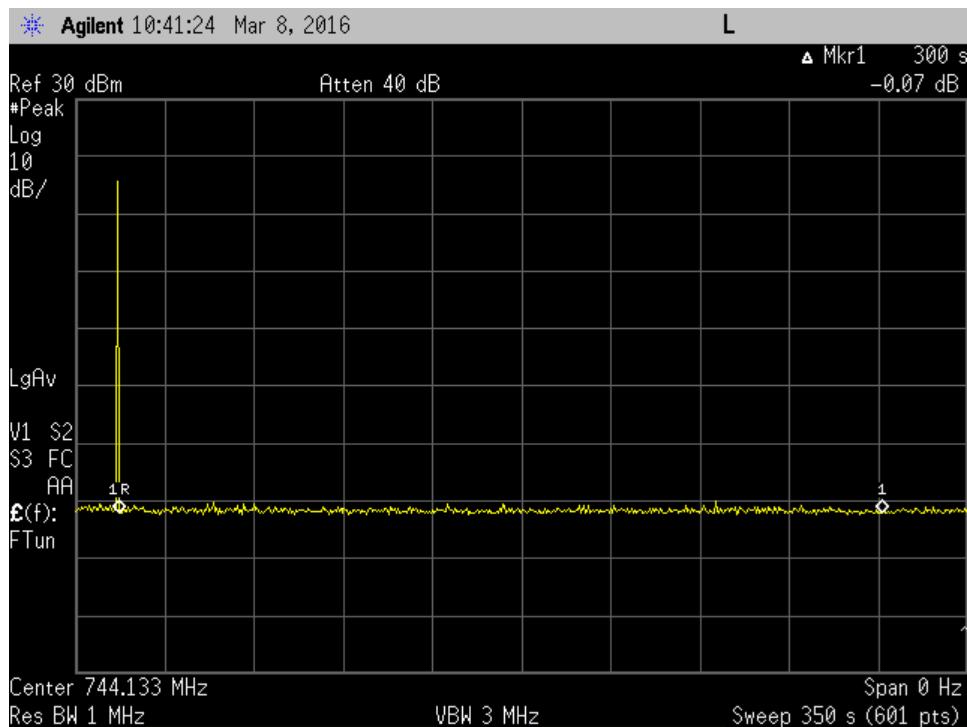


of Restarts_Downlink_2110 - 2155 MHz

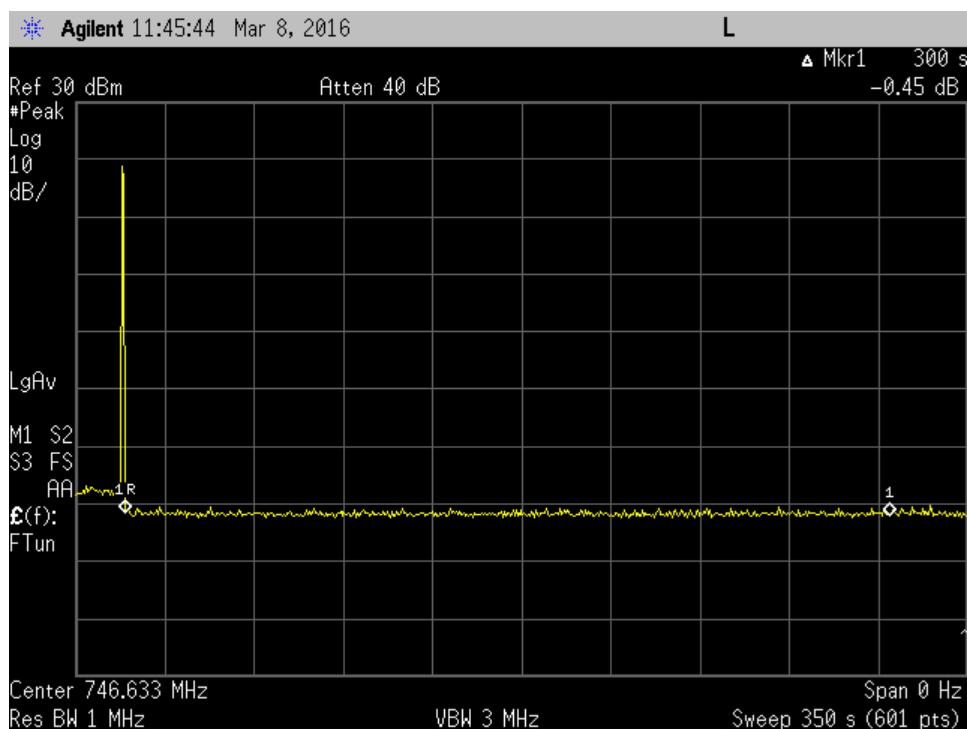




of Restarts_Downlink_728 - 746 MHz

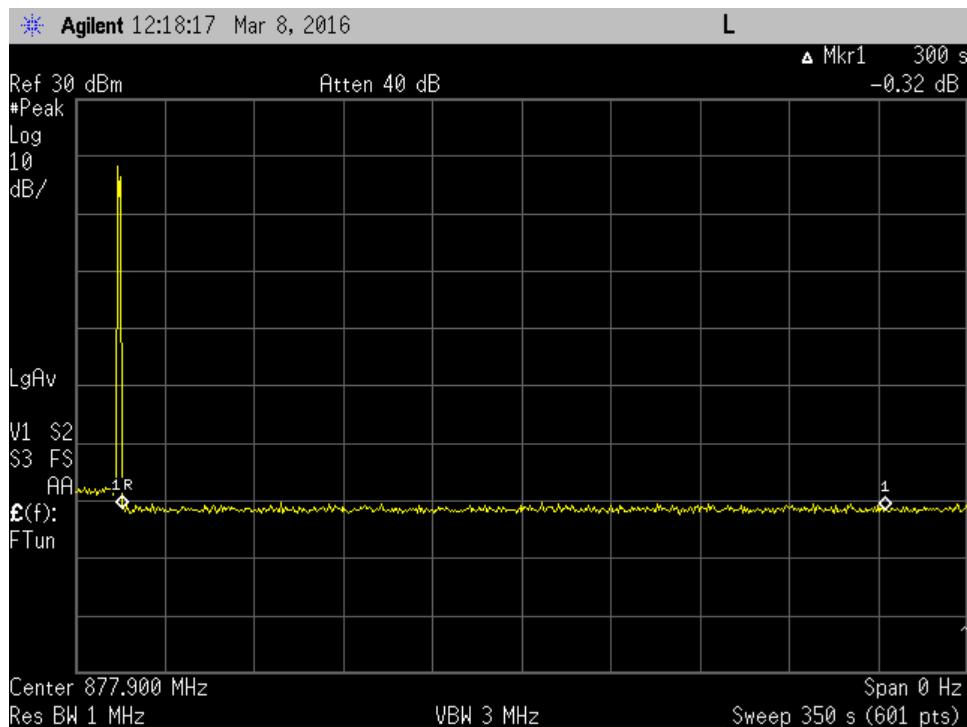


of Restarts_Downlink_746 - 757 MHz

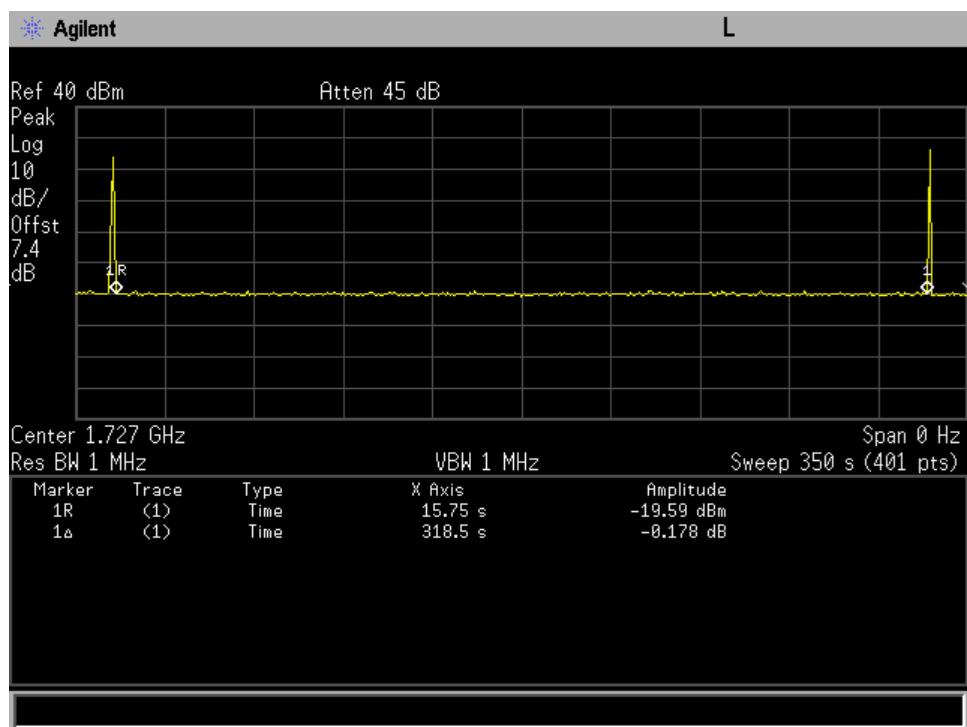




of Restarts_Downlink_869 - 894 MHz

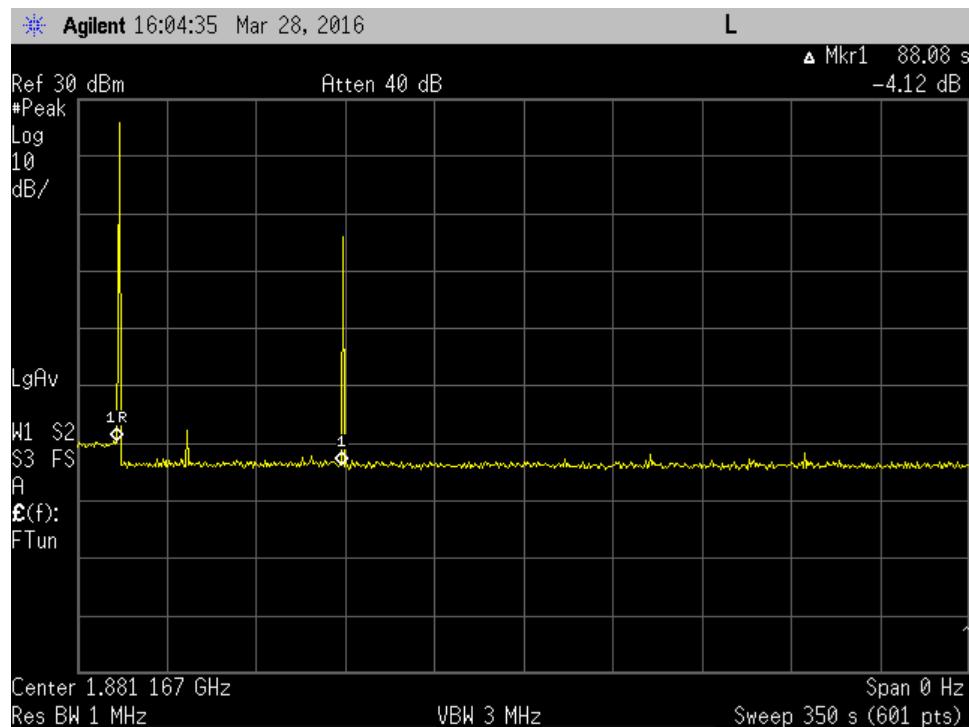


of Restarts_Uplink_1710 - 1755 MHz

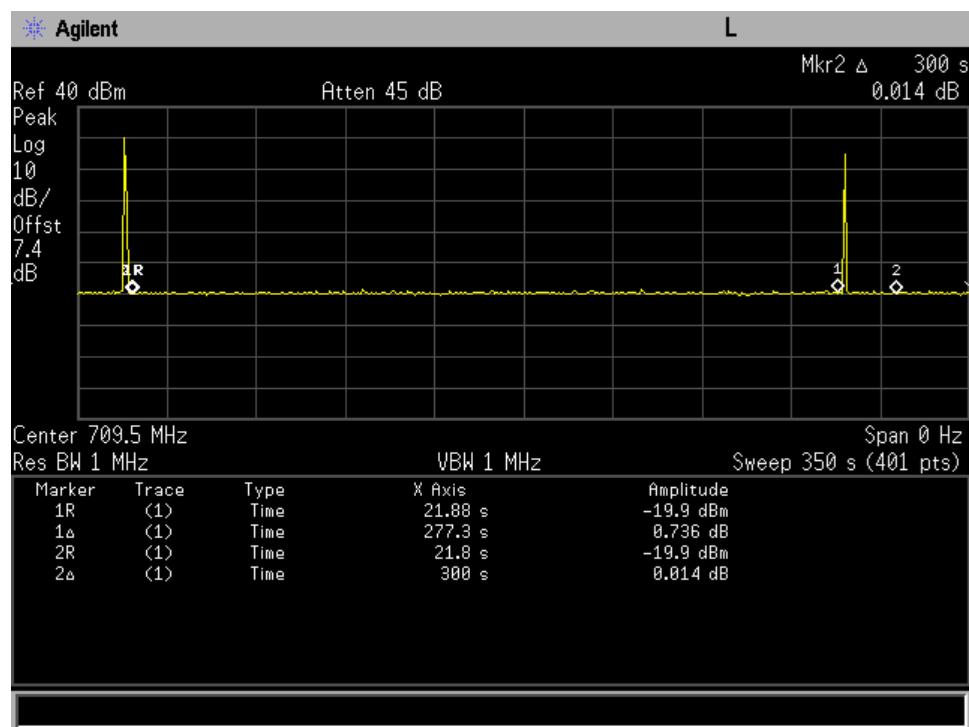




of Restarts_Uplink_1850 - 1910 MHz

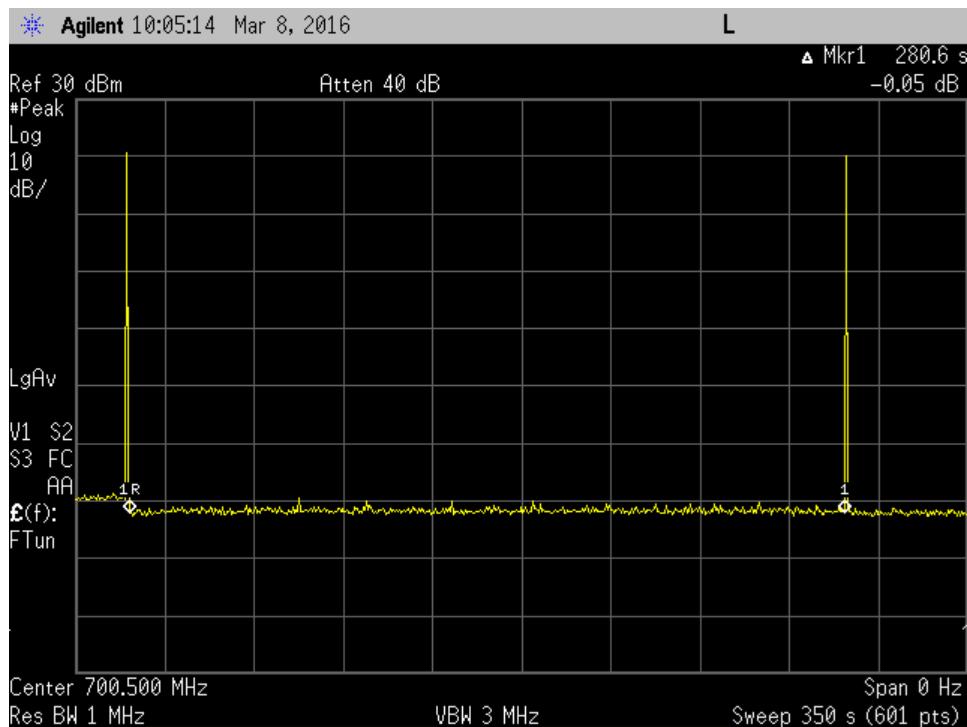


of Restarts_Uplink_698 - 716 MHz

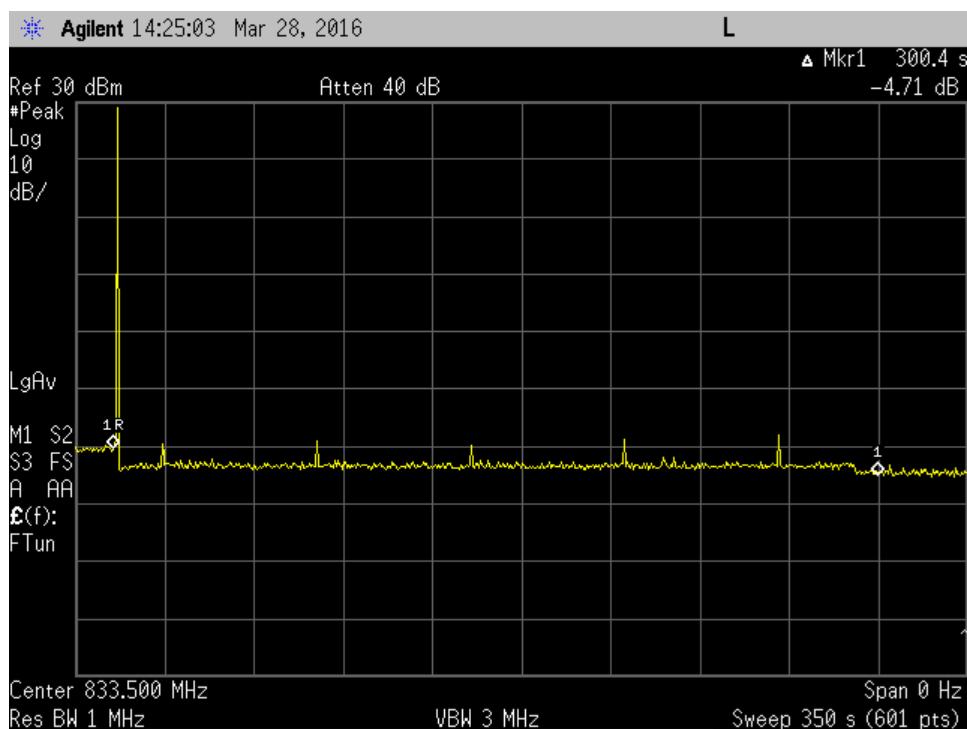




of Restarts_Uplink_776 - 787 MHz

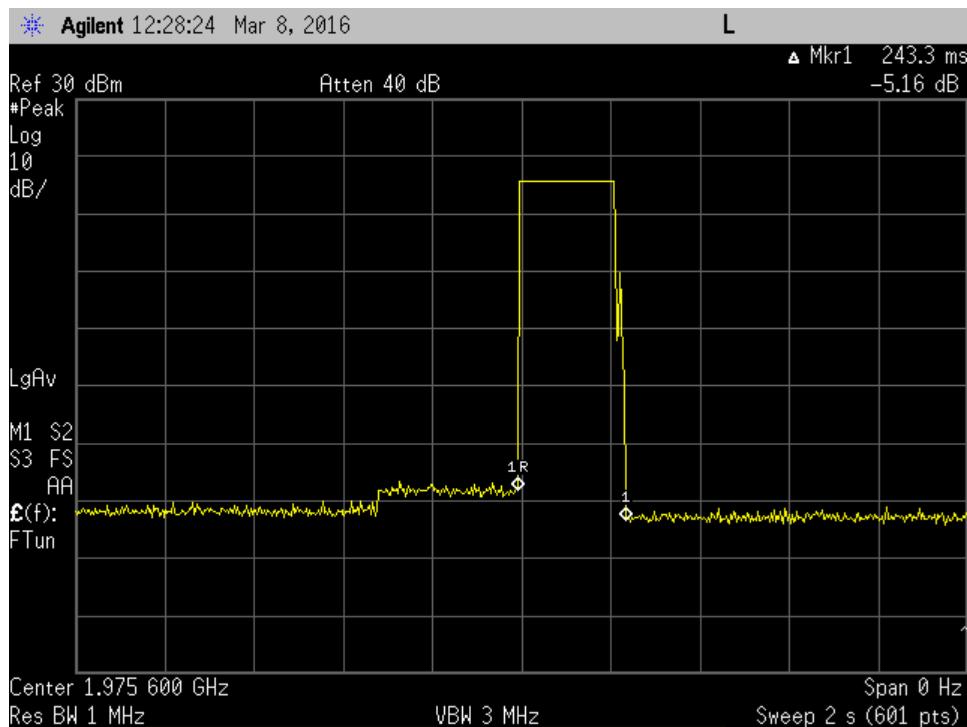


of Restarts_Uplink_824 - 849 MHz

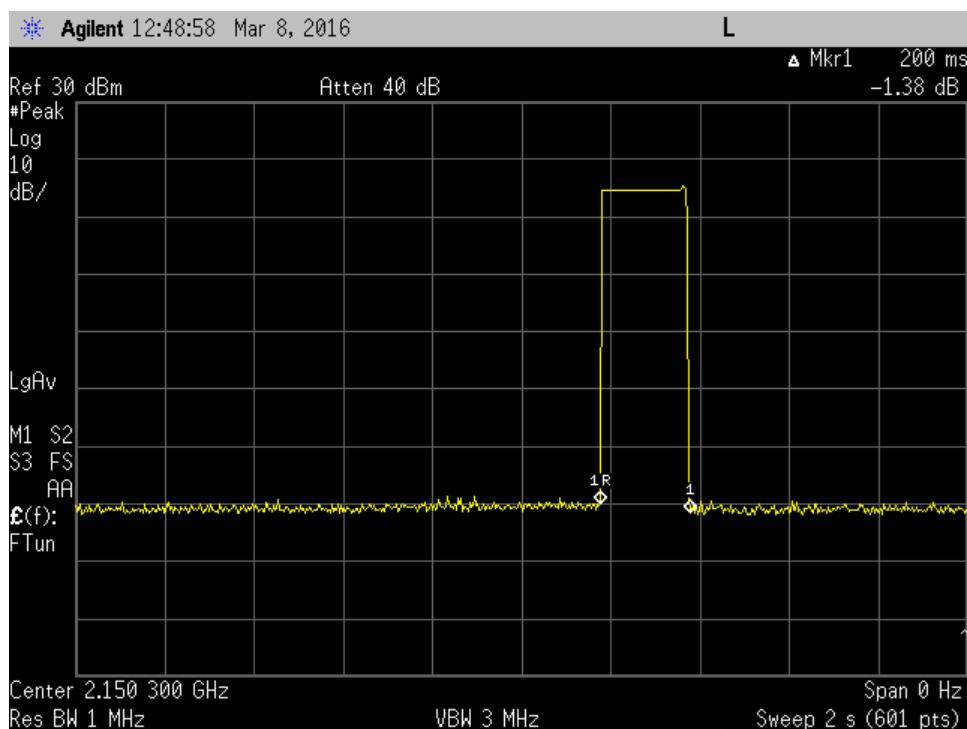




Oscillation Detection_Downlink_1930 - 1990 MHz

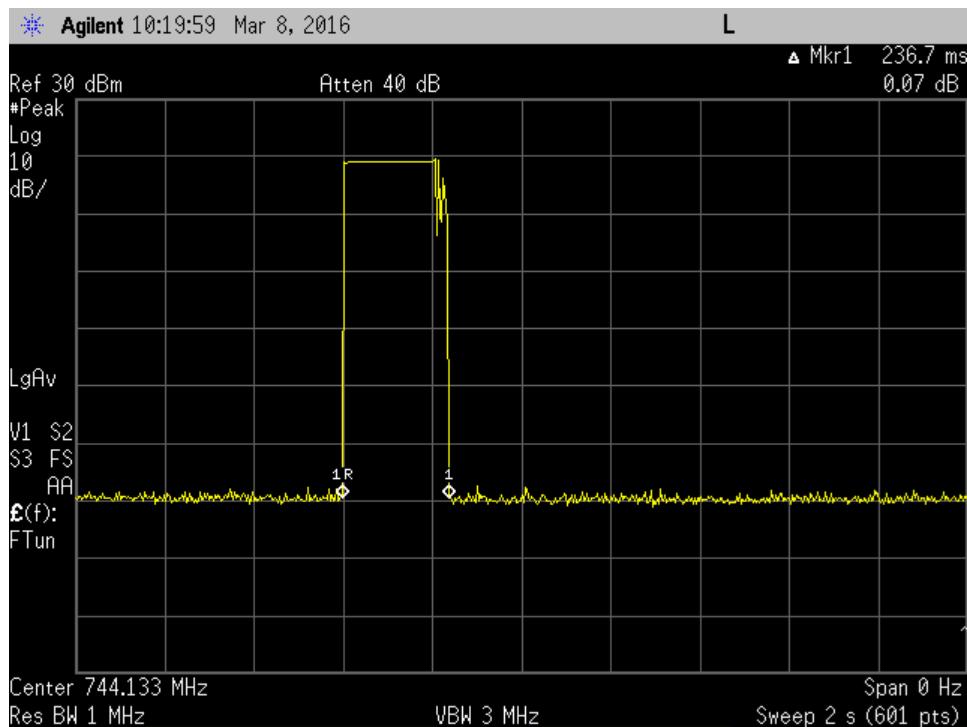


Oscillation Detection_Downlink_2110 - 2155 MHz

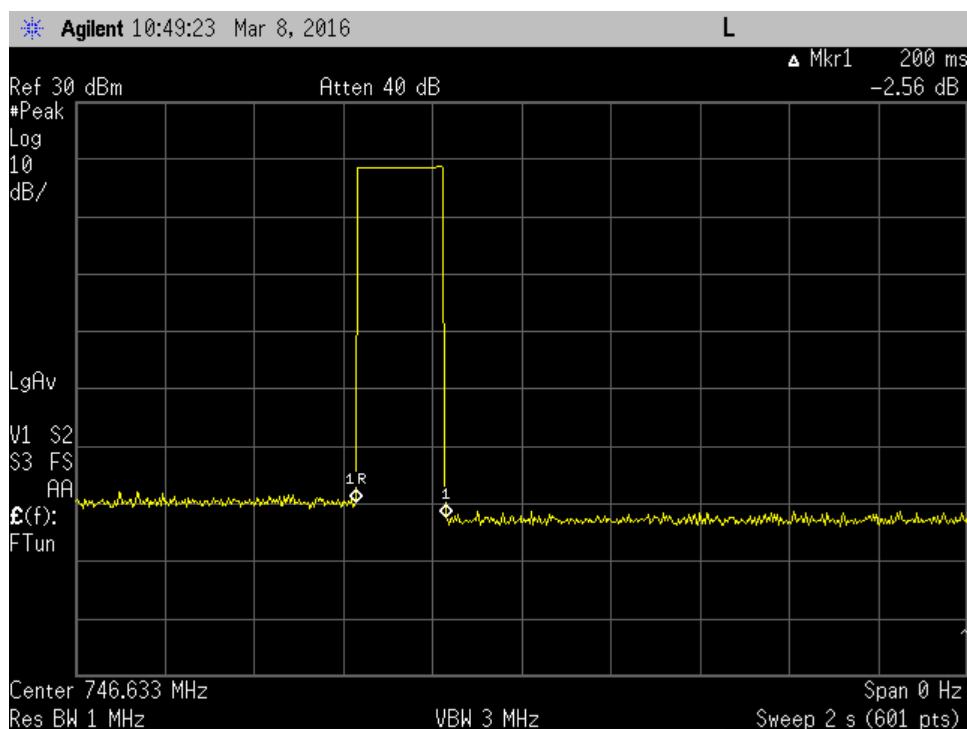




Oscillation Detection_Downlink_728 - 746 MHz

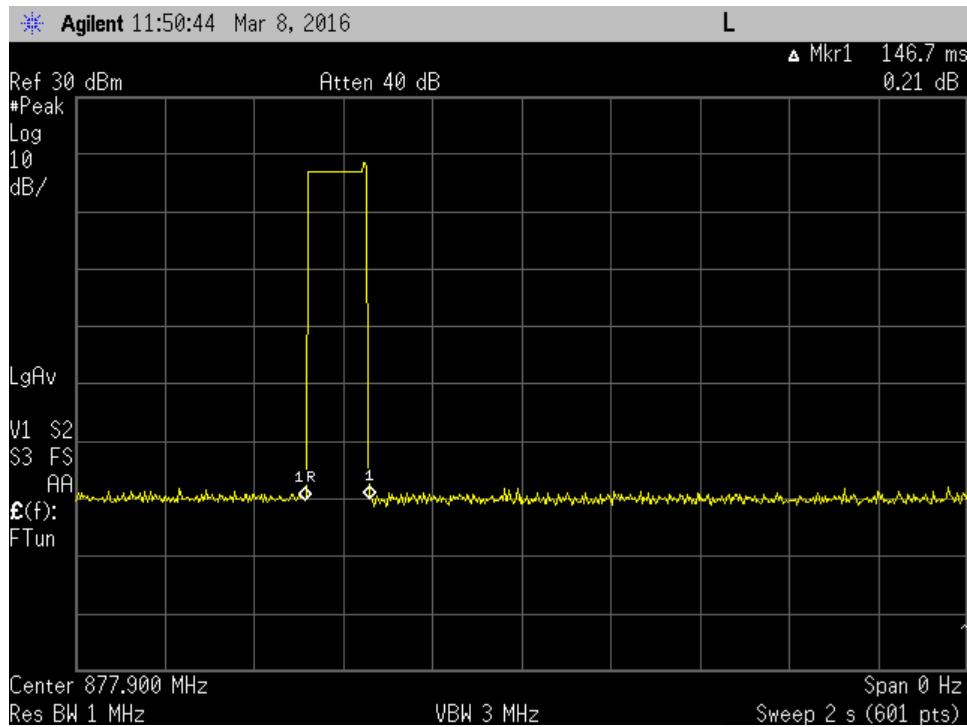


Oscillation Detection_Downlink_746 - 757 MHz

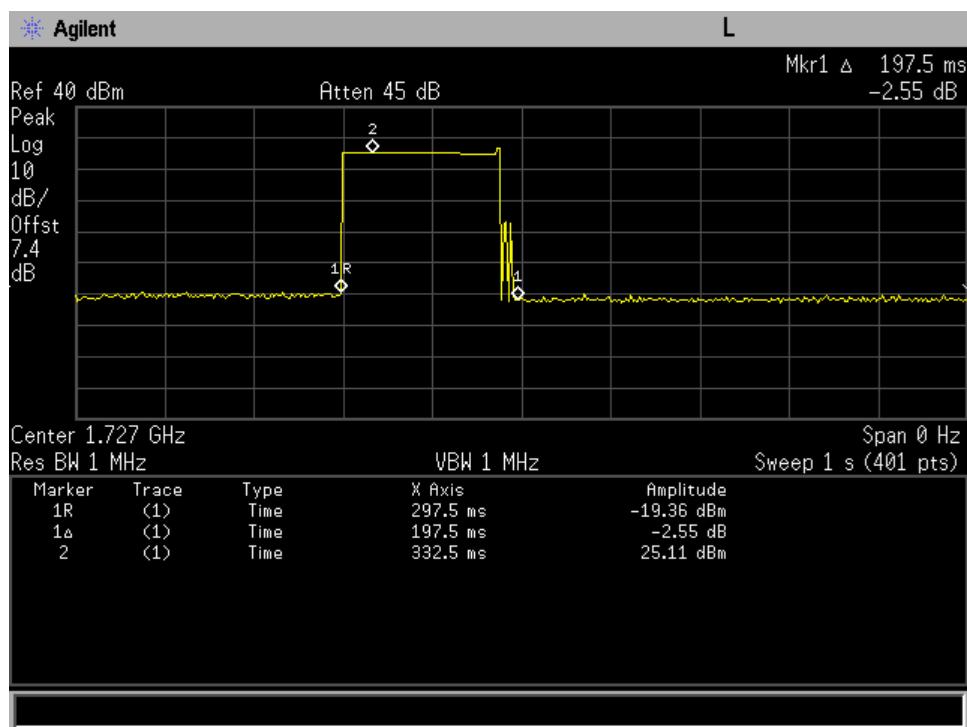




Oscillation Detection_Downlink_869 - 894 MHz

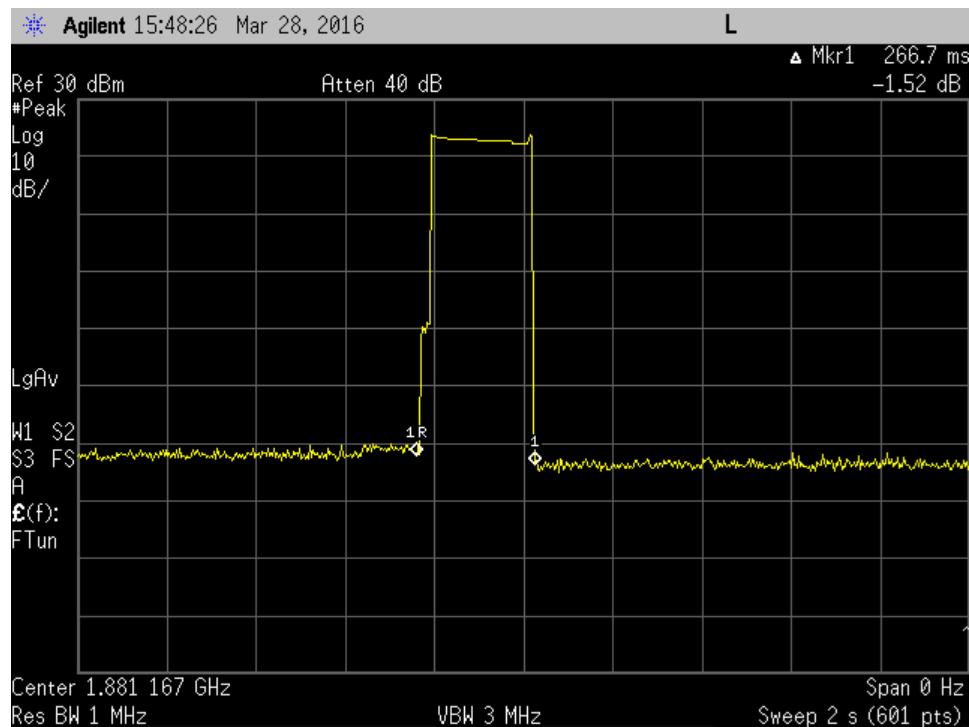


Oscillation Detection_Uplink_1710 - 1755 MHz

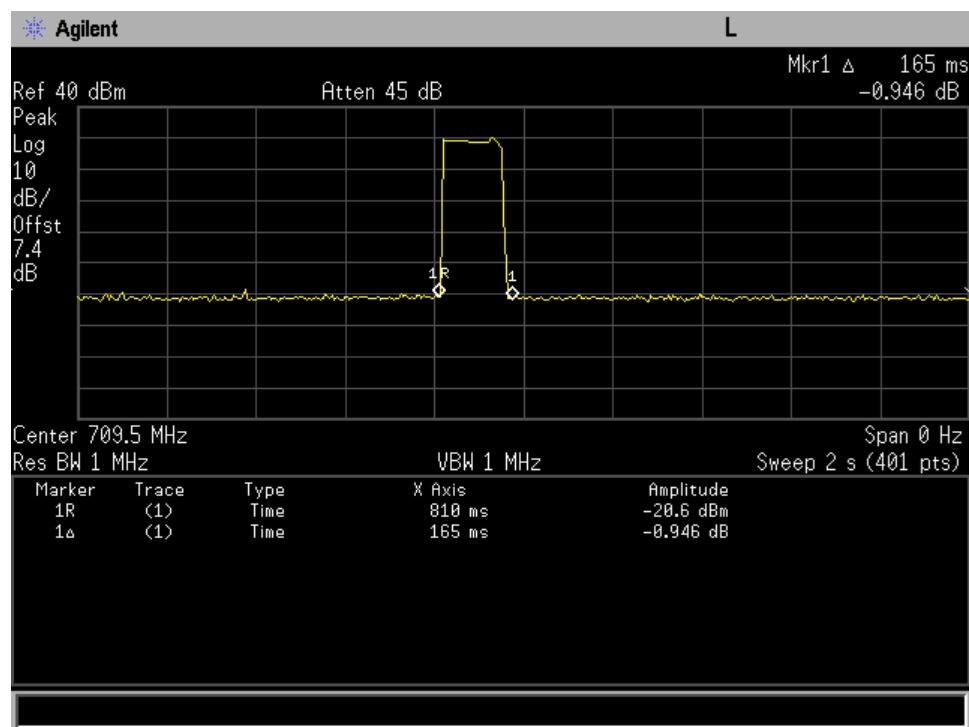




Oscillation Detection_Uplink_1850 - 1910 MHz

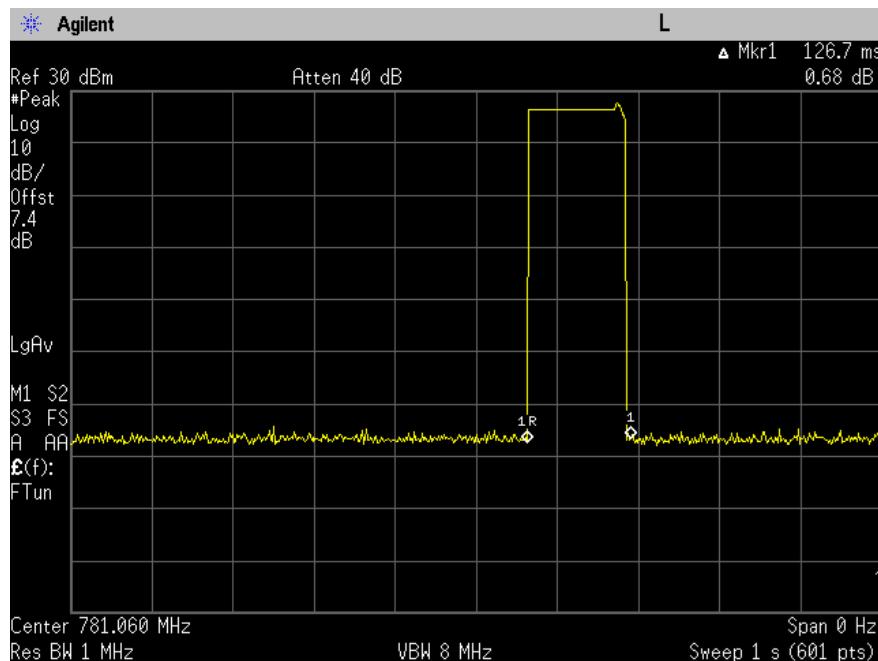


Oscillation Detection_Uplink_698 - 716 MHz

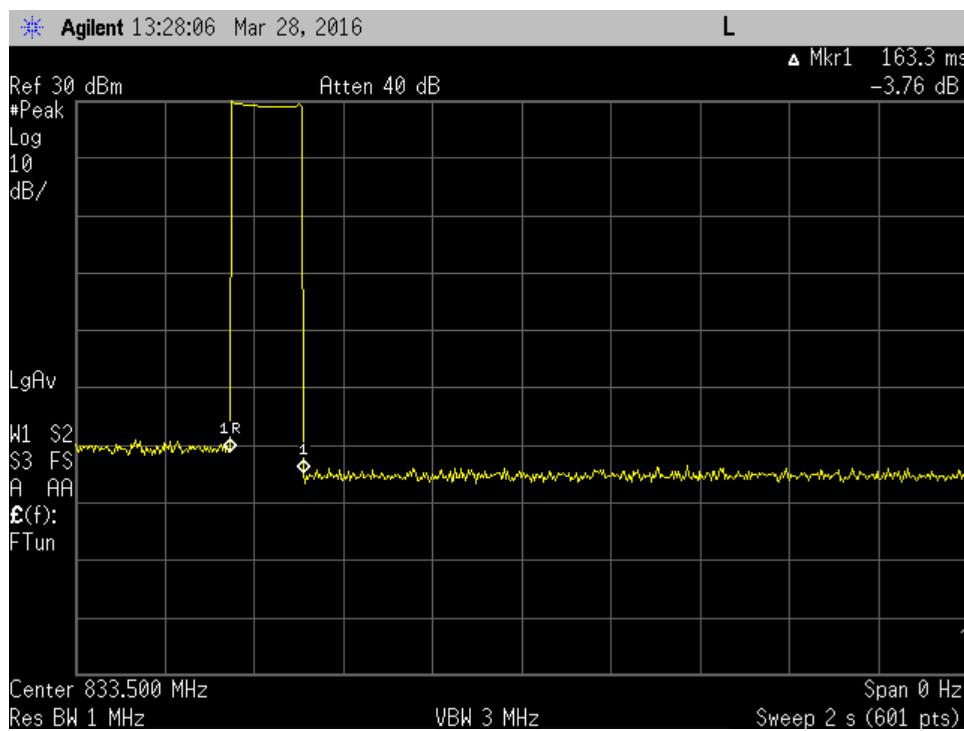




Oscillation Detection_Uplink_776 - 787 MHz

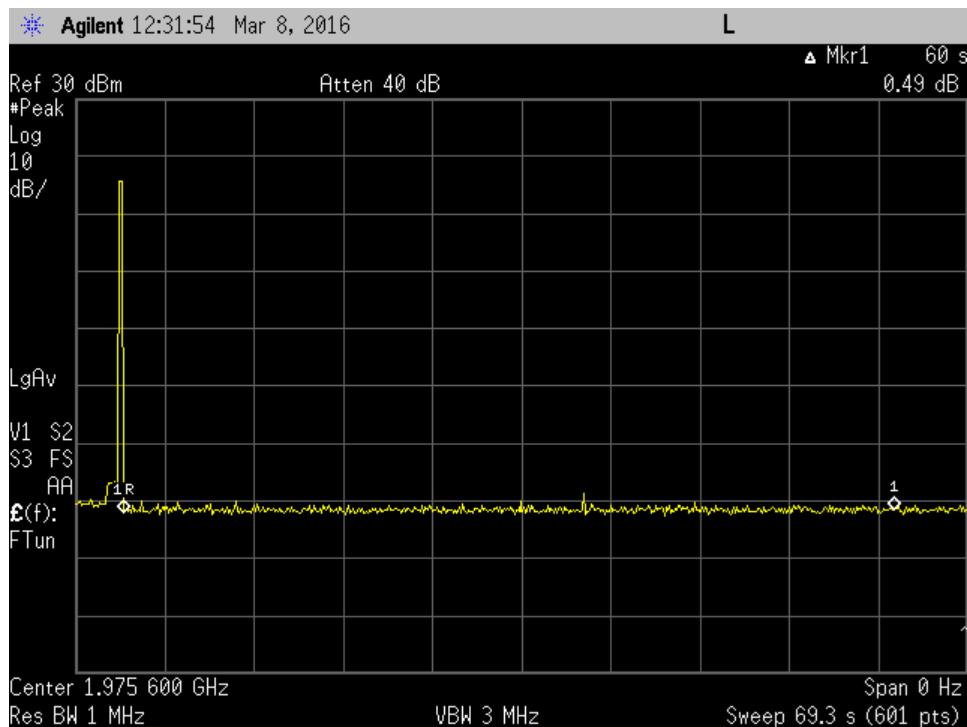


Oscillation Detection_Uplink_824 - 849 MHz

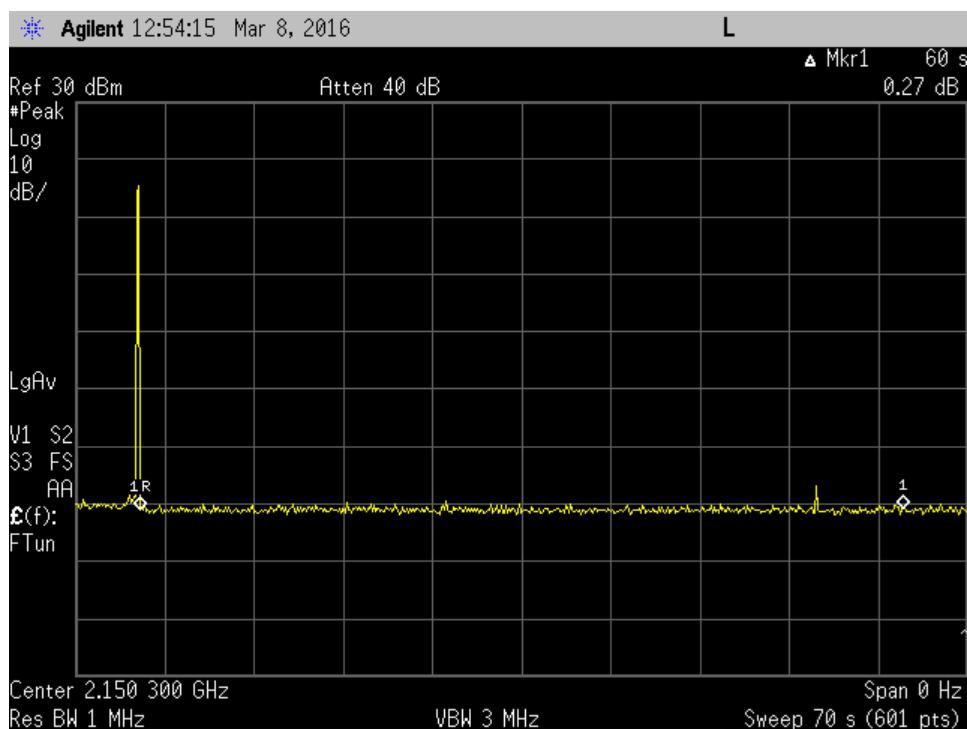




Restart Time_Downlink_1960 - 1990 MHz

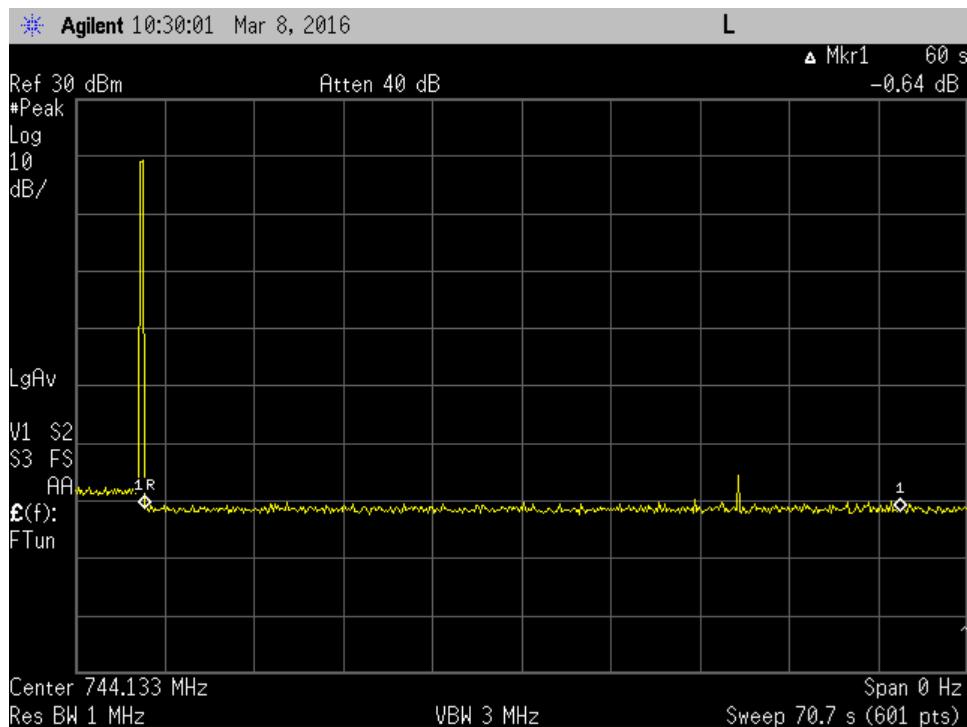


Restart Time_Downlink_2110 - 2155 MHz

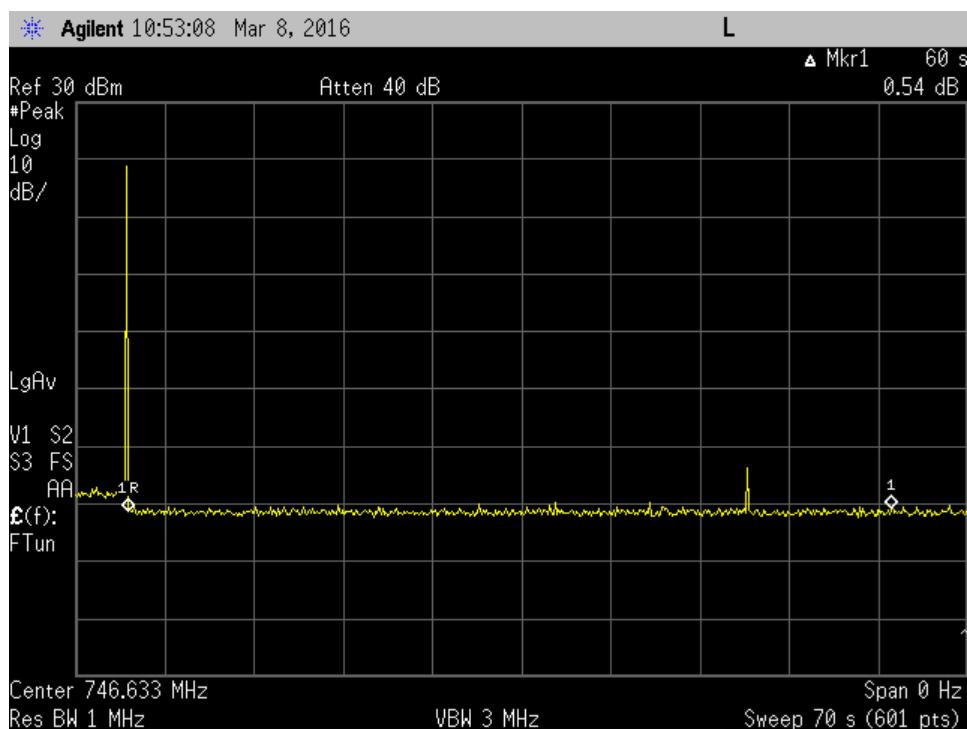




Restart Time_Downlink_728 - 746 MHz

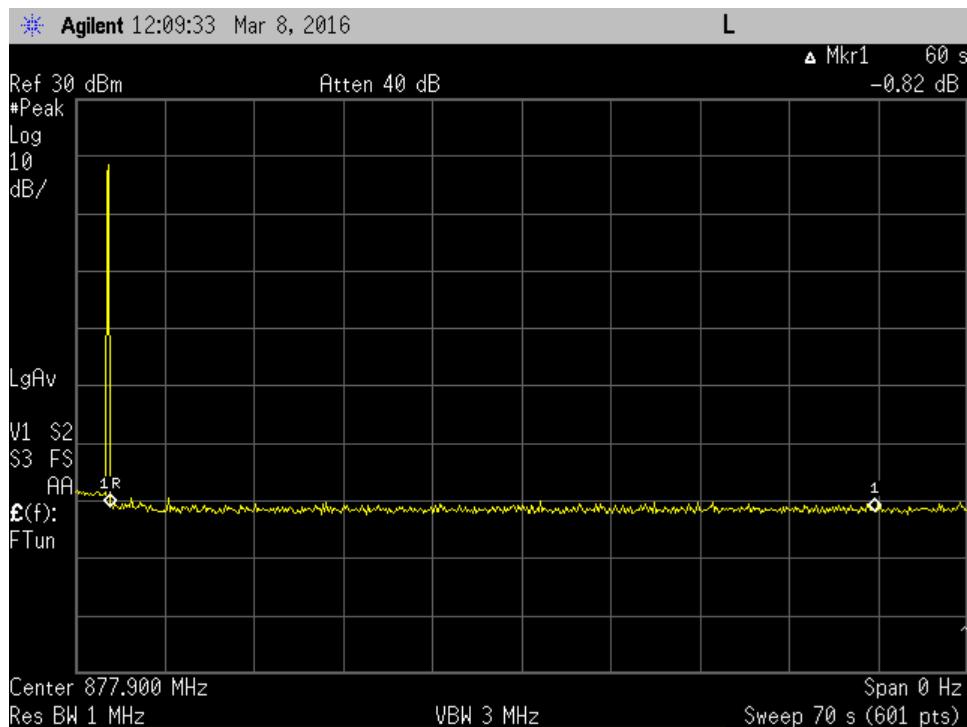


Restart Time_Downlink_747 - 757 MHz

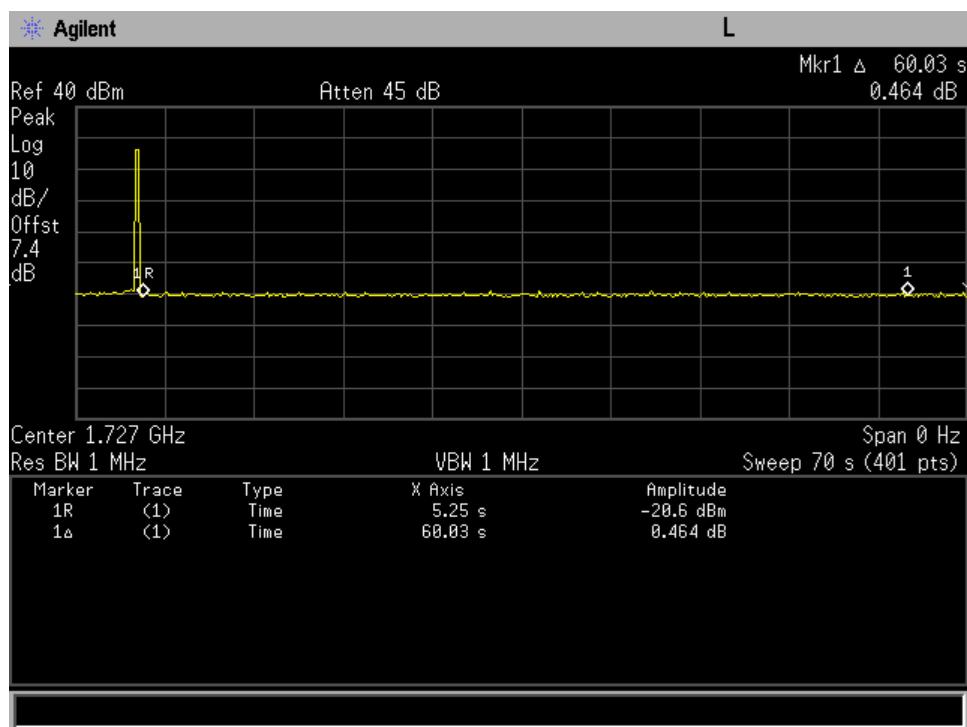




Restart Time_Downlink_869 - 894 MHz

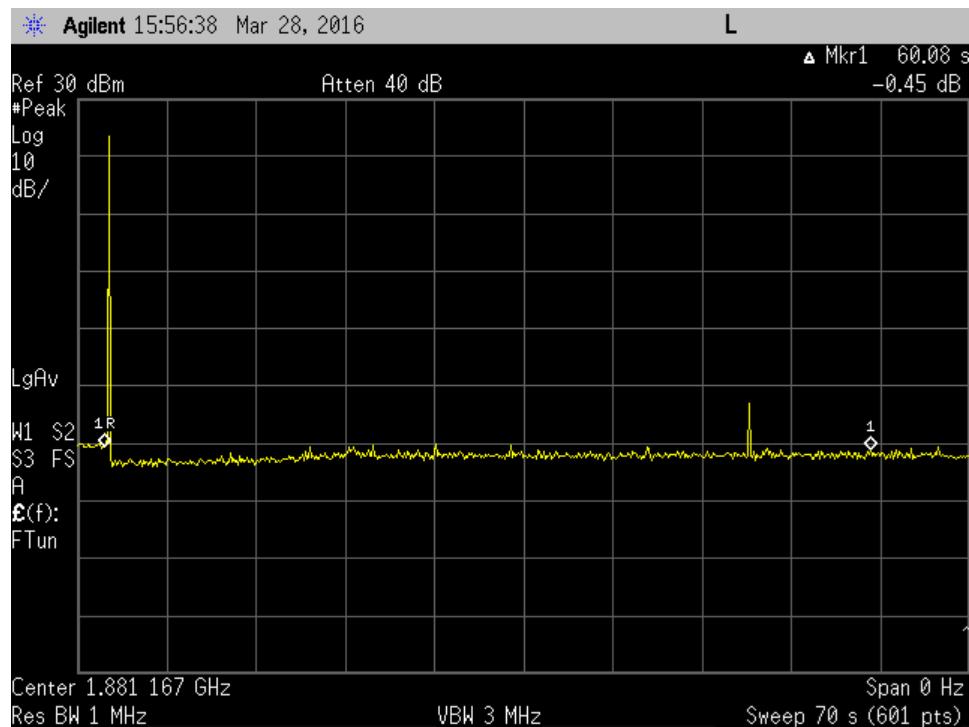


Restart Time_Uplink_1710 - 1755 MHz

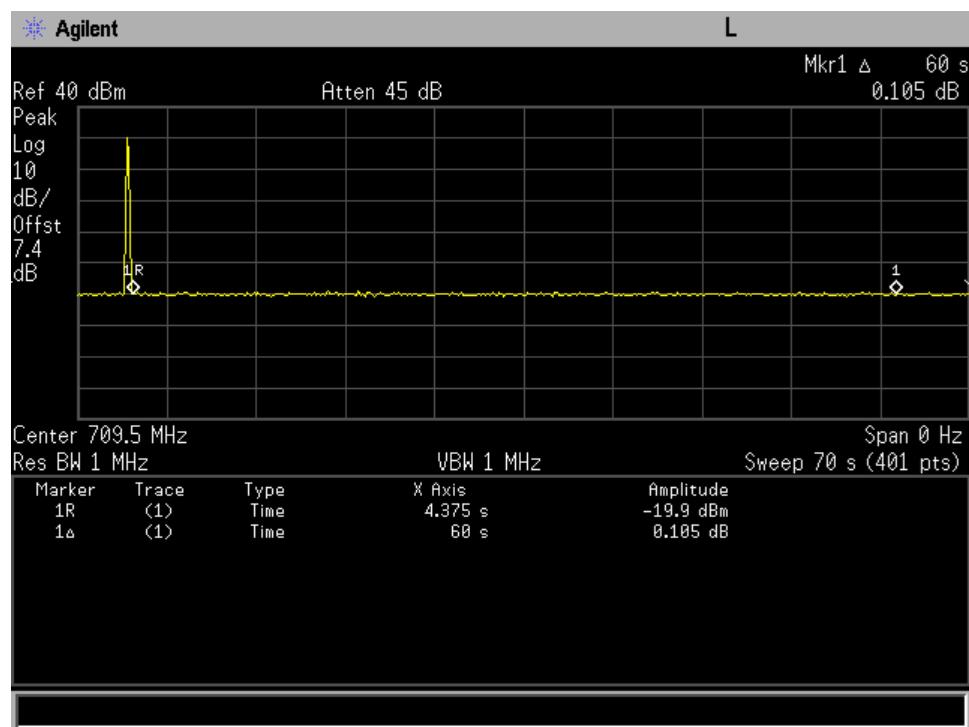




Restart Time_Uplink_1850 - 1910 MHz

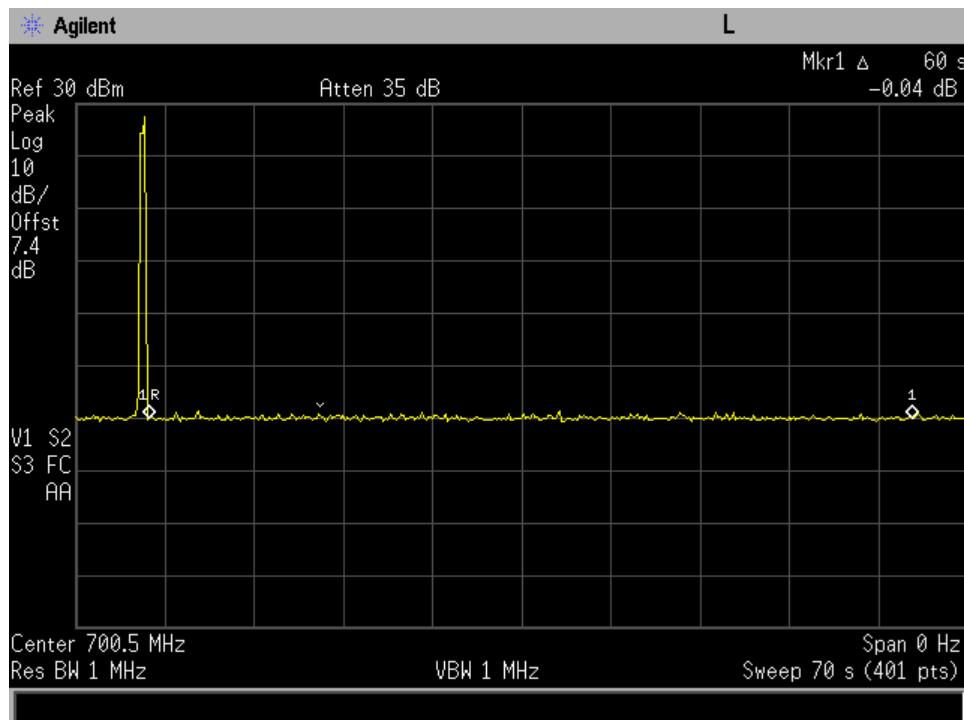


Restart Time_Uplink_698 - 716 MHz





Restart Time_Uplink_776 - 787 MHz



Restart Time_Uplink_824 - 849 MHz

