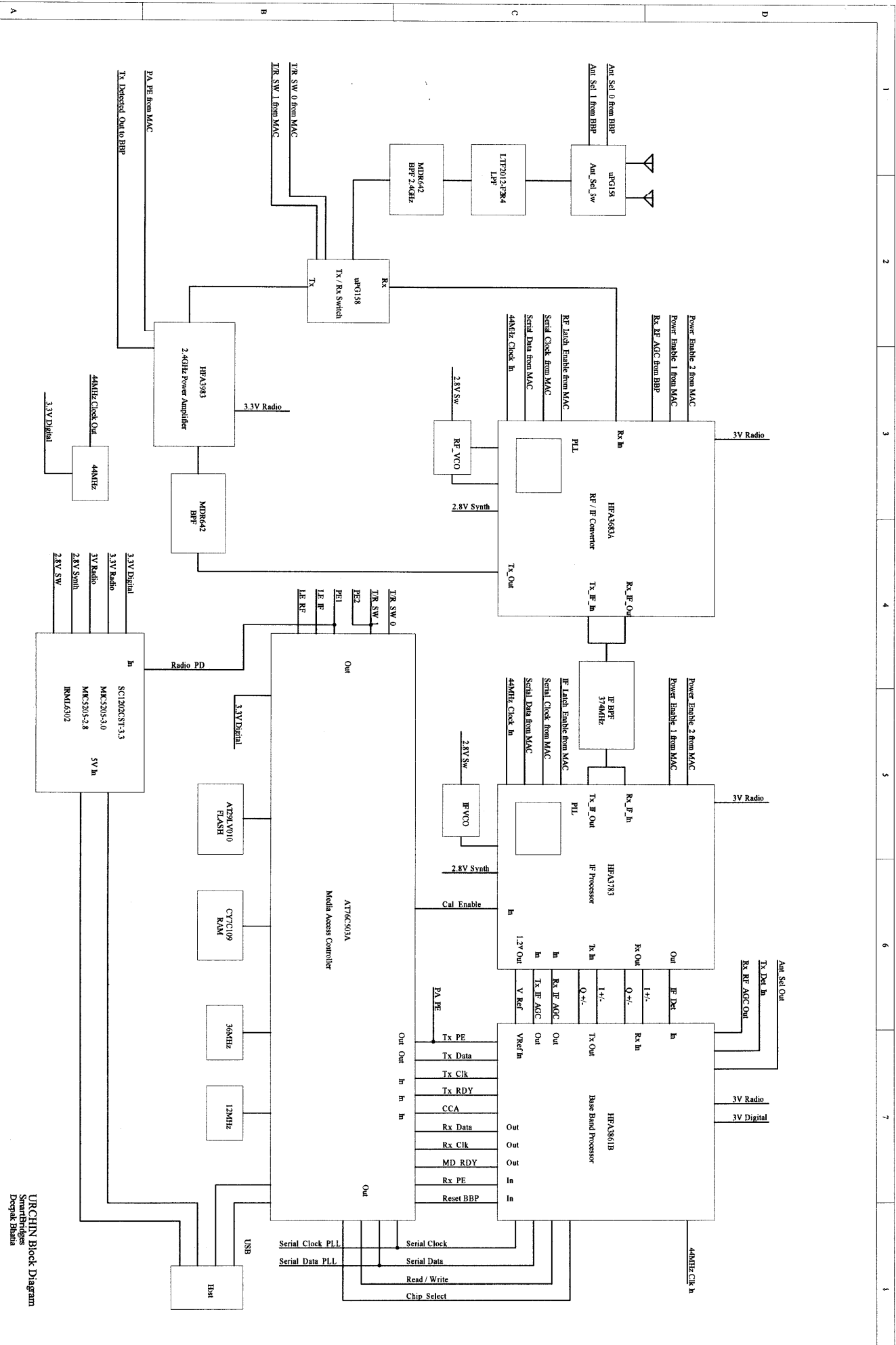
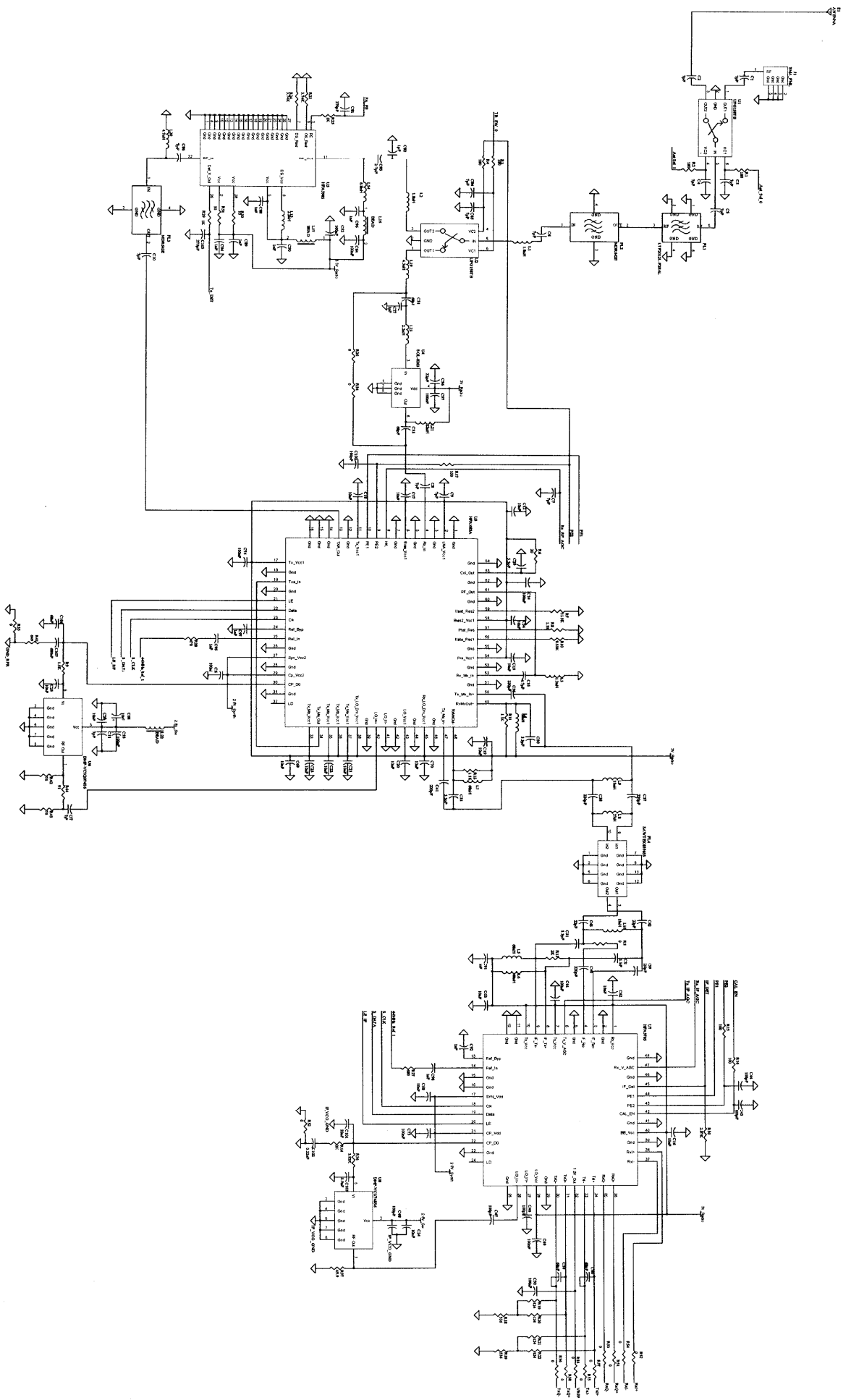


REF	QTY	DESCRIPTION	VALUE
1	1	ATmega16	ATmega16
2	1	MAX232	MAX232
3	1	AMS1117	AMS1117
4	1	TL072	TL072
5	1	TL074	TL074
6	1	10MHz Crystal	10MHz
7	1	Relay K1	Relay K1
8	1	Relay K2	Relay K2
9	1	Relay K3	Relay K3
10	1	Relay K4	Relay K4
11	1	3.3V Resistor	3.3k
12	1	10k Resistor	10k
13	1	100nF Capacitor	100nF
14	1	100pF Capacitor	100pF
15	1	1k Resistor	1k
16	1	10k Resistor	10k
17	1	100k Resistor	100k
18	1	1M Resistor	1M
19	1	10M Resistor	10M
20	1	100M Resistor	100M
21	1	1k Resistor	1k
22	1	10k Resistor	10k
23	1	100k Resistor	100k
24	1	1M Resistor	1M
25	1	10M Resistor	10M
26	1	100M Resistor	100M
27	1	1k Resistor	1k
28	1	10k Resistor	10k
29	1	100k Resistor	100k
30	1	1M Resistor	1M
31	1	10M Resistor	10M
32	1	100M Resistor	100M
33	1	1k Resistor	1k
34	1	10k Resistor	10k
35	1	100k Resistor	100k
36	1	1M Resistor	1M
37	1	10M Resistor	10M
38	1	100M Resistor	100M
39	1	1k Resistor	1k
40	1	10k Resistor	10k
41	1	100k Resistor	100k
42	1	1M Resistor	1M
43	1	10M Resistor	10M
44	1	100M Resistor	100M
45	1	1k Resistor	1k
46	1	10k Resistor	10k
47	1	100k Resistor	100k
48	1	1M Resistor	1M
49	1	10M Resistor	10M
50	1	100M Resistor	100M

**smartBridges Pre Ltd**  
 10 Anson Road #22-14 International Plaza SG  
 TITLE: \_\_\_\_\_  
 DWG NO: \_\_\_\_\_  
 DRAWN: CEIENGPO  
 DATE: 6-Sep-2001  
 CHECKED: \_\_\_\_\_  
 DATE: \_\_\_\_\_  
 APPROVED: \_\_\_\_\_  
 DATE: \_\_\_\_\_  
 REV: \_\_\_\_\_  
 SIZE: \_\_\_\_\_  
 SHE: \_\_\_\_\_  
 3 OF \_\_\_\_\_



URC'HIN Block Diagram  
SmartBridge  
Despek Blana



**SmartBridges Pte Ltd** (51, Serangoon Road #23-14 Singapore) Pte  
 TITLE: \_\_\_\_\_  
 DWG NO: \_\_\_\_\_  
 DRAWN: Daniel Sima DATE: 08/02/2011  
 CHECKED: Clemp DATE: \_\_\_\_\_  
 APPROVED: \_\_\_\_\_ DATE: \_\_\_\_\_  
 REV: \_\_\_\_\_  
 0