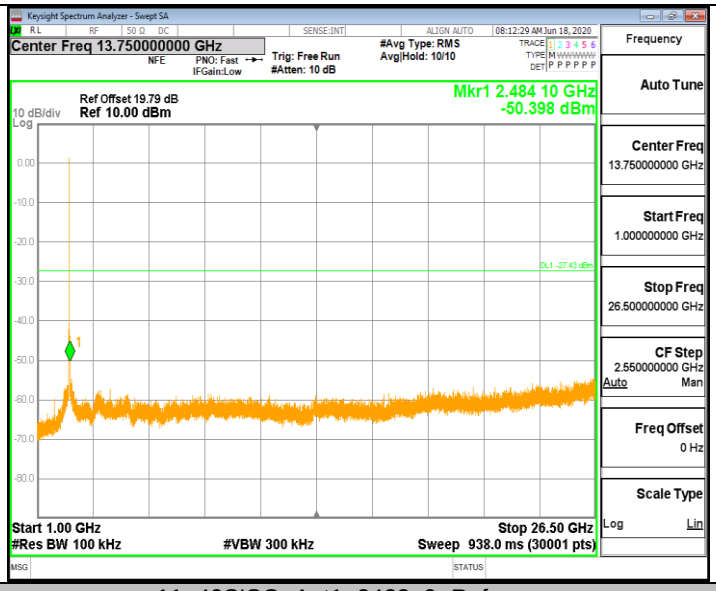
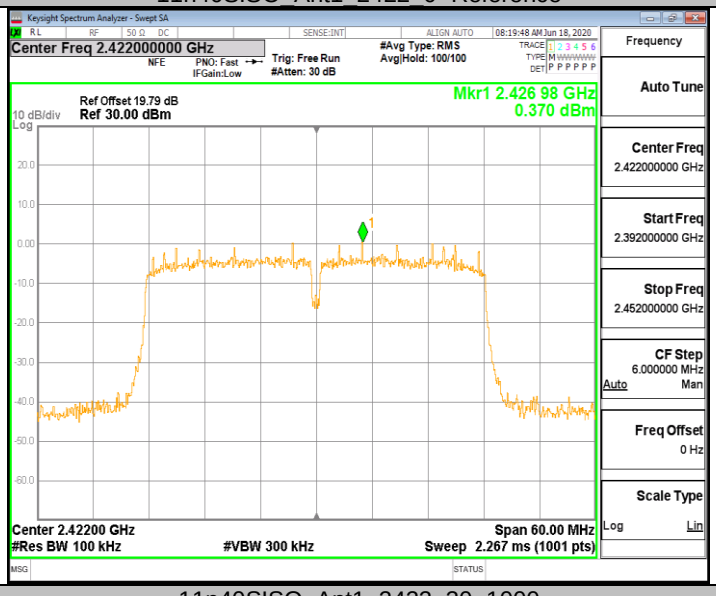
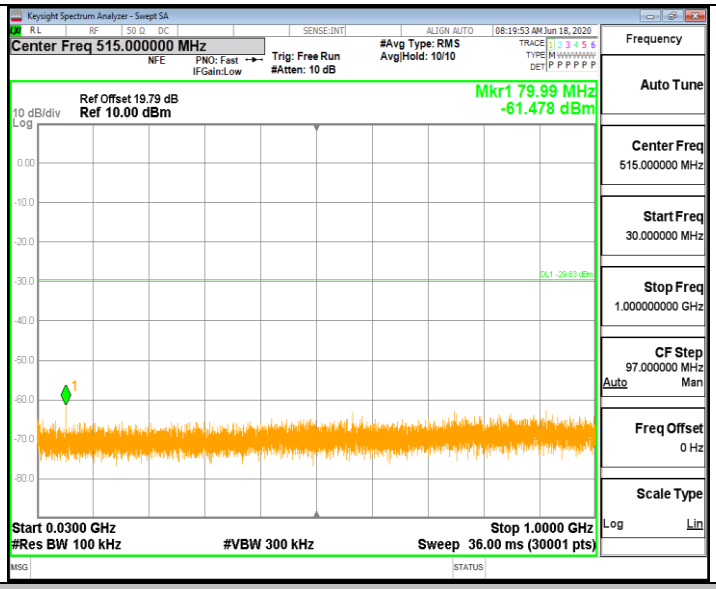


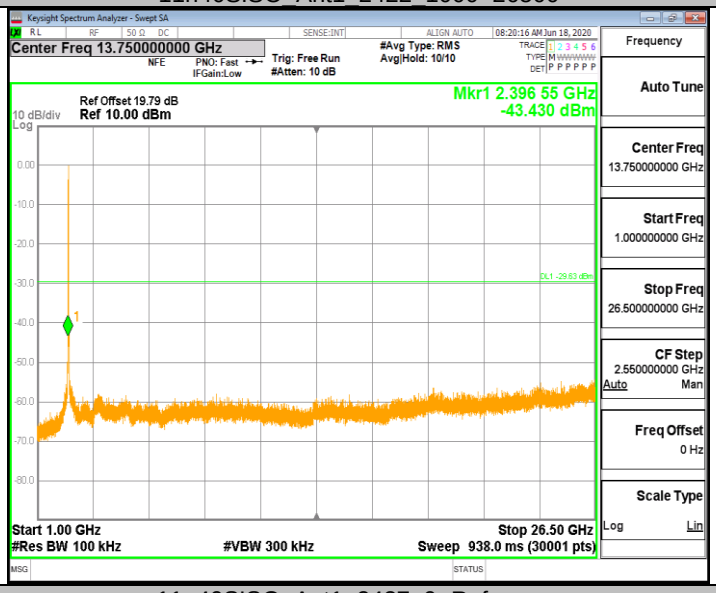

11n40SISO_Ant1_2422_0~Reference



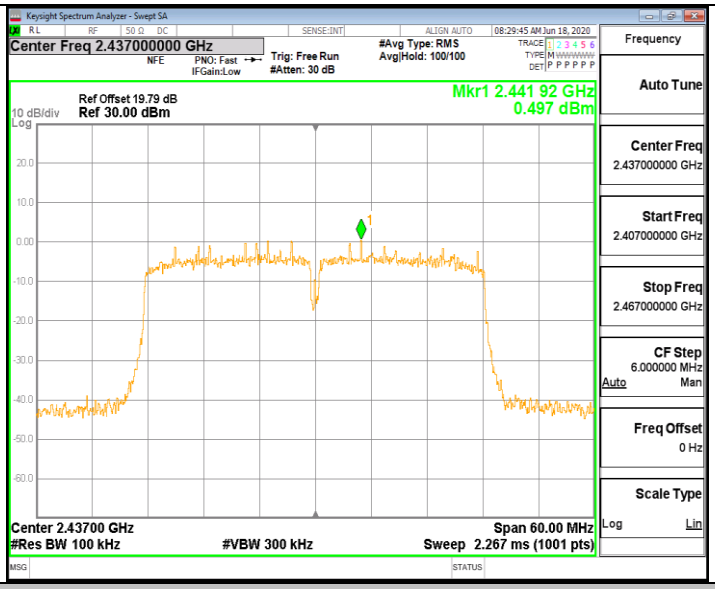

11n40SISO_Ant1_2422_30~1000



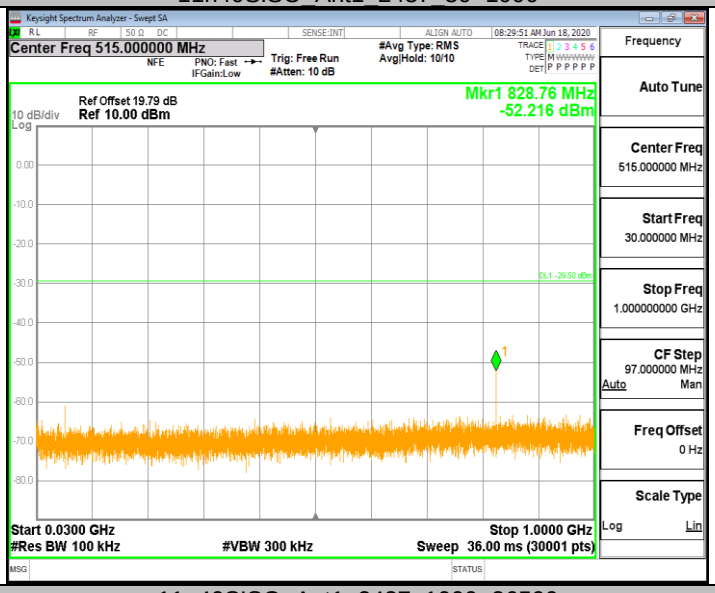

11n40SISO_Ant1_2422_1000~26500



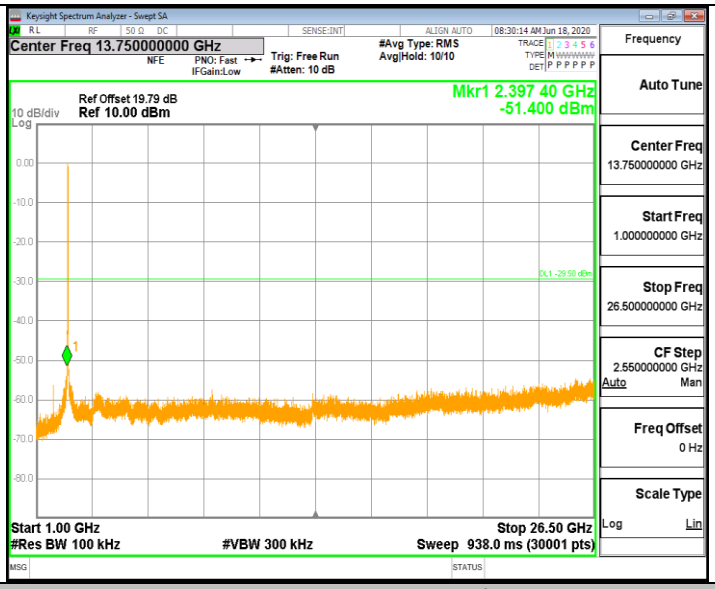

11n40SISO_Ant1_2437_0~Reference



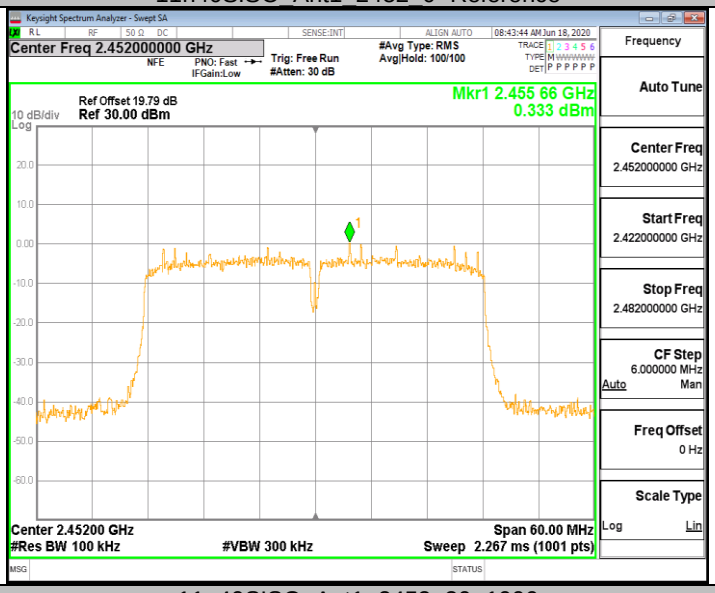

11n40SISO_Ant1_2437_30~1000



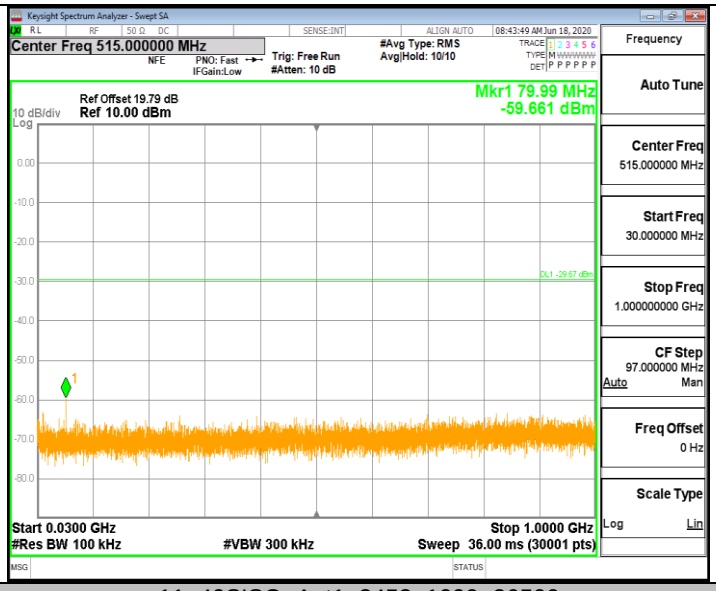

11n40SISO_Ant1_2437_1000~26500



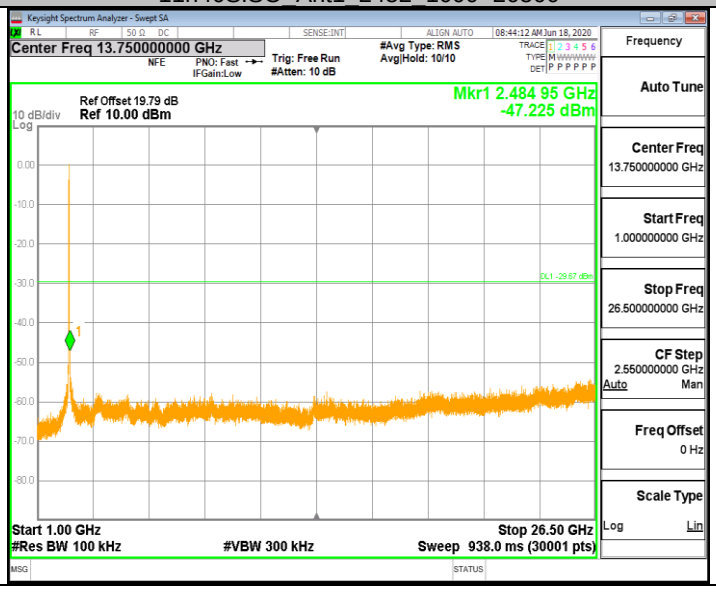

11n40SISO_Ant1_2452_0~Reference




11n40SISO_Ant1_2452_30~1000




11n40SISO_Ant1_2452_1000~26500



## 10.7. Appendix G: Duty Cycle

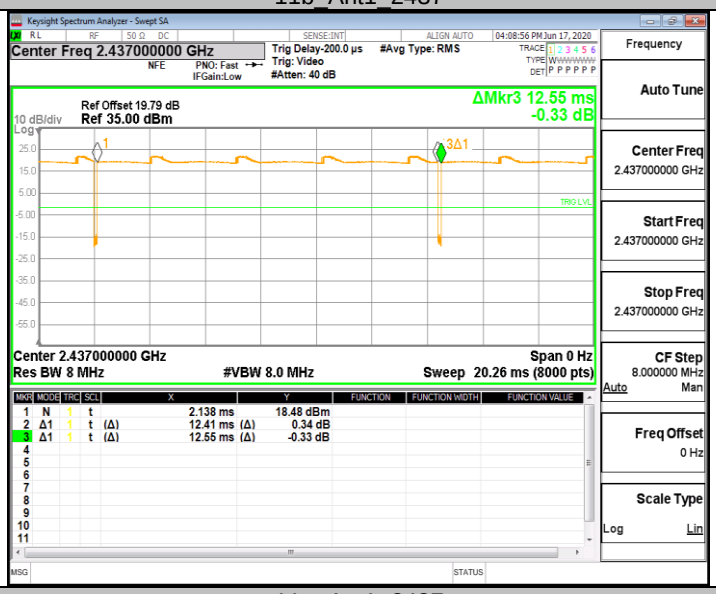
### 10.7.1. Test Result

| Mode | On Time (msec) | Period (msec) | Duty Cycle x (Linear) | Duty Cycle (%) | Duty Cycle Correction Factor (dB) | 1/T Minimum VBW (KHz) | Final setting For VBW (KHz) |
|---|---|---|---|---|---|---|---|
| 11b | 12.41 | 12.55 | 0.964 | 96.4 | 0.16 | 0.17 | 0.01 |
| 11g | 2.062 | 2.194 | 0.940 | 94.0 | 0.27 | 0.28 | 0.5 |
| 11n HT20 | 1.918 | 2.051 | 0.935 | 93.5 | 0.29 | 0.30 | 0.5 |
| 11n HT40 | 0.941 | 1.073 | 0.877 | 92.7 | 0.57 | 0.58 | 1 |

Note:
Duty Cycle Correction Factor=10log (1/x).
Where: x is Duty Cycle (Linear)
Where: T is On Time
If that calculated VBW is not available on the analyzer, then the next higher value should be used.
For mode 11b, the duty cycle is greater than 98%, so it can set VBW to 10Hz.

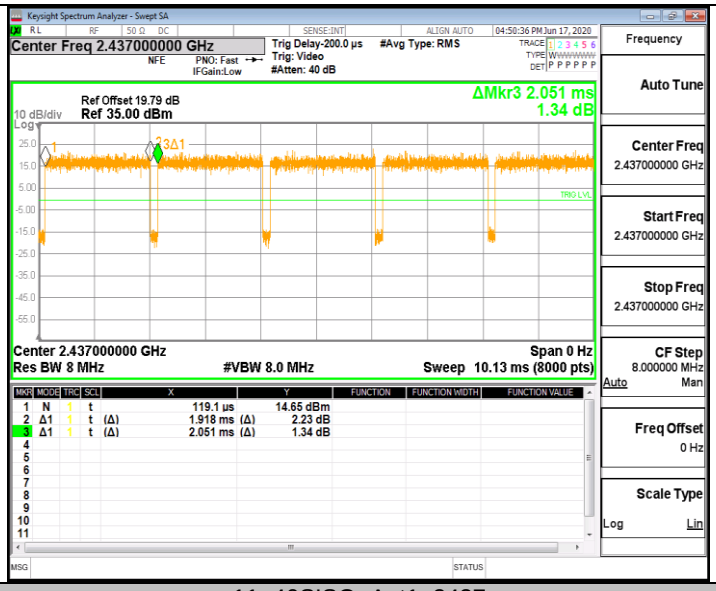
## 10.7.2. Test Graphs

11b_Ant1_2437

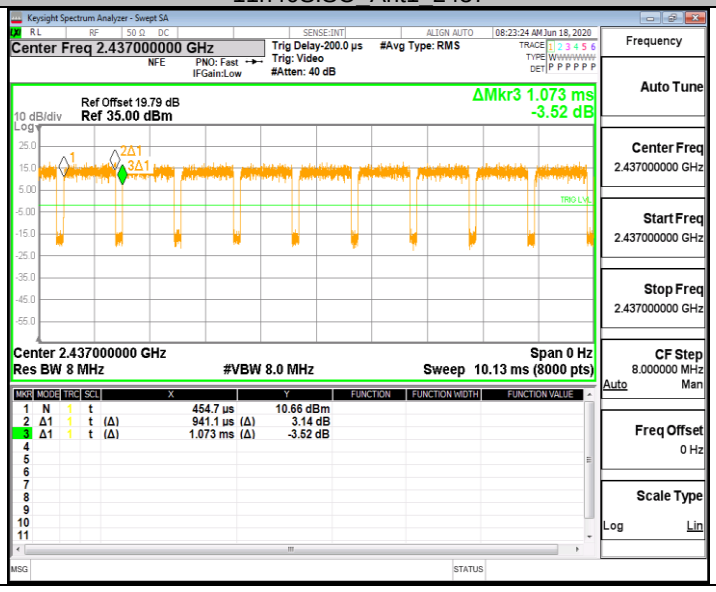



11g_Ant1_2437


11n20SISO_Ant1_2437




11n40SISO_Ant1_2437




# END OF REPORT