

**Reversion History:**

**Rev3.0 to 4.0:**

- 1. EMI improvement.

# **ARESCOM**

## **ND 1760**

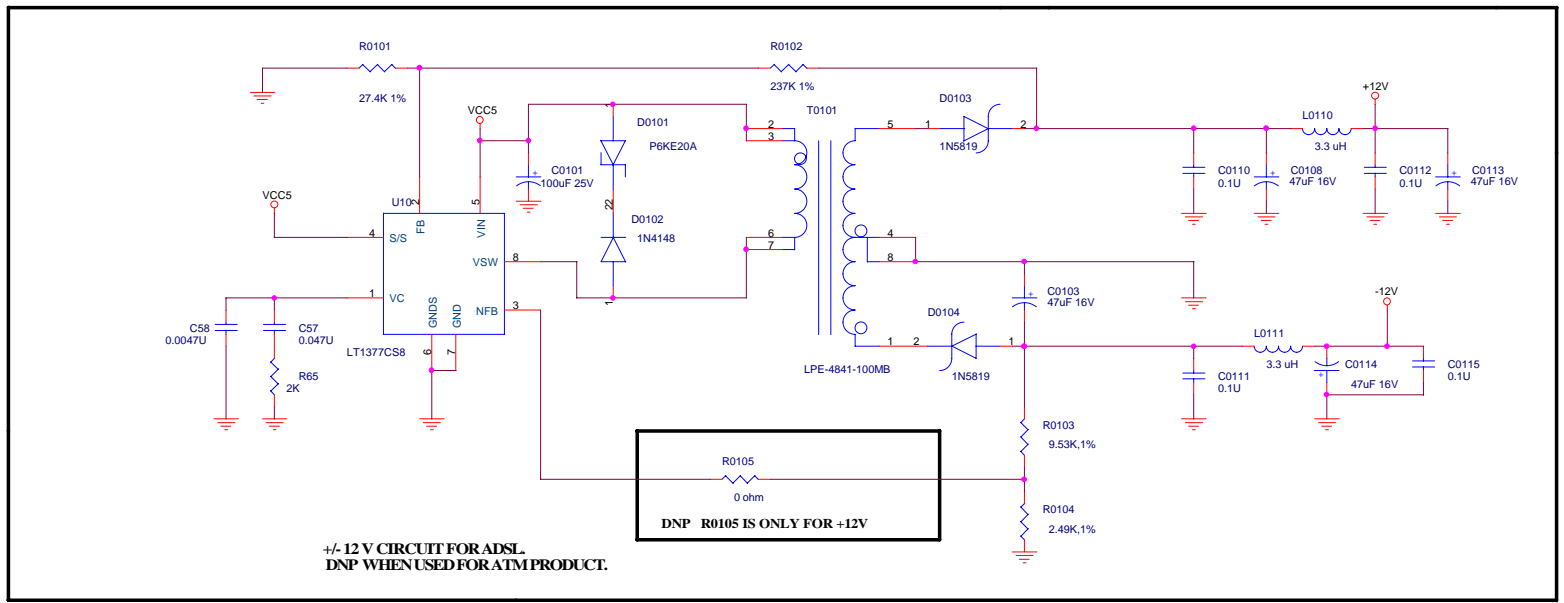
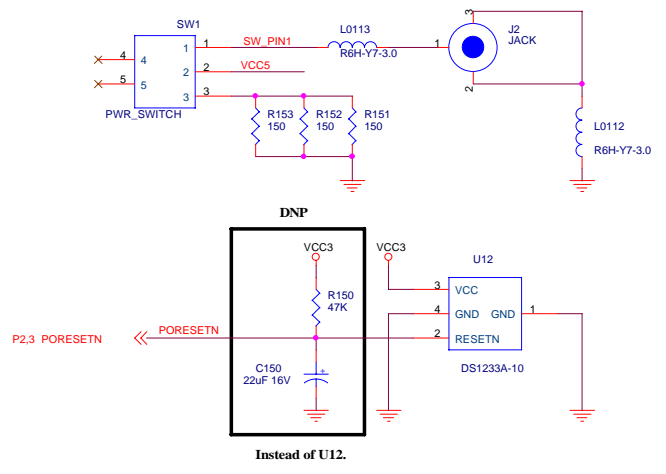
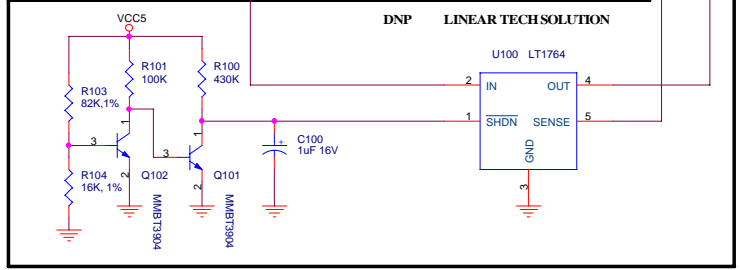
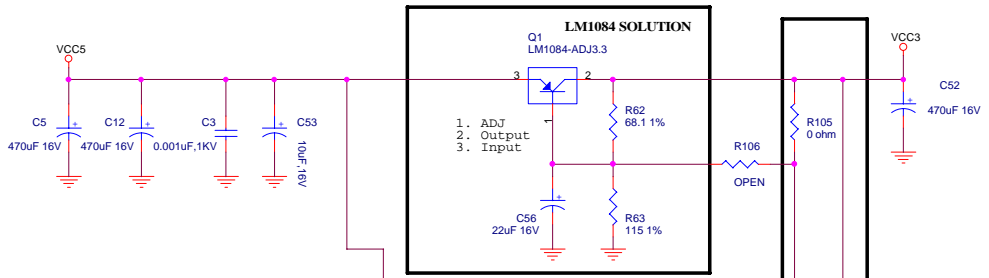
## **ADSL WLAN**

## **MOTOROLA MPC850 MAIN BOARD**

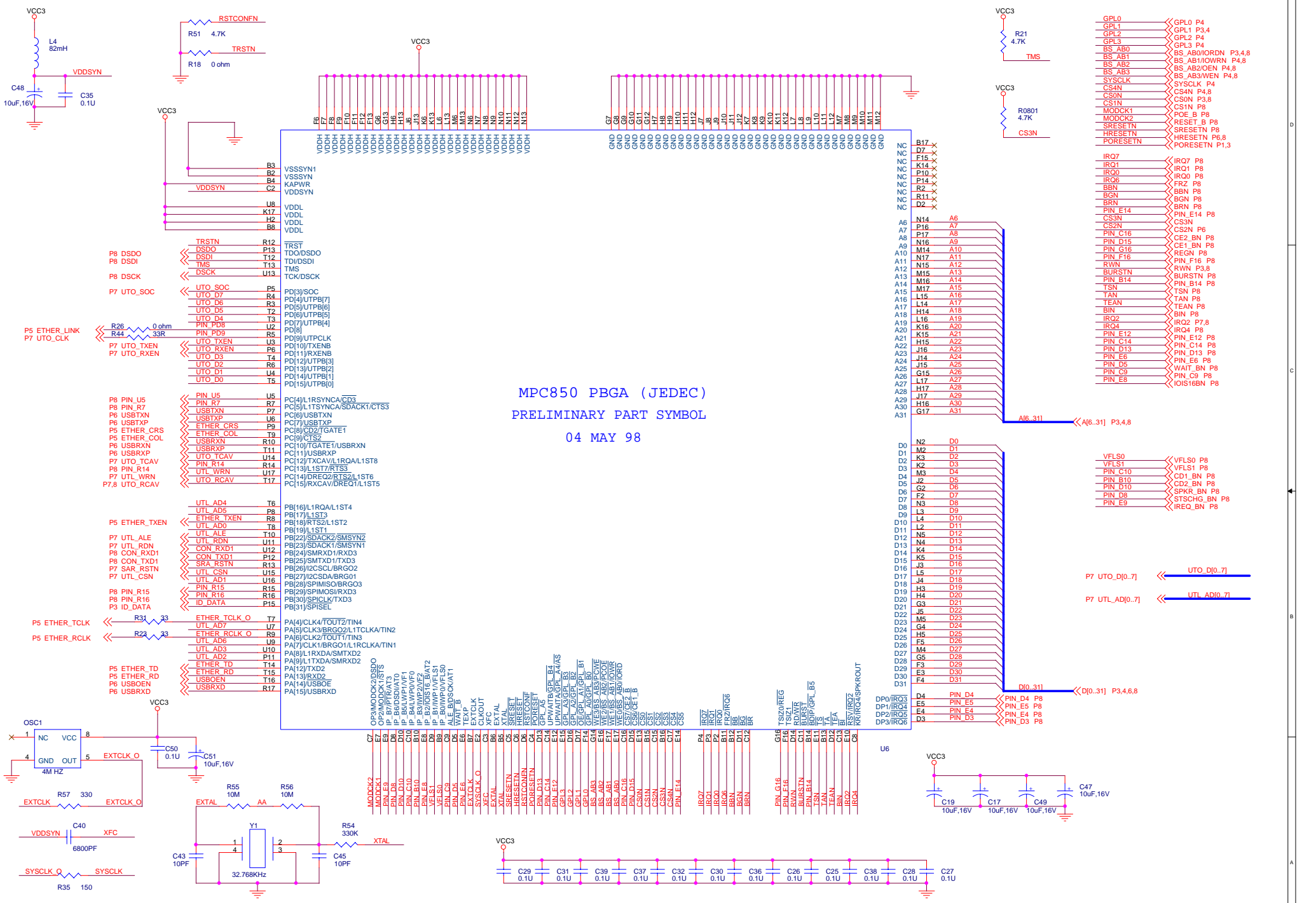
### **P/N :**

### **Revision : 4A**

<b>ARESCOM</b>		
Title ND 1760		
Size	Document Number HEADER	Rev 4.0
Date:	Wednesday, August 08, 2001	Sheet 1 of 9



<b>ARES COM</b>		
Title ND 1760		
Size	Document Number POWER	Rev 4.0
Date:	Wednesday, August 08, 2001	Sheet 2 of 9

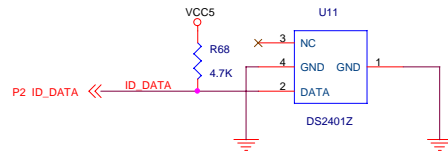
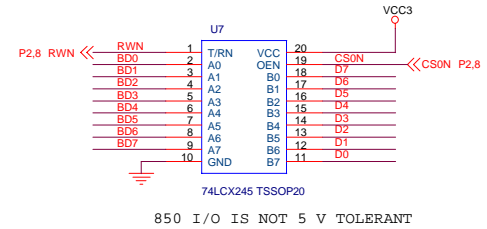
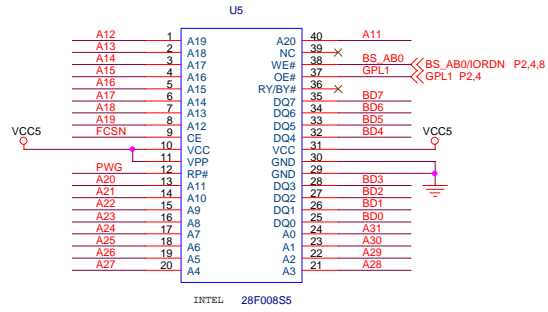
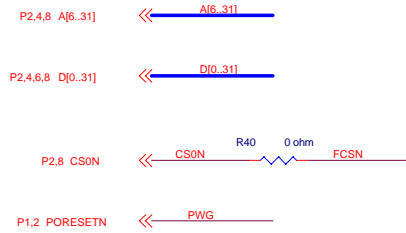
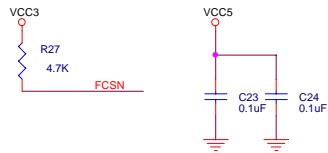


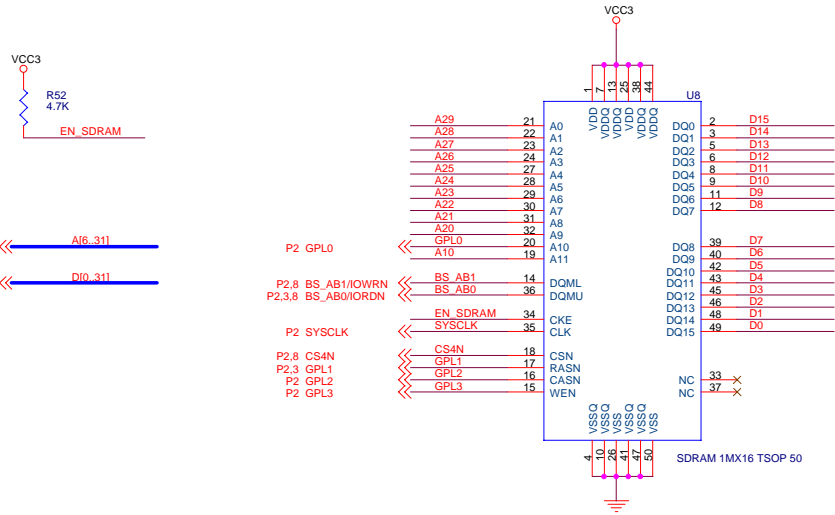
MPC850 PBGA (JEDEC)  
 PRELIMINARY PART SYMBOL  
 04 MAY 98

GPIO0	<<>	GPIO P4
GPIO1	<<>	GPIO P4.4
GPIO2	<<>	GPIO P4
GPIO3	<<>	GPIO P4
BS AB0	<<>	BS AB0/ORDN P3,4,8
BS AB1	<<>	BS AB2
BS AB2	<<>	BS AB2/OEN P4,8
BS AB3	<<>	BS AB3/WEN P4,8
SYSCLK	<<>	SYSCLK P4
CS4N	<<>	CS4N P4,8
CS0N	<<>	CS0N P3,8
CS1N	<<>	CS1N P8
MODCK1	<<>	MODCK1 P8
MODCK2	<<>	MODCK2 P8
SRESETN	<<>	SRESETN P8
HRESETN	<<>	HRESETN P6,8
PORESETN	<<>	PORESETN P1,3
GPIO4	<<>	GPIO P4
GPIO5	<<>	GPIO P4
GPIO6	<<>	GPIO P4
GPIO7	<<>	GPIO P4
GPIO8	<<>	GPIO P4
GPIO9	<<>	GPIO P4
GPIO10	<<>	GPIO P4
GPIO11	<<>	GPIO P4
GPIO12	<<>	GPIO P4
GPIO13	<<>	GPIO P4
GPIO14	<<>	GPIO P4
GPIO15	<<>	GPIO P4
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GPIO17	<<>	GPIO P4
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GPIO19	<<>	GPIO P4
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GPIO23	<<>	GPIO P4
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GPIO26	<<>	GPIO P4
GPIO27	<<>	GPIO P4
GPIO28	<<>	GPIO P4
GPIO29	<<>	GPIO P4
GPIO30	<<>	GPIO P4
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GPIO32	<<>	GPIO P4
GPIO33	<<>	GPIO P4
GPIO34	<<>	GPIO P4
GPIO35	<<>	GPIO P4
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GPIO147	<<>	GPIO P4
GPIO148	<<>	GPIO P4
GPIO149	<<>	GPIO P4
GPIO150	<<>	GPIO P4

EMI improvement:  
 R31/R23 change from 10R to 33R.  
 R57 change from 10R to 330.  
 R35 change from 10R to 150.  
 Add R44=33R.

<b>ARESCOM</b>		
Title	ND 1760	
Size	Document Number	Rev
	MPC850SAR	4.0
Date:	Wednesday, August 08, 2001	Sheet 3 of 9





P2.3.8 A[6..31] << A[6..31]

P2.3.6,8 D[0..31] << D[0..31]

P2 GPL0 << GPL0

P2.8 BS\_AB1/IOWRN  
P2.3.8 BS\_AB0/IORDN << BS\_AB1, BS\_AB0

P2 SYSCLK << SYSCLK

P2.8 CS4N << CS4N

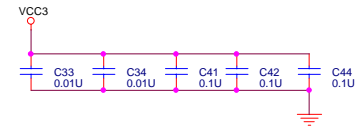
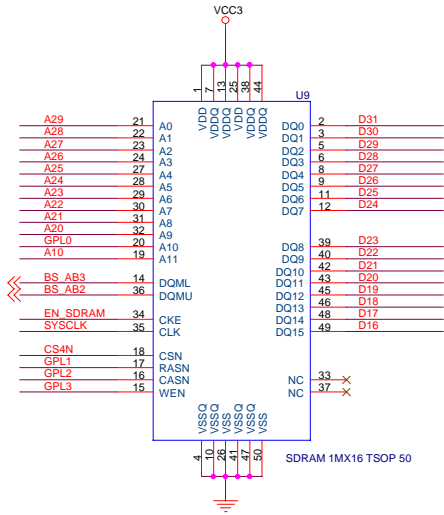
P2.3 GPL1 << GPL1

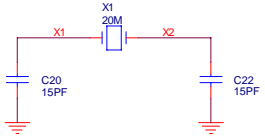
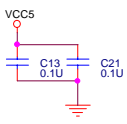
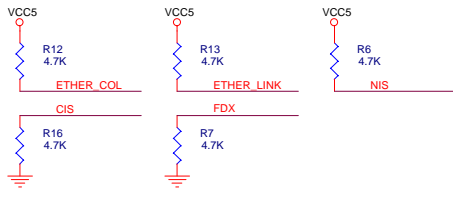
P2 GPL2 << GPL2

P2 GPL3 << GPL3

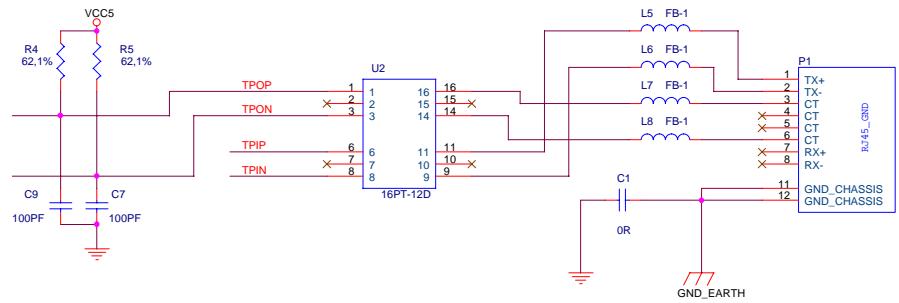
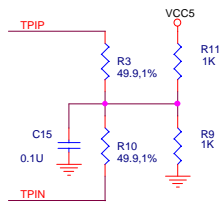
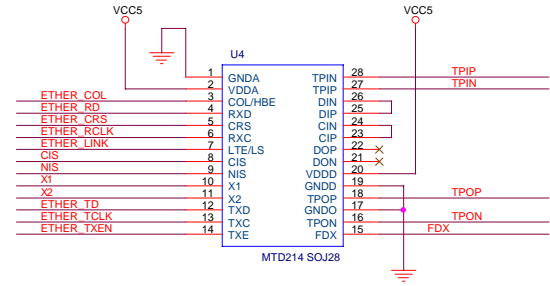
P2.8 BS\_AB3/WEN << BS\_AB3

P2.8 BS\_AB2/OEN << BS\_AB2



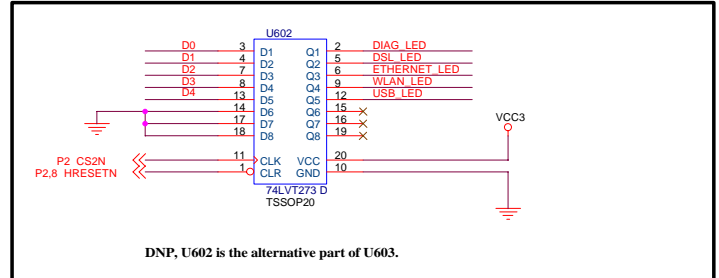
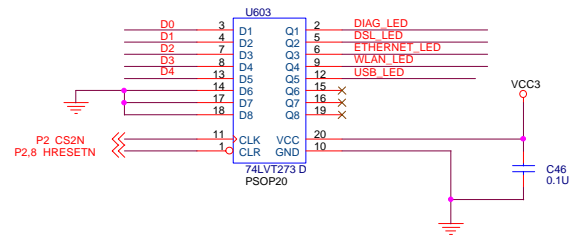
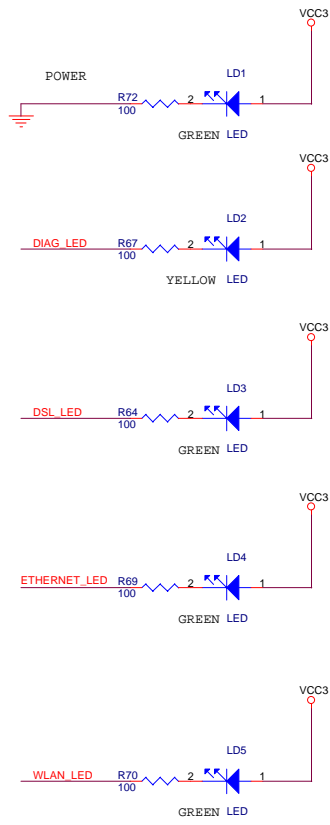


- ETHER\_CRS << ETHER\_CRS P2
- ETHER\_RCLK << ETHER\_RCLK P2
- ETHER\_TD << ETHER\_TD P2
- ETHER\_TXEN << ETHER\_TXEN P2
- P2 ETHER\_COL << ETHER\_COL
- P2 ETHER\_RD << ETHER\_RD
- P2 ETHER\_LINK << ETHER\_LINK
- P2 ETHER\_TCLK << ETHER\_TCLK

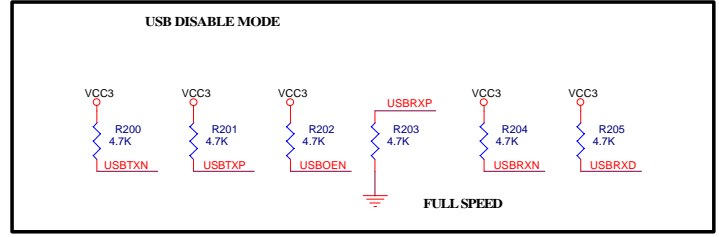


EMI improvement:  
 Add L5,L6,L7 and L8.  
 C1 change from 0.01uF to 0R.  
 C20,C22 change from 30pF to 15pF.

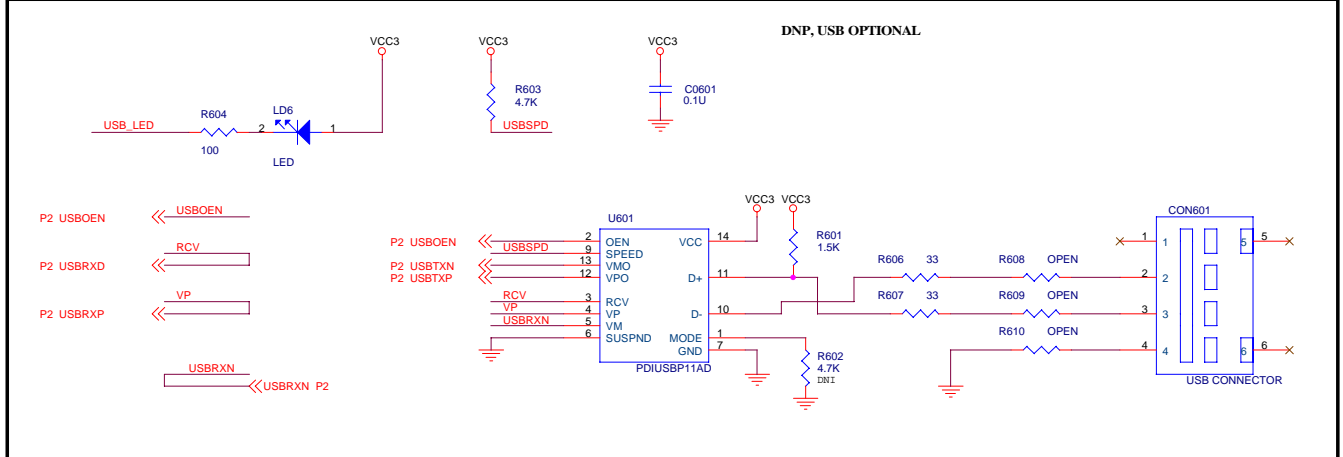
ARES COM		
Title	ND 1760	
Size	Document Number	Rev
	ETHERNET_PHY	4.0
Date:	Wednesday, August 08, 2001	Sheet 6 of 9

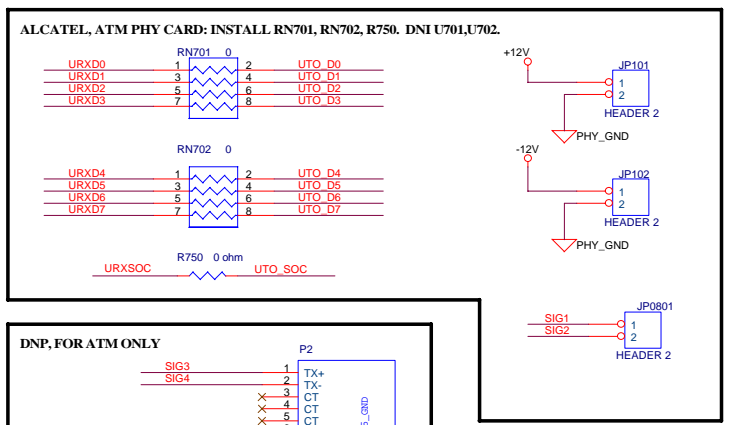
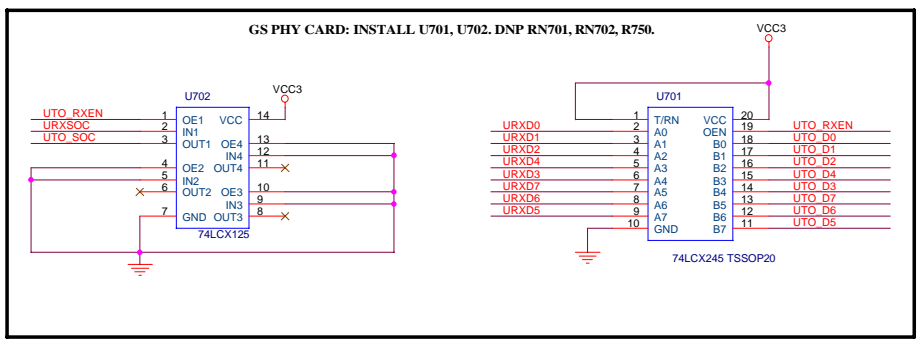
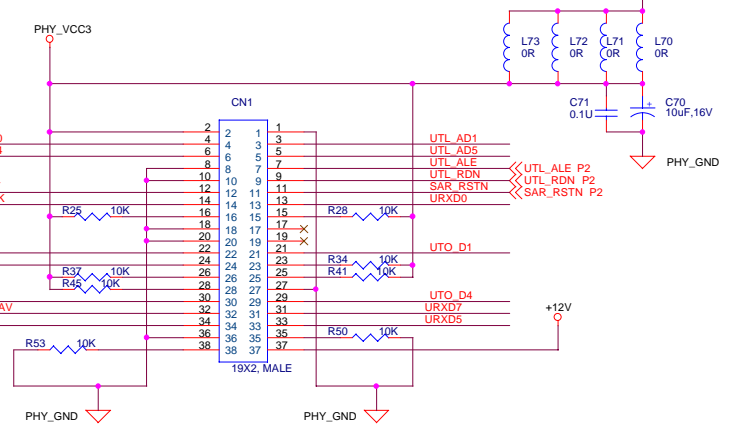
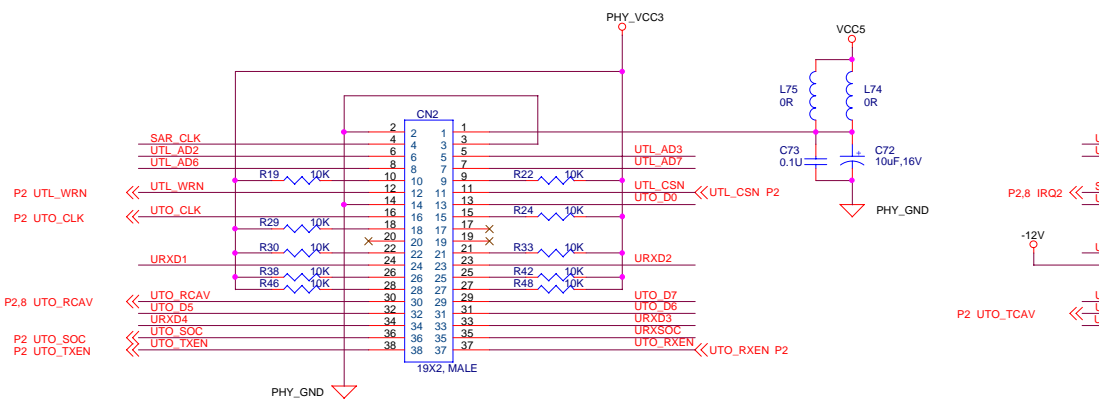


DNP, U602 is the alternative part of U603.

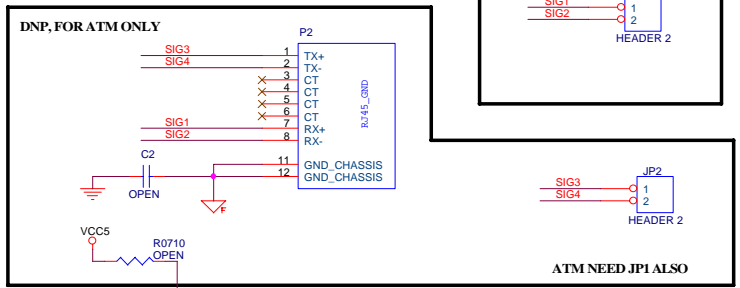
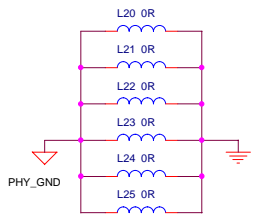


P2.3,4,8 D[0:31] ← D[0:31]

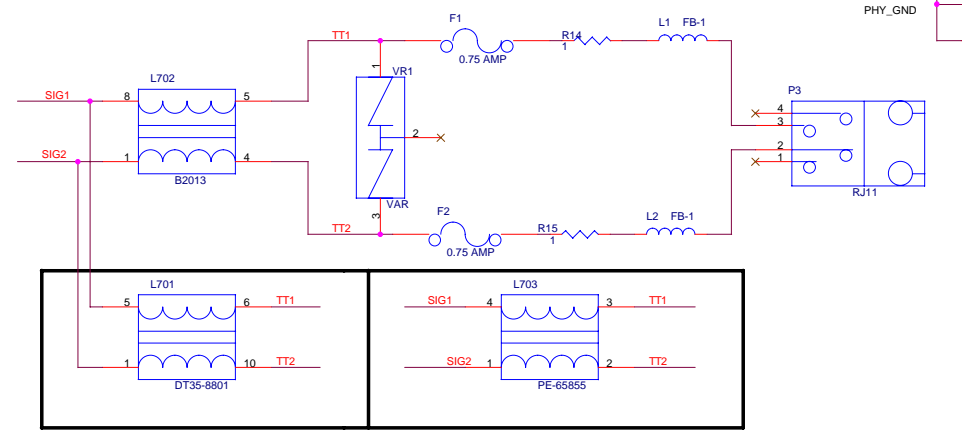




P2\_UTO\_D[0..7] << UTO\_D[0..7]  
 P2\_UTL\_AD[0..7] << UTL\_AD[0..7]



ATM NEED JPI ALSO

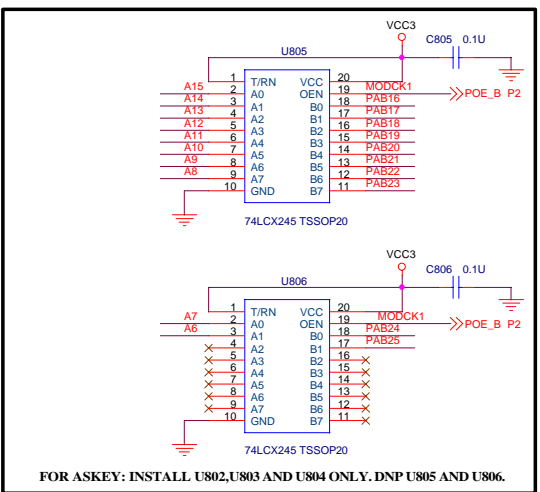
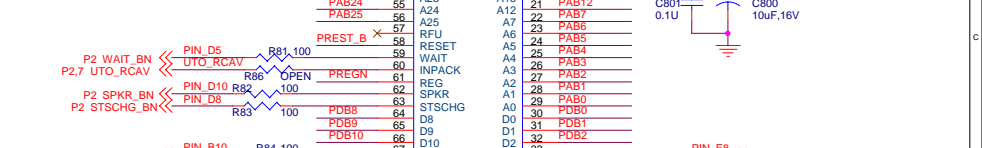
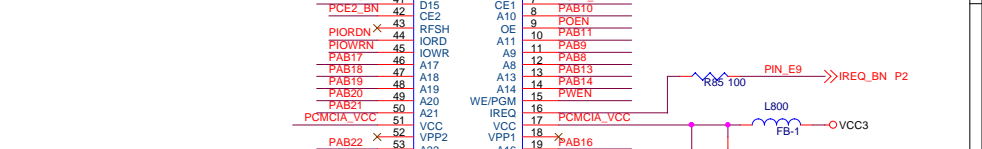
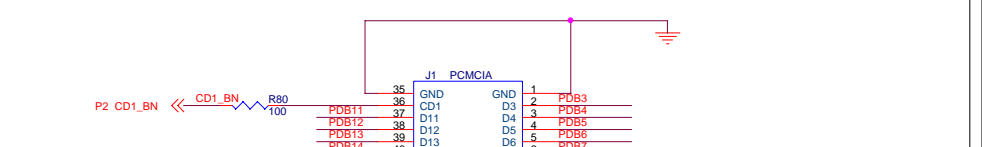
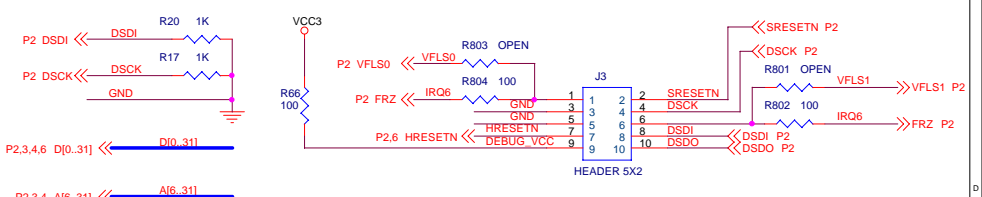
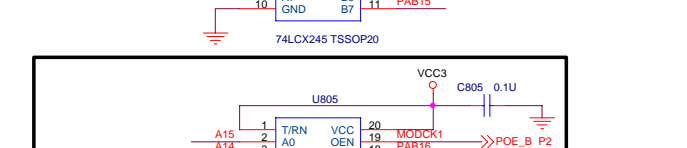
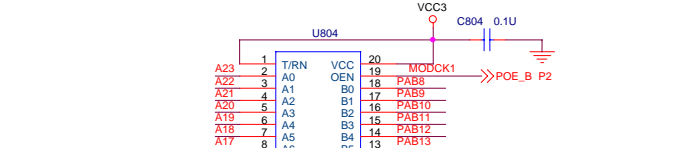
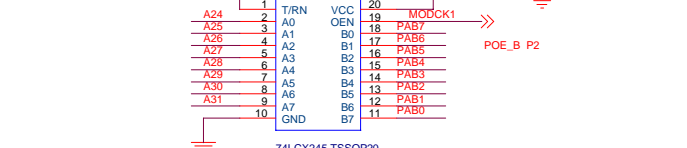
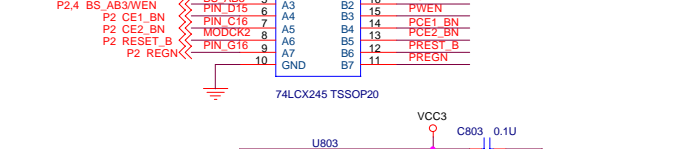
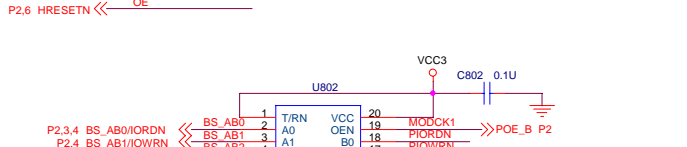
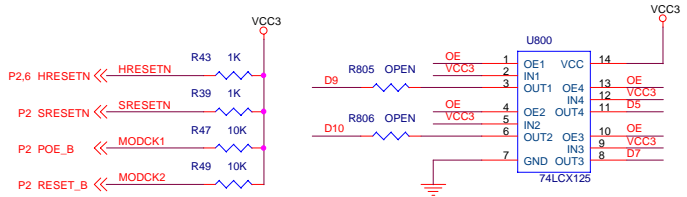
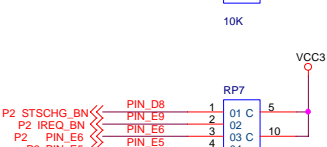
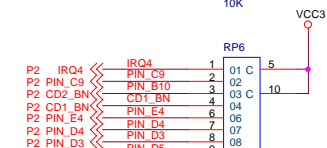
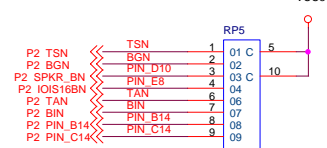
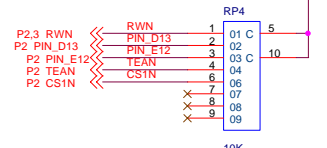
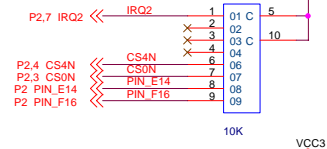
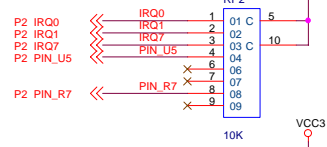
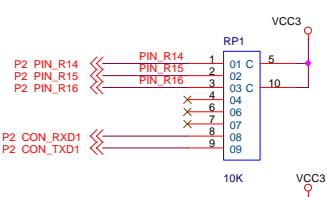


R0710 for 32M HZ ATM solution  
 R0711 for 34.56M HZ ADSL solution

EMI improvement:  
 L20 to L25 and L70 to L75 change from FB-1 to 0R.

<b>ARESCOM</b>		
Title	ND 1760	
Size	Document Number	Rev
	ADSL PHY CONNECTOR	4.0
Date:	Wednesday, August 08, 2001	Sheet 8 of 9





Note: Read is data from PCMCIA to MPC850. RWN=L, A to B

