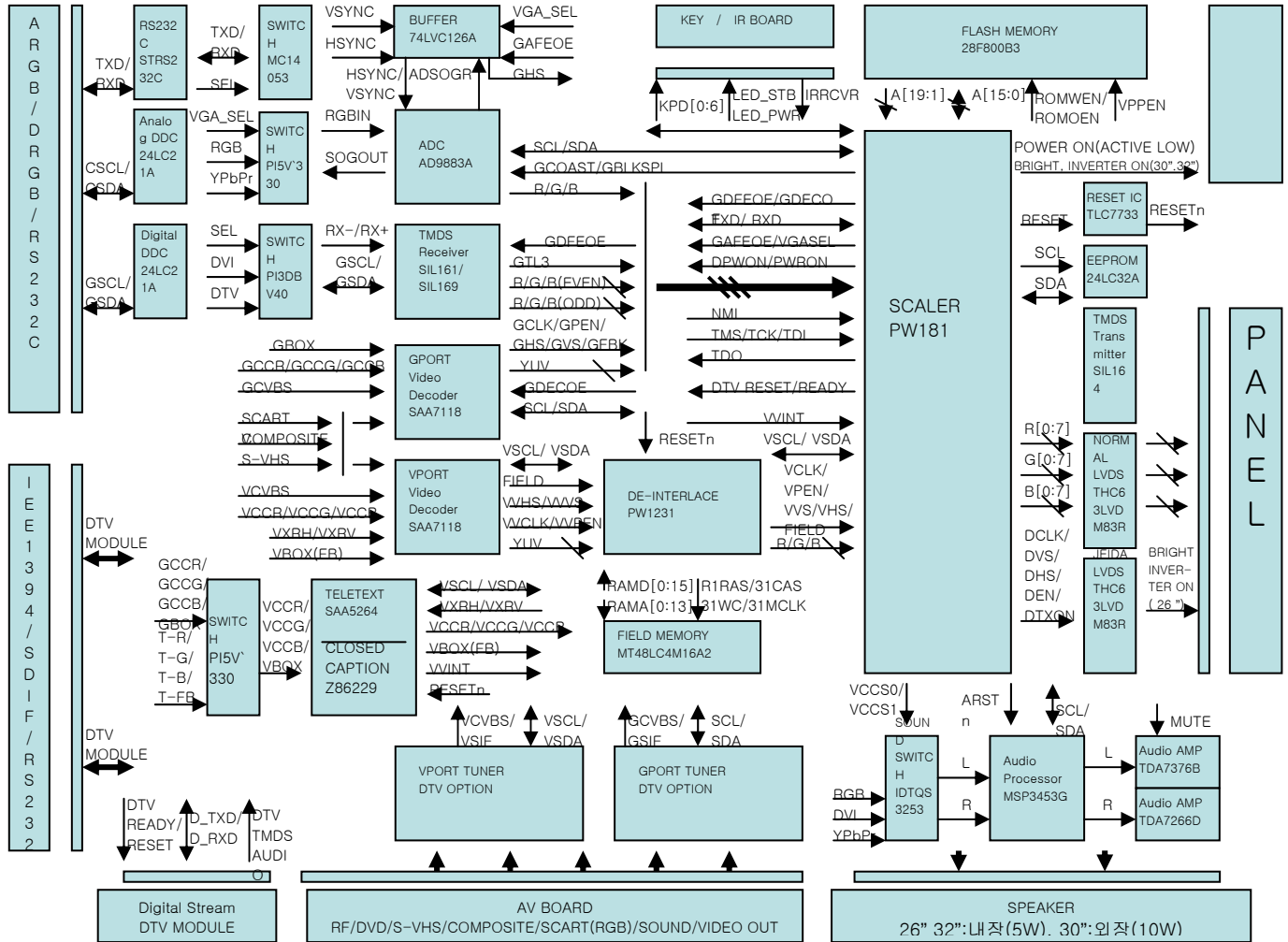


Appendix 5. Block Diagram

Signal Block Diagram



Power Block diagram

