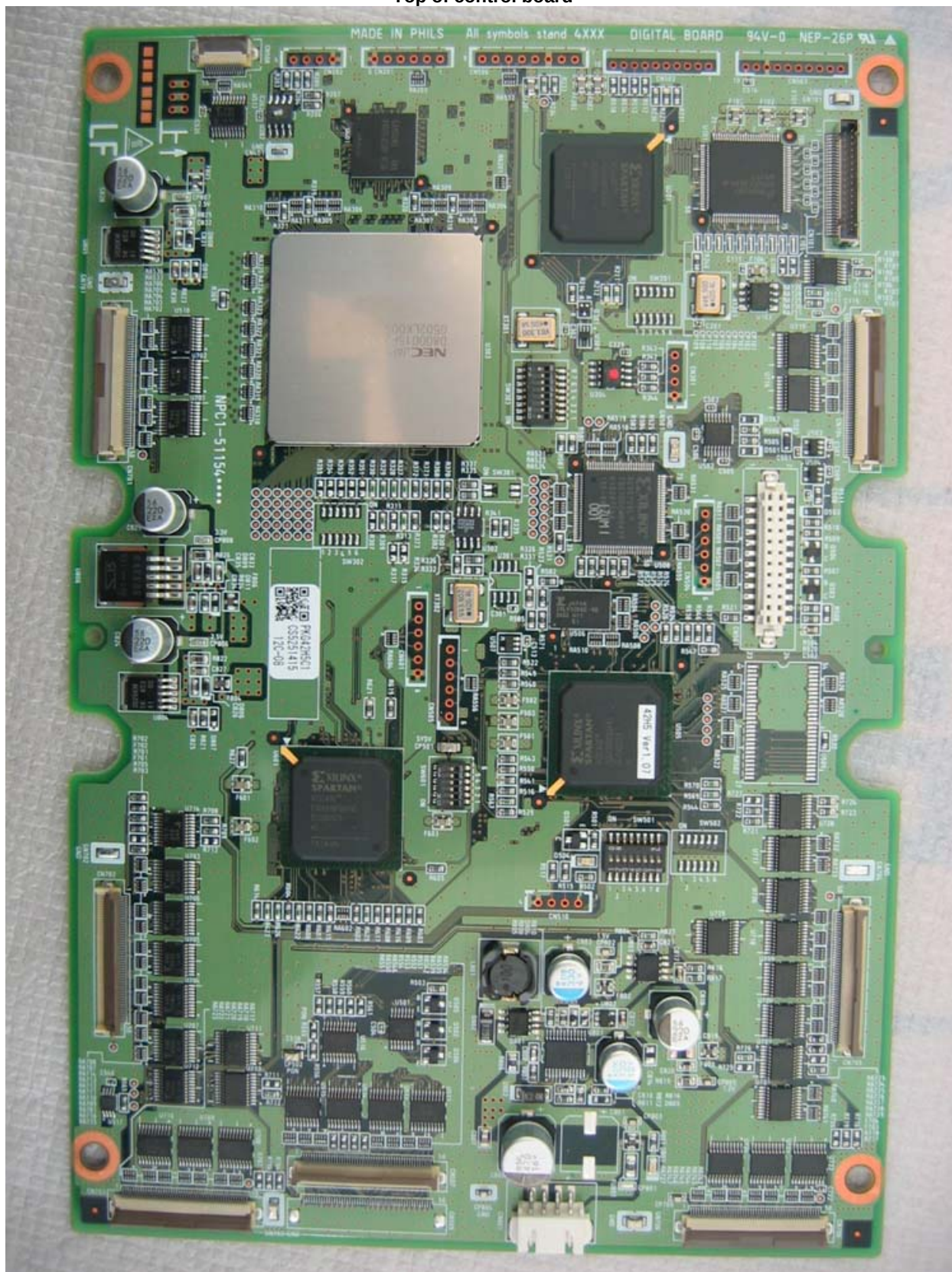
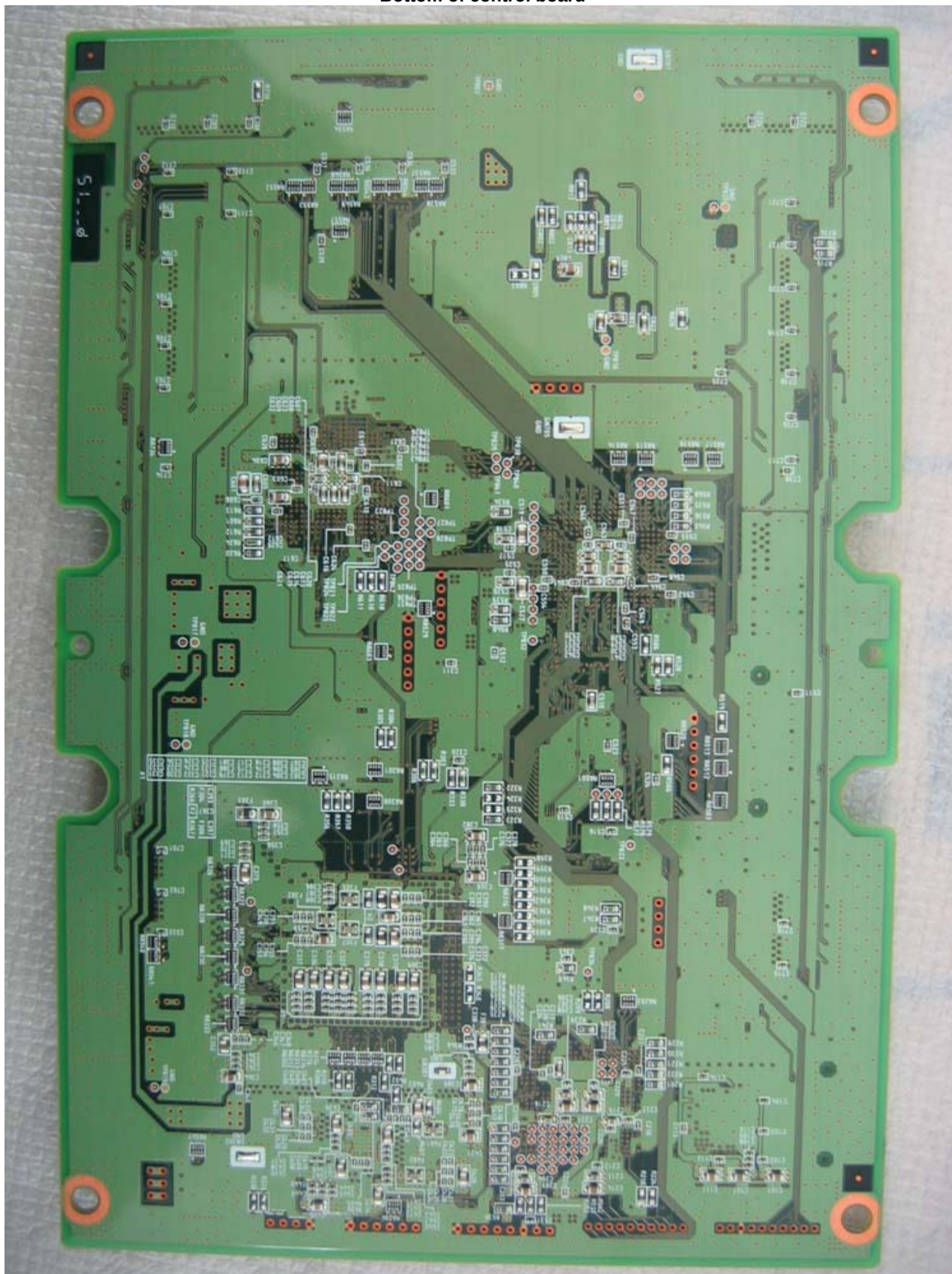


Appendix 4. Internal Photographs of EUT

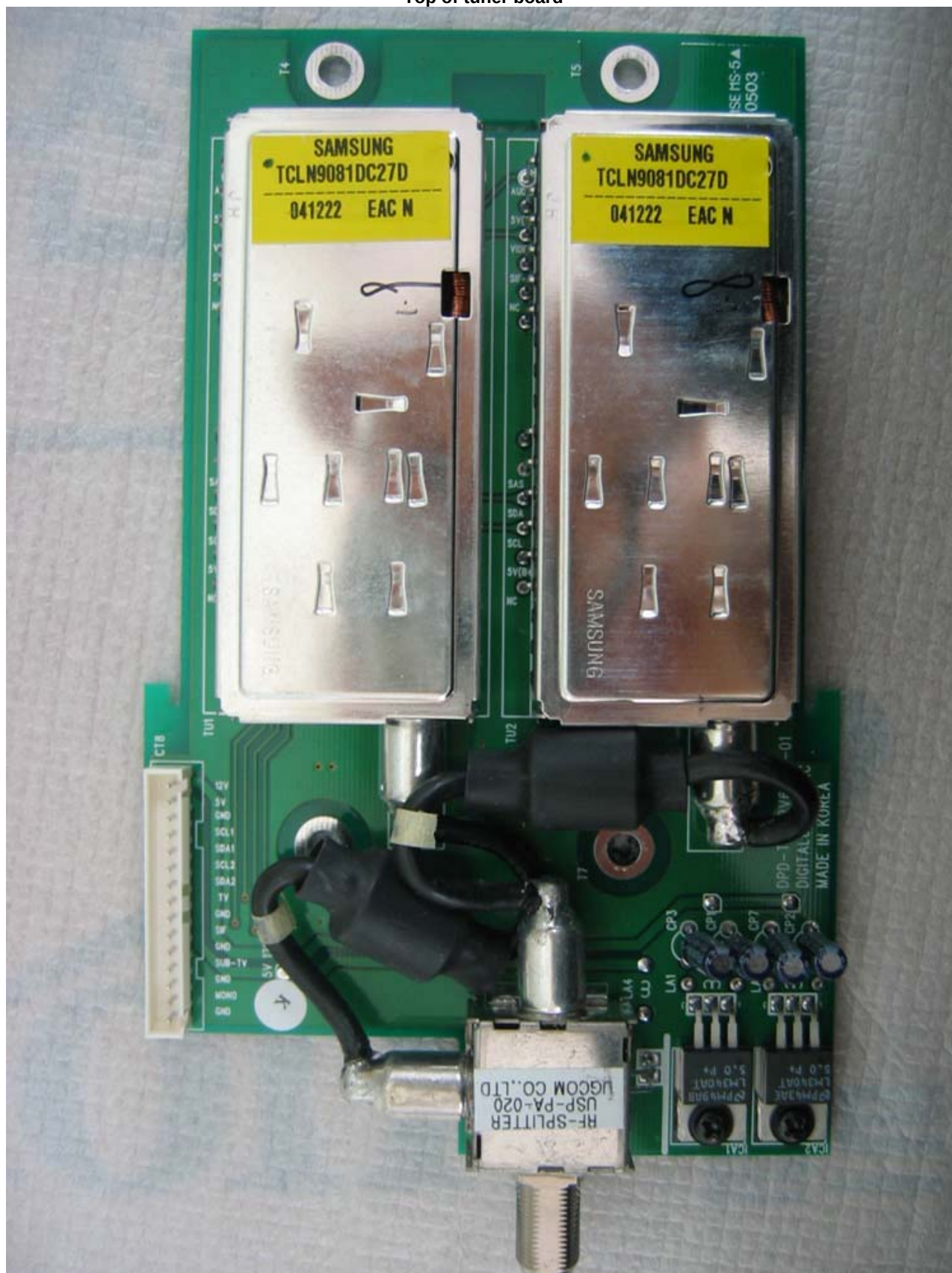
Top of control board



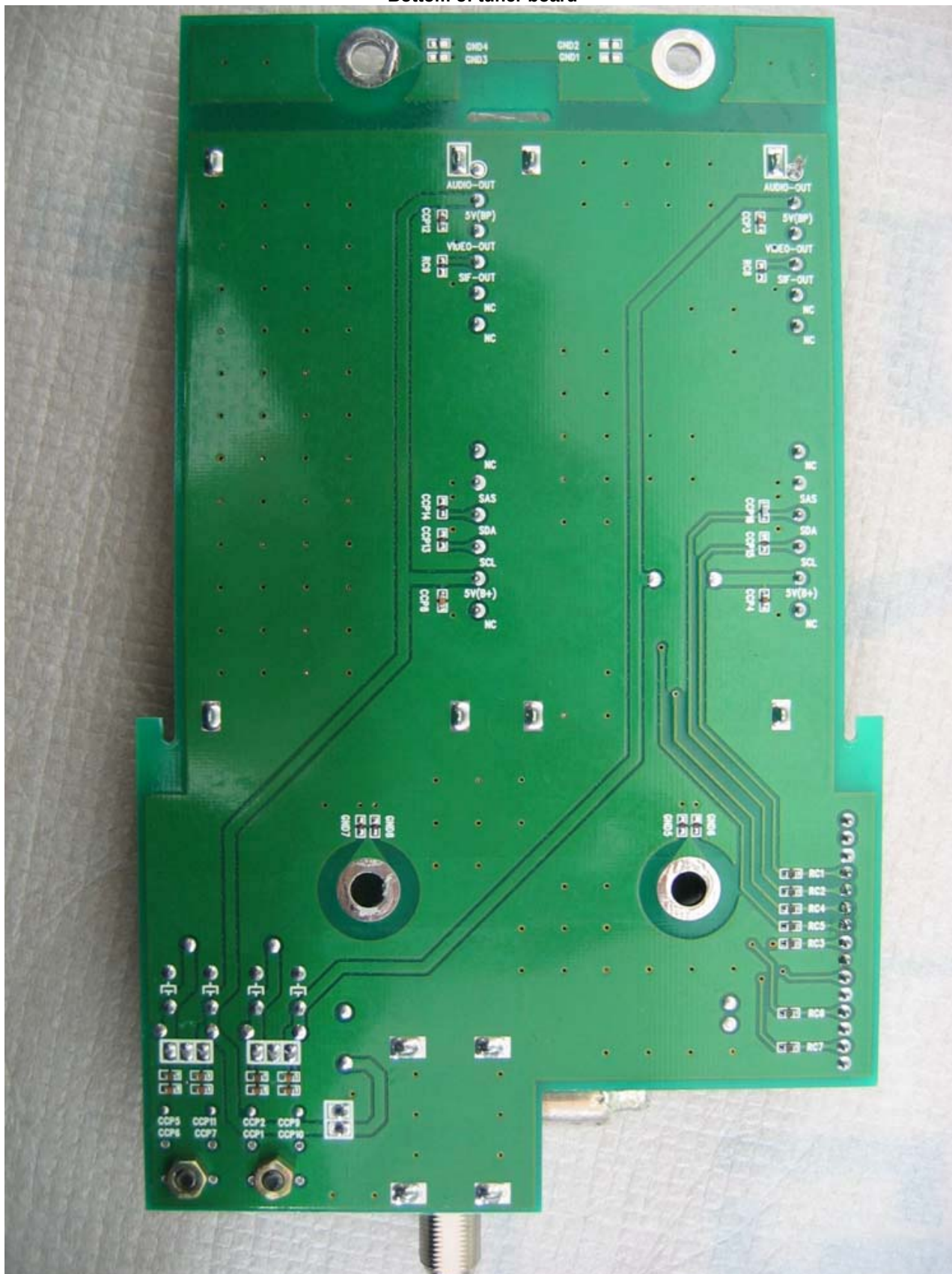
Bottom of control board



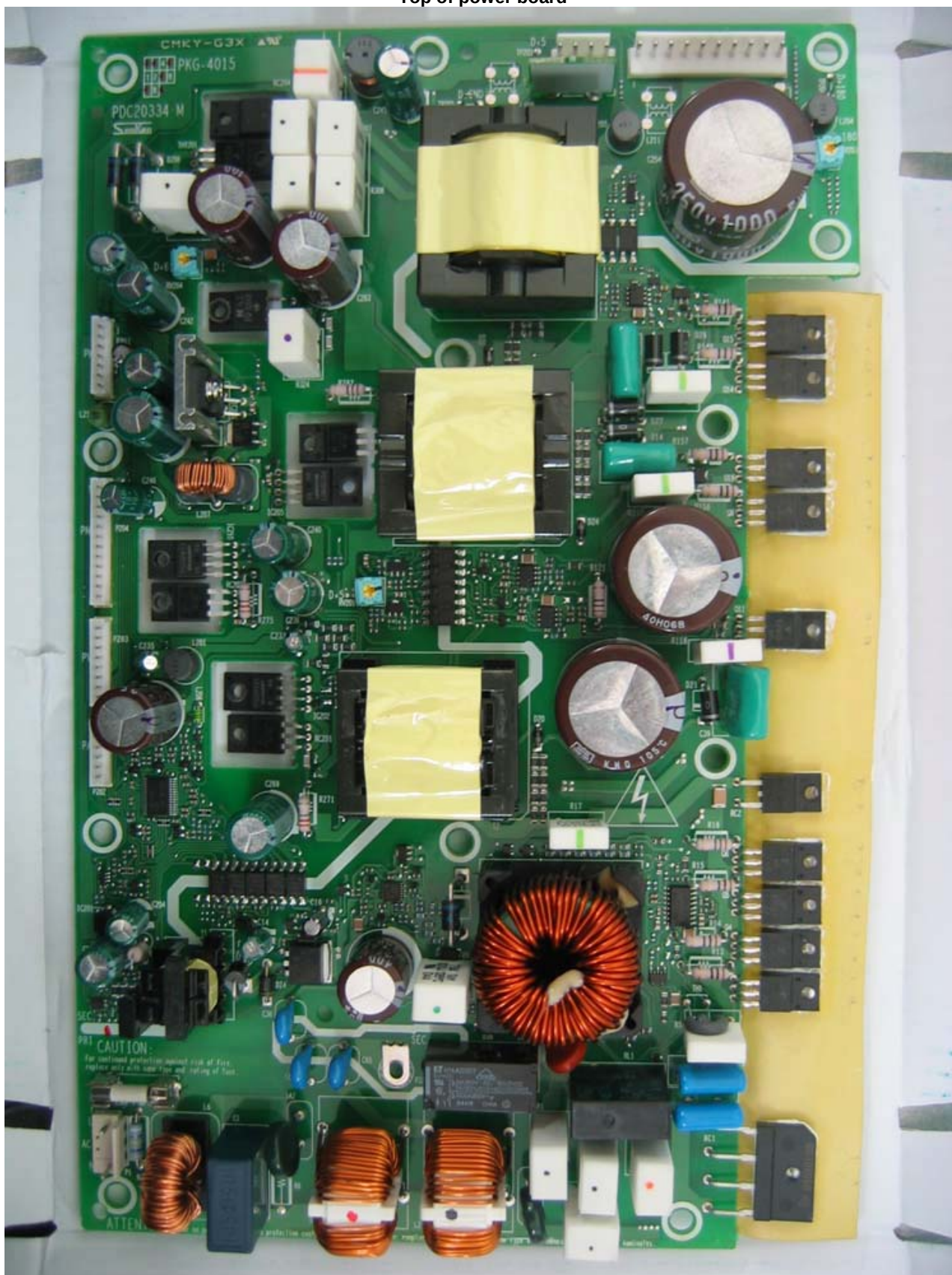
Top of tuner board



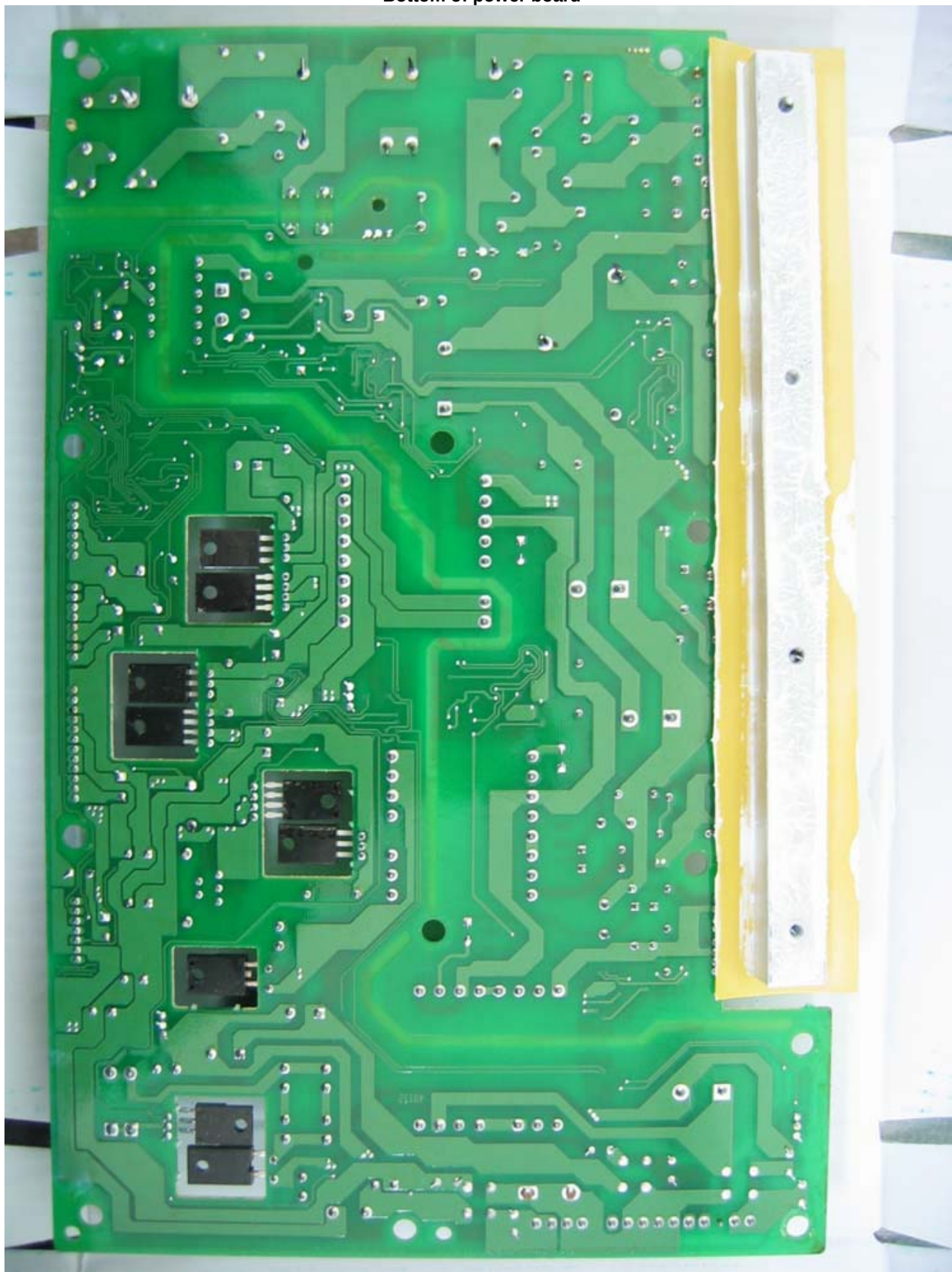
Bottom of tuner board



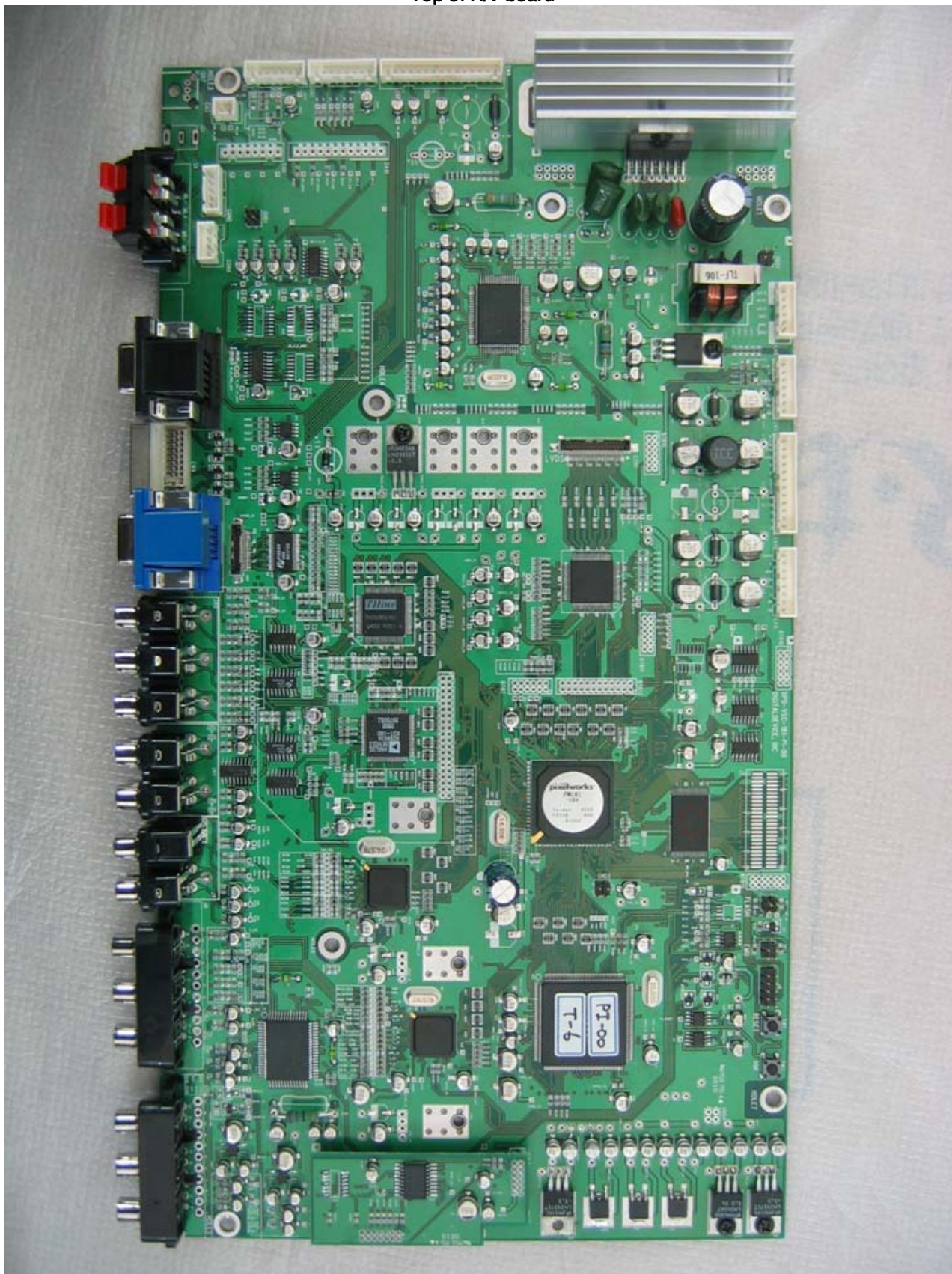
Top of power board



Bottom of power board



Top of A/V board



Bottom of A/V board



Top of interface board



Bottom of interface board



Top of XRLBT(Right) board



Bottom of XRLBT(Right) board



Top of XRLBT(Left) board



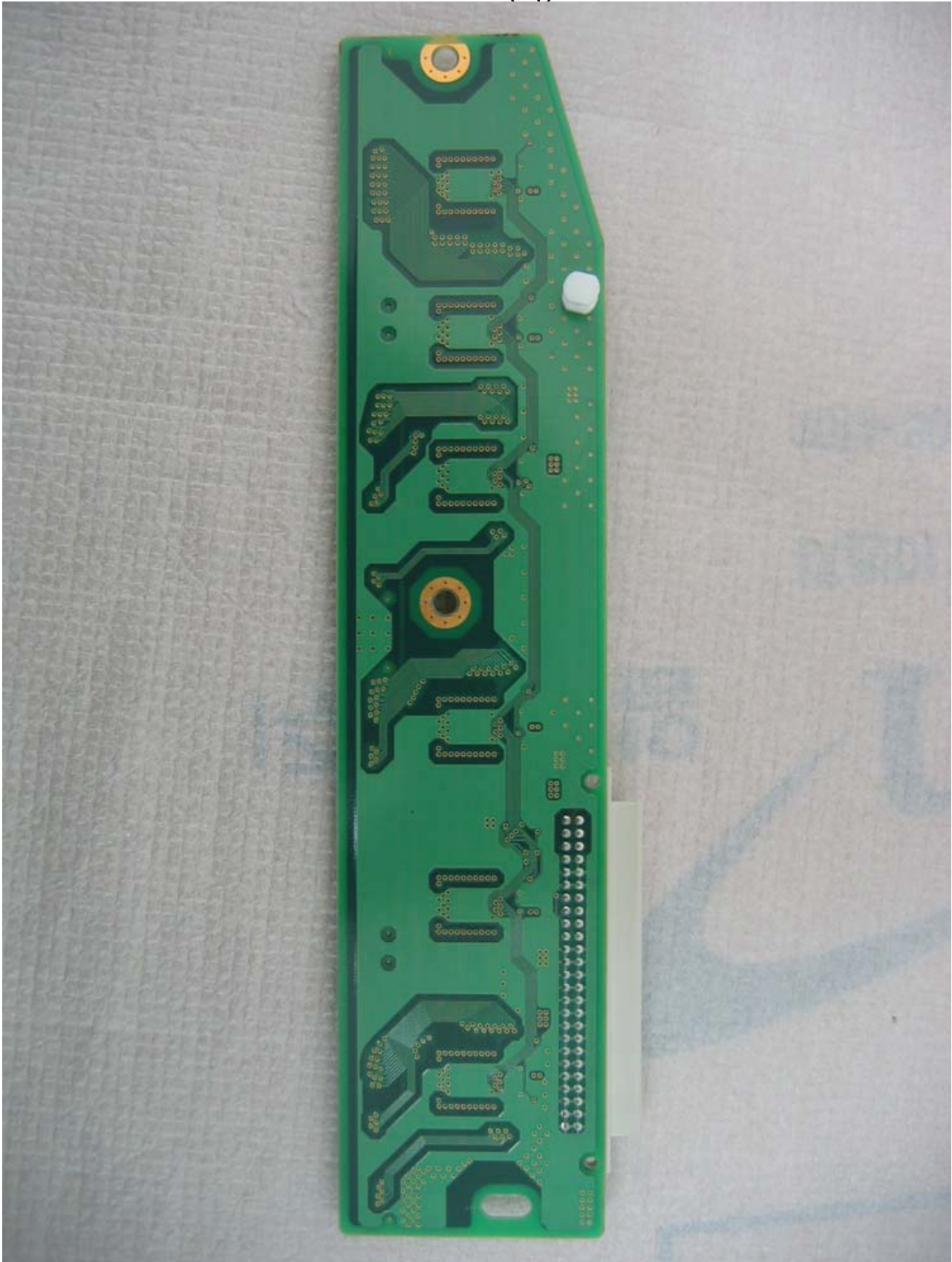
Bottom of XRLBT(Left) board



Top of YDRVBT(Top) board



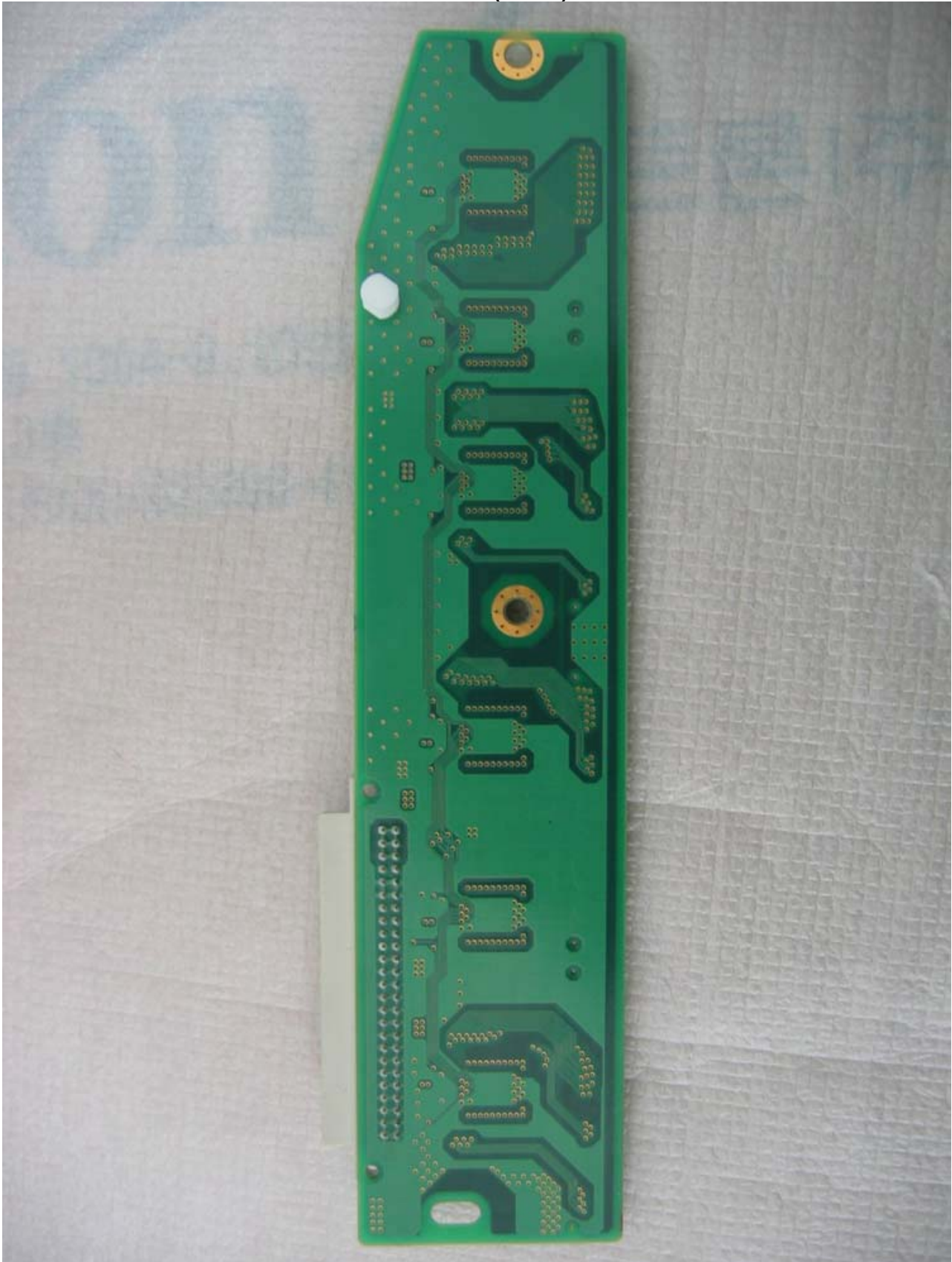
Bottom of YDRVBT(Top) board



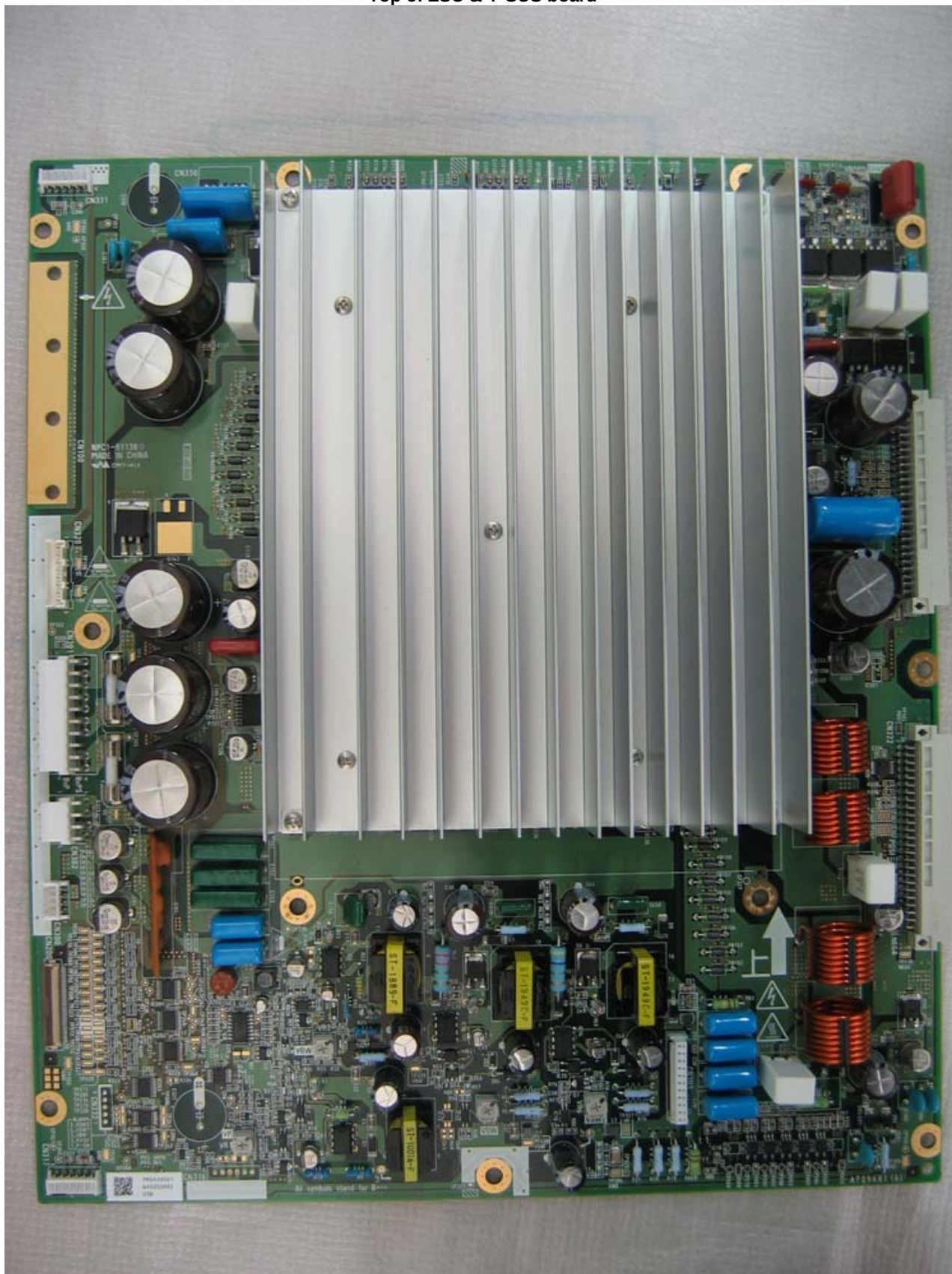
Top of YDRVBT(Bottom) board



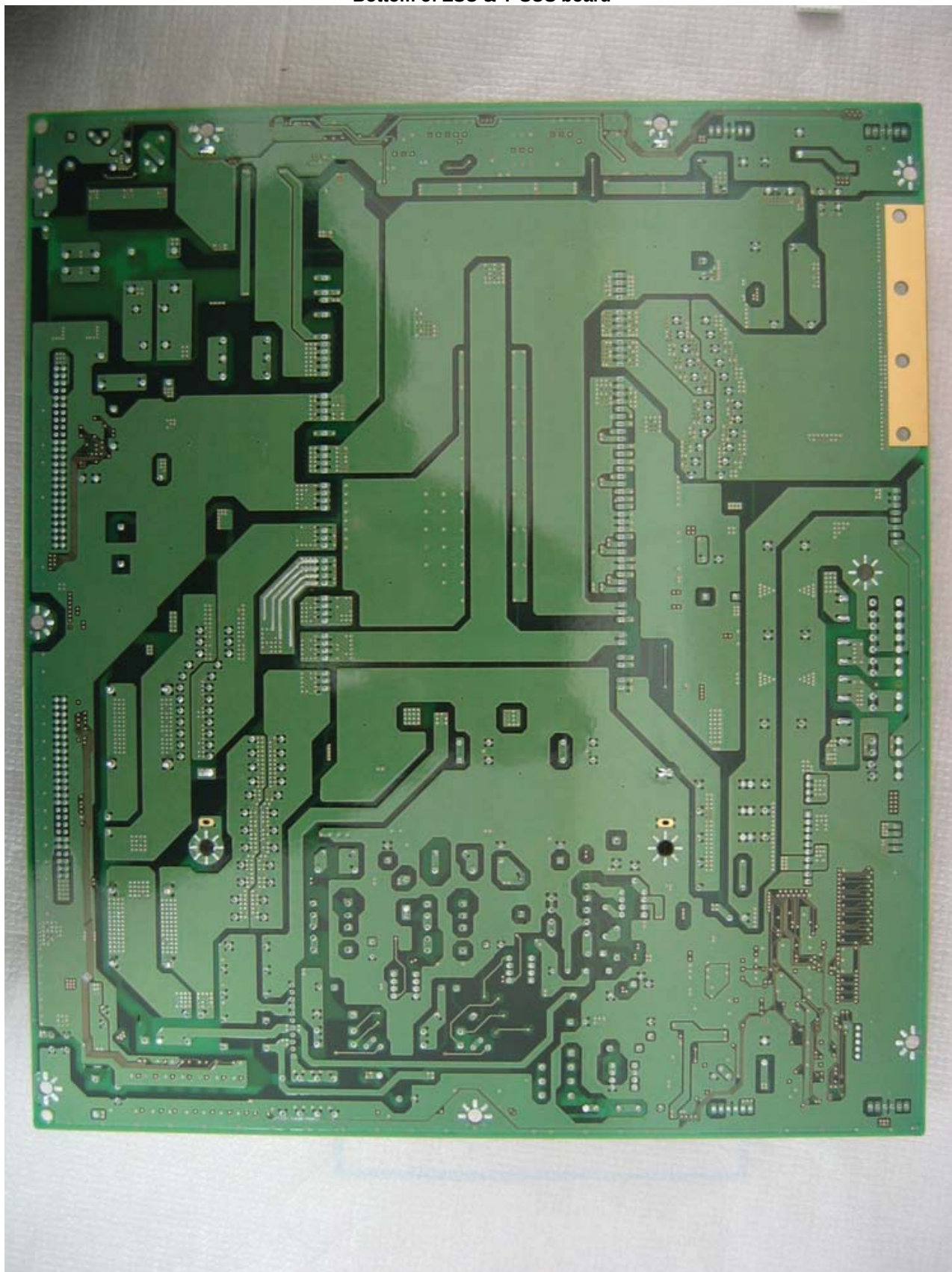
Bottom of YDRVBT(Bottom) board



Top of ZSU & Y-SUS board



Bottom of ZSU & Y-SUS board



PDP Panel label

