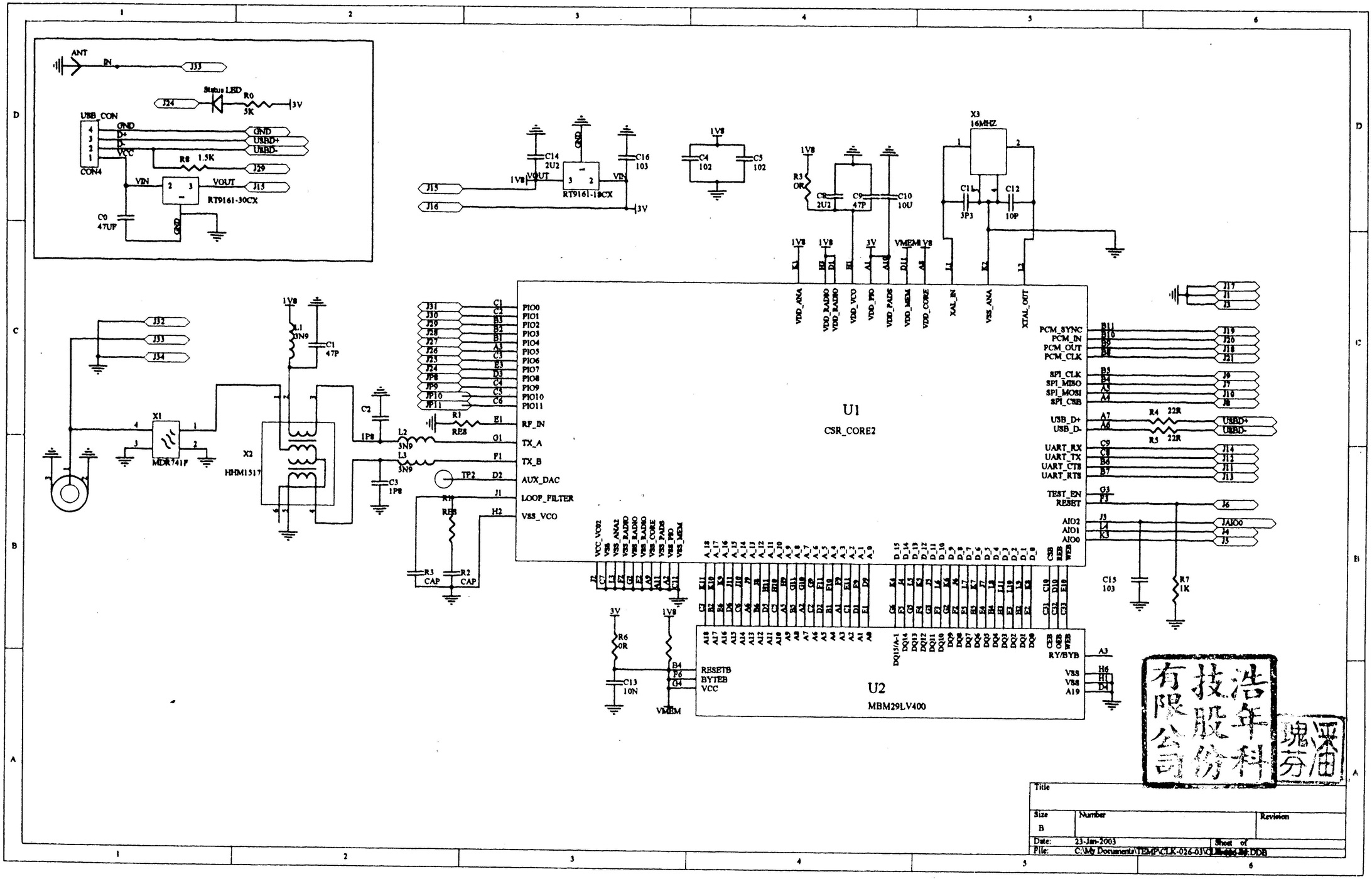
ANT
IN
J33
Status LED
R0
5K
3V
J24
USB_CON
GND
D+
D-
VCC
CON4
GND
USBD+
USBD-
R8 1.5K
J29
VIN
VOUT
J15
RT9161-30CX
C0
47UF
C14
2U2
VOUT
VIN
RT9161-18CX
C16
103
J15
J16
C4
102
C5
102
X3
16MHZ
R3
0R
C8
2U2
C9
47P
C10
10U
C11
3P3
C12
10P
VDD_ANA
VDD_RADIO
VDD_VCO
VDD_PIO
VDD_PADS
VDD_MEM
VDD_CORE
XAL_IN
VSS_ANA
XTAL_OUT
PCM_SYNC
PCM_IN
PCM_OUT
PCM_CLK
SPI_CLK
SPI_MISO
SPI_MOSI
SPI_CSB
USB_D+
USB_D-
R4 22R
R5 22R
UART_RX
UART_TX
UART_CTS
UART_RTS
TEST_EN
RESET
AIO2
AIO1
AIO0
PIO0
PIO1
PIO2
PIO3
PIO4
PIO5
PIO6
PIO7
PIO8
PIO9
PIO10
PIO11
RF_IN
TX_A
TX_B
AUX_DAC
LOOP_FILTER
VSS_VCO
U1
CSR_CORE2
X1
MDR741F
X2
HHM1517
L1
3N9
C1
47P
C2
1P8
L2
3N9
L3
3N9
C3
1P8
TP2
R1
RES
R3
CAP
R2
CAP
C15
103
R7
1K
R6
0R
C13
10N
RESETB
BYTEB
VCC
RY/BYB
U2
MBM29LV400
浩年科技股份有限公司
Title
Size
B
Number
Revision
Date: 23-Jan-2003
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File: C:\My Documents\TEMP\CLK-026-03V