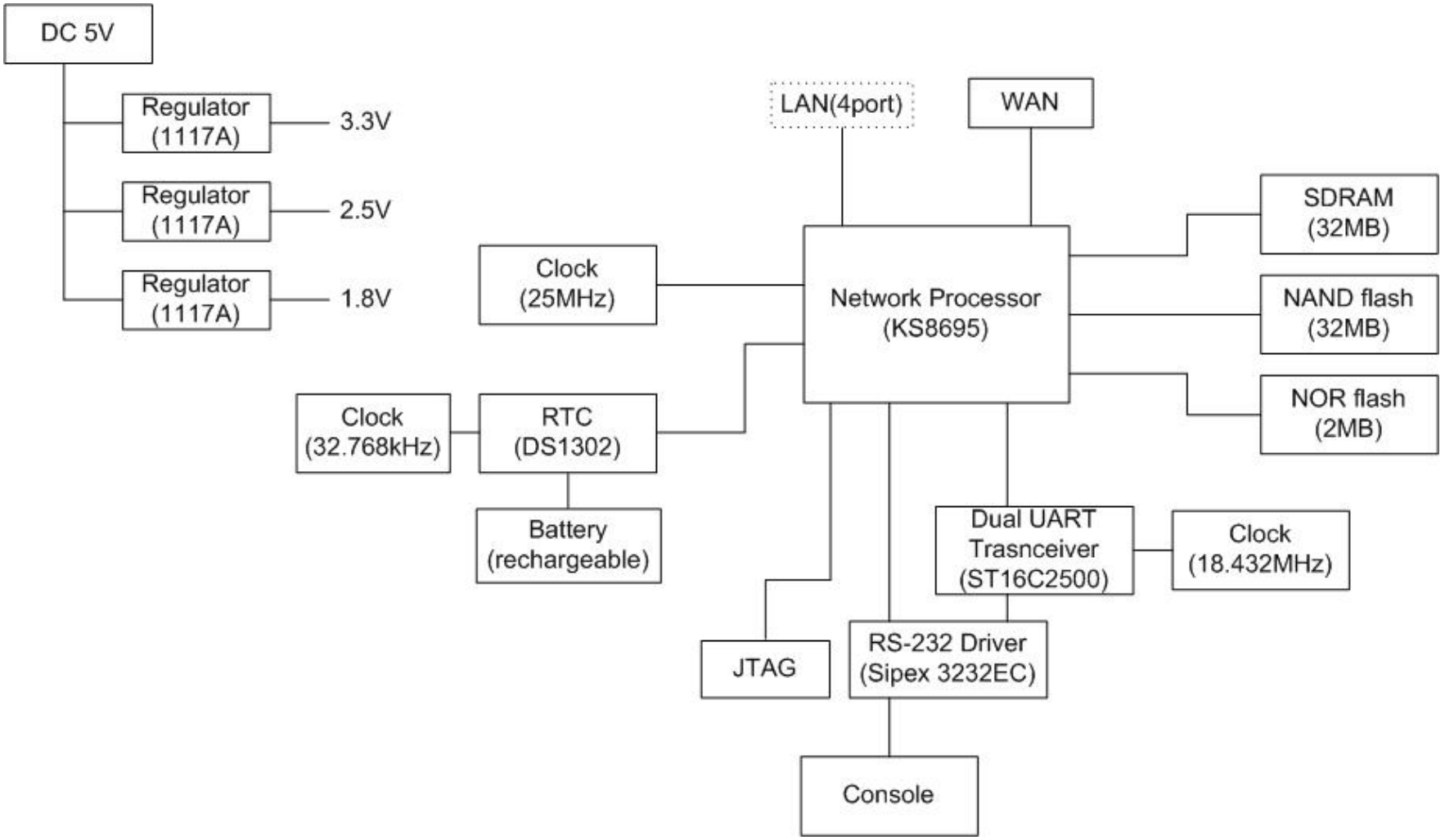
Block Diagram for EU-200BX



DC 5V

External DC 5V is used for system power supply. 1117A converts 5V to 3.3V, 2.5V and 1.8VDC

WAN

Provide network connection with EU-200BX and support 10/100Base with auto MDIX

Network Processor

Integrated hardware is as follows:

Layer2 managed switch

Five Ethernet transceiver and MAC

ARM922T processor with MMU and I/D cache

JTAG debugging interface

Shared programmable 8/16/32bit data bus and 22bit address bus with up to 64MB memory space.

NAND flash

NAND flash is used for system disk to store user application and configuration files. SNMP daemon stored in this space interpret get/set message from WAN, and call a routine corresponding message. Another role of SNMP daemon is gathering information and sending response message from/to stations under its control.

NOR flash :

Boot loader and Linux kernel are stored in NOR flash.

Dual UART Transceiver

Dual UART Transceiver provides the user with the capability to bidirectionally transfer information between an external CPU and an external serial device. It supports channel selection by chip select pin.

Console

Connecting serial cable from console port of EU-200BX to PC, Linux kernel boot time messages can be monitored. After the boot, a shell prompt is displayed for accepting Linux shell command.

JTAG

JTAG interface is a standard 20-pin connector for the Multi-ICE. It is possible to Download code to a flash, and debug using this interface.

RTC

RTC is operated by DC 3.3V power and 32.768kHz clock input. Real time clock counts seconds, minutes, hours, date of the month, month, day of the week, and year with leap year compensation valid up to 2100. Interfacing the DS1302 with network processor is synchronous serial communication.

Battery

Backup power supply for RTC.

Clock

25MHz : Network processor

18.43MHz : Dual UART Transceiver

32.768kHz : RTC