

About channel setting

Channel of FX-DS540-MPCI4WAP

At first a driver of FX-DS540-MPCI4WAP reads a value of a regulatory domain written in an EEPROM of the FX-DS540-MPCI4WAP inside.

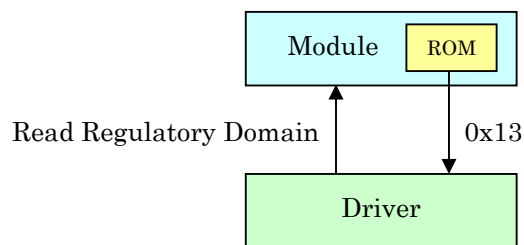


Figure 1 : Read Regulatory Domain

The channel which can use a value of a regulatory domain for the cause is decided.
There is the following meaning in value 0x13 of a regulatory domain of FX-DS540-MPCI4WAP.

Table 1 : Supported Channel

Supported Channel	IEEE802.11b/g :	
	1-11ch	(2412-2462MHz)
	IEEE802.11a :	
	36-48ch	(5180-5240MHz)
	149-165ch	(5745-5825MHz)

A user cannot change a value of a regulatory domain so that it is written in an EEPROM.