



A			14-May-2002	
REV	DESCRIPTIONS		DATE	APPROVED
DWG TITLE: Block Diagram of WP221P-X				
	DRAWN	JACK	DATE	14-May-2002
	CHECKED	JACK	DATE	14-May-2002
	APPROVED		DATE	
Size	B	Sheet	1 of 1	File Name: D:\wlan\wlan\WP221P\FCC\temp\方塊圖.Ddb - Documents\Sheet1.Sch

External Memory Interface

Internal Memory

ARM7TDMI

Decoder/Arbiter/Bridge

Common Memory Interface Controller

Wireless Physical Attachment Interface (PAI)

Timers

PCMCIA Interface

Interrupt Controller

CCA

Wired Equivalence Privacy(WEP)

RF3000

Demodulator

Modulator

Control Port

802.11 Preamble/Header

Q-IN DAC

I-IN DAC

TXVGC DAC

I-OUT DAC

Q-OUT DAC

RXVGC DAC

CCA

RX VGC

LNA GS

802.11 Preamble/Header

RX Data

RX RDY

DATA CLK

CSb/ADO

TX-RDY

TX-Data

RX-PE

TX-PE

802.11 Preamble/Header

Tx Length

Tx Signal

Service

MCLK 44MHz

RF2948

IF Amp

Base Band Amp. Active Selectable LPF (fc=1MHz to 35 MHz) 0 ~ 30 dB Gain

Filter

Filter

Selectable LPF

RF

IF

TX

RX

RX Q

RX I

REF_1

TX I

TX Q

TX VGC

TXVGC DAC

I-OUT DAC

Q-OUT DAC

Q-IN DAC

I-IN DAC

Demodulator

Control Port

802.11 Preamble/Header

RX Data

RX RDY

DATA CLK

CSb/ADO

TX-RDY

TX-Data

RX-PE

TX-PE

802.11 Preamble/Header

Tx Length

Tx Signal

Service

MCLK 44MHz

SI4126

IF VCO

RF VCO

IF

RF

RF2494

SAW

FC=3741MHz

FC=2442MHz

Filter

FC=2442MHz

Diversity switch

T/R switch

RF2189