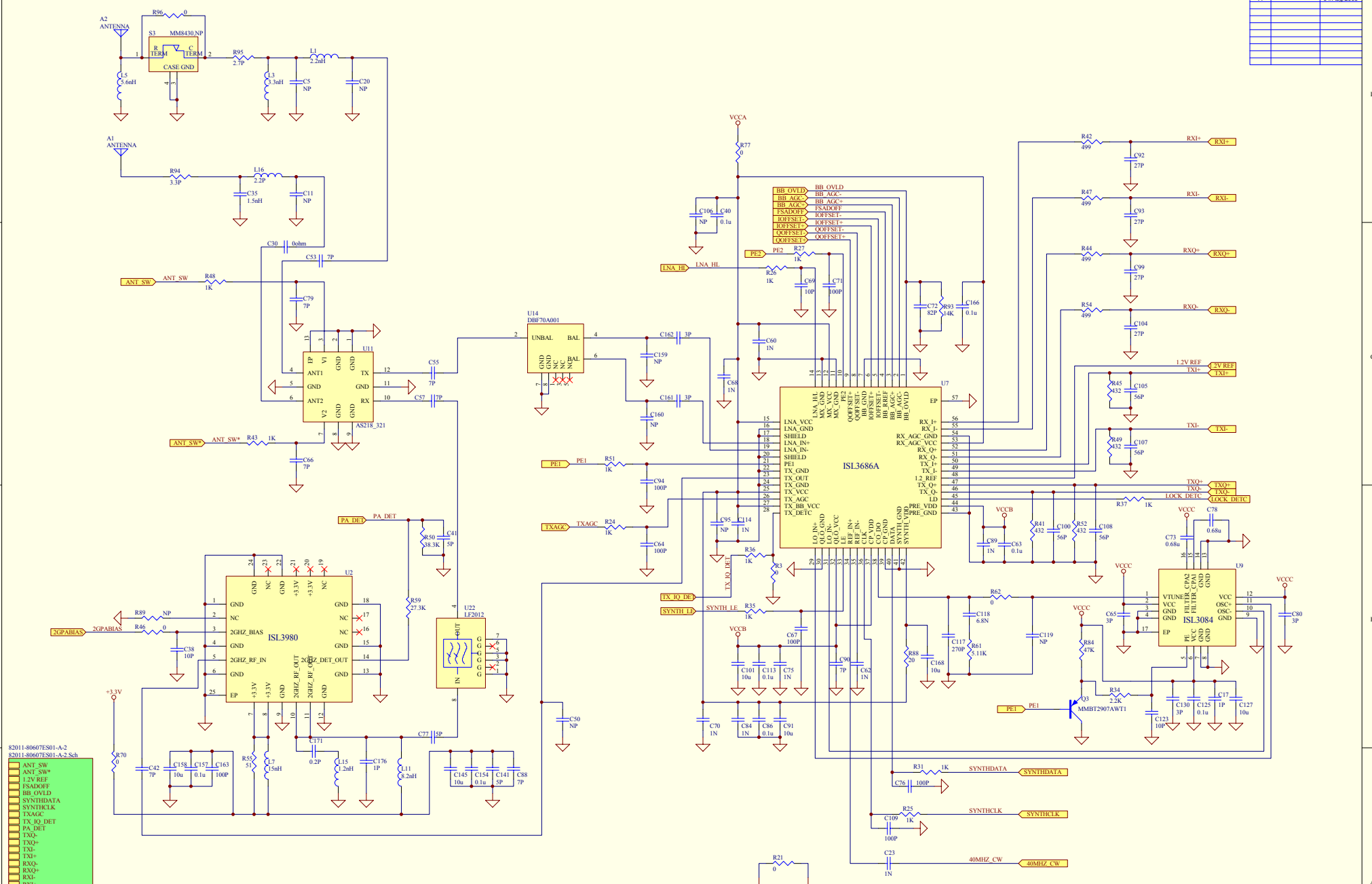


REVISION RECORD		
REV	ECO	DATE
A		04/Aug/2003



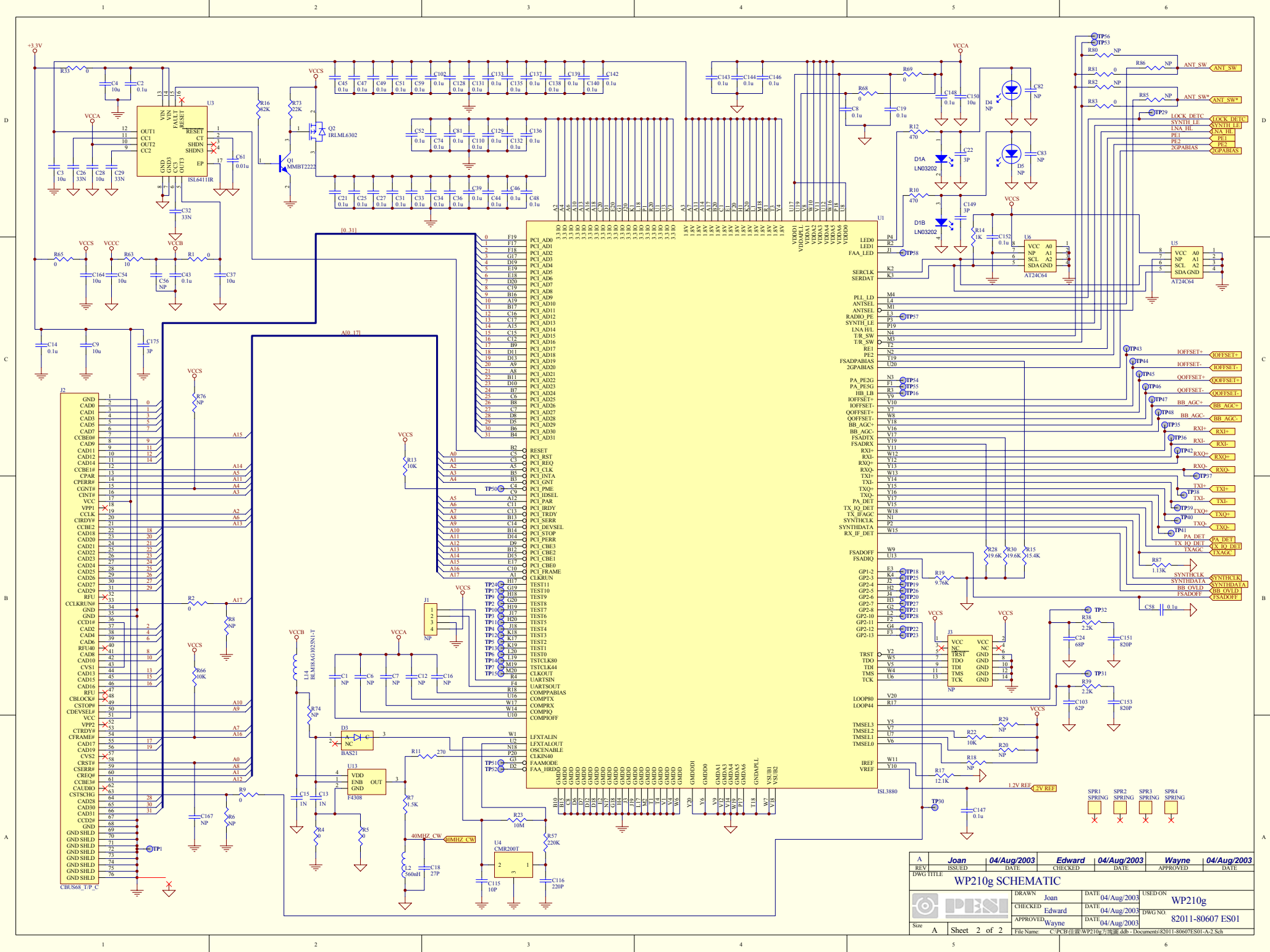
- 82011-80607ES01-A-2
82011-80607ES01-A-2 Sch
- ANT_SW
 - ANT_SW*
 - 1.2V REF
 - FSADOFF
 - BB_OVLD
 - SYNTHDATA
 - SYNTHCLK
 - TXAGC
 - TX_IO_DET
 - PA_DET
 - TXQ+
 - TXI+
 - TX-
 - TXI-
 - RXQ+
 - RXL+
 - RXI+
 - BB_AGC
 - BB_AGC*
 - QOFFSET+
 - QOFFSET*
 - IOFFSET+
 - IOFFSET*
 - LOCK_DET
 - LOCK_DET*
 - SYNTH_LE
 - LNA_HL
 - PE1
 - PE2
 - 2GPABIAS
 - 40MHZ_CW

REV	ISSUED	DATE	CHECKED	DATE	APPROVED	DATE
A	Joan	04/Aug/2003	Edward	04/Aug/2003	Wayne	04/Aug/2003

DWG TITLE: WP210g SCHEMATIC

DRAWN	Joan	DATE	04/Aug/2003	USED ON	WP210g
CHECKED	Edward	DATE	04/Aug/2003	DWG NO.	82011-80607 ES01
APPROVED	Wayne	DATE	04/Aug/2003	File Name:	C:\PCB\佳源\WP210g方案\瑞星.dwg - Documents\82011-80607ES01-A-1 Sch

Size: A Sheet 1 of 2



GND	1	0
CADD0	2	1
CADD1	3	1
CADD2	4	3
CADD3	5	5
CADD5	7	7
CADD7	7	7
CCBE0#	8	9
CCAD9	9	9
CAD11	10	11
CAD12	10	11
CAD14	11	14
CCBE1#	13	A5
CPAR	14	A4
CPERR#	15	A3
CNT#	16	A1
VCC	17	A2
VPP1	19	A6
CLK	20	A11
CIRDY#	21	A1
CCBE2	22	18
CAD18	23	20
CAD20	24	21
CAD21	25	22
CAD22	26	23
CAD23	27	24
CAD24	28	25
CAD25	29	26
CAD26	30	27
CAD27	31	28
CAD29	31	29
RFU	32	30
CCLKRUN#	34	A9
GND	35	A9
CAD34	36	2
CAD37	37	2
CAD38	38	4
CAD4	39	8
CAD6	41	8
RFU40	40	10
CAD8	42	10
CAD10	43	13
CVS1	44	13
CAD13	45	15
CAD15	46	16
CAD16	47	16
RFU	48	AT
CBLOCK	49	A10
CSTOP#	50	A9
CDESEL#	51	A7
VCC	52	A7
VPP2	53	A7
CTRDY#	54	A16
CFRAME#	55	17
CAD17	56	17
CAD19	57	15
CVS2	57	15
CRST#	59	A0
CSERR#	60	A1
CREQ#	60	A1
CCBE3#	61	A11
CALD0	62	28
CAD28	64	30
CAD10	65	30
CAD31	66	31
CCD2	67	31
CCD2	68	31
GND	69	31
GND SHLD	70	31
GND SHLD	71	31
GND SHLD	72	31
GND SHLD	73	31
GND SHLD	74	31
GND SHLD	75	31
GND SHLD	76	31
CBS68_TF_C	76	31

REV	Joan	04/Aug/2003	Edward	04/Aug/2003	Wayne	04/Aug/2003
DWG TITLE	WP210g SCHEMATIC					
Size	A	Sheet	2	of	2	

DRAWN	Joan	DATE	04/Aug/2003	USED ON	WP210g
CHECKED	Edward	DATE	04/Aug/2003	DWG NO.	82011-80607 ES01
APPROVED	Wayne	DATE	04/Aug/2003	File Name	C:\PCB\佳能\WP210g\方案\WP210g_1-80607ES01-A-2.Sch