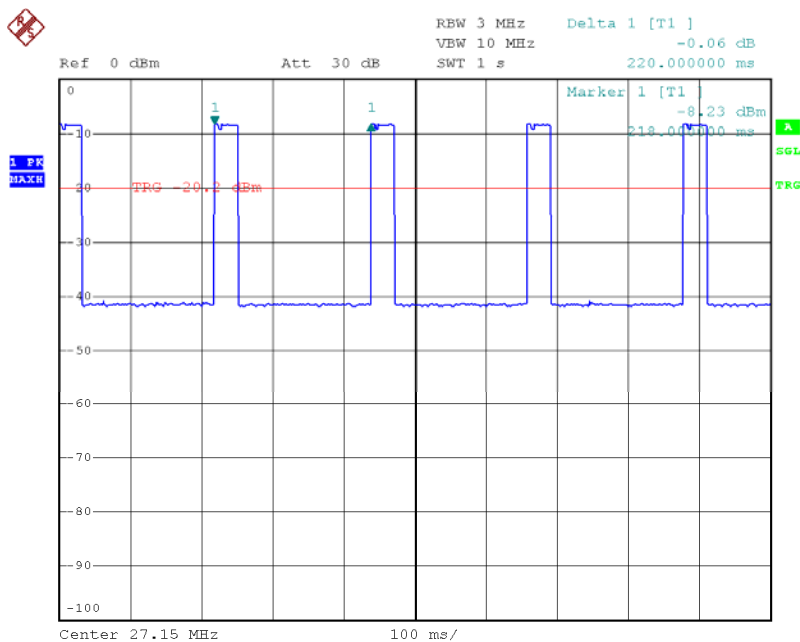
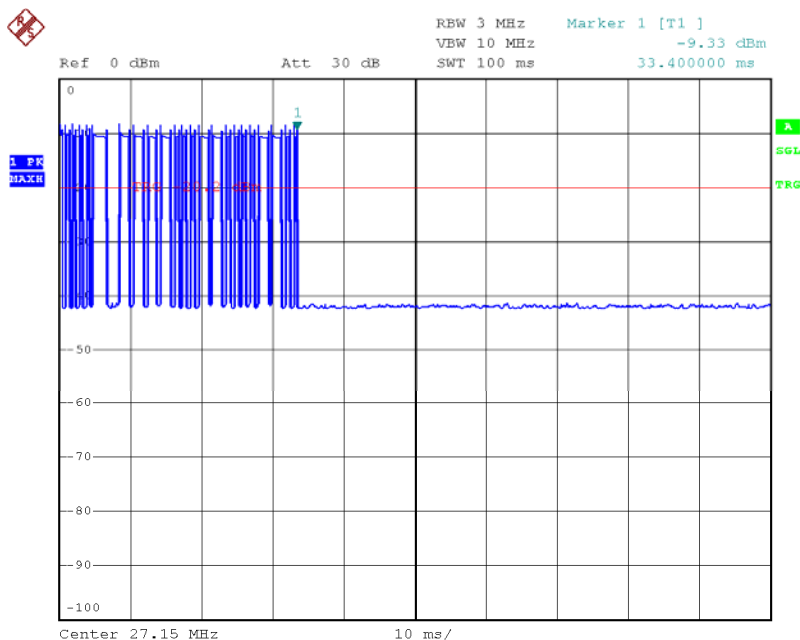


INTERTEK TESTING SERVICES

Report No.: HK10041112-1
Sample No.: 1/2



Date: 28.APR.2010 14:05:28

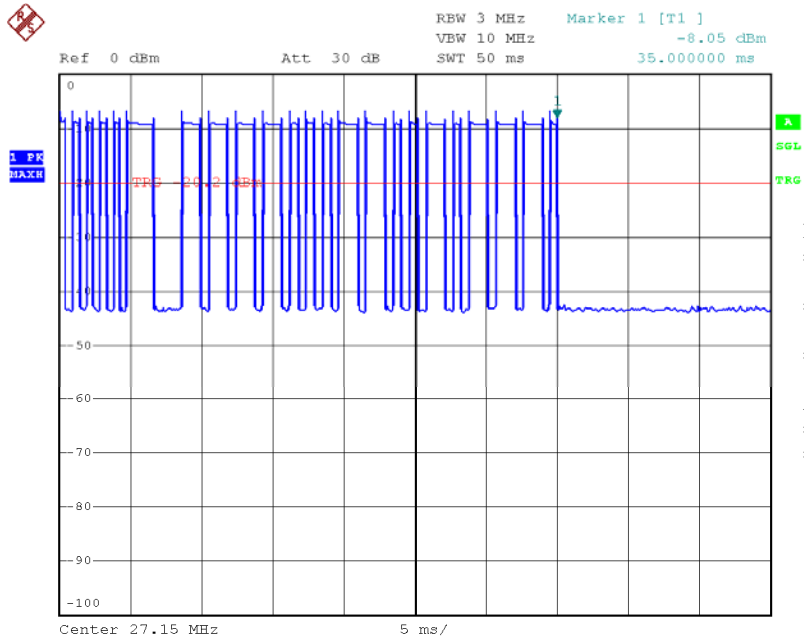


Date: 28.APR.2010 14:06:43

Bandwidth / Bandedge Plot – Pursuant to FCC Part 15 Section 15. 227 Requirement (Pass/Fail)

INTERTEK TESTING SERVICES

Report No.: HK10041112-1
Sample No.: 1/2



Duty Cycle

$$= \frac{5 \times 0.48 + 1 \times 1.92 + 10 \times 1.32 + 7 \times 0.56}{100} \text{ ms}$$

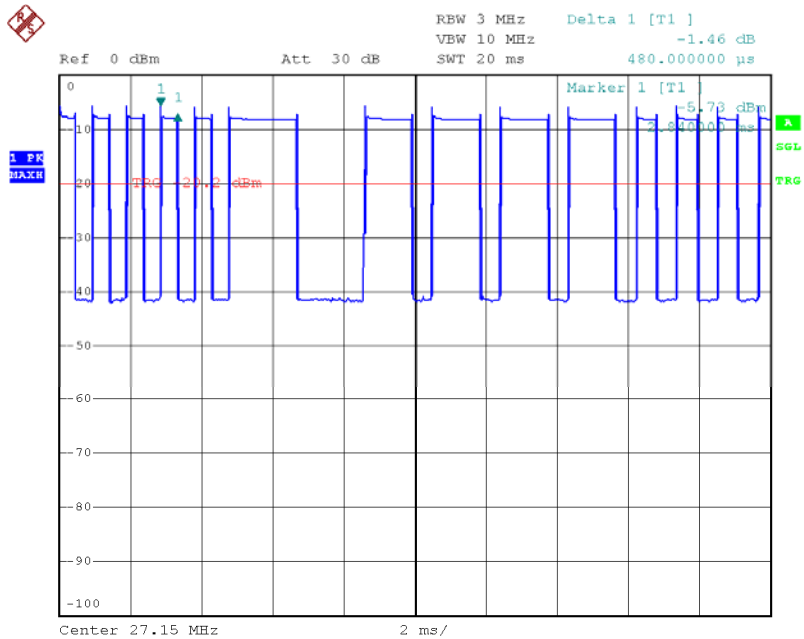
$$= \frac{21.44}{100}$$

$$= 0.2144$$
Average Factor

$$= 20 \log(0.2144)$$

$$= -13.3$$

Date: 28.APR.2010 14:10:52

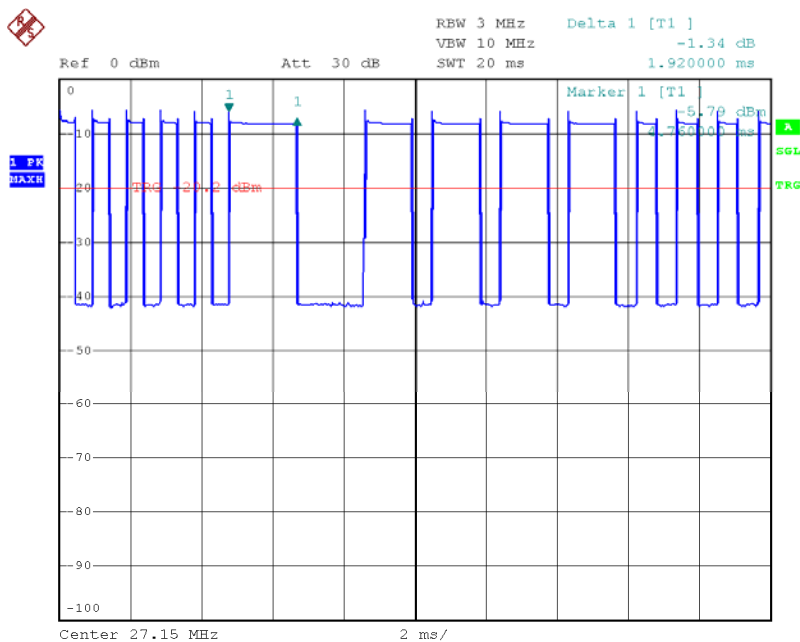


Date: 28.APR.2010 14:11:59

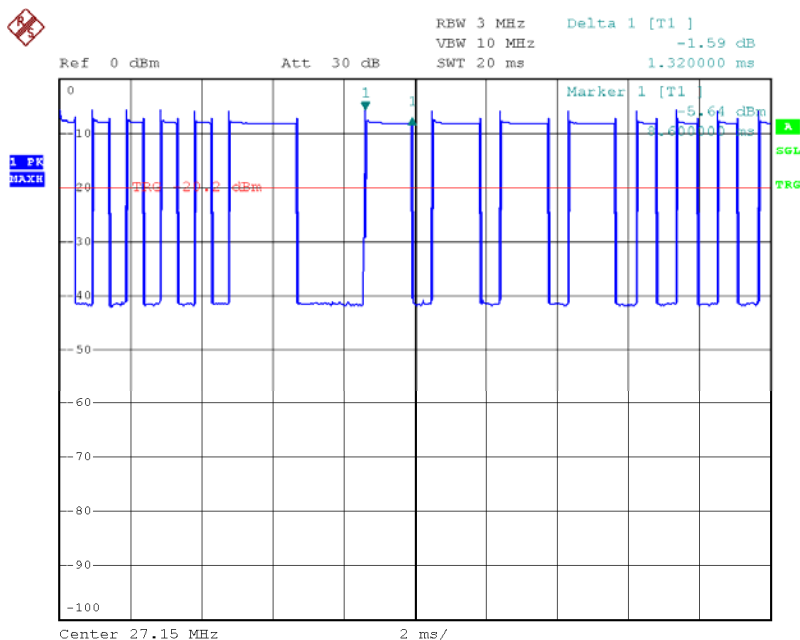
Bandwidth / Bandedge Plot – Pursuant to FCC Part 15 Section 15. 227 Requirement (Pass)

INTERTEK TESTING SERVICES

Report No.: HK10041112-1
Sample No.: 1/2



Date: 28.APR.2010 14:12:29

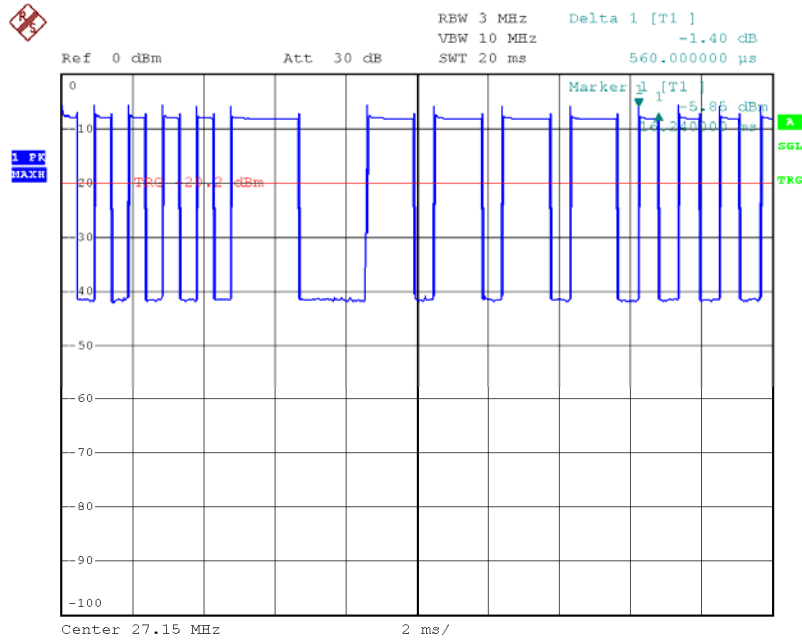


Date: 28.APR.2010 14:12:58

Bandwidth / Bandedge Plot – Pursuant to FCC Part 15 Section 15. 227 Requirement (Pass)

INTERTEK TESTING SERVICES

Report No.: HK10041112-1
Sample No.: 1/2



Date: 28.APR.2010 14:13:28

Bandwidth / Bandedge Plot – Pursuant to FCC Part 15 Section 15. 227 Requirement (Pass)