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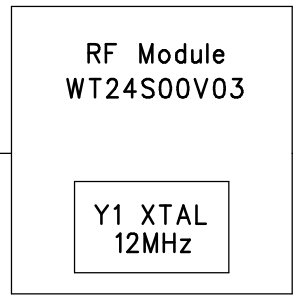
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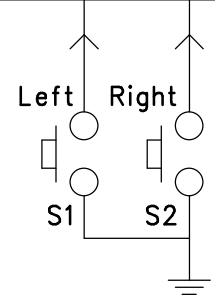
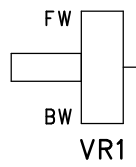
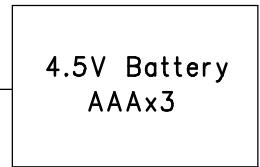
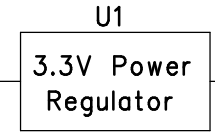
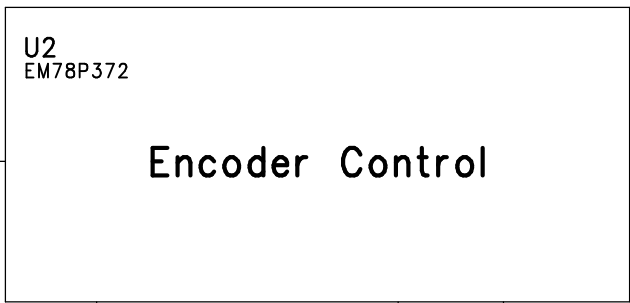
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LTR	ECO NO:	APPROVED:	DATE:

2404-2479MHz

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Module 1



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CHECKED:	DATED:	CODE:	SIZE:	DRAWING NO:		REV:	
		0	F	20426 Fast Five (2.4GHz TX Block).SCH		0	
QUALITY CONTROL:	DATED:	SCALE: 1 : 1		SHEET: 1 OF 1			

DRAWN:	DATED:
So Jin Oung	07-Dec-12
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
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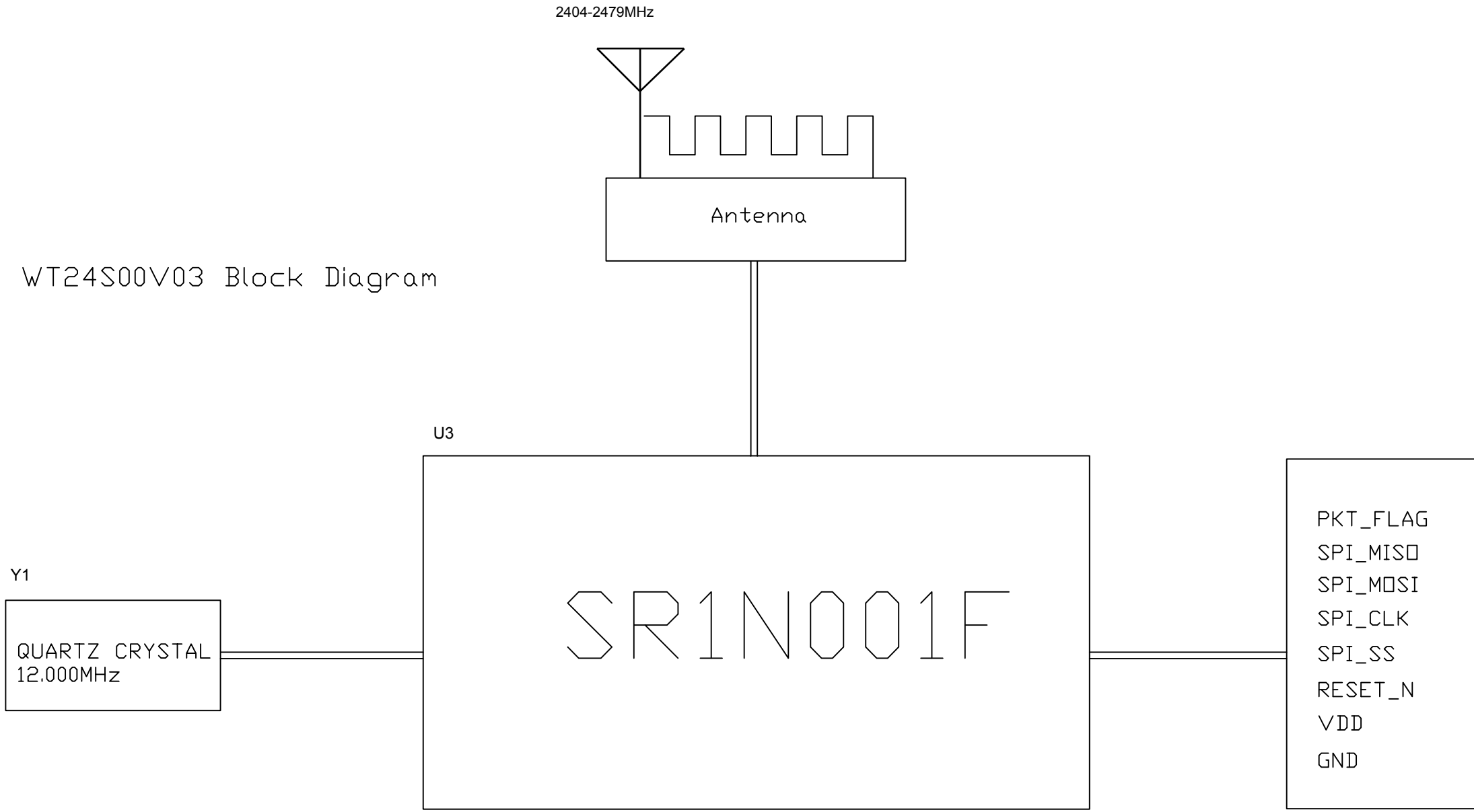
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WT24S00V03 Block Diagram



1.3 Bloc Diagram

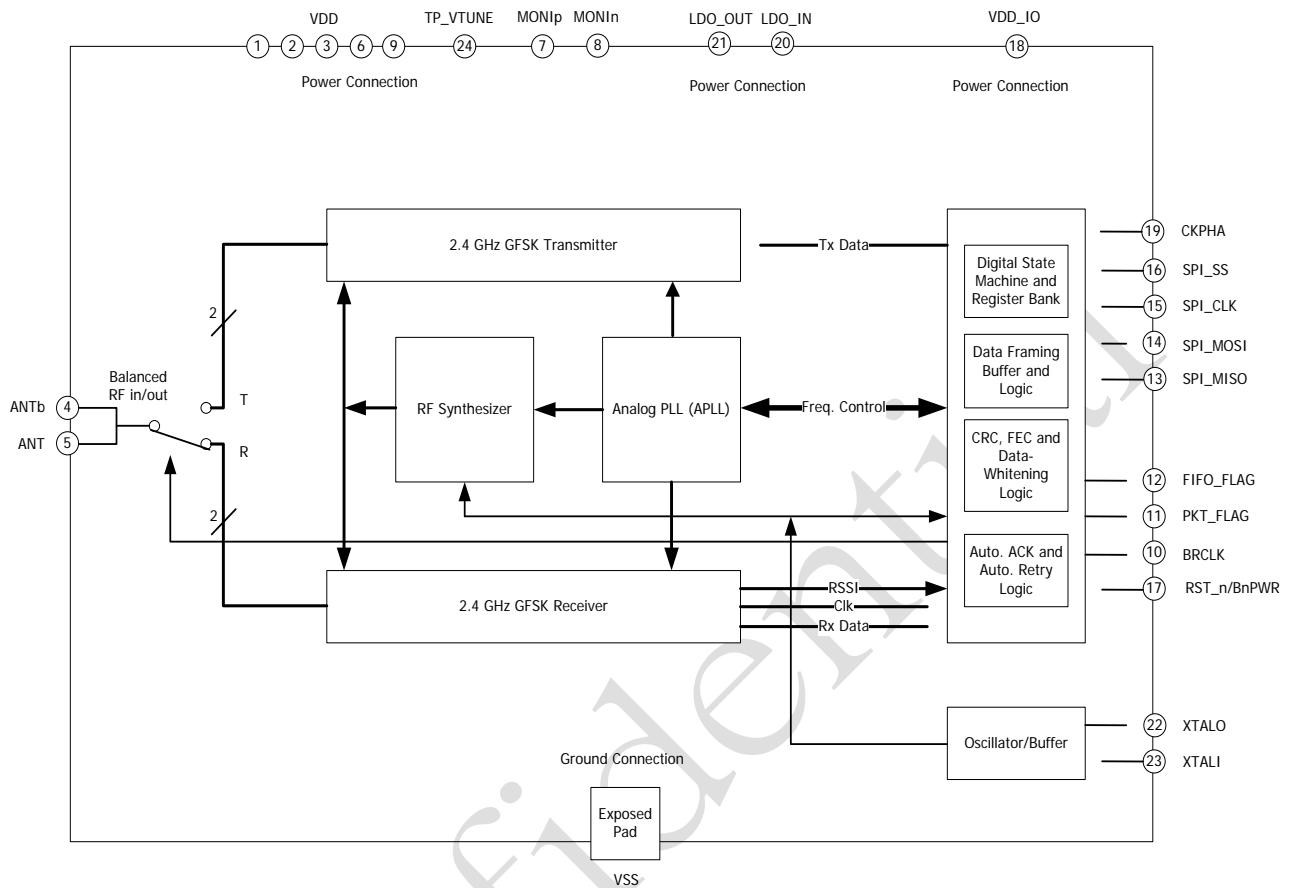


Figure 1-1 SR1N001F Block Diagram (QFN24 Package)