

## Annex B Photographs of the EUT

Photo documentation:

Photo 1: Interface board with test board and EUT

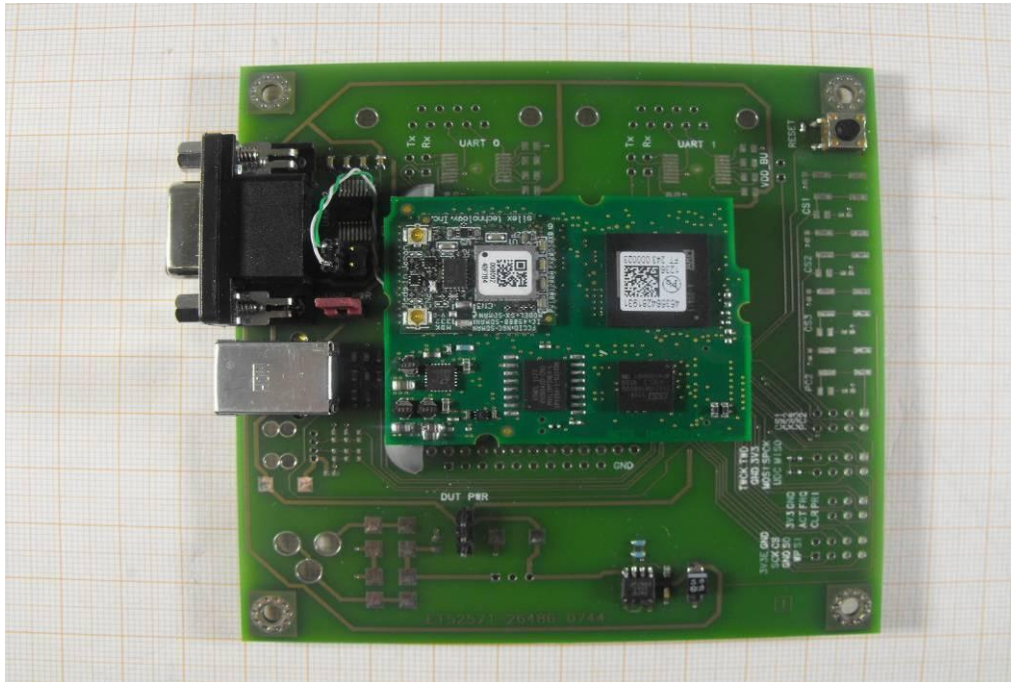


Photo 2: Interface board

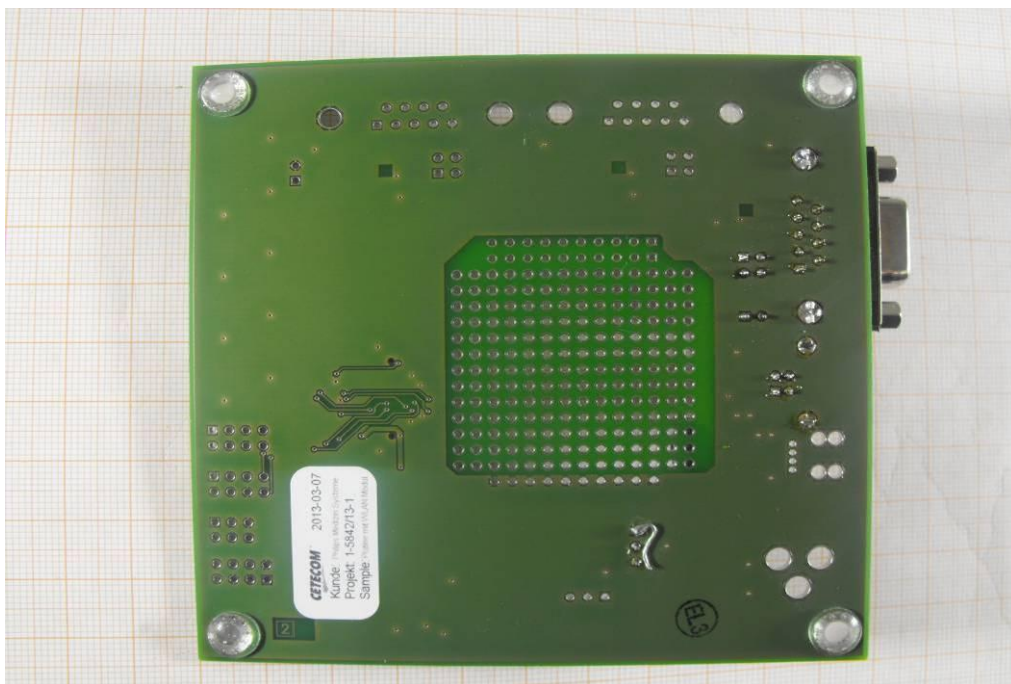


Photo 3: Interface board

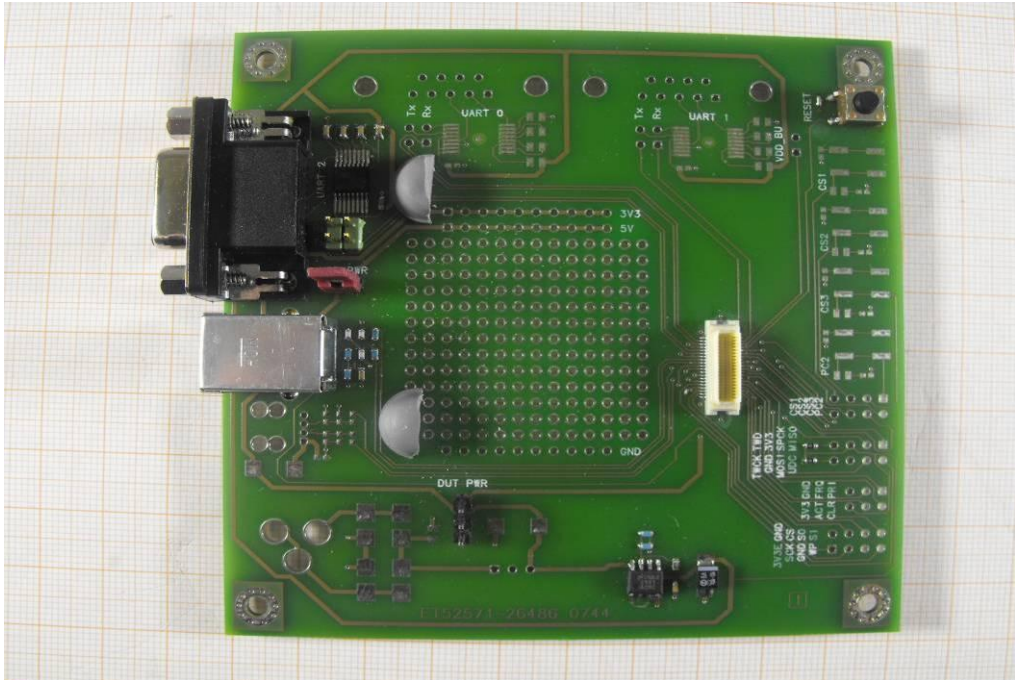


Photo 4: Testboard top view

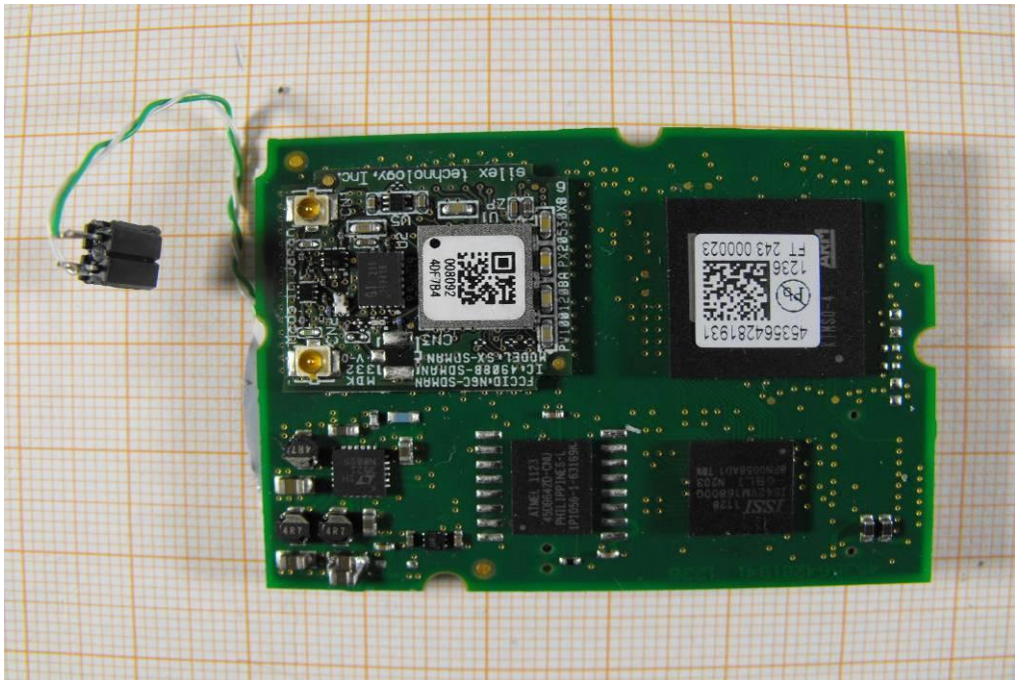


Photo 5: Testboard bottom view

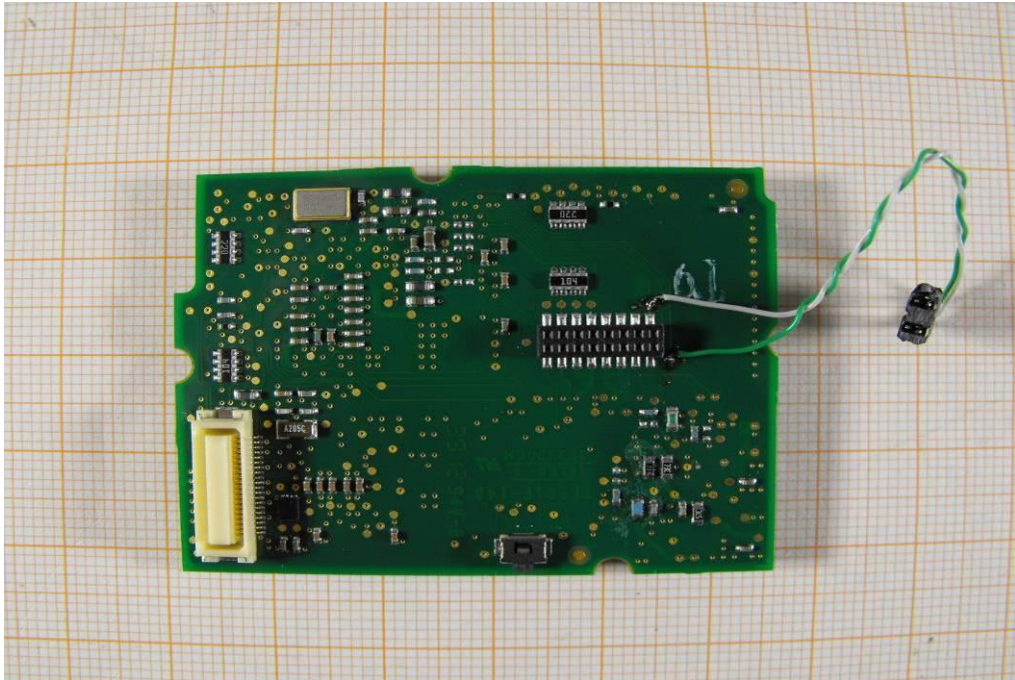


Photo 6: EUT

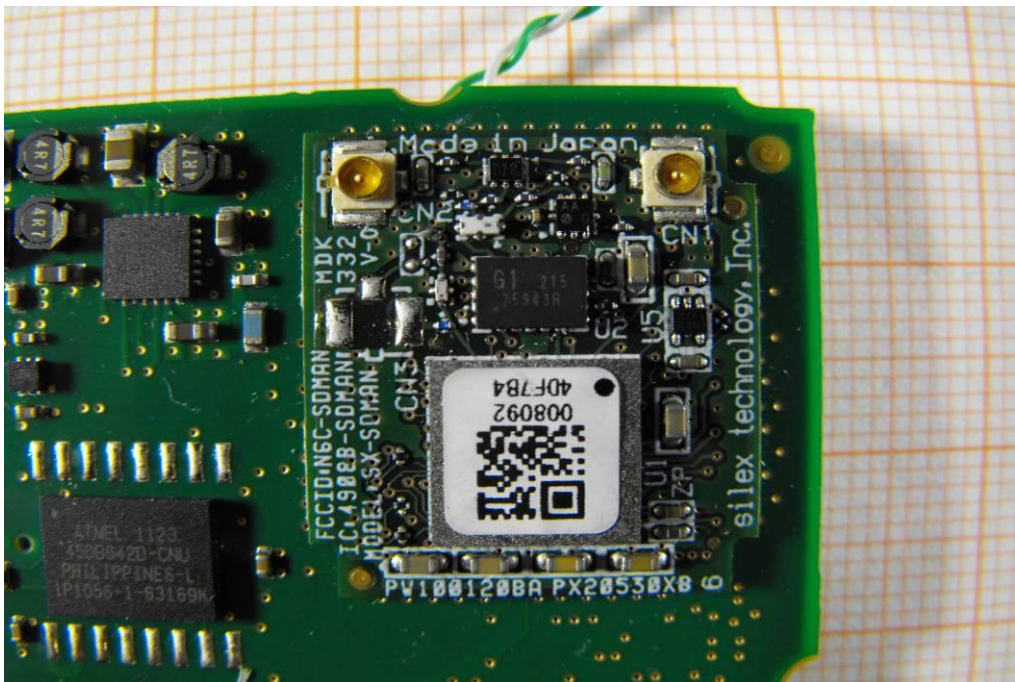


Photo 7: EUT with Ant M3002-66494

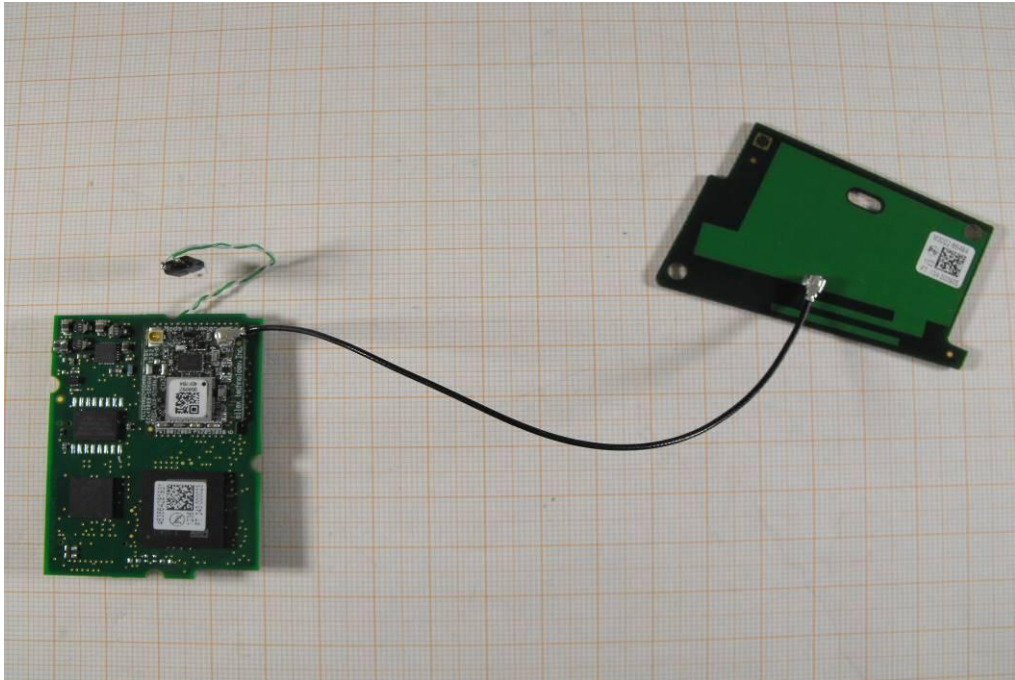


Photo 8: Ant M3002-66494

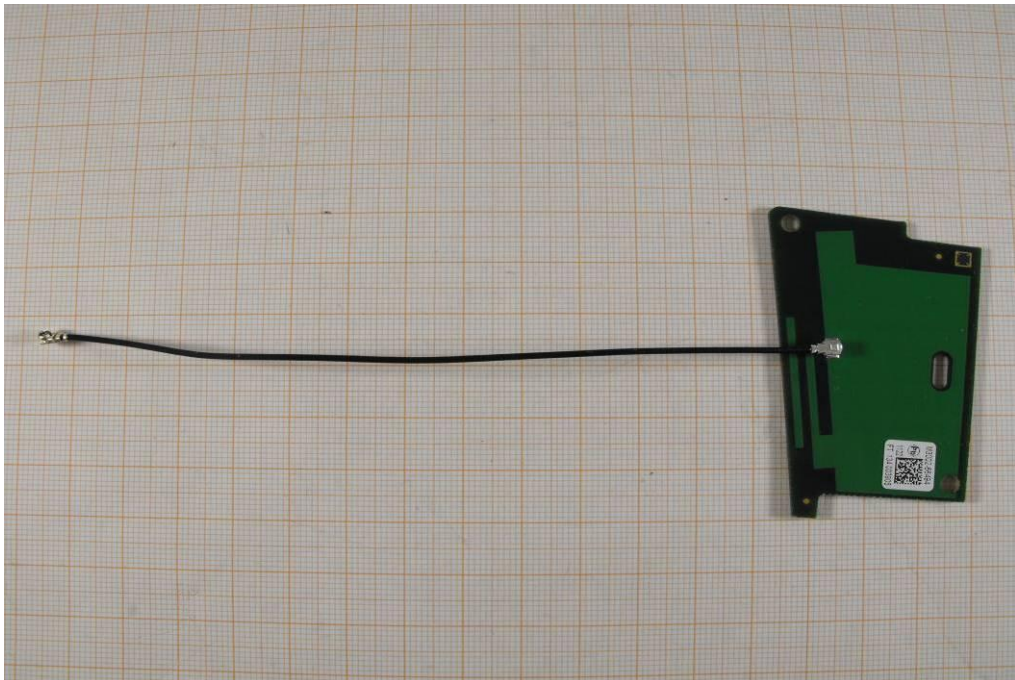


Photo 9: Ant M3002-66494

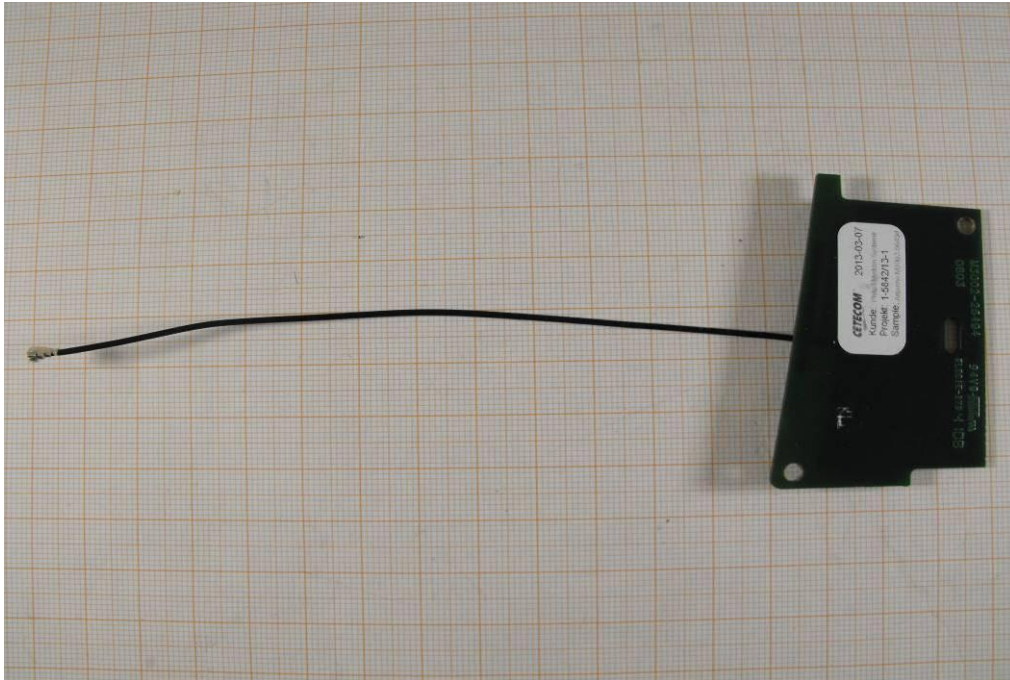


Photo 10: Ant M3002-66494



Photo 11: Ant M3002-66494

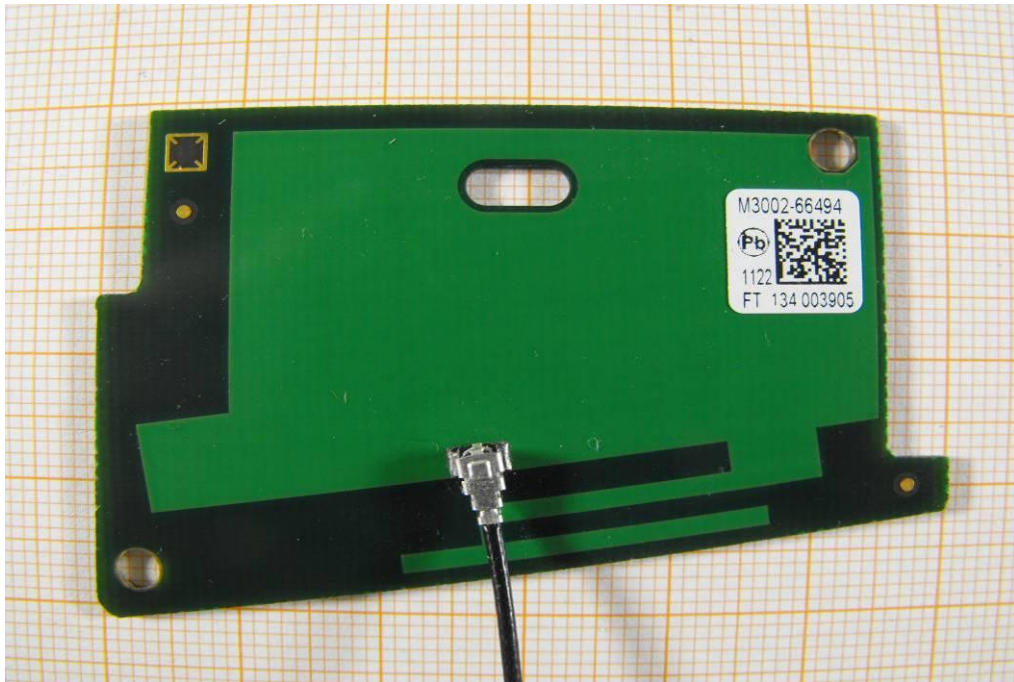


Photo 12: Ant 453564154611

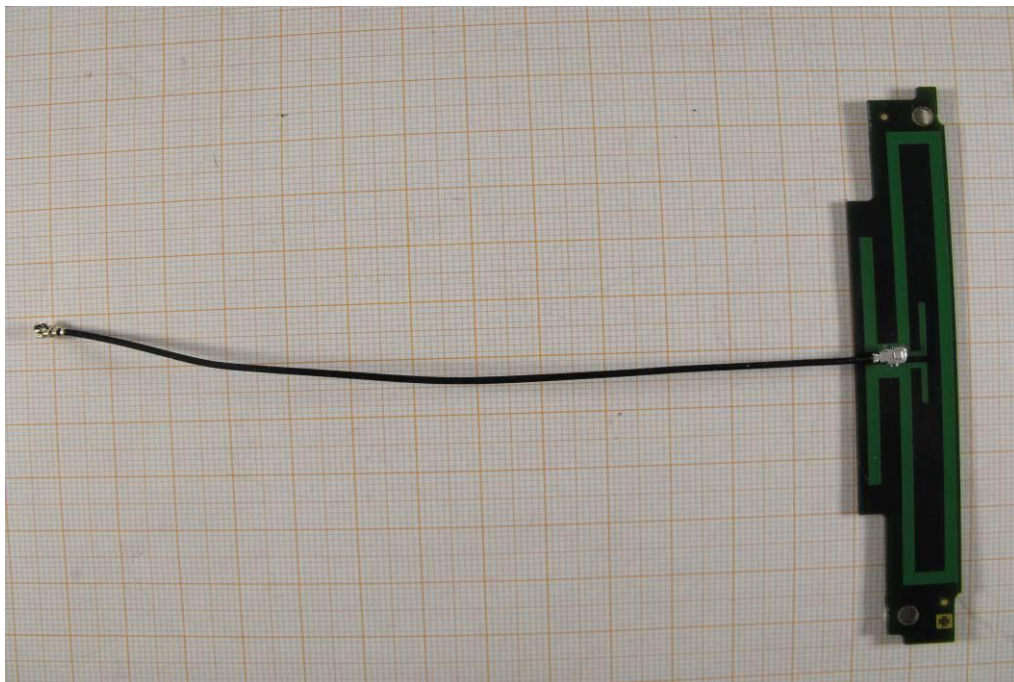


Photo 13: Ant 453564154611

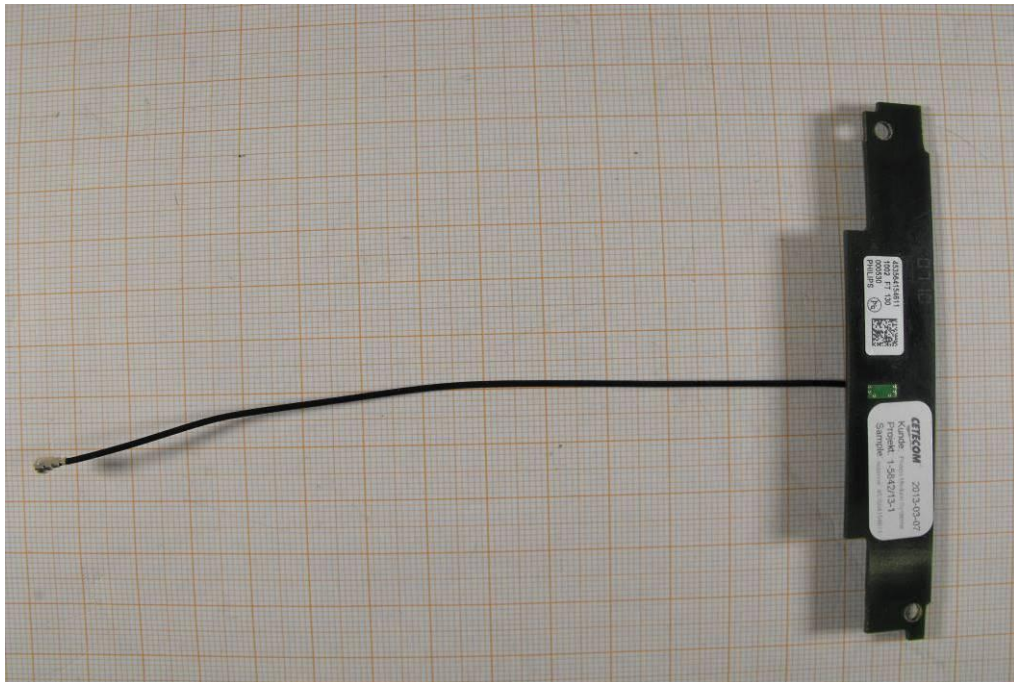


Photo 14: Ant 453564154611

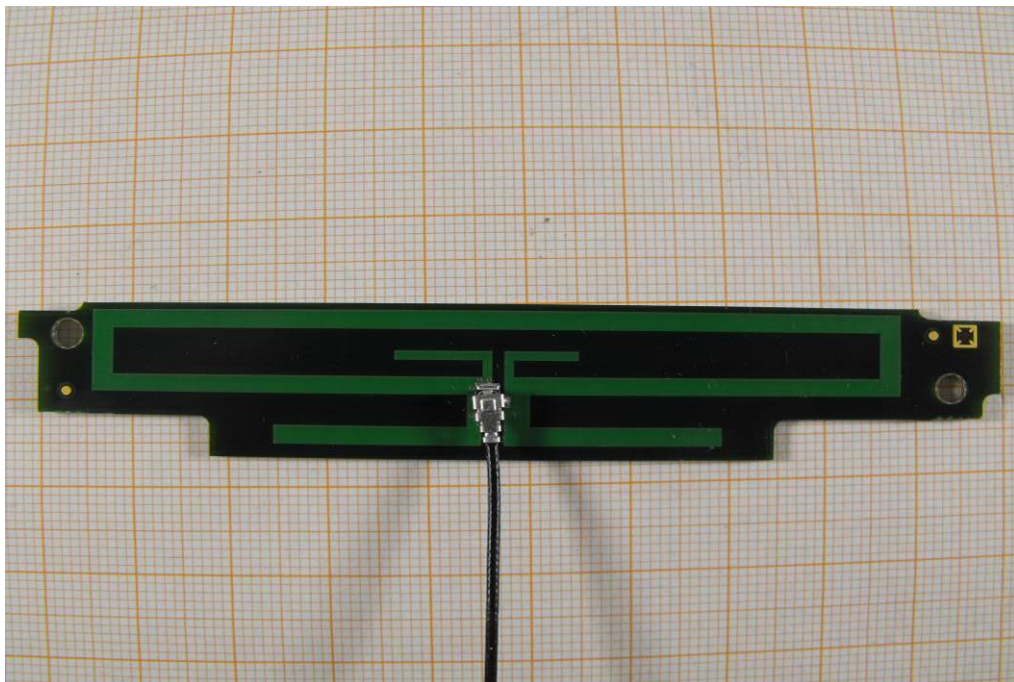


Photo 15: Ant 453564154611



Photo 16: Ant 453564175981

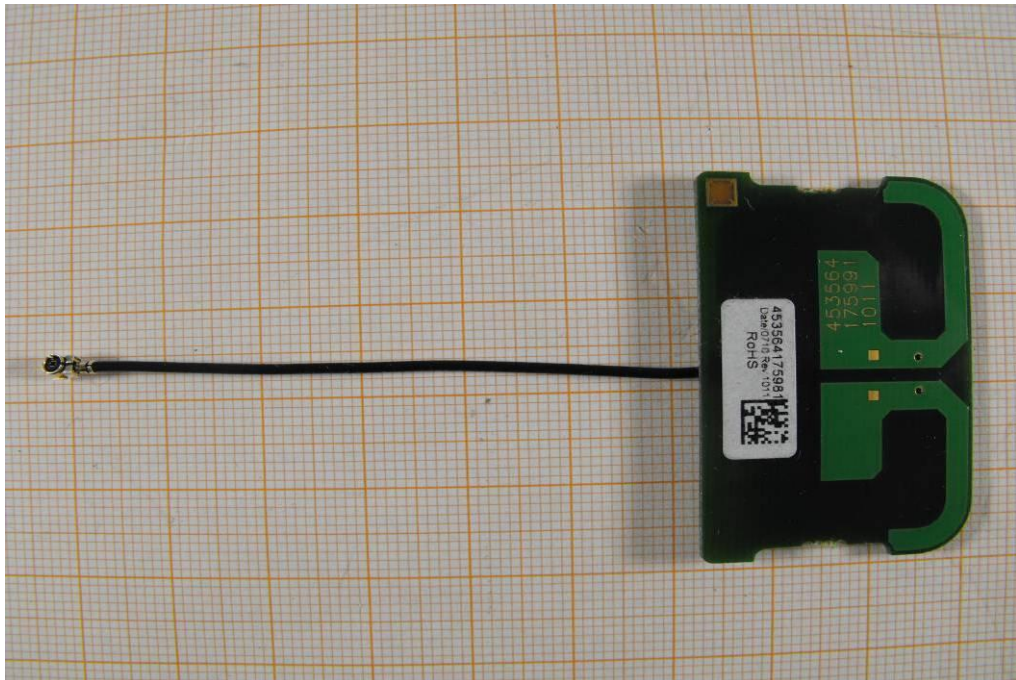




Photo 17: Ant 453564175981

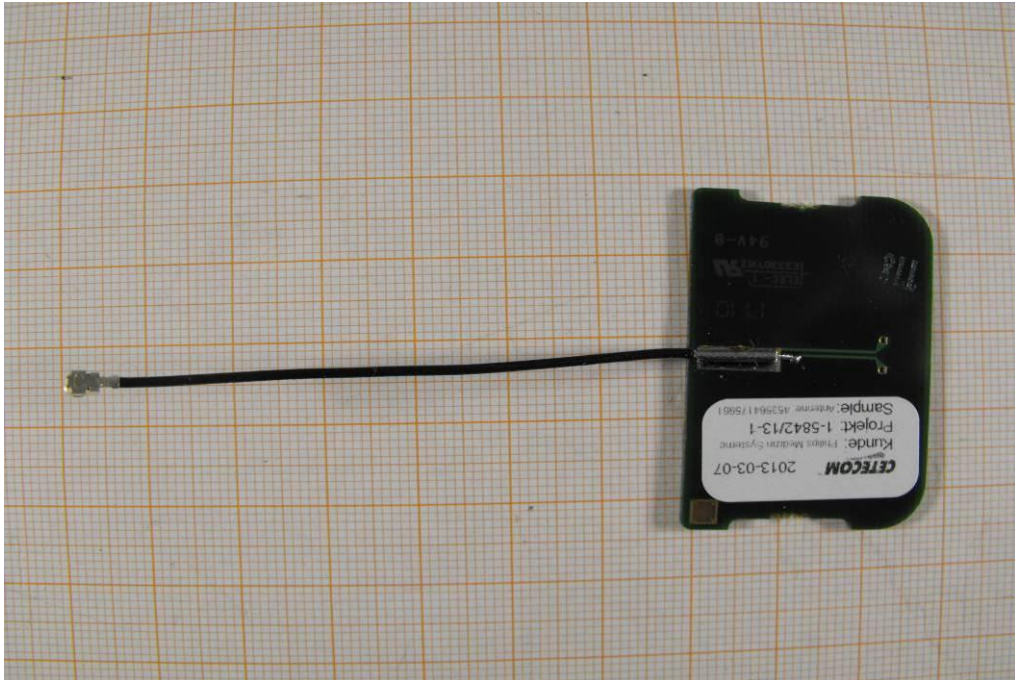


Photo 18: Ant 453564175981

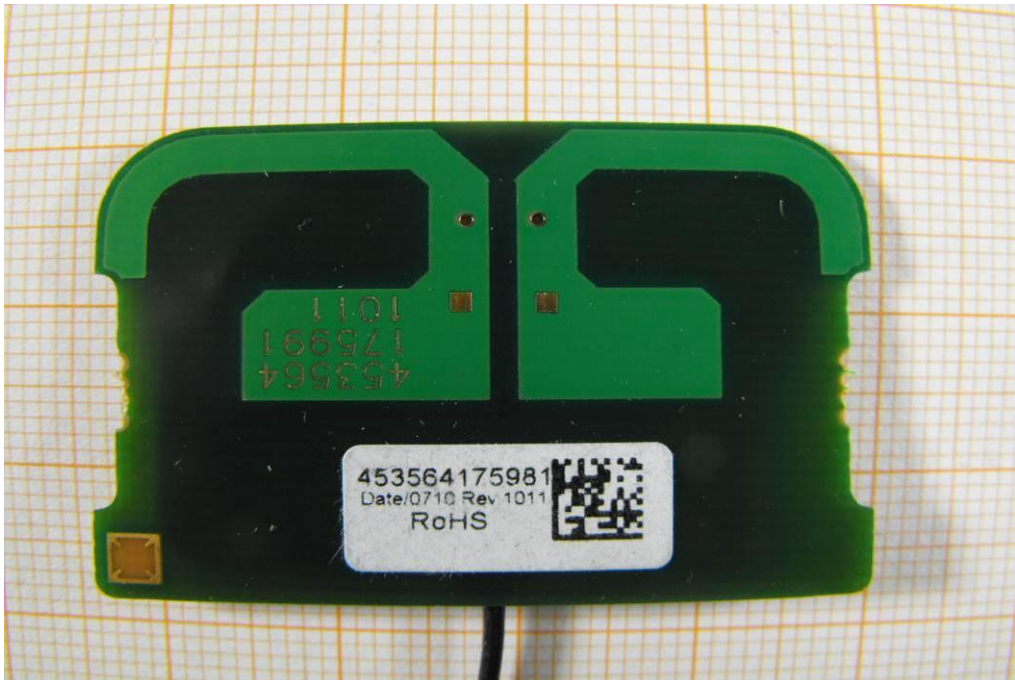


Photo 19: Ant 453564175981



Photo 20: Ant 453564271931

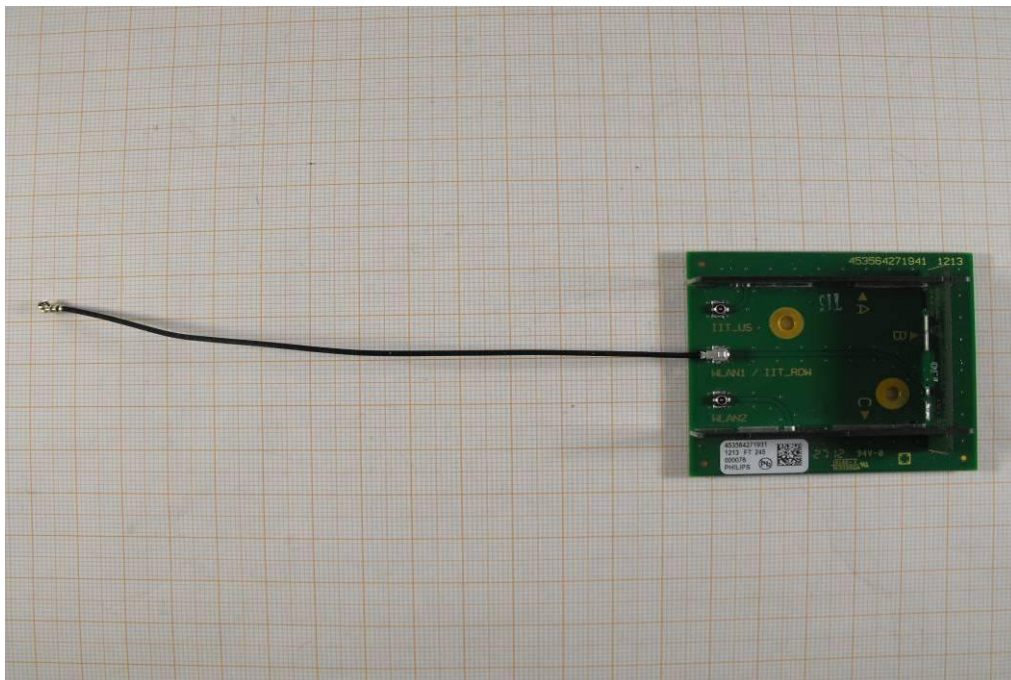


Photo 21: Ant 453564271931

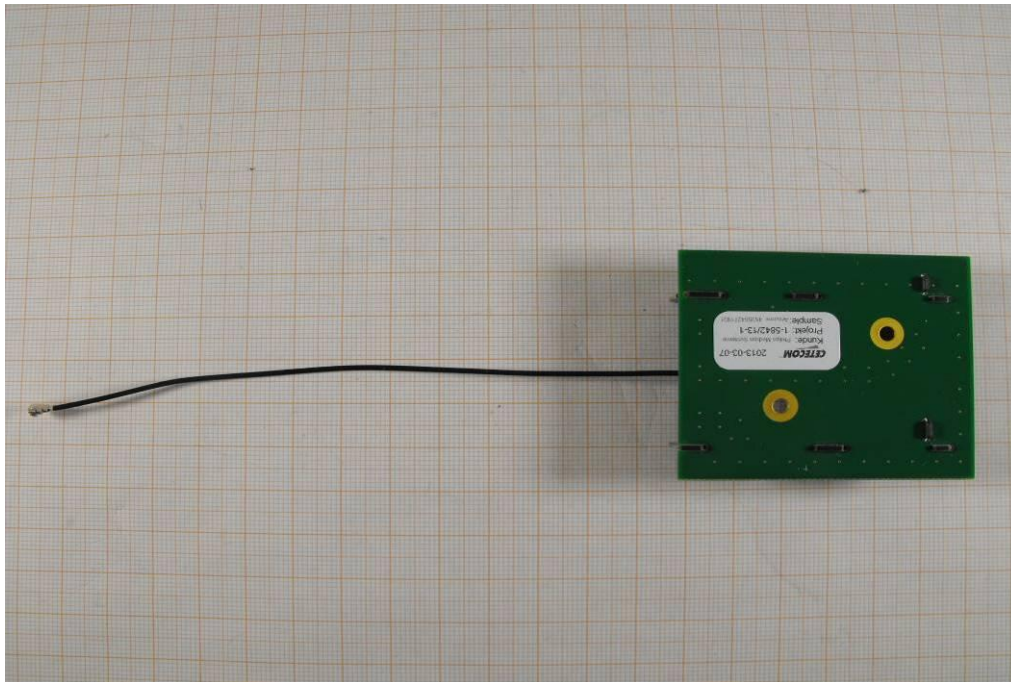


Photo 22: Ant 453564271931

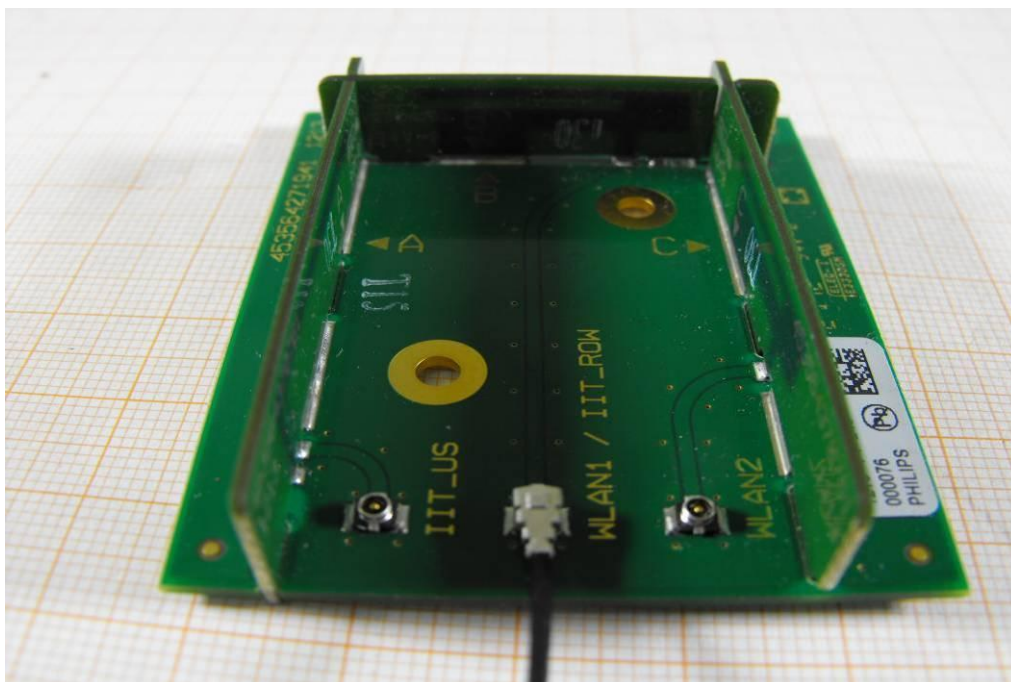


Photo 23: Ant 453564271931

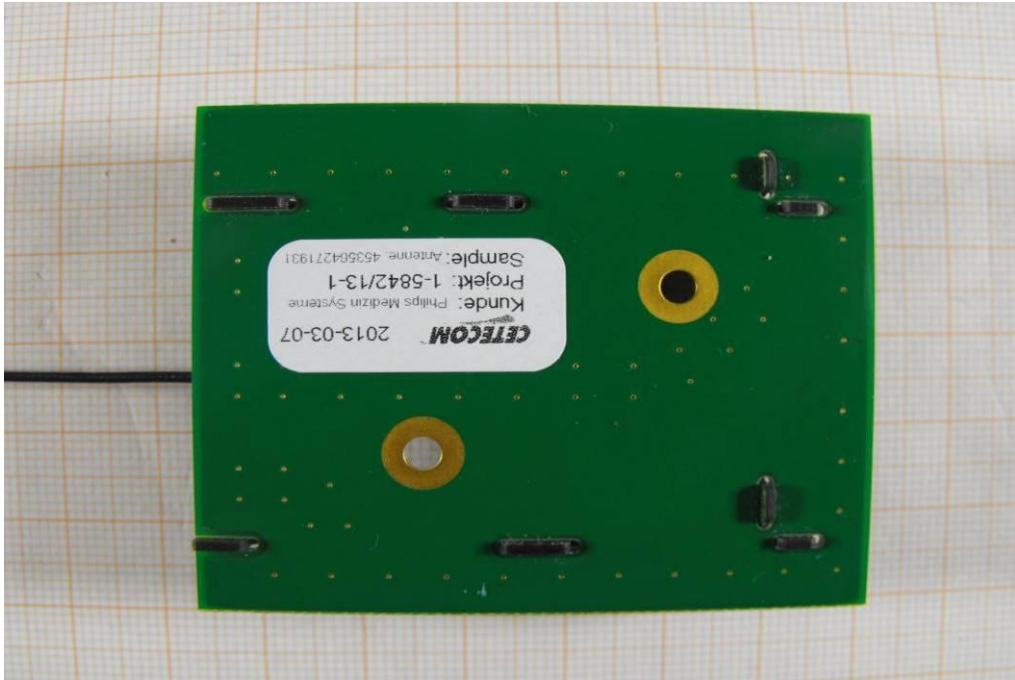


Photo 24: Ant 453564271931

