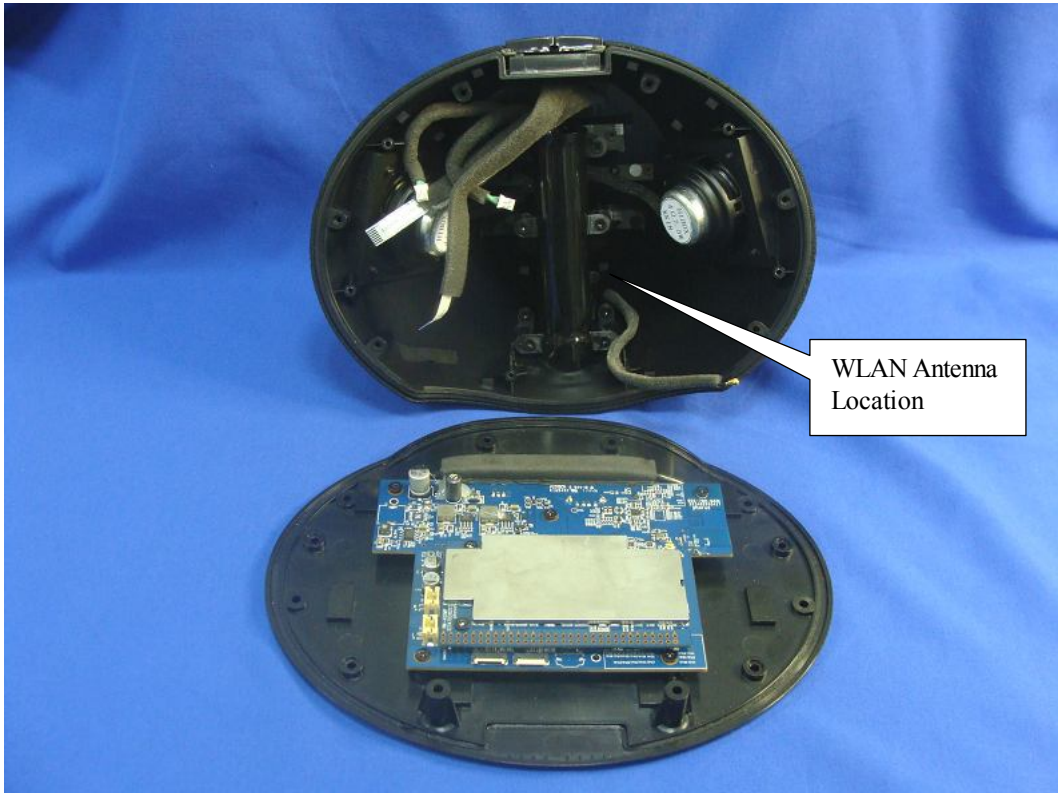
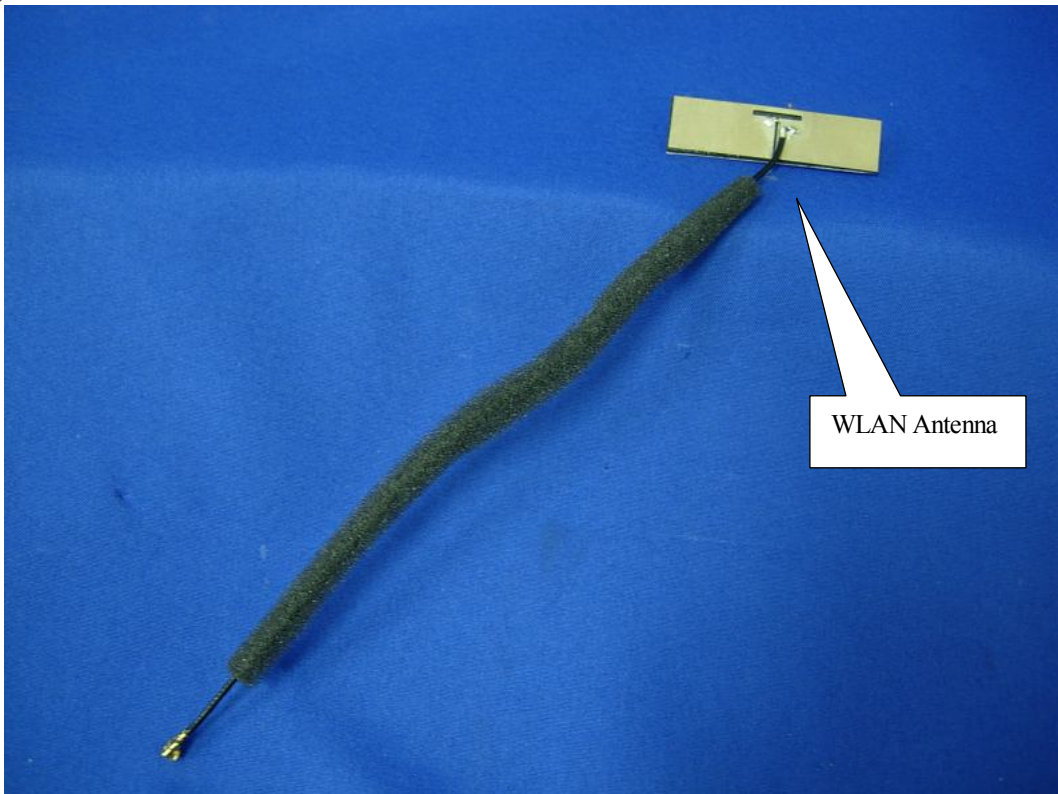


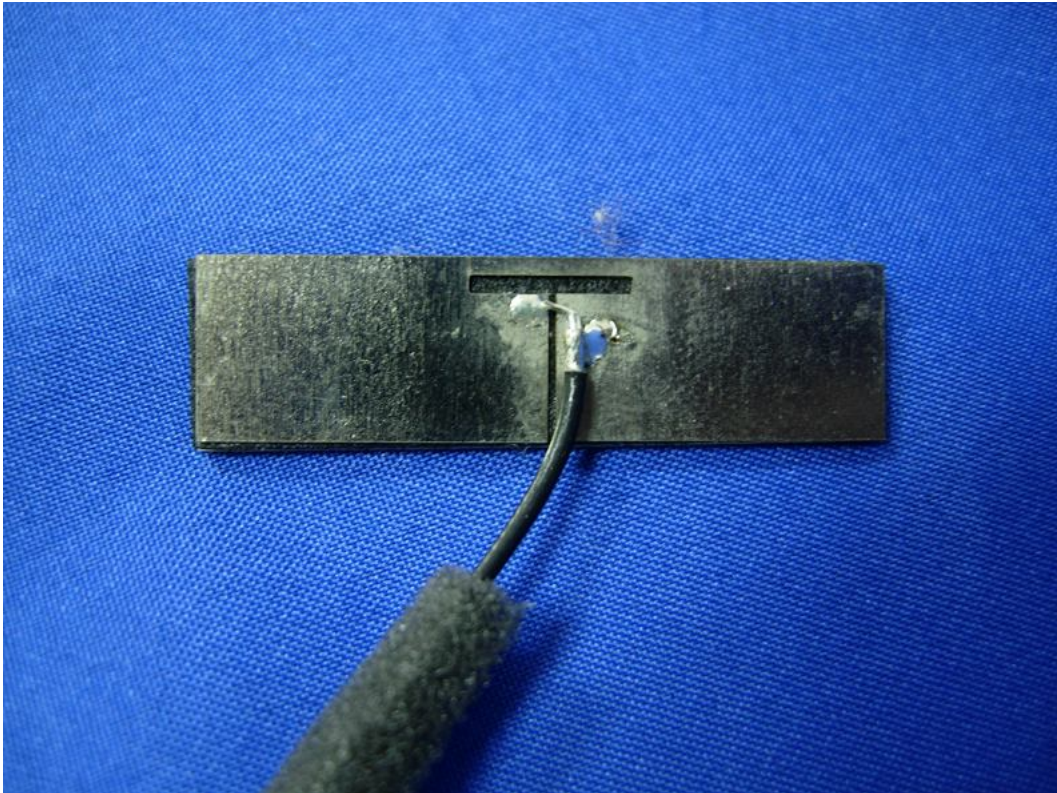
(5) EUT Photo



(6) EUT Photo



(7) EUT Photo



(8) EUT Photo



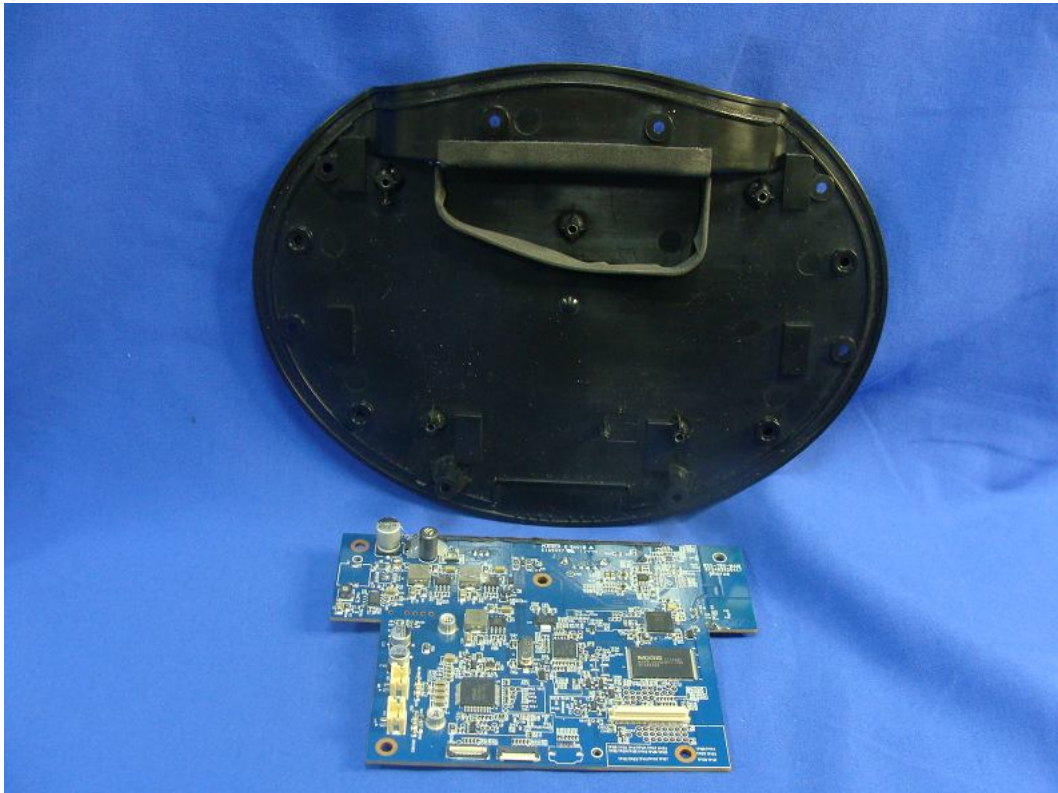
(9) EUT Photo



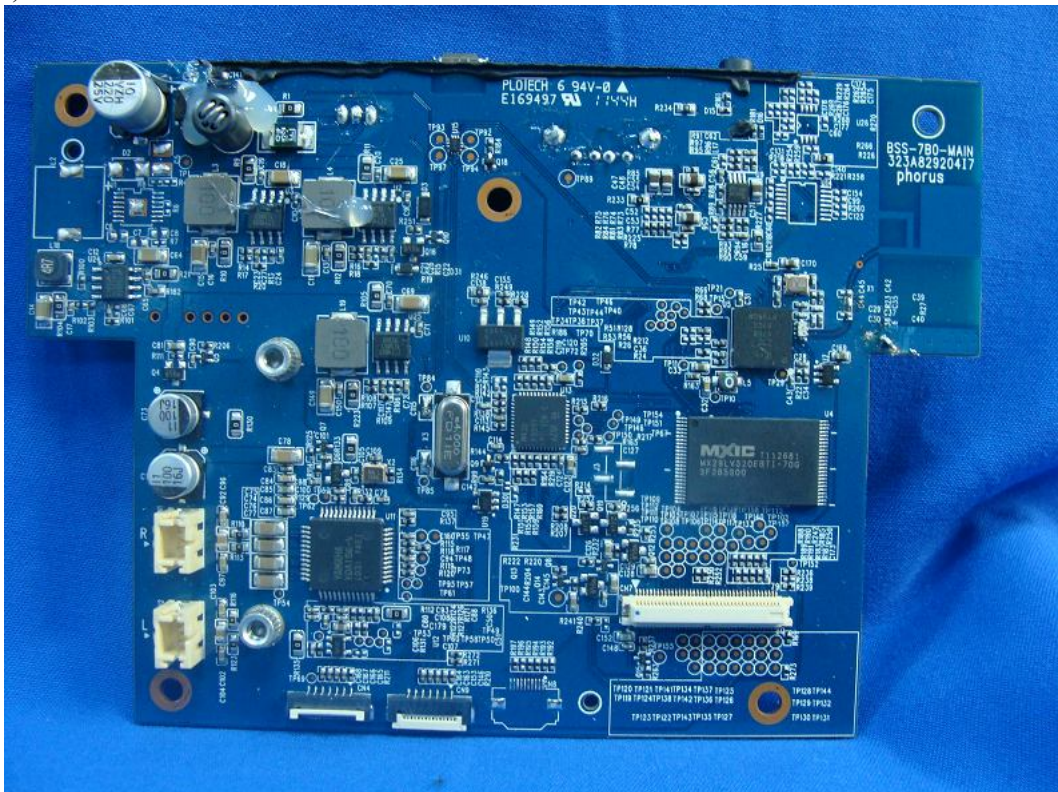
(10) EUT Photo



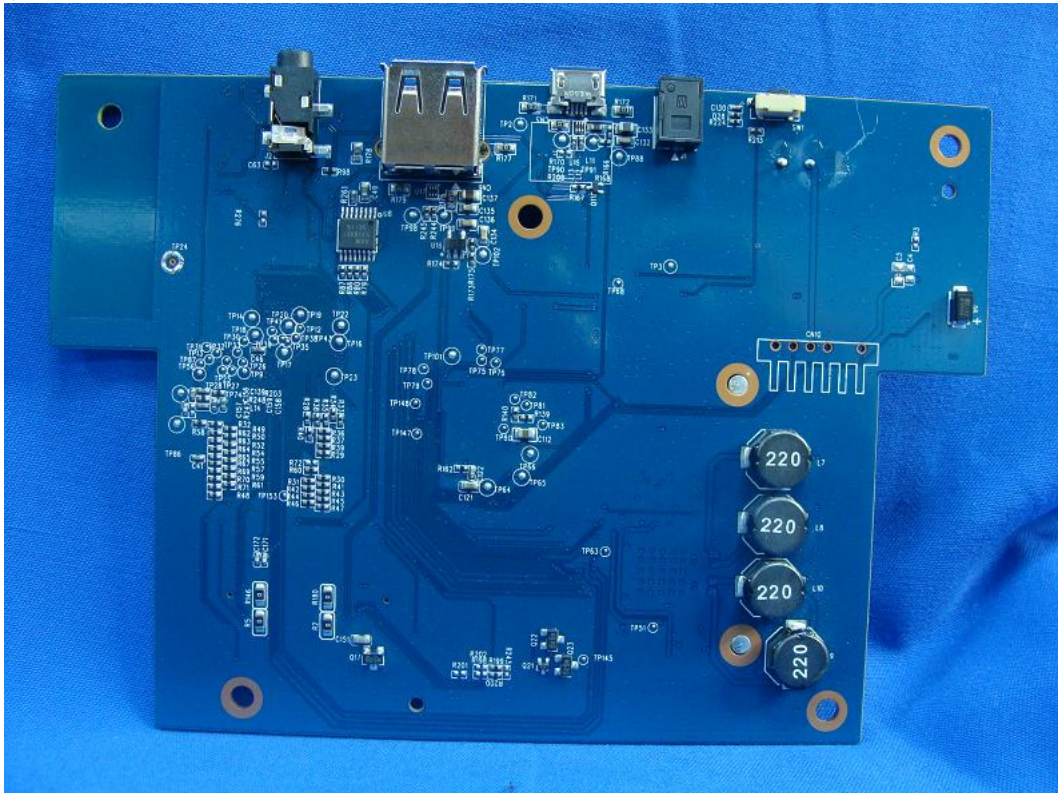
(11) EUT Photo



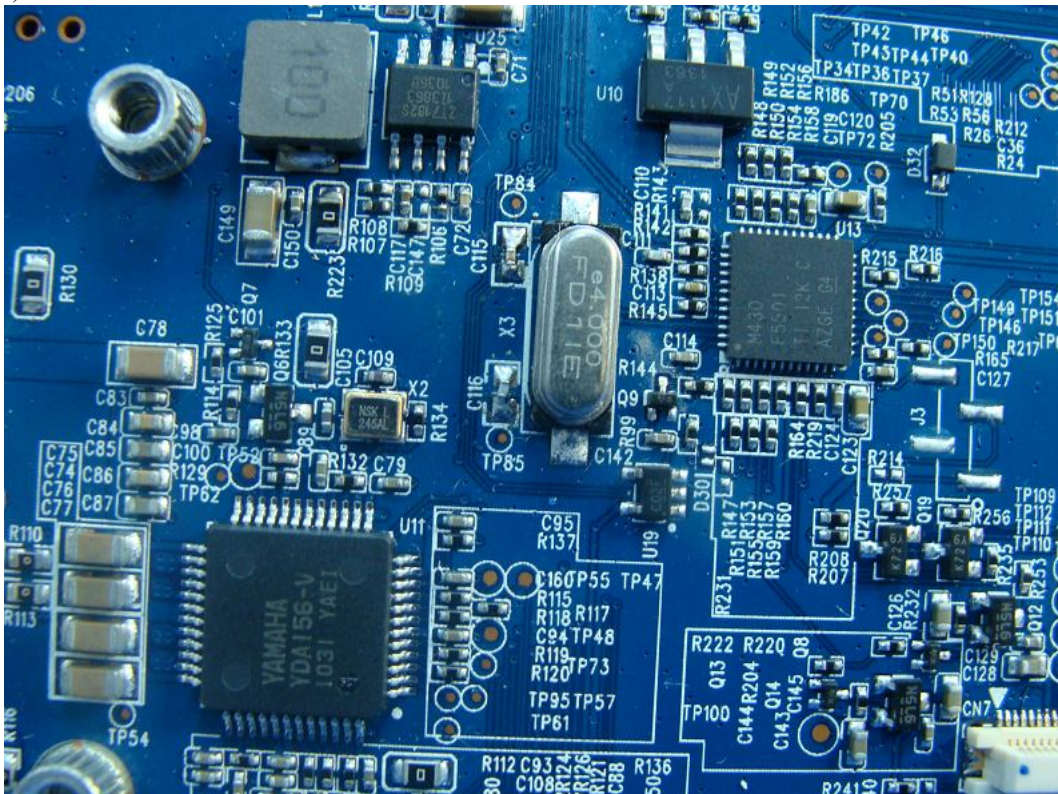
(12) EUT Photo



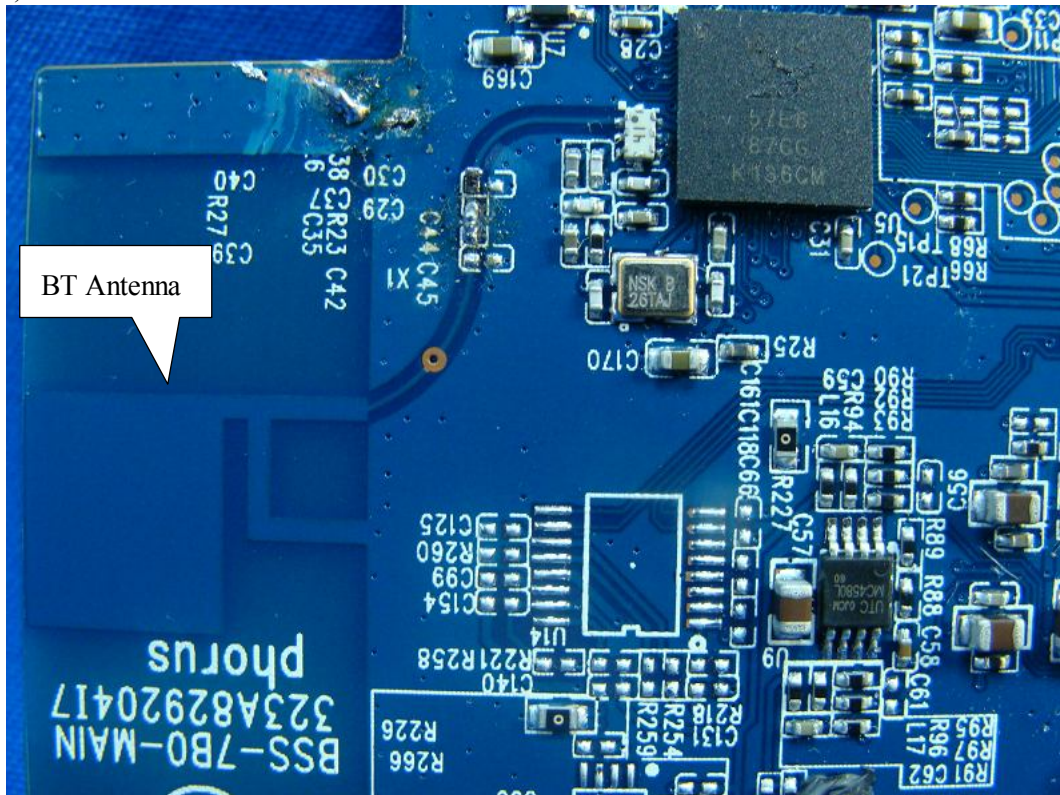
(13) EUT Photo



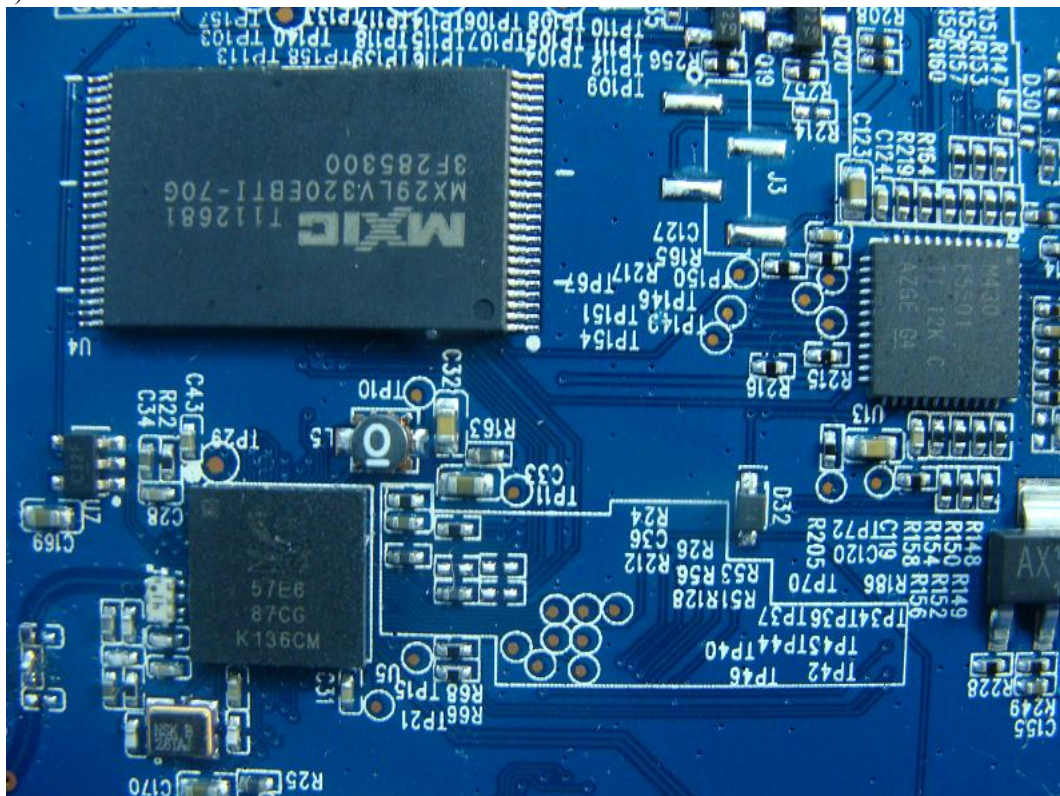
(14) EUT Photo



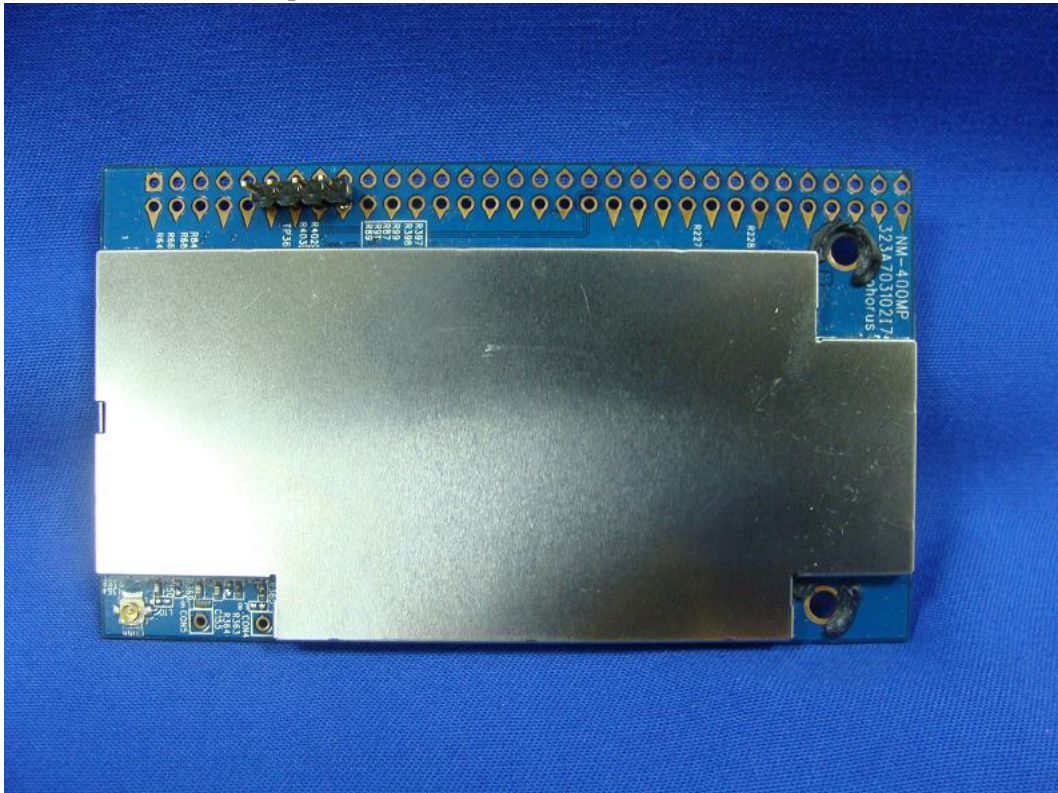
(15) EUT Photo



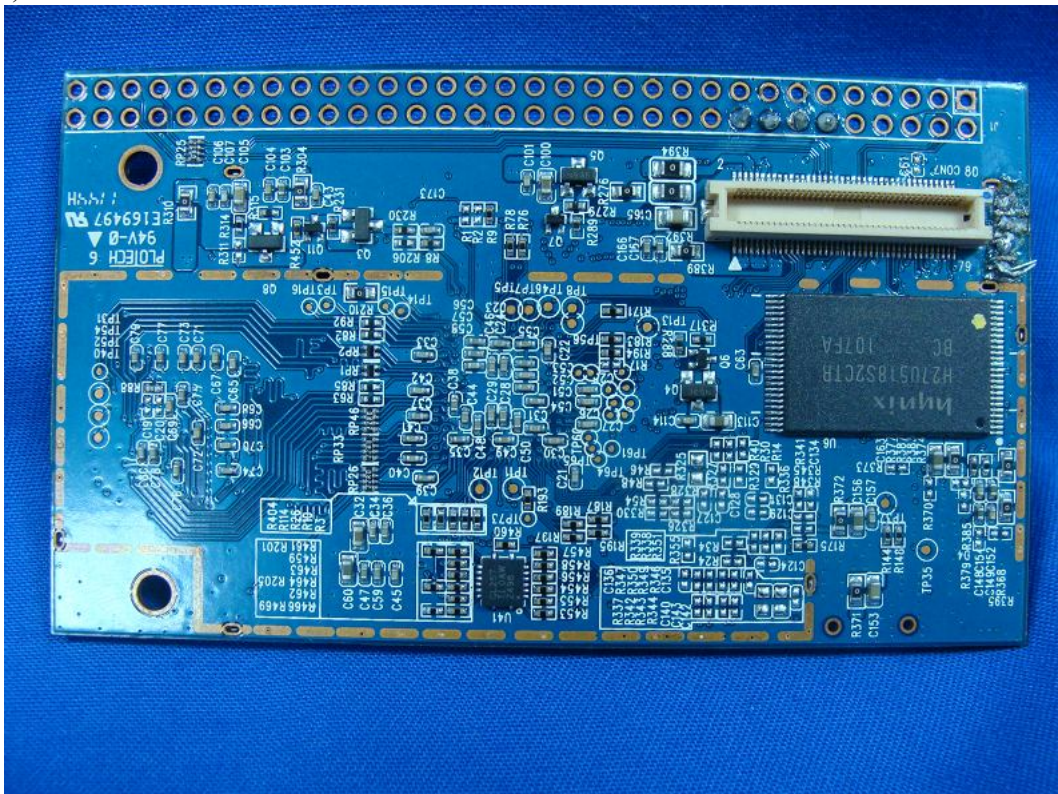
(16) EUT Photo



(17) EUT Photo (Main chip-88AP162) -winbond



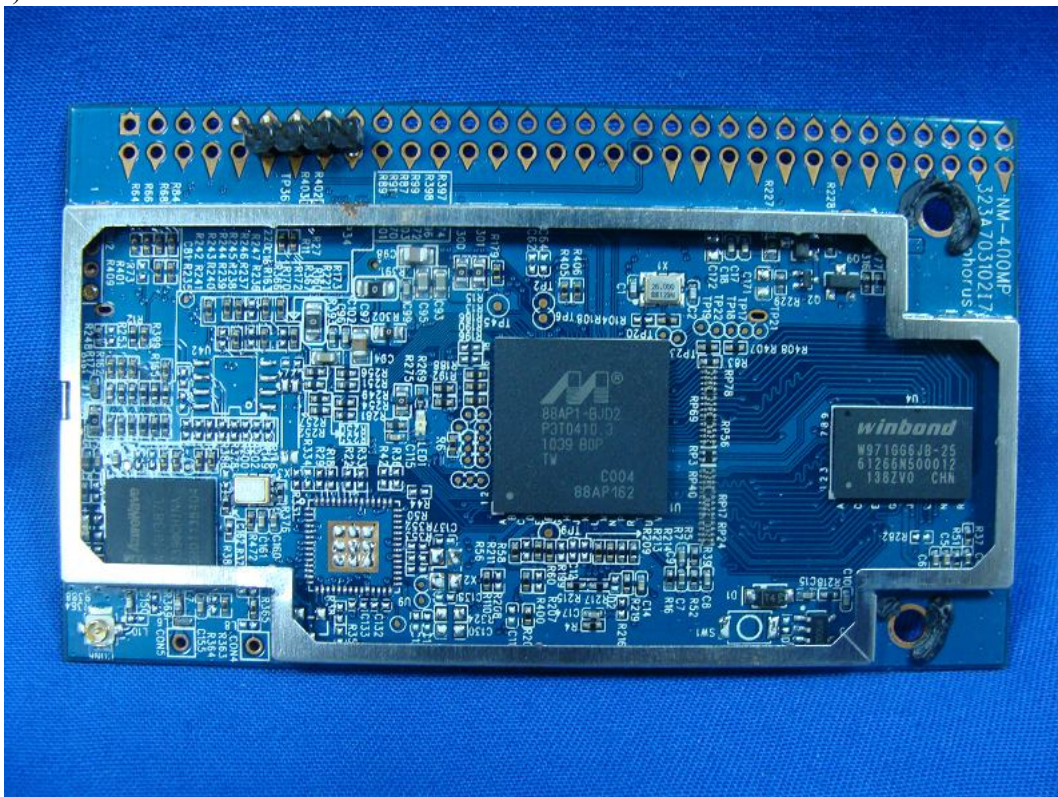
(18) EUT Photo



(19) EUT Photo

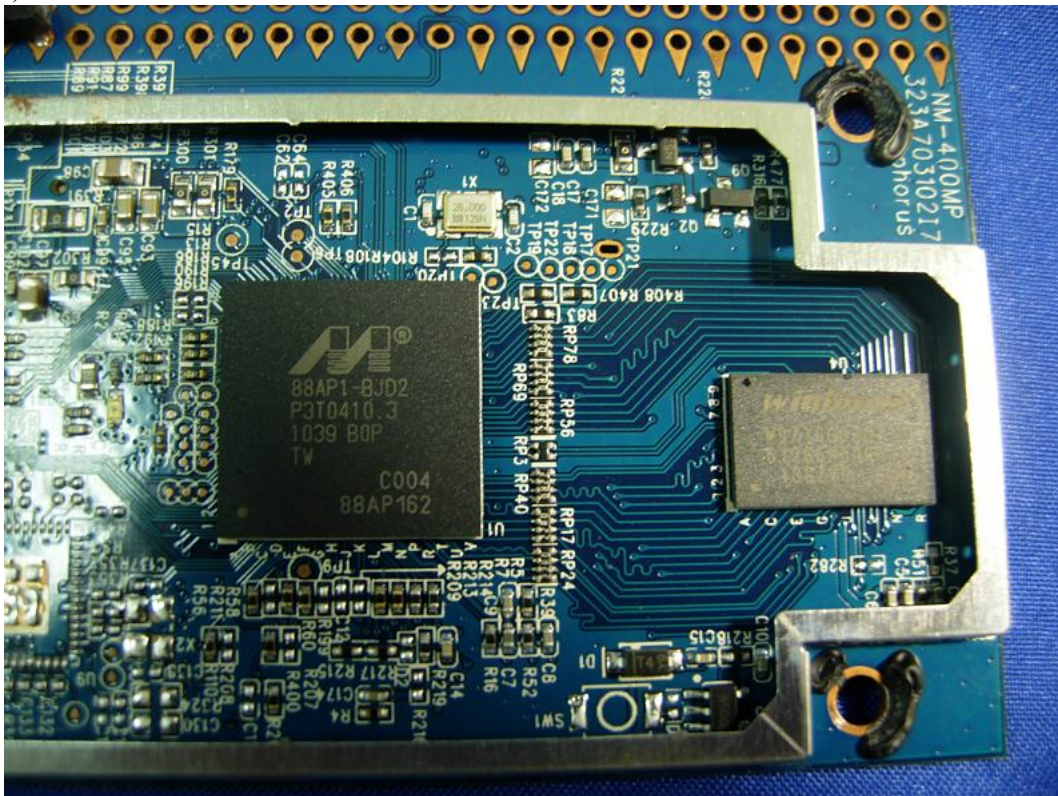


(20) EUT Photo

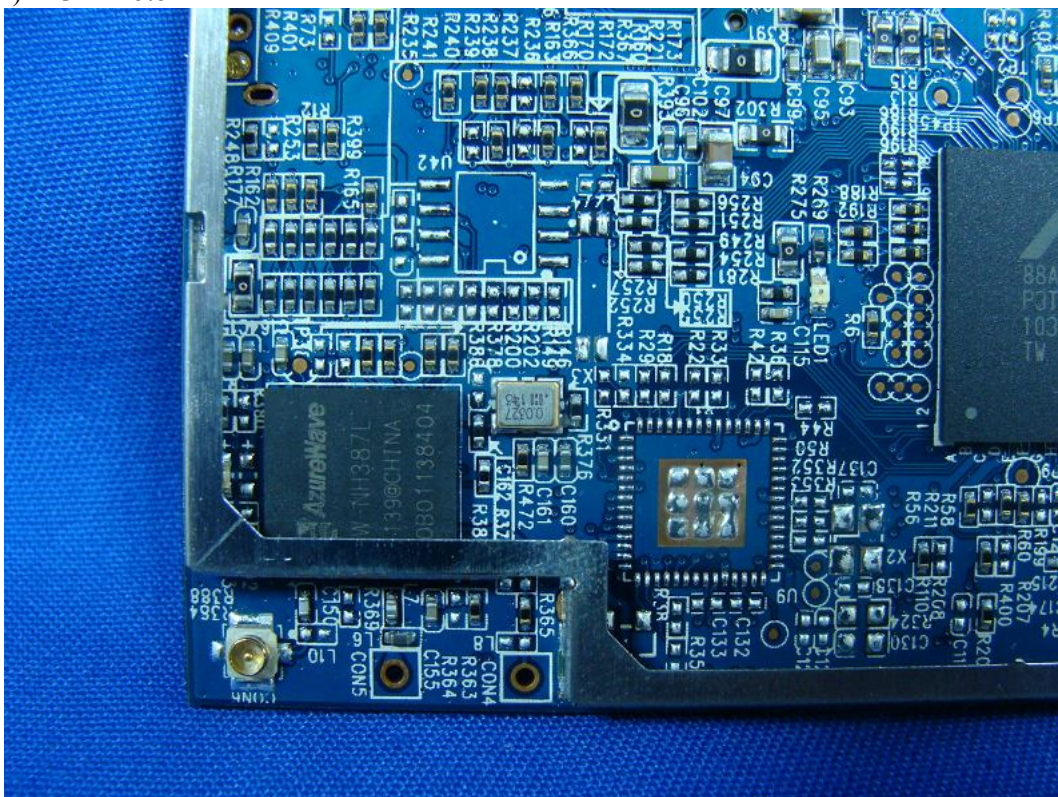




(21) EUT Photo



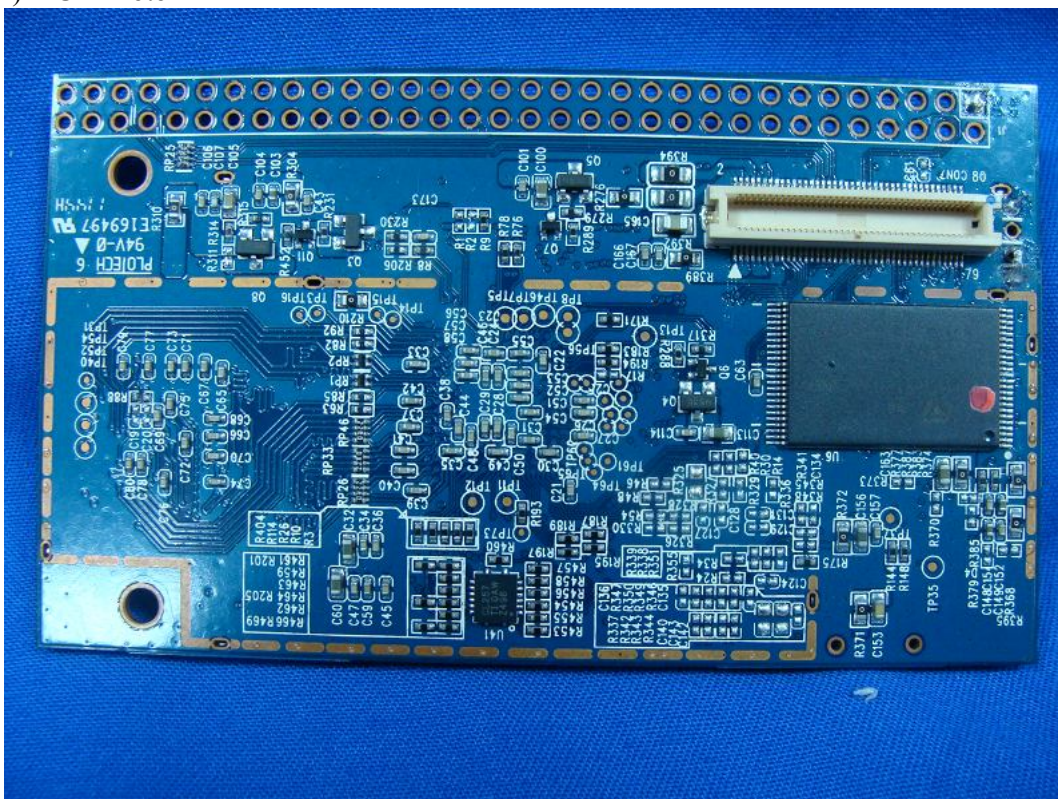
(22) EUT Photo



(23) EUT Photo (Main chip-88AP166) -Hynix



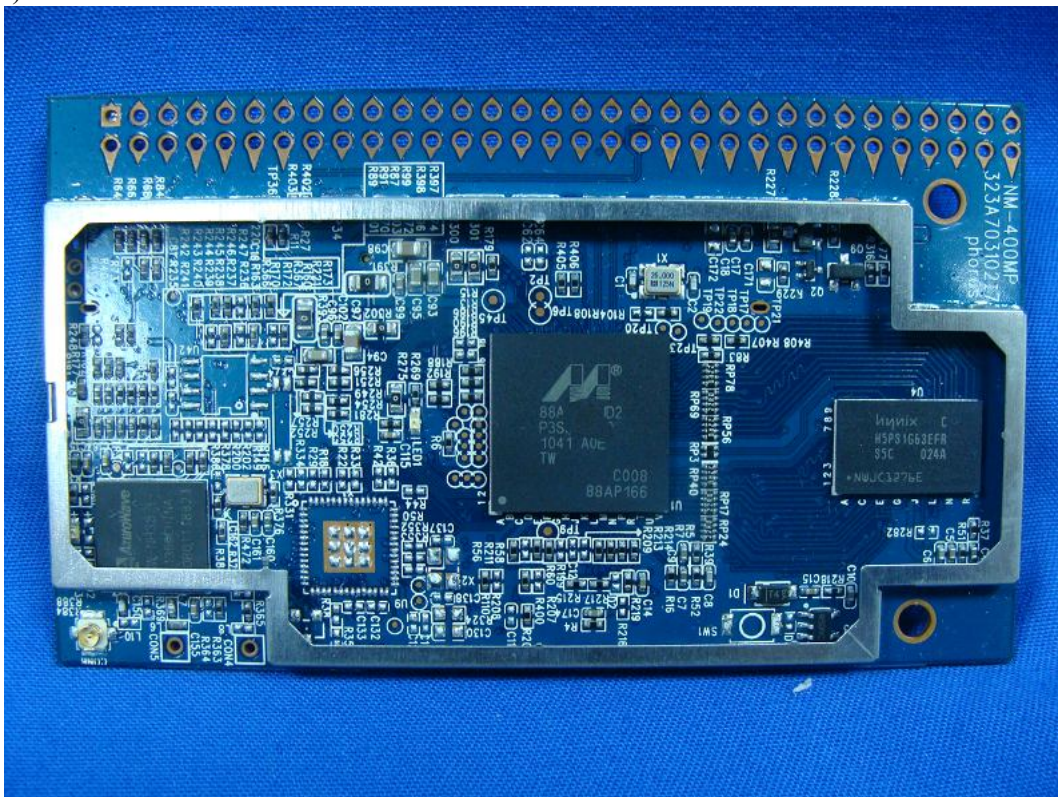
(24) EUT Photo



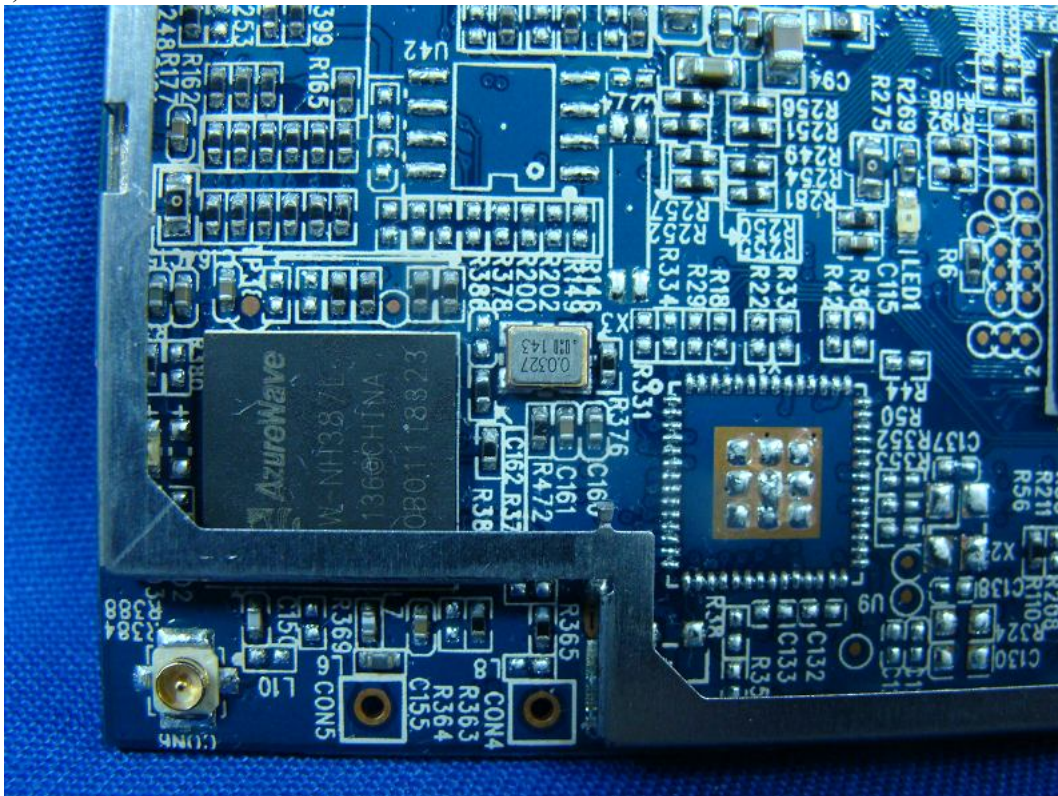
(25) EUT Photo



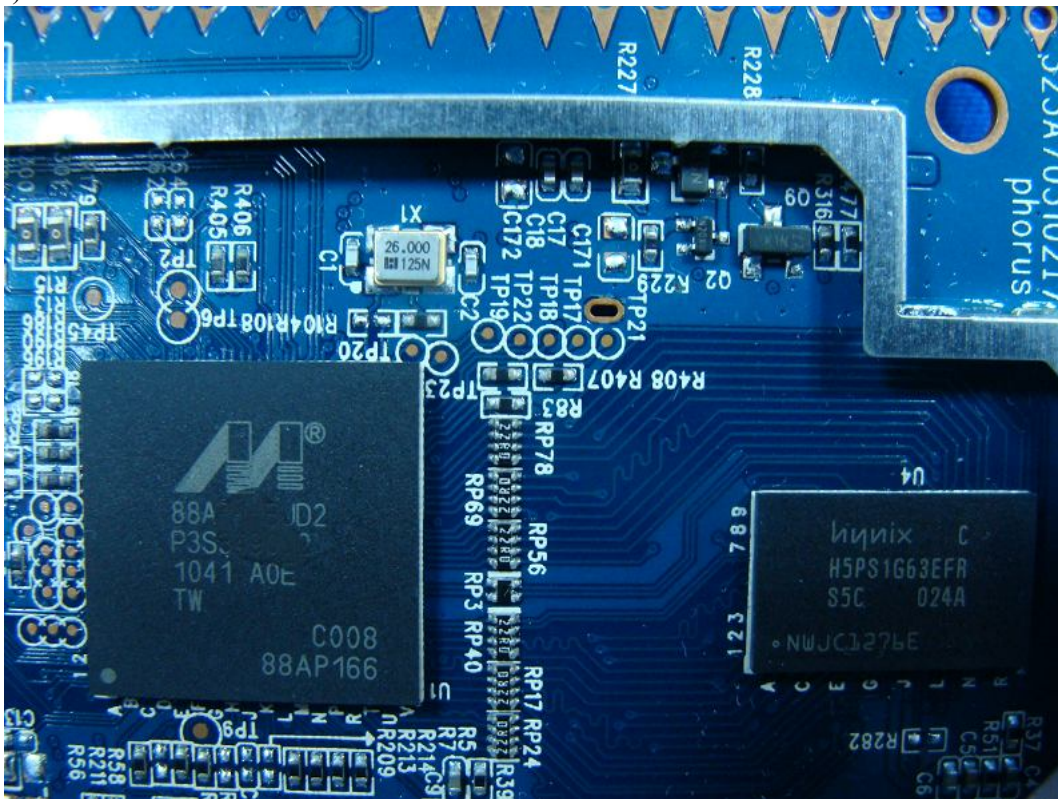
(26) EUT Photo



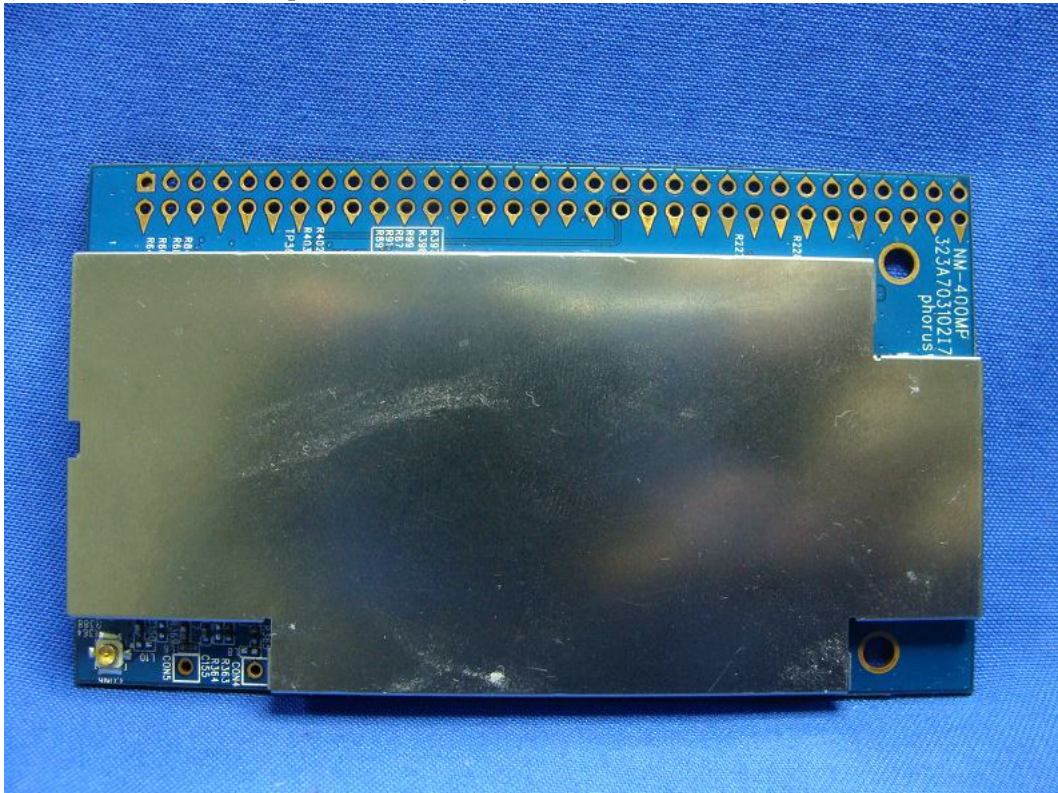
(27) EUT Photo



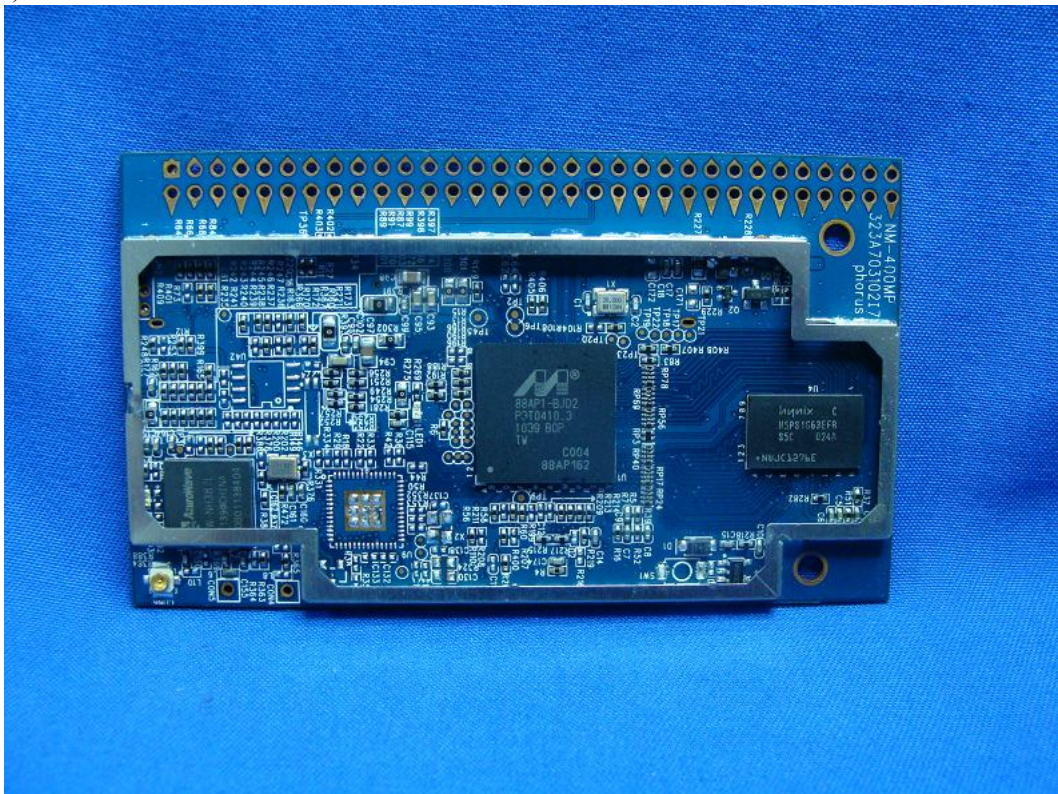
(28) EUT Photo



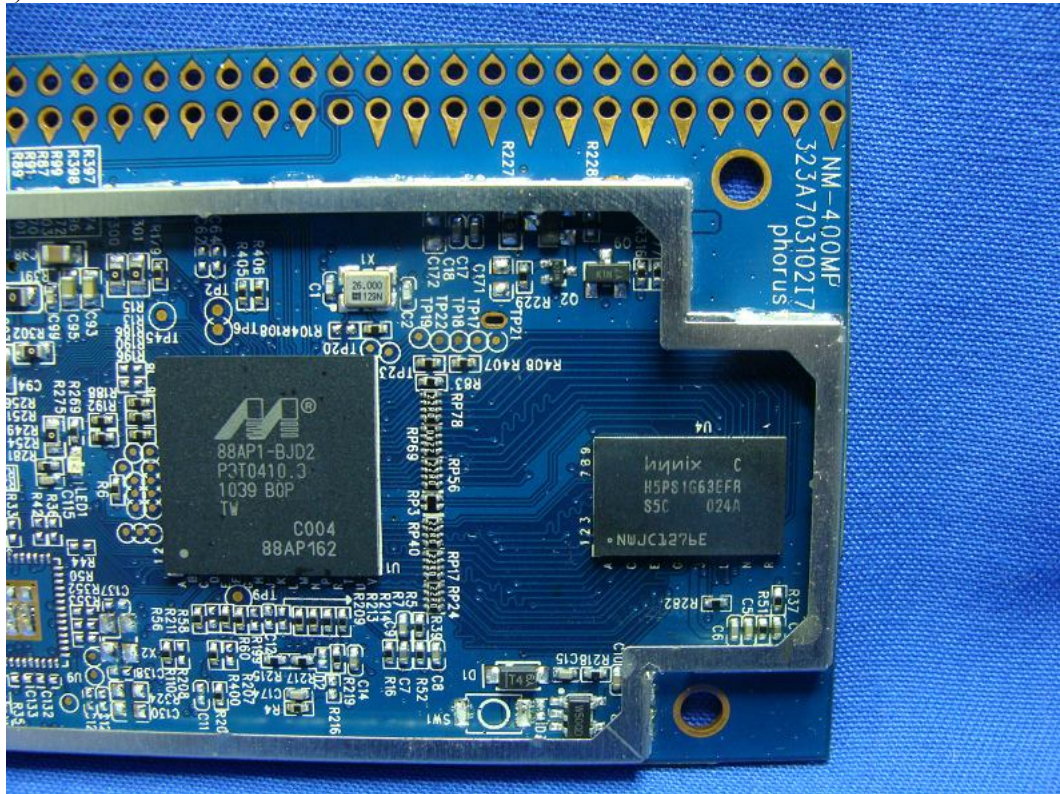
(29) EUT Photo (Main chip-88AP162)-Hynix



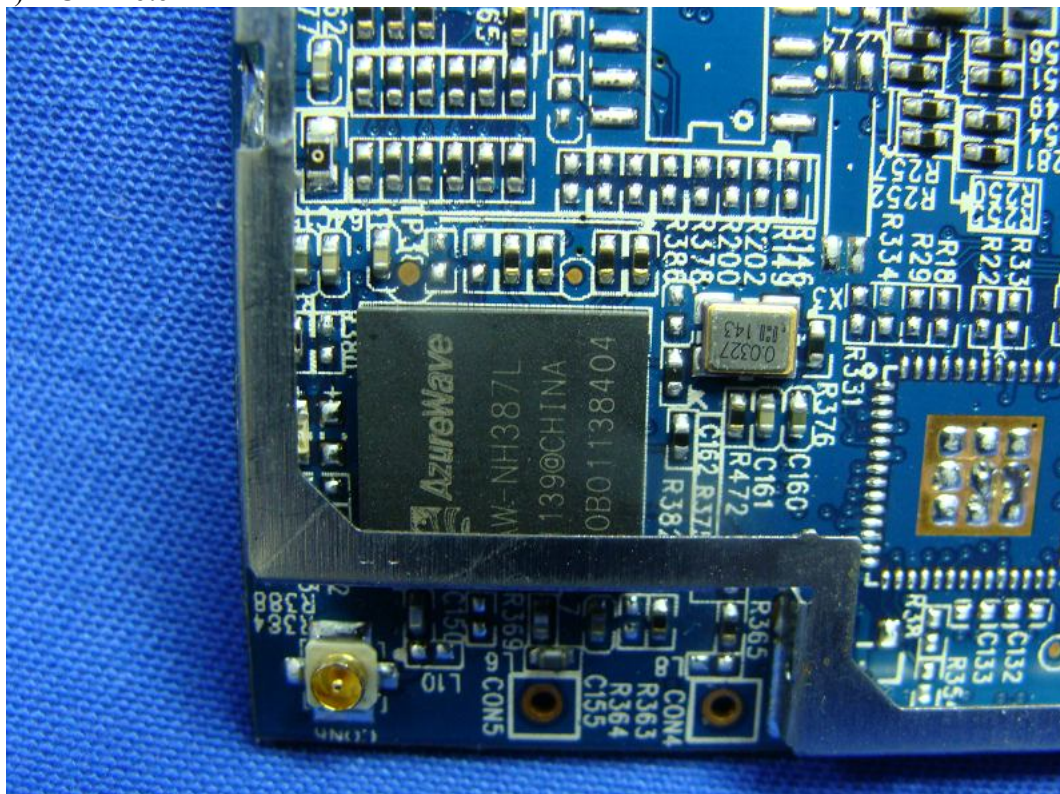
(30) EUT Photo



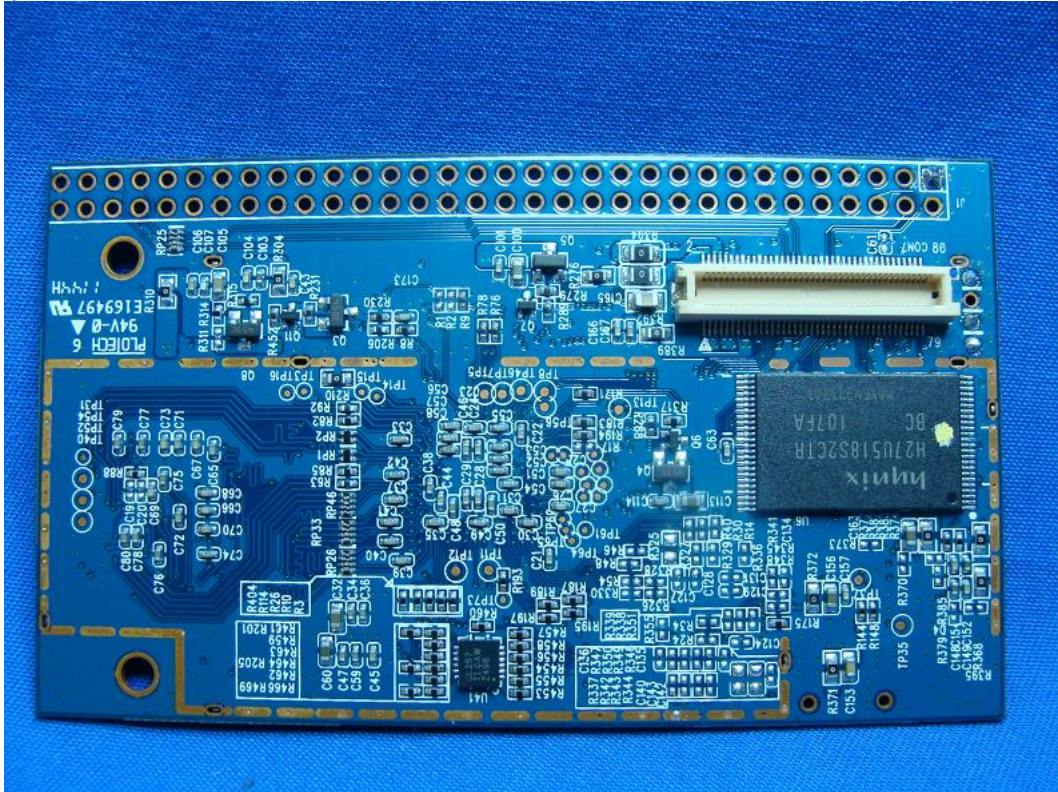
(31) EUT Photo



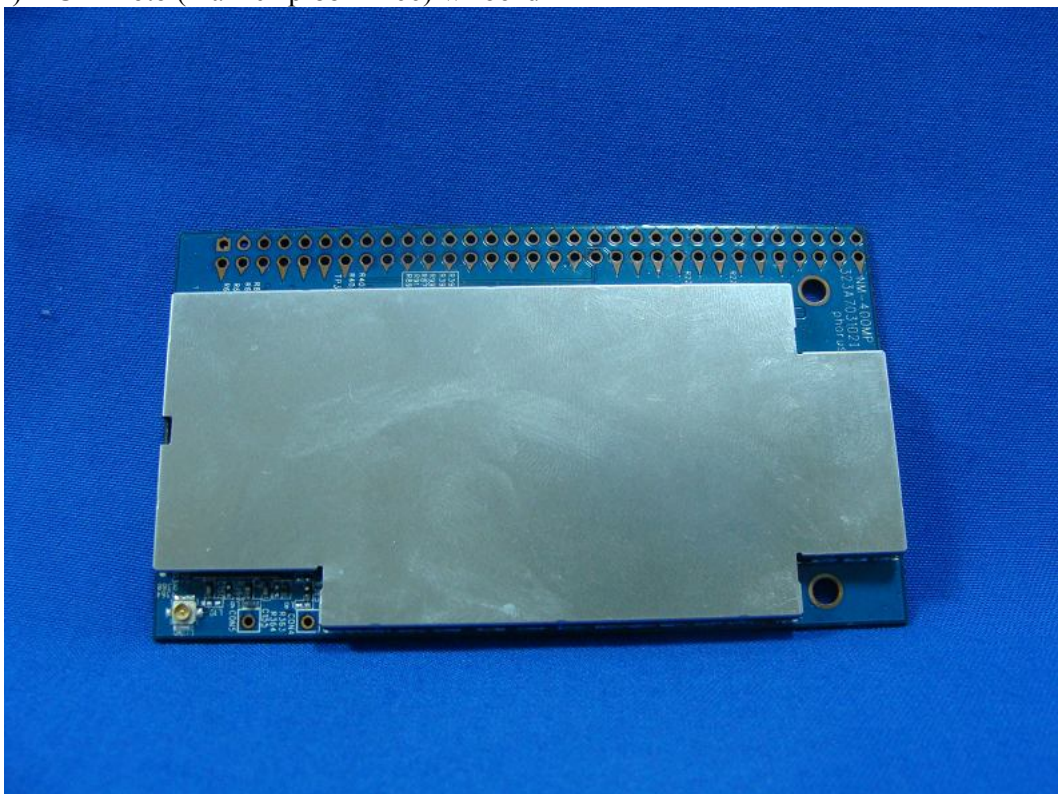
(32) EUT Photo



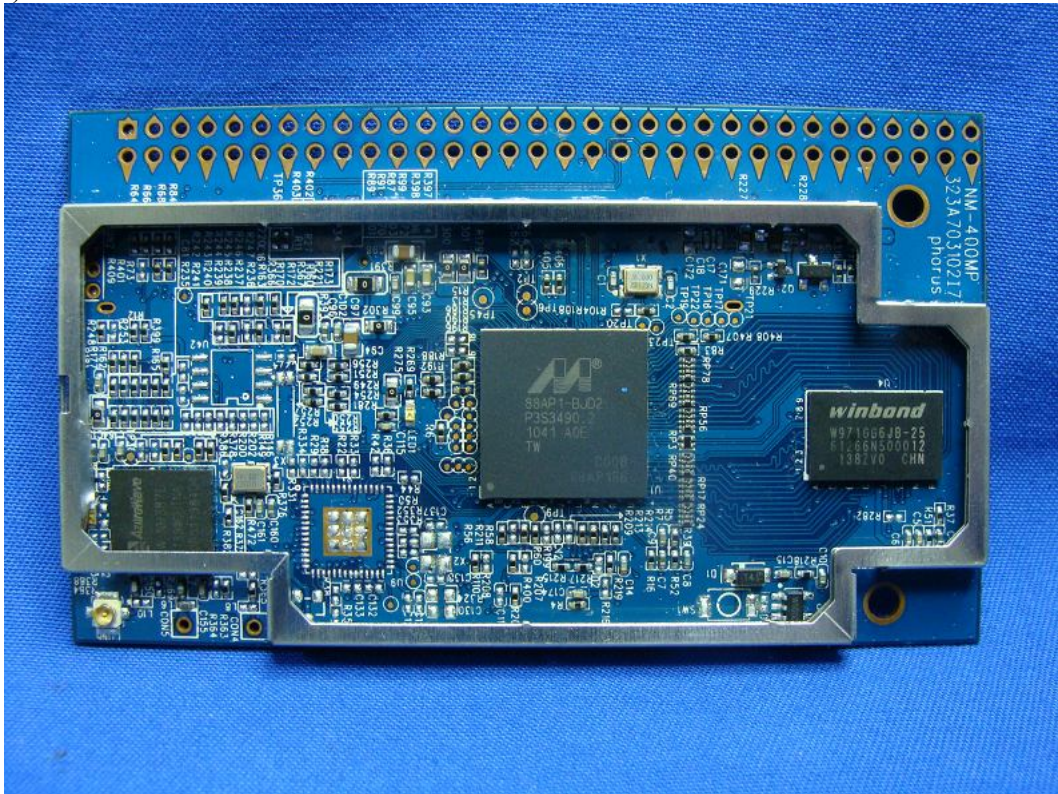
(33) EUT Photo



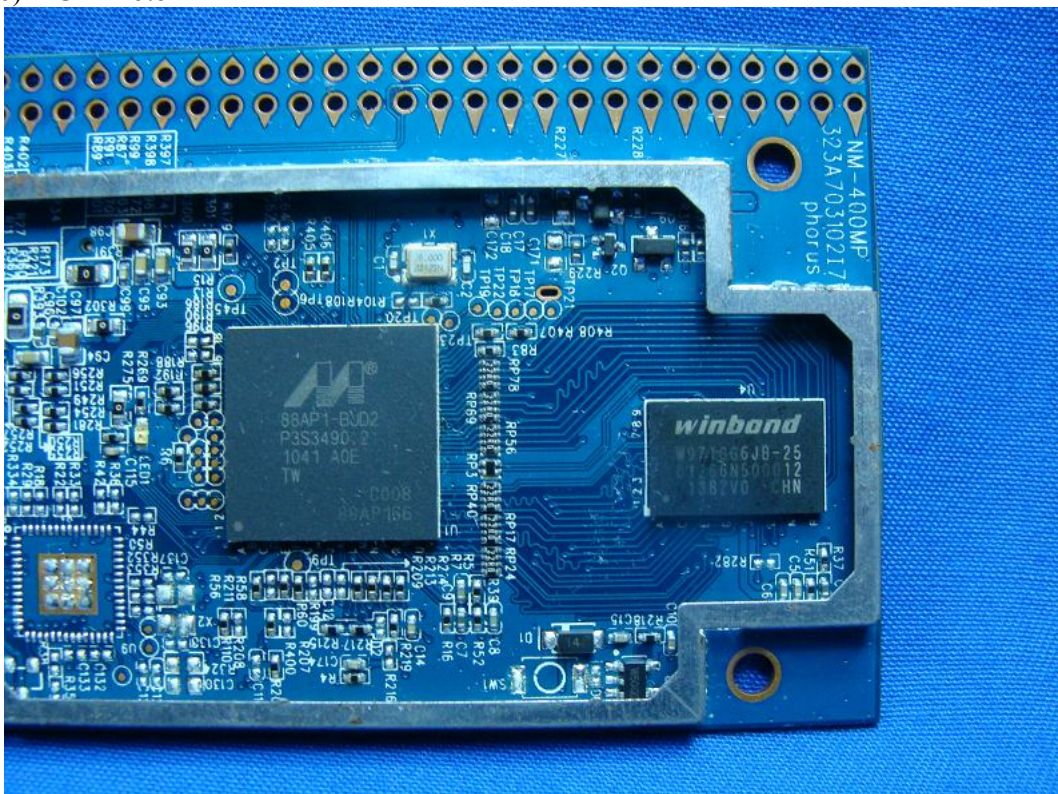
(34) EUT Photo (Main chip-88AP166)-winbond



(35) EUT Photo

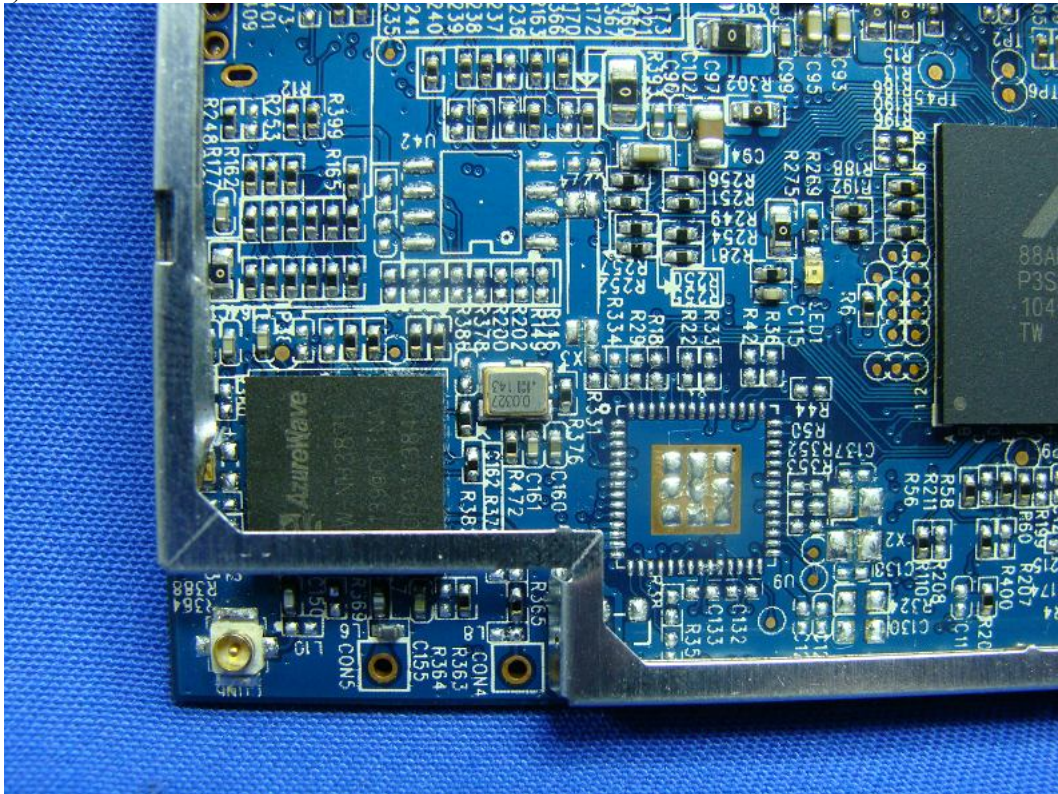


(36) EUT Photo

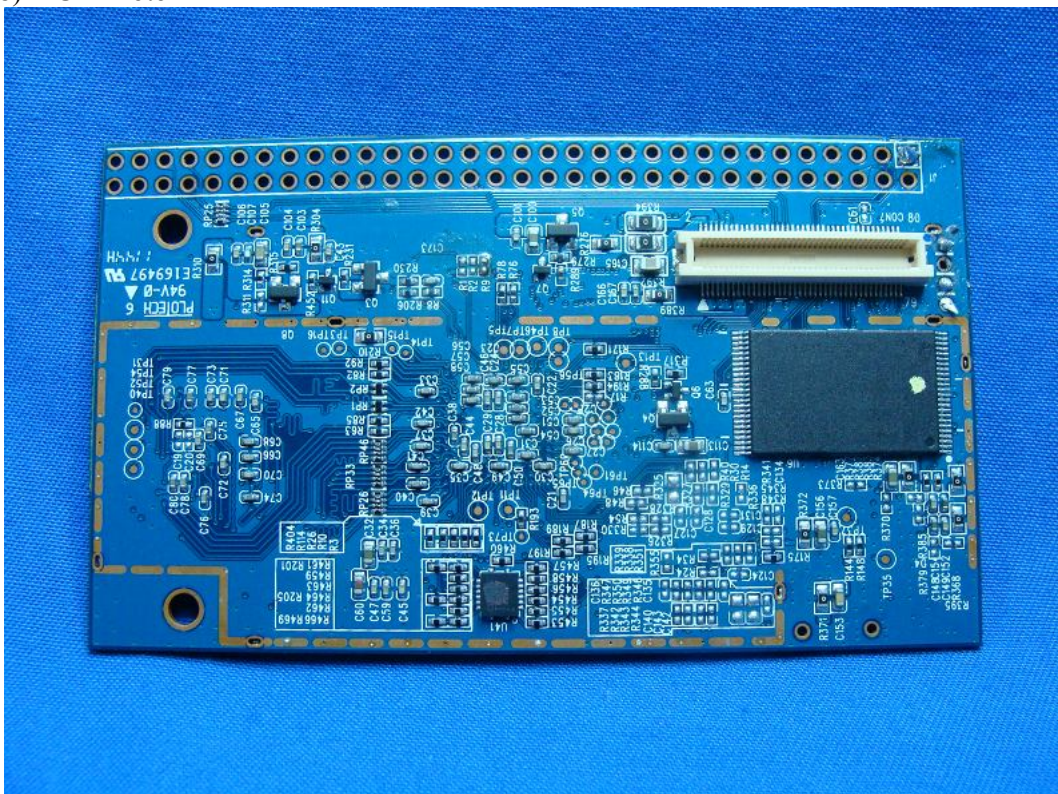




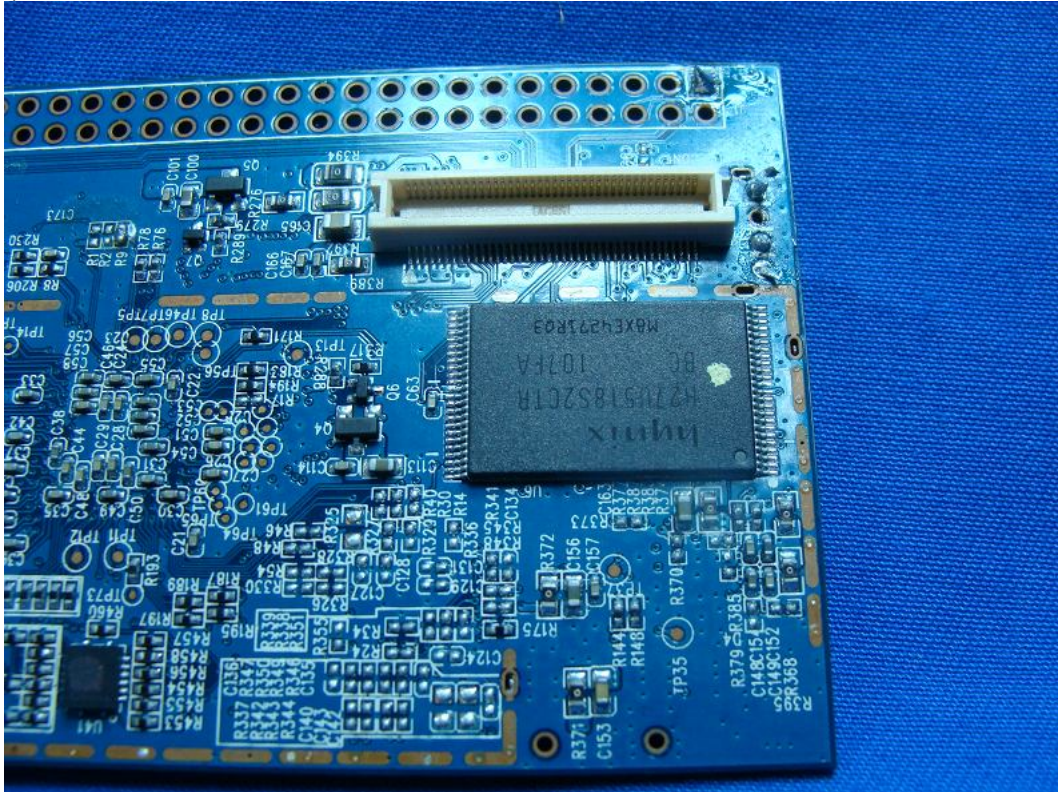
(37) EUT Photo



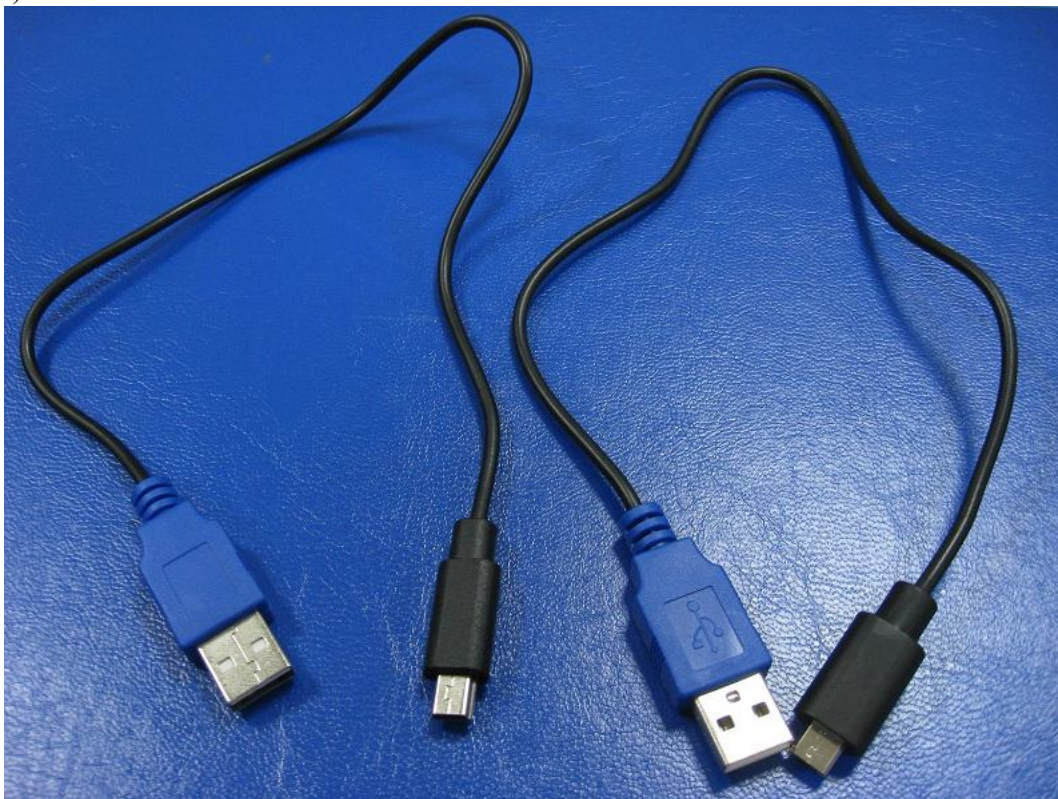
(38) EUT Photo



(39) EUT Photo



(40) EUT Photo



(41) EUT Photo



(42) EUT Photo



(43) EUT Photo

