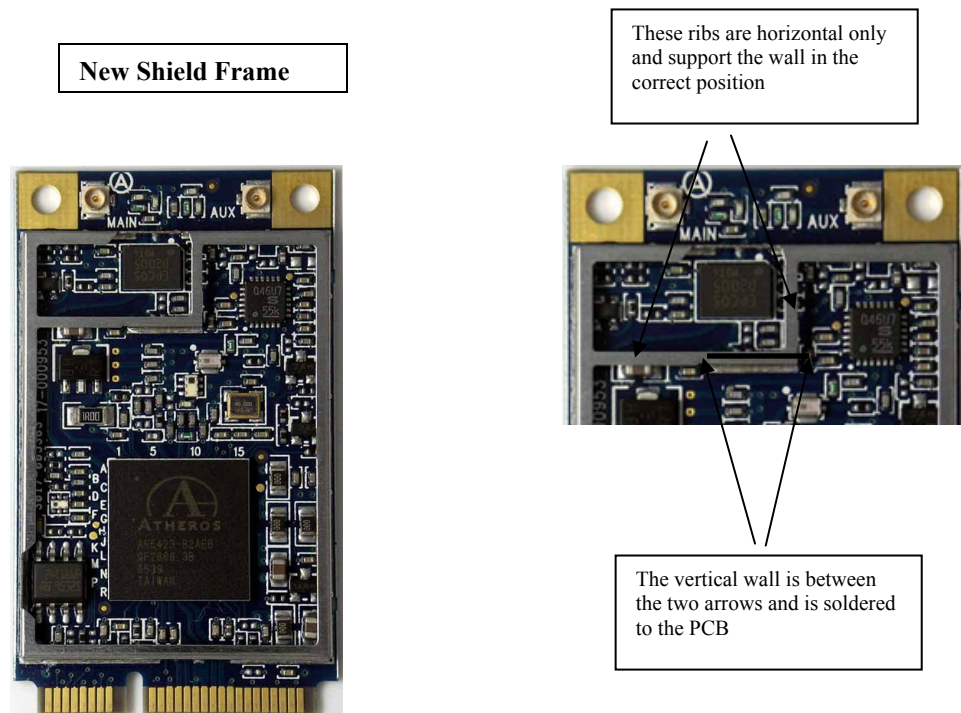


### Changes from Originally Certified Design:

1. Added internal shield wall  
This change provides a grounded wall within the shield cavity between the radio I.C. and the 5GHz receiver input to suppress interference from harmonics of the PCI Express bus signals. The PCB design was changed only to open a window in the solder mask material over the ground plane so that the shield can be soldered to the PCB original ground plane during manufacture.



2. Removed eccosorb material attached to inside of shield cover. This material previously provided some suppression of the PCI-Express bus harmonic. This is no longer need due to shield wall addition (#1 above).
3. Added R159 51ohm resistor in series with C17 at the terminated input of the LNA. This improves receiver sensitivity.
4. Changed some trace routing in the digital circuits (not RF section) for BT\_Active and Rx\_Clear signals.
5. Changed reset circuit RC values to 47kohms (R6) and 0.1uF (C1) located in digital section (not RF).
6. Disabled Turbo Modes (modulations using channel bonding) in hardware. Turbo mode(s) cannot be enabled/disabled using firmware or software.