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Subpart 2.1033 (continued)

(c)(8): Voltages & Currents in All Elements in Final RF Stage, Including Final Transistor or Solid State Device:

Collector Current	=	Cell 1013	440 mA
		Cell 384	400 mA
		Cell 777	450 mA
		PCS 25	430 mA
		PCS 600	390 mA
		PCS 1175	440 mA
Collector Voltage, Vdc	=	3.47	
Supply Voltage, Vdc	=	3.6	

(c)(9): Tune-Up Procedure:

Please See Attached Exhibits

(c)(10): **Circuit Diagram/Circuit Description**: Including description of circuitry & devices provided for determining and stabilizing frequency, for suppression of spurious radiation, for limiting modulation and limiting power.

Please See Attached Exhibits

(c)(11): Label Information:

Please See Attached Exhibits

(c)(12): **Photographs**:

Please See Attached Exhibits

(c)(13): **Digital Modulation Description**:

____ Attached Exhibits ____ N/A

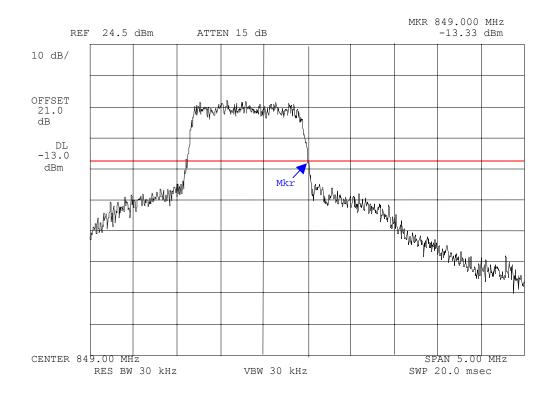
(c)(14): **Test and Measurement Data**:

Follows

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Name of Test:Emission Masks (Occupied Bandwidth)g0470047 (Modified): 2004-Aug-23 Fri 13:14:51State: 2:High Power / Cellular / High Channel



Power: Modulation: HIGH (24.5dBm) CDMA Plot taken with Average Detector

De

David E. Lee, Compliance Test Manager

Performed By:

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