

Helen Zhao

Subject: FW: Alinco Incorporated, FCC ID: PH3XE972, Assessment NO.: AN07T6898, Notice#1

Importance: High



03_PH3XE972 User 12_PH3XE972_Test 05_PH3XE972 CC2500_Data_She
Manual_R.pdf Report (excep... Operational Descri... et_1_1.pdf

Hi Claire,

Question #1: This device has UART port designed for data transmission, so it can be classified as digital device. Please add user information outlined in FCC15.105 into the user manual.

Answer #1: See attached revised User manual.

Question #2: The test report page 6 indicates the middle channel was tested at channel 11, 2406.5MHz. But some of the following sections show middle channel at 2405.5MHz, (e.g. page 12, 18). Please correct the discrepancy. By the way, the test plots embedded in the test report are not clear enough to view. Please provide a clearer version of the test report.

Answer #2: See attached revised test report.

Question #3: Please address the following FHSS requirements as specified in FCC15.247(a):

Answer #3: See attached revised Operational Description.

FYI, I also attached the data sheet of RF chipset.

Regards,

Mika

-----Original Message-----

Sent: Wednesday, June 13, 2007 5:45 PM

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AN07T6898, Notice#1

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Question #2: The test report page 6 indicates the middle channel was tested at channel 11, 2406.5MHz. But some of the following sections show middle channel at 2405.5MHz, (e.g. page 12, 18). Please correct the discrepancy. By the way, the test plots embedded in the test report are not clear enough to view. Please provide a clearer version of the test report.

Question #3: Please address the following FHSS requirements as specified in FCC15.247(a):

Equal Hopping Frequency Use

Describe how each individual EUT meets the requirement that each of its hopping channels is used equally on average (e.g., that each new transmission event begins on the next channel in the hopping sequence after the final channel used in the previous transmission event).

System Receiver Input Bandwidth

Describe how the associated receiver(s) complies with the requirement that its input bandwidth (either RF or IF) matches the bandwidth of the transmitted signal.

System Receiver Hopping Capability

Describe how the associated receiver(s) has the ability to shift frequencies in synchronization with the transmitted signals.

Best Regards,
Helen Zhao

The items indicated above must be submitted before processing can continue on the above referenced application. Failure to provide the requested information within 30 days of the original e-mail date may result in application dismissal and forfeiture of the filing fee. Also, please note that partial responses increase processing time and should not be submitted. Any questions about the content of this correspondence should be directed to the e-mail address listed below the name of the sender.