APPENDIX D: ATTESTATION LETTER(S) (IF APPLICABLE)

Please refer to the following page.

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23 July, 2002

Federal Communications Commissions

RE: PH3 DR-620T / 800MHz analog cellular telephone band blocking

Dear Sir or Madam,

This is to declare that the device in application PH3 DR-620T has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.

The device uses double super heterodyne PLL synthesizer circuitry as a receiver circuit and its first oscillation frequency is determined by a [n] figure generated in CPU. The first local oscillation frequencies determined by the [n] figures are as follows:

FM broadcasting band: 98.2 ~ 118.7MHz
AM aviation band: １２９．７～１５７．７ＭＨｚ
VHF band: １１４．３～１５２．３ＭＨｚ
UHF LOW band: (４００－４２０ＭＨｚ) ４４５．１～４６５．１ＭＨｚ
UHF HIGH band: (４２０－４８０ＭＨｚ) ３７４．９～４３４．９ＭＨｚ
VHF-side 360MHz band ３５６．７～４２１．７ＭＨｚ
UHF-side 360MHz band ３８０．１～４４５．１ＭＨｚ
SUB-band VHF: １８１．１～２１９．１ＭＨｚ
SUB-band UHF: ３７８．３～４５８．３ＭＨｚ

In addition, the harmonics are filtered out with Low-pass filter circuit before the signal goes into the mixer circuit, therefore 800MHz range can't be received. These oscillation frequencies can't be altered. Also the Radio Frequency circuits for above declared bands are all composed of Low-pass filters and synthesizer. The CPU used in this device, our parts code XA0913, vender's code M30620FCAGP is exclusively programmed and burned for US export model. Alinco, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the cellular frequencies declared above.

Respectfully,

Kusuhara

Kazuhiro Kusuhara
General manager, Production Section,
Alinco, Inc. Electronics Division