Rhein Tech Laboratories, Inc.
360 Herndon Parkway
Suite 1400
Herndon, VA 20170
http://www.rheintech.com

Client: Alinco, Inc.
Model: DR-435T MkIII
Standards: FCC 15.121
& IC RSS-215
Report: 2006016

Appendix D: FCC Attestation Letter

Please refer to the following page.

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2/28/2006

Federal Communications Commissions

RE:PH3DR-435TMk3 / 800MHz analog cellular telephone band blocking

Dear Sir or Madam.

   This is to declare that the device in application PH3DR-435TMk3 has been blocked for any and all access of 824.000 to 849.9975MHz and 869.000 to 894.9975MHz.

   The device uses double super heterodyne as a receiver circuit and frequency is generated by a PLL synthesizer circuitry. The first local oscillation frequencies are determined by the N value data of the CPU. The 350.000 – 424.995MHz range is determined by N-value data of 380.850 – 455.845MHz, while 425.000 – 511.995MHz range is determined by 394.150 – 481.145MHz data, and such values are not able to changed by any means.

   The 2 band-pass filters are used to filter out the unwanted bands. The CPU used in this device, our parts code XA1130 , vender's code M38268MCA-075GP is exclusively programmed and burns for this US export model. ALINCO, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the declared cellular frequencies. Moreover, the entire circuitry of this device is not designed to cover the cellular frequencies anyway.

   To my best of knowledge being informed by the chief-engineer in charge of PH3DR-435TMk3, above declared is true.

   Sincerely

Kazuhiro Kusuhara
Vice-Chief, Production Section
Electronics Div., Alinco,Inc.