APPENDIX D: MANUFACTURER'S ATTESTATION LETTER

Please see the following page.

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11/18/2002

Federal Communications Commissions

RE: PH3 DR135TMKII / 800MHz analog cellular telephone band blocking

Dear Sir or Madam,

This is to declare that the device in application PH3 DR135TMKII has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.

The device uses double super heterodyne as a receiver circuit and frequency is generated by a PLL synthesizer circuitry. The first local oscillation frequencies are determined by the N value data of the CPU. The 118.000 -135.995MHZ range is determined by N-value data of 139.7 to 157.695MHz,while 136.000-173.995MHz range is determined by 114.3 to 152.295MHz data, and such values are not able to be changed by any means.

The 4 low-pass filters and 4 tuning circuits are used to filter-out the unwanted bands. The CPU used in this device, our parts code XA0851, vender's code M38267M8L272GP is exclusively programmed and burned for this US export model. Alinco, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the declared cellular frequencies. Moreover, the entire circuitry of this device is not designed to cover the cellular frequencies anyway.

To my best of knowledge being informed by the chief-engineer in charge of PH3 DR135TMKII, above declared is true.

Sincerely,

Kusuhara

Kazuhiro Kusuhara
Vice-Chief, Production Section
Electronics Div., Alinco,Inc.